



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-2-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
 Right-1@216-3985~3990(3988.065MHz,-34.
 097MHz,Limit-13,Margin[21.097])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-3-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
 216@0-3985~3990(3987.045MHz,-34.074M
 Hz,Limit-13,Margin[21.074])-PASS-see graph



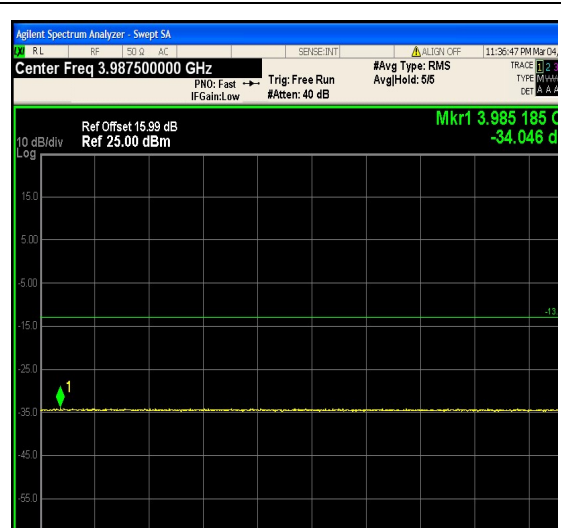
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-4-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Lef
 t-1@0-3985~3990(3986.315MHz,-34.194MH
 z,Limit-13,Margin[21.194])-PASS-see graph



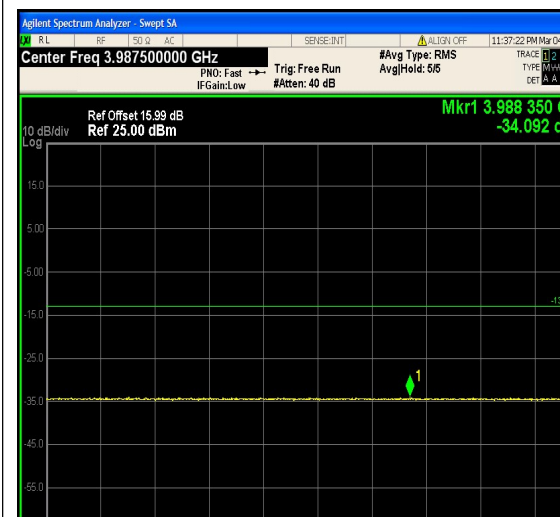
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-5-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Rig
 ht-1@216-3985~3990(3986.715MHz,-34.023
 MHz,Limit-13,Margin[21.023])-PASS-see
 graph



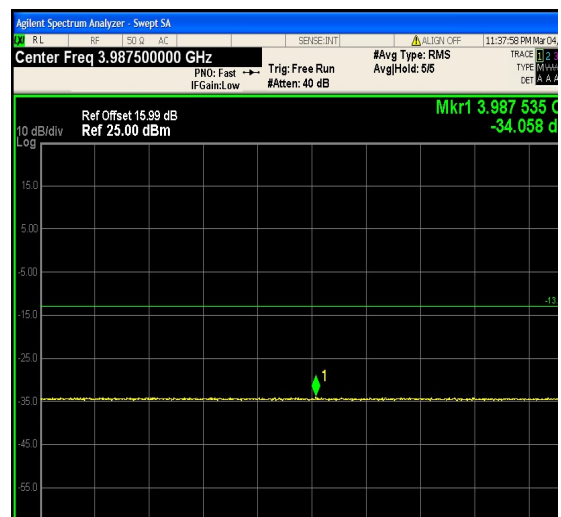
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-6-N/A-N/A-N/A-DFT-QPSK-Outer_Full-216
 @0-3985~3990(3987.83MHz,-34.081MHz, Li
 mit-13,Margin[21.081])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-7-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_L
 eft-1@0-3985~3990(3985.185MHz,-34.046M
 Hz,Limit-13,Margin[21.046])-PASS-see graph



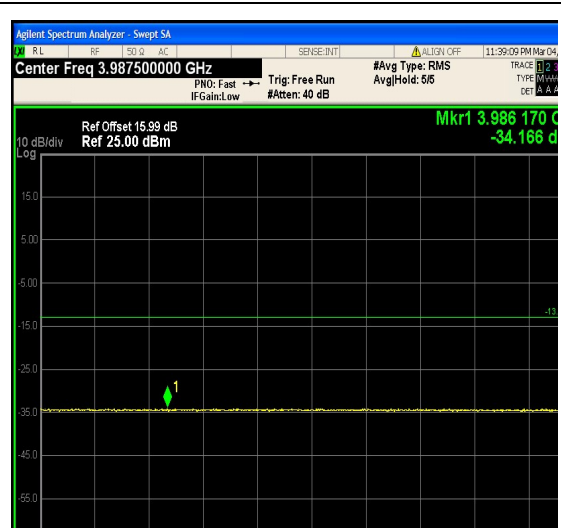
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-8-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_R
 ight-1@216-3985~3990(3988.35MHz,-34.092
 MHz,Limit-13,Margin[21.092])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-9-N/A-N/A-N/A-DFT-16QAM-Outer_Full-21
 6@0-3985~3990(3987.535MHz,-34.058MHz,
 Limit-13,Margin[21.058])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-10-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
 Left-1@0-3985~3990(3987.025MHz,-34.094
 MHz,Limit-13,Margin[21.094])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-11-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
 Right-1@216-3985~3990(3986.17MHz,-34.1
 66MHz,Limit-13,Margin[21.166])-PASS-see
 graph



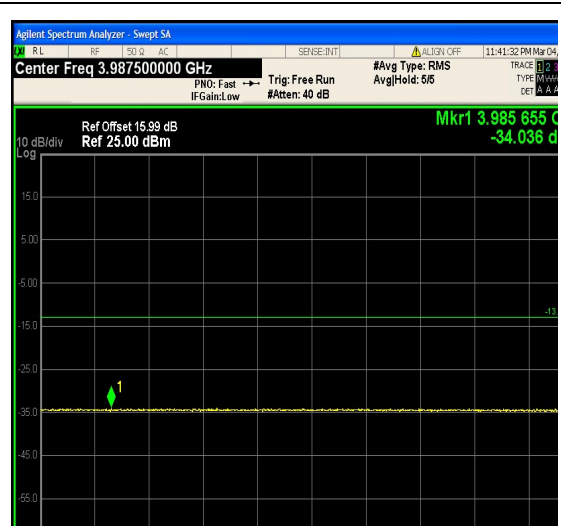
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-12-N/A-N/A-N/A-DFT-64QAM-Outer_Full-2
 16@0-3985~3990(3986.375MHz,-34.037MH
 z,Limit-13,Margin[21.037])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-13-N/A-N/A-N/A-DFT-256QAM-Edge_1RB
 _Left-1@0-3985~3990(3987.065MHz,-34.13
 1MHz,Limit-13,Margin[21.131])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-14-N/A-N/A-N/A-DFT-256QAM-Edge_1RB
 _Right-1@216-3985~3990(3988.49MHz,-34.
 087MHz,Limit-13,Margin[21.087])-PASS-see
 graph



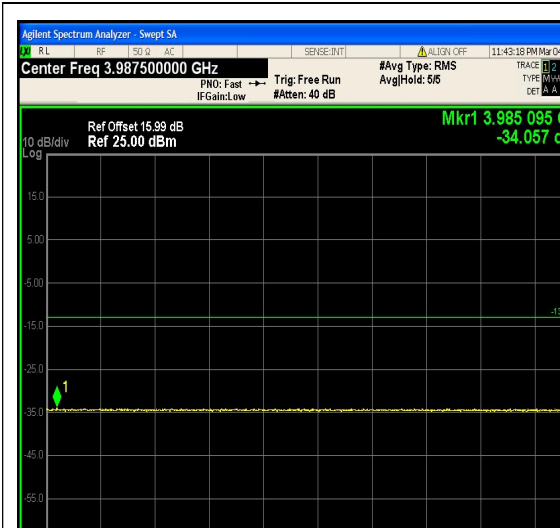
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-15-N/A-N/A-N/A-DFT-256QAM-Outer_Full-
 216@0-3985~3990(3985.655MHz,-34.036M
 Hz,Limit-13,Margin[21.036])-PASS-see graph



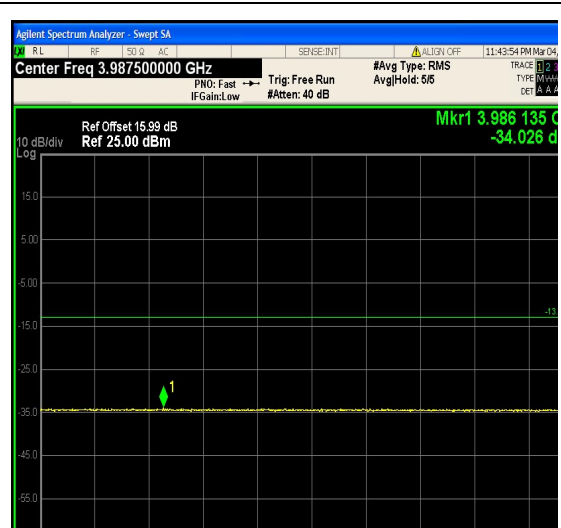
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-16-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Lef
 t-1@0-3985~3990(3989.785MHz,-34.147MH
 z,Limit-13,Margin[21.147])-PASS-see graph



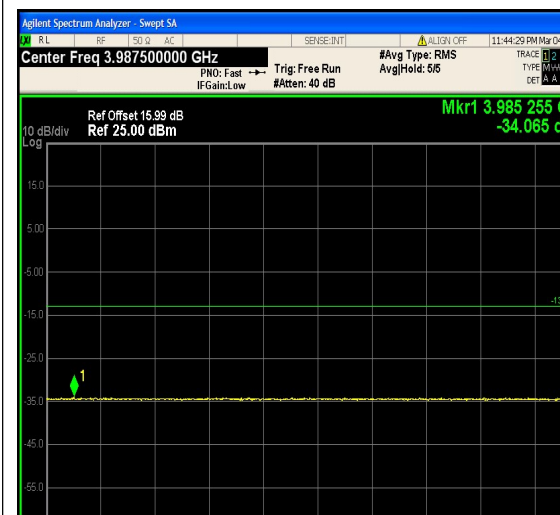
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-17-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Ri
 ght-1@216-3985~3990(3986.5MHz,-34.129
 MHz,Limit-13,Margin[21.129])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-217
 @0-3985~3990(3985.095MHz,-34.057MHz,L
 imit-13,Margin[21.057])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-19-N/A-N/A-N/A-CP-16QAM-Edge_1RB_L
 eft-1@0-3985~3990(3986.135MHz,-34.026M
 Hz,Limit-13,Margin[21.026])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-20-N/A-N/A-N/A-CP-16QAM-Edge_1RB_R
 ight-1@216-3985~3990(3985.255MHz,-34.06
 5MHz,Limit-13,Margin[21.065])-PASS-see
 graph



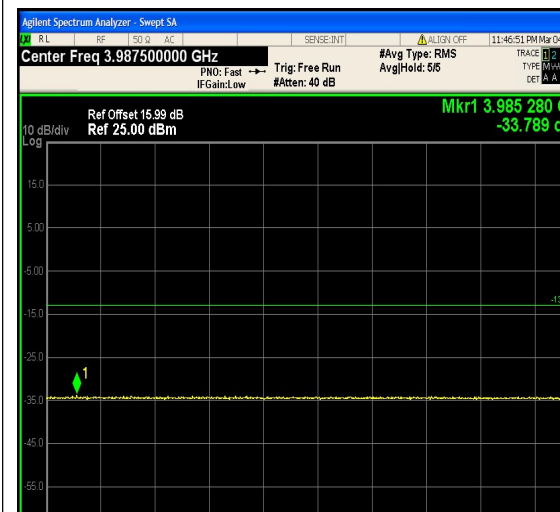
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-21-N/A-N/A-N/A-CP-16QAM-Outer_Full-21
 7@0-3985~3990(3985.105MHz,-33.998MHz,
 Limit-13,Margin[20.998])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-22-N/A-N/A-N/A-CP-64QAM-Edge_1RB_L
 eft-1@0-3985~3990(3986.515MHz,-34.105M
 Hz,Limit-13,Margin[21.105])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-23-N/A-N/A-N/A-CP-64QAM-Edge_1RB_R
 ight-1@216-3985~3990(3985.89MHz,-34.117
 MHz,Limit-13,Margin[21.117])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-21
 7@0-3985~3990(3985.28MHz,-33.789MHz,L
 imit-13,Margin[20.789])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-25-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
 Left-1@0-3985~3990(3986.79MHz,-34.111M
 Hz,Limit-13,Margin[21.111])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-26-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
 Right-1@216-3985~3990(3989.21MHz,-34.1
 16MHz,Limit-13,Margin[21.116])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
 H-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-2
 17@0-3985~3990(3985.3MHz,-34.011MHz,Li
 mit-13,Margin[21.011])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-1-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB
 _Left-1@0-3699.995~3799.995(3700.795MH
 z,9.62MHz,Limit30,Margin[20.38])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-2-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB
 _Right-1@272-3699.995~3799.995(3798.89
 5MHz,11.215MHz,Limit30,Margin[18.785])-P
 ASS-see graph



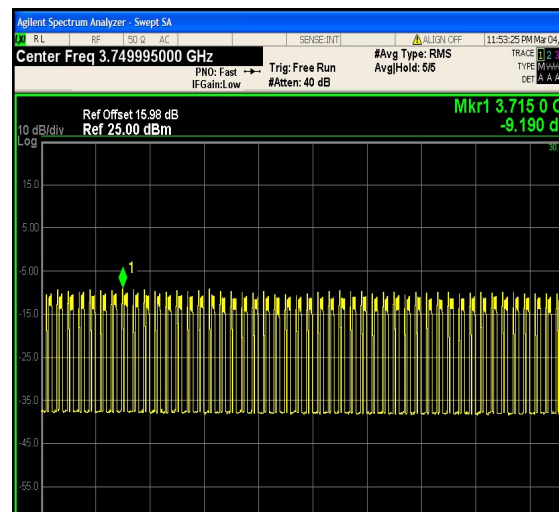
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-3-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
 270@0-3699.995~3799.995(3713.395MHz,-8
 .656MHz,Limit30,Margin[38.656])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-4-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Le
 ft-1@0-3699.995~3799.995(3700.995MHz,12
 .319MHz,Limit30,Margin[17.681])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-5-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Ri
 ght-1@272-3699.995~3799.995(3799.195M
 Hz,8.513MHz,Limit30,Margin[21.487])-PASS-
 see graph



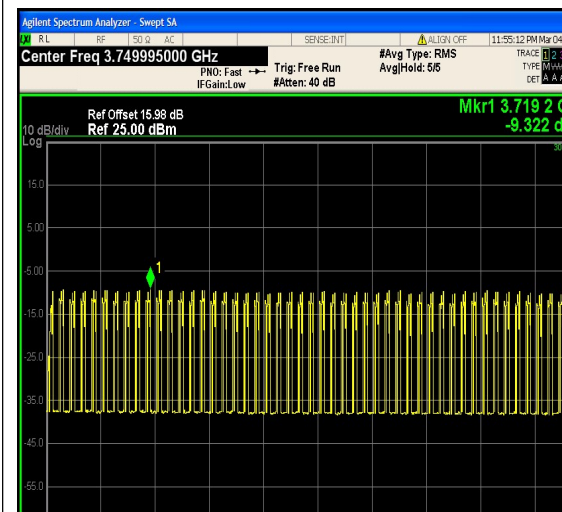
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-6-N/A-N/A-N/A-DFT-QPSK-Outer_Full-27
 0@0-3699.995~3799.995(3714.995MHz,-9.1
 9MHz,Limit30,Margin[39.19])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-7-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_
 Left-1@0-3699.995~3799.995(3700.895MHz,
 11.875MHz,Limit30,Margin[18.125])-PASS-se
 e graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-8-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_
 Right-1@272-3699.995~3799.995(3798.795
 MHz,10.443MHz,Limit30,Margin[19.557])-PA
 SS-see graph



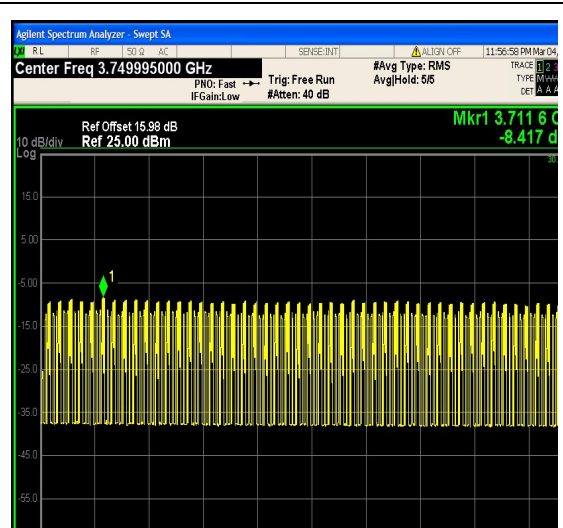
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-9-N/A-N/A-N/A-DFT-16QAM-Outer_Full-2
 70@0-3699.995~3799.995(3719.195MHz,-9.
 322MHz,Limit30,Margin[39.322])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-10-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
 Left-1@0-3699.995~3799.995(3701.295MH
 z,9.8MHz,Limit30,Margin[20.2])-PASS-see
 graph



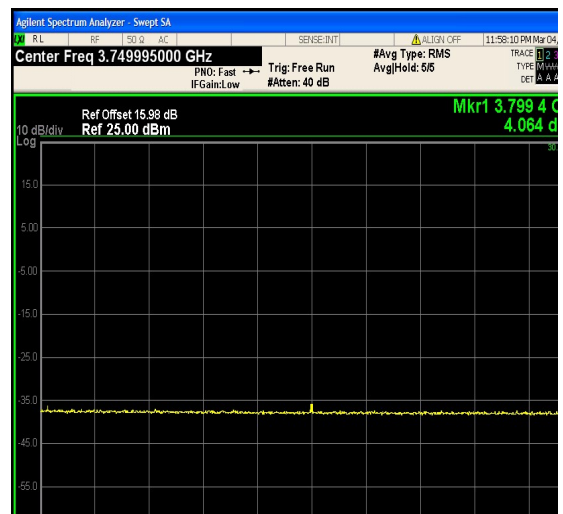
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-11-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
 Right-1@272-3699.995~3799.995(3798.795
 MHz,9.141MHz,Limit30,Margin[20.859])-PAS
 S-see graph



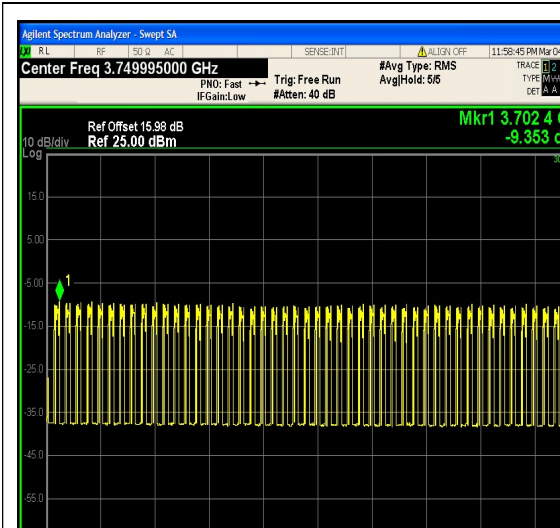
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-12-N/A-N/A-N/A-DFT-64QAM-Outer_Full-
 270@0-3699.995~3799.995(3711.595MHz,-8
 .417MHz,Limit30,Margin[38.417])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-13-N/A-N/A-N/A-DFT-256QAM-Edge_1R
 B_Left-1@0-3699.995~3799.995(3701.095M
 Hz,11.919MHz,Limit30,Margin[18.081])-PAS
 S-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-14-N/A-N/A-N/A-DFT-256QAM-Edge_1R
 B_Right-1@272-3699.995~3799.995(3799.3
 95MHz,4.064MHz,Limit30,Margin[25.936])-P
 ASS-see graph



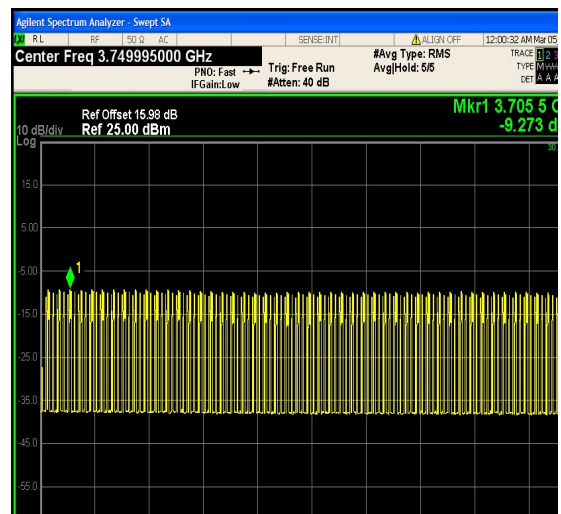
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-15-N/A-N/A-N/A-DFT-256QAM-Outer_Full
 -270@0-3699.995~3799.995(3702.395MHz,-
 9.353MHz,Limit30,Margin[39.353])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-16-N/A-N/A-N/A-CP-QPSK-Edge_1RB_L
 eft-1@0-3699.995~3799.995(3701.095MHz,1
 2.758MHz,Limit30,Margin[17.242])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-17-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Ri
 ght-1@272-3699.995~3799.995(3798.895M
 Hz,11.007MHz,Limit30,Margin[18.993])-PAS
 S-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
 +L-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-27
 3@0-3699.995~3799.995(3705.495MHz,-9.2
 73MHz,Limit30,Margin[39.273])-PASS-see
 graph