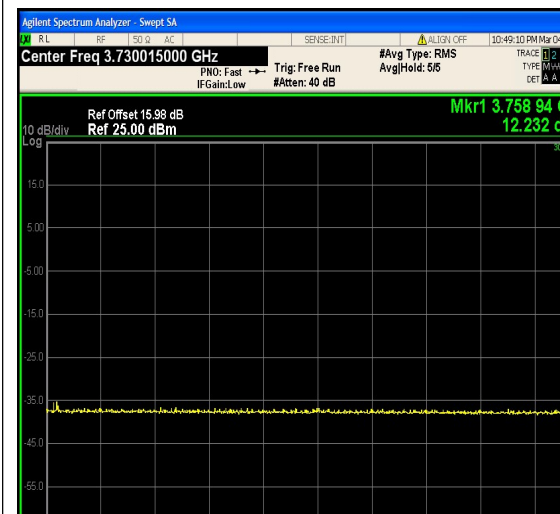


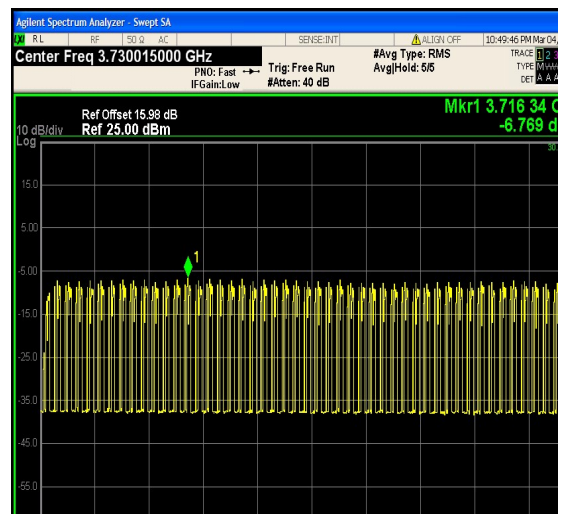
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-9-N/A-N/A-N/A-DFT-16QAM-Outer_Full-16
 2@0-3700.015~3760.015(3727.015MHz,-6.9
 8MHz,Limit30,Margin[36.98])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-10-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
 Left-1@0-3700.015~3760.015(3700.915MHz,
 12.3MHz,Limit30,Margin[17.7])-PASS-see
 graph



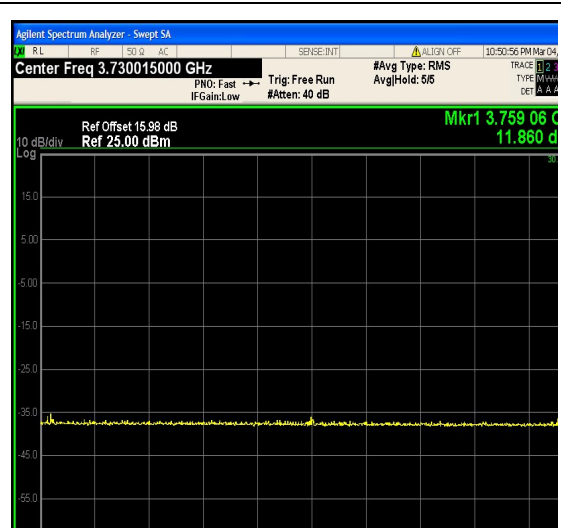
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-11-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
 Right-1@161-3700.015~3760.015(3758.935
 MHz,12.232MHz,Limit30,Margin[17.768])-PA
 SS-see graph



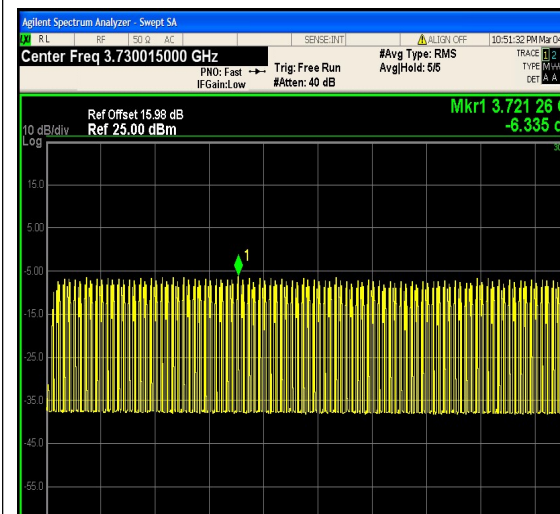
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-12-N/A-N/A-N/A-DFT-64QAM-Outer_Full-1
 62@0-3700.015~3760.015(3716.335MHz,-6.
 769MHz,Limit30,Margin[36.769])-PASS-see
 graph



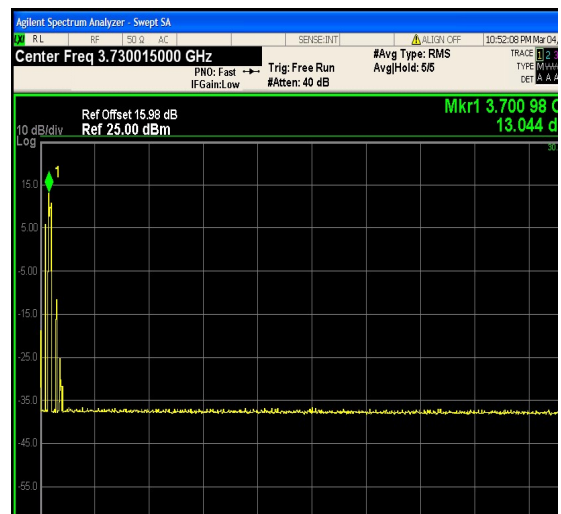
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-13-N/A-N/A-N/A-DFT-256QAM-Edge_1RB
_Left-1@0-3700.015~3760.015(3700.855MH
z,10.593MHz,Limit30,Margin[19.407])-PASS-
see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-14-N/A-N/A-N/A-DFT-256QAM-Edge_1RB
_Right-1@161-3700.015~3760.015(3759.05
5MHz,11.86MHz,Limit30,Margin[18.14])-PAS
S-see graph



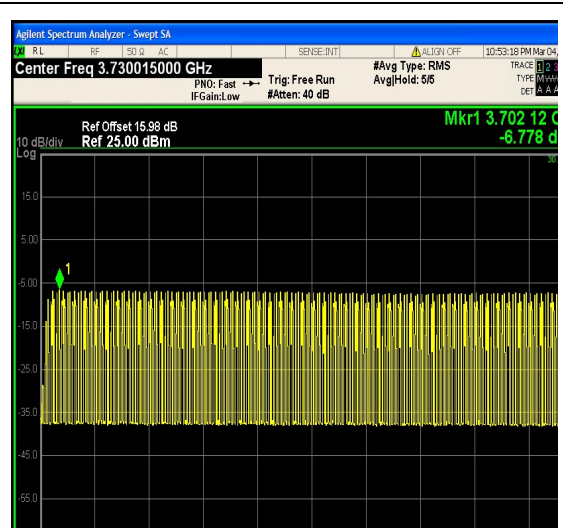
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-15-N/A-N/A-N/A-DFT-256QAM-Outer_Full-
162@0-3700.015~3760.015(3721.255MHz,-6
.335MHz,Limit30,Margin[36.335])-PASS-see
graph



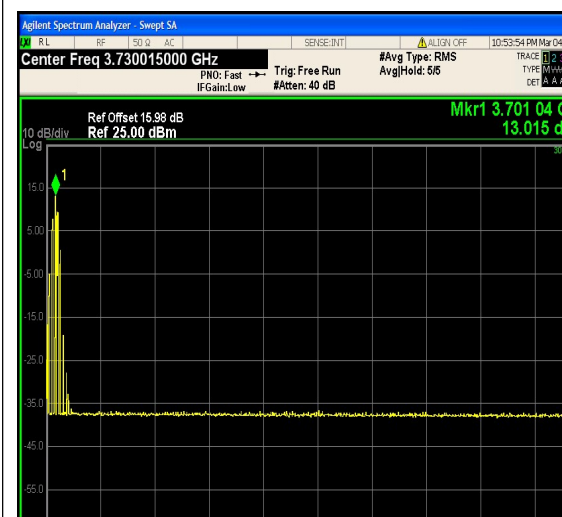
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-16-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Lef
t-1@0-3700.015~3760.015(3700.975MHz,13.
044MHz,Limit30,Margin[16.956])-PASS-see
graph



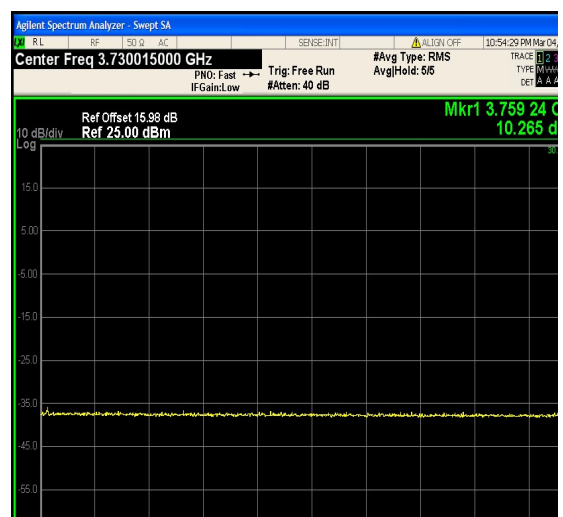
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+L-17-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Right-1@161-3700.015~3760.015(3759.055MHz, 12.181MHz, Limit30, Margin[17.819])-PASS-see graph



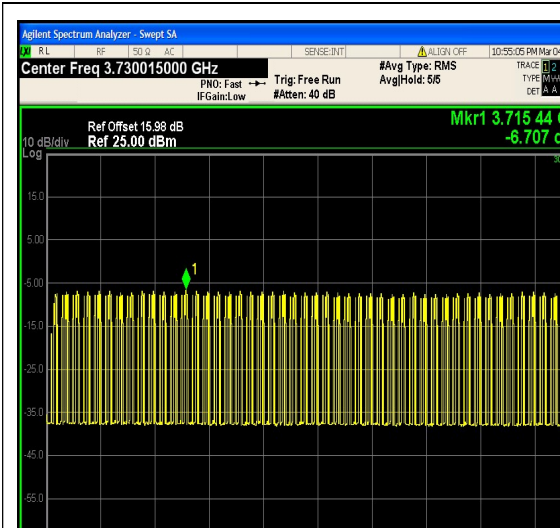
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+L-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-162@0-3700.015~3760.015(3702.115MHz, -6.778MHz, Limit30, Margin[36.778])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+L-19-N/A-N/A-N/A-CP-16QAM-Edge_1RB_Left-1@0-3700.015~3760.015(3701.035MHz, 13.015MHz, Limit30, Margin[16.985])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+L-20-N/A-N/A-N/A-CP-16QAM-Edge_1RB_Right-1@161-3700.015~3760.015(3759.235MHz, 10.265MHz, Limit30, Margin[19.735])-PASS-see graph



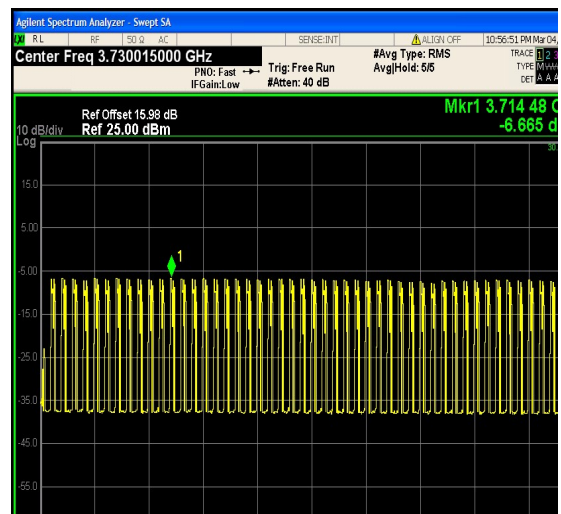
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-21-N/A-N/A-N/A-CP-16QAM-Outer_Full-16
 2@0-3700.015~3760.015(3715.435MHz,-6.7
 07MHz,Limit30,Margin[36.707])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-22-N/A-N/A-N/A-CP-64QAM-Edge_1RB_L
 eft-1@0-3700.015~3760.015(3701.035MHz,1
 0.652MHz,Limit30,Margin[19.348])-PASS-see
 graph



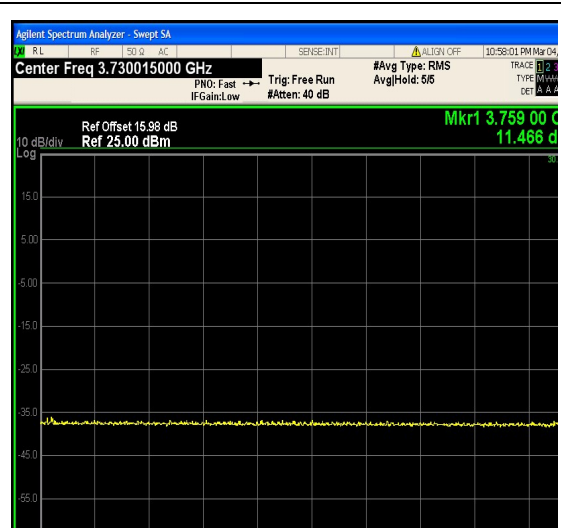
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-23-N/A-N/A-N/A-CP-64QAM-Edge_1RB_Ri
 ght-1@161-3700.015~3760.015(3759.115MH
 z,9.412MHz,Limit30,Margin[20.588])-PASS-s
 ee graph



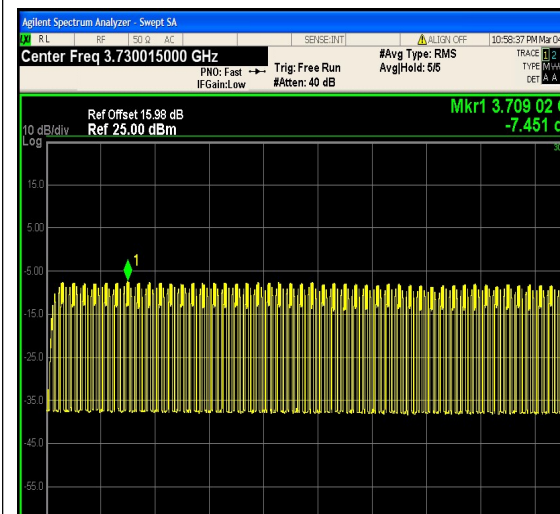
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-16
 2@0-3700.015~3760.015(3714.475MHz,-6.6
 65MHz,Limit30,Margin[36.665])-PASS-see
 graph



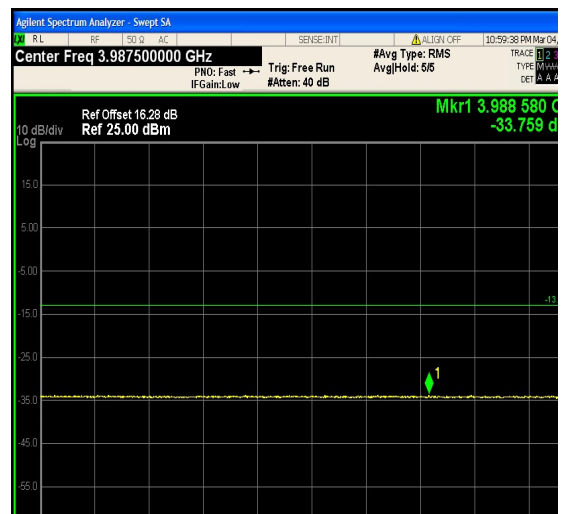
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-25-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
 Left-1@0-3700.015~3760.015(3700.855MHz,
 9.697MHz,Limit30,Margin[20.303])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-26-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
 Right-1@161-3700.015~3760.015(3758.995
 MHz,11.466MHz,Limit30,Margin[18.534])-PA
 SS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 L-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-1
 62@0-3700.015~3760.015(3709.015MHz,-7.
 451MHz,Limit30,Margin[37.451])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-1-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
 Left-1@0-3985~3990(3988.58MHz,-33.759M
 Hz,Limit-13,Margin[20.759])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-2-N/A-N/A-N/A-DFT-PI2BPSK-Edge_1RB_
 Right-1@161-3985~3990(3985.395MHz,-33.
 76MHz,Limit-13,Margin[20.76])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-3-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
 162@0-3985~3990(3988.53MHz,-33.607MH
 z,Limit-13,Margin[20.607])-PASS-see graph



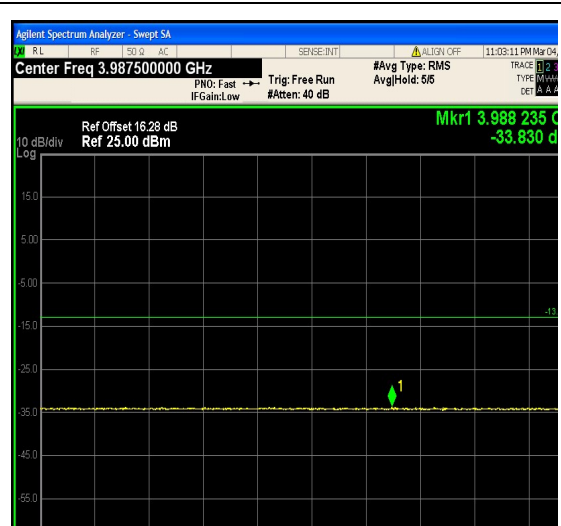
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-4-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Lef
 t-1@0-3985~3990(3988.49MHz,-33.827MHz,
 Limit-13,Margin[20.827])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-5-N/A-N/A-N/A-DFT-QPSK-Edge_1RB_Rig
 ht-1@161-3985~3990(3989.65MHz,-33.766
 MHz,Limit-13,Margin[20.766])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-6-N/A-N/A-N/A-DFT-QPSK-Outer_Full-162
 @0-3985~3990(3985.075MHz,-33.628MHz,L
 imit-13,Margin[20.628])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-7-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_L
 eft-1@0-3985~3990(3988.235MHz,-33.83MH
 z,Limit-13,Margin[20.83])-PASS-see graph



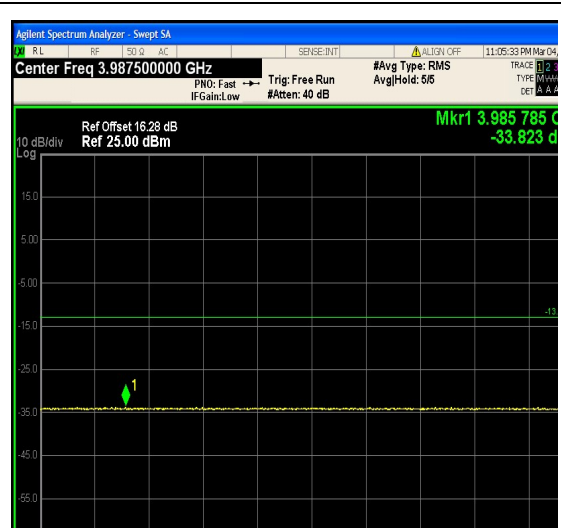
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-8-N/A-N/A-N/A-DFT-16QAM-Edge_1RB_R
 ight-1@161-3985~3990(3986.085MHz,-33.8
 MHz,Limit-13,Margin[20.8])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-9-N/A-N/A-N/A-DFT-16QAM-Outer_Full-16
 2@0-3985~3990(3989.695MHz,-33.594MHz,
 Limit-13,Margin[20.594])-PASS-see graph



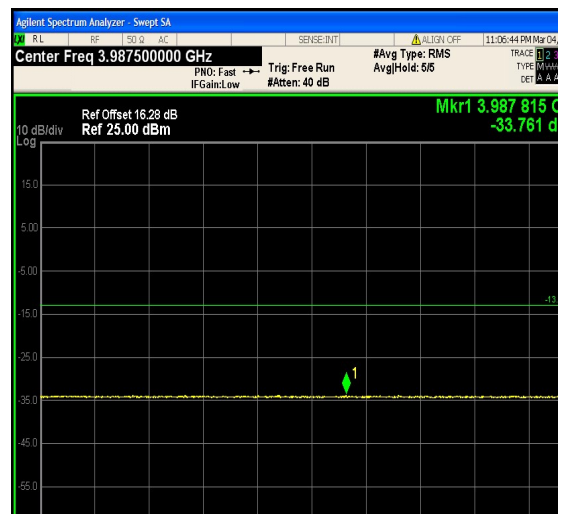
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-10-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
 Left-1@0-3985~3990(3985.655MHz,-33.754
 MHz,Limit-13,Margin[20.754])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-11-N/A-N/A-N/A-DFT-64QAM-Edge_1RB_
 Right-1@161-3985~3990(3985.785MHz,-33.
 823MHz,Limit-13,Margin[20.823])-PASS-see
 graph



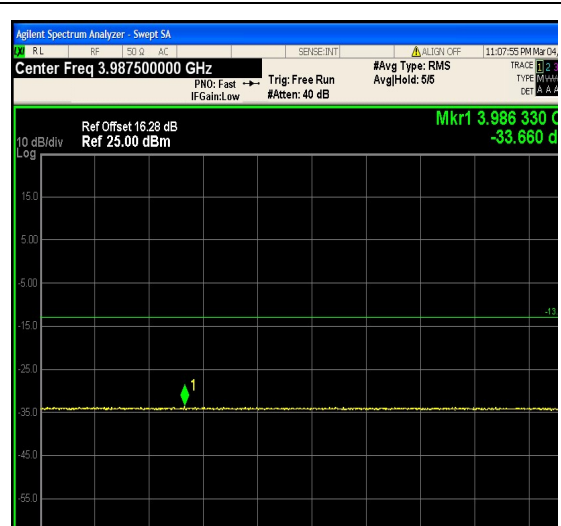
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-12-N/A-N/A-N/A-DFT-64QAM-Outer_Full-1
 62@0-3985~3990(3987.775MHz,-33.677MH
 z,Limit-13,Margin[20.677])-PASS-see graph



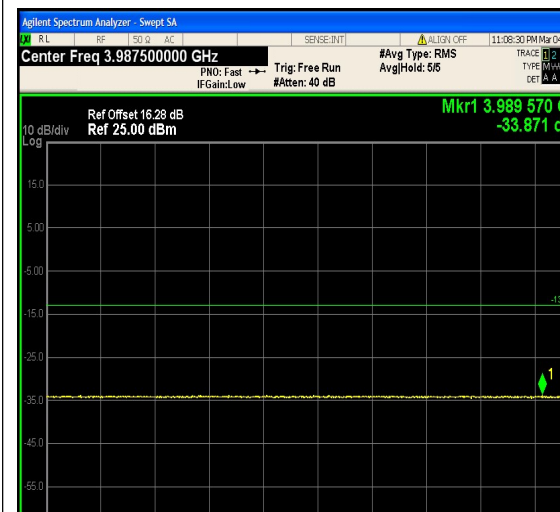
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-13-N/A-N/A-N/A-DFT-256QAM-Edge_1RB
 _Left-1@0-3985~3990(3987.815MHz,-33.76
 1MHz,Limit-13,Margin[20.761])-PASS-see
 graph



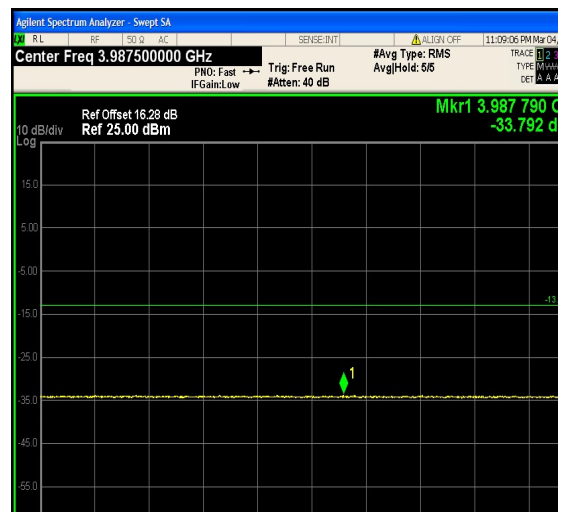
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-14-N/A-N/A-N/A-DFT-256QAM-Edge_1RB
 _Right-1@161-3985~3990(3985.715MHz,-33
 .806MHz,Limit-13,Margin[20.806])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-15-N/A-N/A-N/A-DFT-256QAM-Outer_Full-
 162@0-3985~3990(3986.33MHz,-33.66MHz,
 Limit-13,Margin[20.66])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-16-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Lef
 t-1@0-3985~3990(3989.57MHz,-33.871MHz,
 Limit-13,Margin[20.871])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-17-N/A-N/A-N/A-CP-QPSK-Edge_1RB_Ri
 ght-1@161-3985~3990(3987.79MHz,-33.792
 MHz,Limit-13,Margin[20.792])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-18-N/A-N/A-N/A-CP-QPSK-Outer_Full-162
 @0-3985~3990(3985.92MHz,-33.567MHz,Limit-13,Margin[20.567])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-19-N/A-N/A-N/A-CP-16QAM-Edge_1RB_L
 eft-1@0-3985~3990(3986.145MHz,-33.822MHz,Limit-13,Margin[20.822])-PASS-see graph



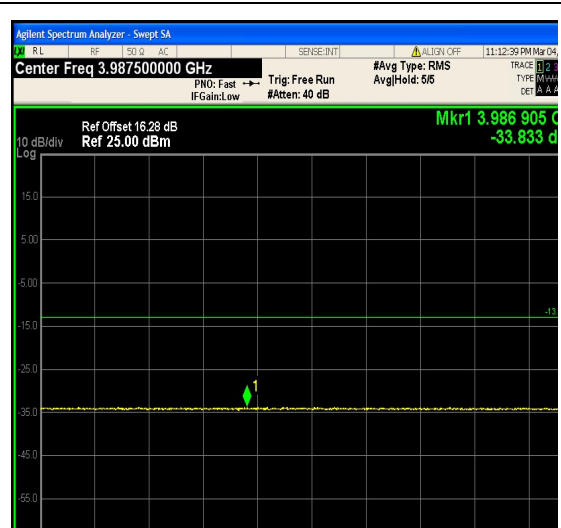
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-20-N/A-N/A-N/A-CP-16QAM-Edge_1RB_R
 ight-1@161-3985~3990(3986.295MHz,-33.804MHz,Limit-13,Margin[20.804])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-21-N/A-N/A-N/A-CP-16QAM-Outer_Full-162
 @0-3985~3990(3985.56MHz,-33.591MHz,Limit-13,Margin[20.591])-PASS-see graph



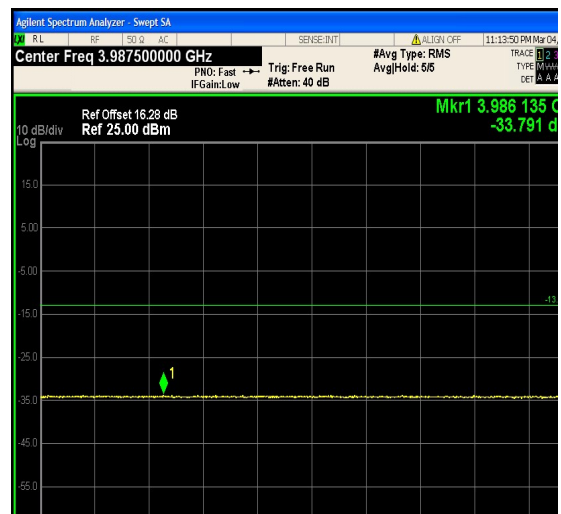
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-22-N/A-N/A-N/A-CP-64QAM-Edge_1RB_L
 eft-1@0-3985~3990(3986.64MHz,-33.86MHz
 ,Limit-13,Margin[20.86])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-23-N/A-N/A-N/A-CP-64QAM-Edge_1RB_R
 ight-1@161-3985~3990(3986.905MHz,-33.83
 3MHz,Limit-13,Margin[20.833])-PASS-see
 graph



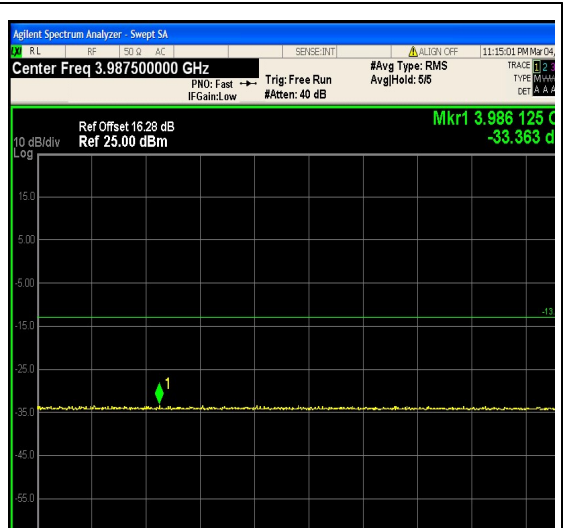
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-24-N/A-N/A-N/A-CP-64QAM-Outer_Full-16
 2@0-3985~3990(3986.245MHz,-33.468MHz,
 Limit-13,Margin[20.468])-PASS-see graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-25-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
 Left-1@0-3985~3990(3986.135MHz,-33.791
 MHz,Limit-13,Margin[20.791])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-26-N/A-N/A-N/A-CP-256QAM-Edge_1RB_
 Right-1@161-3985~3990(3988.235MHz,-33.
 69MHz,Limit-13,Margin[20.69])-PASS-see
 graph



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-27-N/A-N/A-N/A-CP-256QAM-Outer_Full-1
 62@0-3985~3990(3986.125MHz,-33.363MH
 z,Limit-13,Margin[20.363])-PASS-see graph

Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Result	Limit	Verdict
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-70.23	-43	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.98	-33	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-63.68	-23	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-28.19	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.35	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.43	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-42.95	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Rig ht	0.009	0.15	-69.07	-43	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Rig ht	0.15	30	-66.86	-33	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Rig ht	30	1000	-63.80	-23	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Rig ht	1000	3000	-49.65	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Rig ht	3000	6000	-49.22	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Rig ht	6000	26000	-33.63	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Rig ht	26000	40000	-43.12	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-69.22	-43	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.66	-33	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-63.86	-23	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-48.23	-13	PASS

DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.52	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.57	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-43.21	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Rig ht	0.009	0.15	-69.89	-43	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Rig ht	0.15	30	-66.21	-33	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Rig ht	30	1000	-63.63	-23	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Rig ht	1000	3000	-45.60	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Rig ht	3000	6000	-49.43	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Rig ht	6000	26000	-33.46	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Rig ht	26000	40000	-43.18	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-67.28	-43	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.29	-33	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-63.11	-23	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-33.20	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.46	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.80	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-43.05	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Rig ht	0.009	0.15	-69.71	-43	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Rig ht	0.15	30	-65.40	-33	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Rig ht	30	1000	-63.58	-23	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Rig ht	1000	3000	-52.72	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Rig ht	3000	6000	-49.42	-13	PASS

DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Rig ht	6000	26000	-33.31	-13	PASS
DC_2A_n77 A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Rig ht	26000	40000	-43.15	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-69.67	-43	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-64.89	-33	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-63.64	-23	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-52.90	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.49	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.51	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-43.12	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Rig ht	0.009	0.15	-68.45	-43	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Rig ht	0.15	30	-65.08	-33	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Rig ht	30	1000	-63.38	-23	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Rig ht	1000	3000	-52.86	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Rig ht	3000	6000	-49.26	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Rig ht	6000	26000	-33.71	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Rig ht	26000	40000	-42.90	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.55	-43	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-64.48	-33	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-62.61	-23	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-52.72	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.41	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.77	-13	PASS

DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-43.29	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Rig ht	0.009	0.15	-68.79	-43	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Rig ht	0.15	30	-66.12	-33	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Rig ht	30	1000	-63.68	-23	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Rig ht	1000	3000	-52.80	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Rig ht	3000	6000	-49.39	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Rig ht	6000	26000	-33.37	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Rig ht	26000	40000	-43.24	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-67.03	-43	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-67.32	-33	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-63.58	-23	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-52.49	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.63	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.55	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-42.96	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Rig ht	0.009	0.15	-68.65	-43	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Rig ht	0.15	30	-65.84	-33	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Rig ht	30	1000	-63.71	-23	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Rig ht	1000	3000	-52.02	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Rig ht	3000	6000	-49.35	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Rig ht	6000	26000	-33.37	-13	PASS
DC_2A_n77 A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Rig ht	26000	40000	-43.13	-13	PASS

DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-68.70	-43	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-66.44	-33	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-63.47	-23	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-52.86	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.52	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.73	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-42.96	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Rig ht	0.009	0.15	-69.84	-43	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Rig ht	0.15	30	-64.85	-33	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Rig ht	30	1000	-63.62	-23	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Rig ht	1000	3000	-52.74	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Rig ht	3000	6000	-49.55	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Rig ht	6000	26000	-33.77	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Rig ht	26000	40000	-43.29	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-68.86	-43	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-67.28	-33	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-63.66	-23	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-52.79	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.45	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.55	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-43.15	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Rig ht	0.009	0.15	-70.05	-43	PASS

DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Rig ht	0.15	30	-66.71	-33	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Rig ht	30	1000	-63.89	-23	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Rig ht	1000	3000	-52.53	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Rig ht	3000	6000	-49.51	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Rig ht	6000	26000	-33.77	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Rig ht	26000	40000	-42.91	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-69.95	-43	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-66.51	-33	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-63.55	-23	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-52.64	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.44	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.44	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-43.37	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Rig ht	0.009	0.15	-70.00	-43	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Rig ht	0.15	30	-67.32	-33	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Rig ht	30	1000	-63.80	-23	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Rig ht	1000	3000	-52.80	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Rig ht	3000	6000	-49.31	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Rig ht	6000	26000	-33.51	-13	PASS
DC_2A_n77 A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Rig ht	26000	40000	-43.16	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-67.87	-43	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-1000. 00	-33	PASS

DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-63.68	-23	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-52.83	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	3000	6000	-49.25	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-33.73	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-42.95	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Rig ht	0.009	0.15	-70.66	-43	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Rig ht	0.15	30	-67.18	-33	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Rig ht	30	1000	-63.26	-23	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Rig ht	1000	3000	-52.71	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Rig ht	3000	6000	-49.45	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Rig ht	6000	26000	-33.74	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Rig ht	26000	40000	-43.11	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-69.93	-43	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-65.87	-33	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-63.90	-23	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-52.69	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	6000	-49.40	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-33.63	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-43.18	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Rig ht	0.009	0.15	-69.54	-43	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Rig ht	0.15	30	-65.65	-33	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Rig ht	30	1000	-63.83	-23	PASS

DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Rig ht	1000	3000	-52.15	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Rig ht	3000	6000	-49.30	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Rig ht	6000	26000	-33.77	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Rig ht	26000	40000	-43.26	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-68.75	-43	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-68.78	-33	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-63.84	-23	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-52.69	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	6000	-49.35	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-33.77	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-43.07	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Rig ht	0.009	0.15	-69.14	-43	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Rig ht	0.15	30	-66.12	-33	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Rig ht	30	1000	-63.87	-23	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Rig ht	1000	3000	-52.72	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Rig ht	3000	6000	-49.37	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Rig ht	6000	26000	-33.67	-13	PASS
DC_2A_n77 A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Rig ht	26000	40000	-43.15	-13	PASS
DC_2A_n77 A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-68.80	-43	PASS
DC_2A_n77 A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-64.95	-33	PASS
DC_2A_n77 A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-63.63	-23	PASS
DC_2A_n77 A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-52.04	-13	PASS