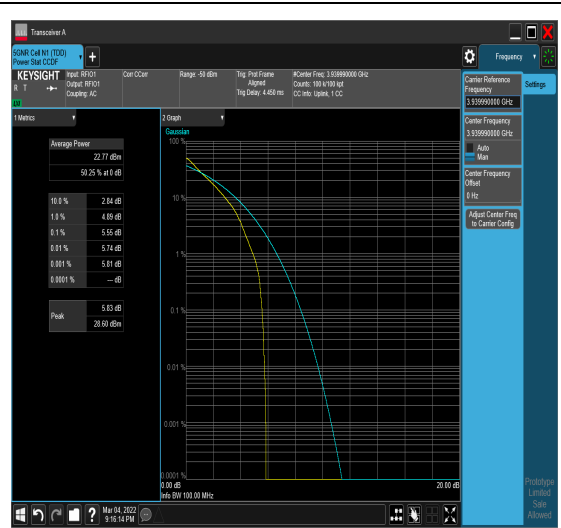
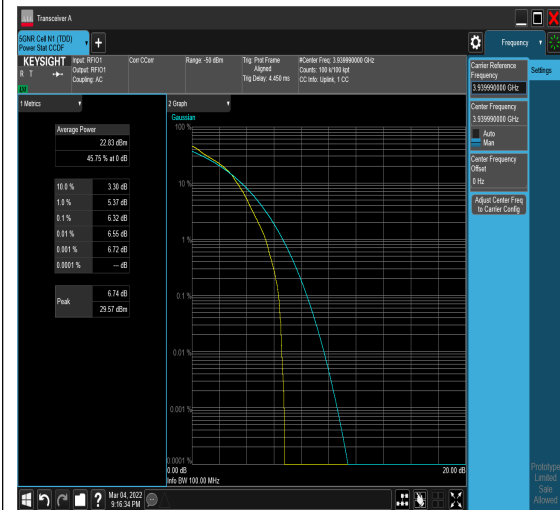


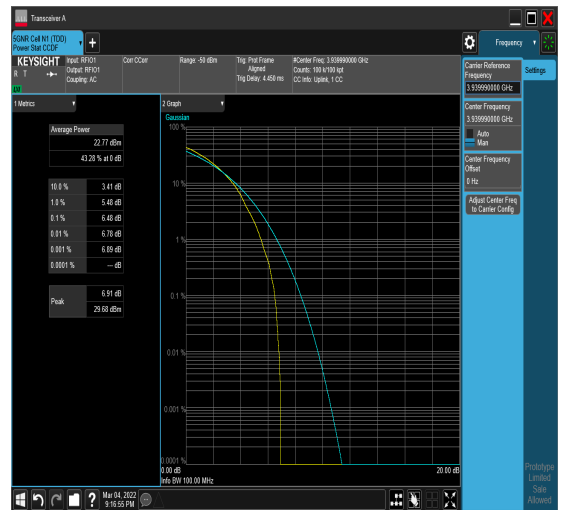
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
H-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
216@0-4.56-100%-0.00-4.56-PASS_≤13



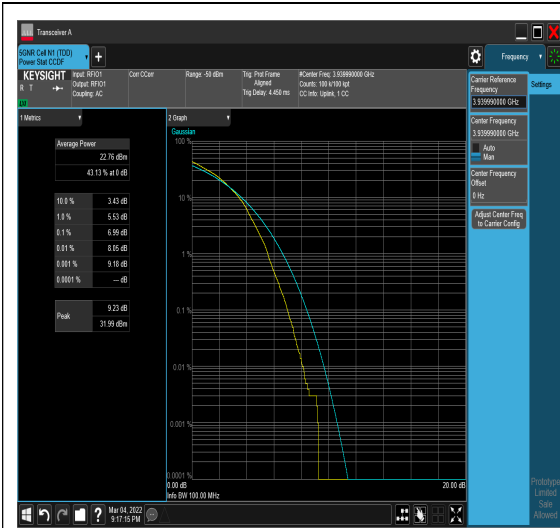
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
H-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-216
@0-5.55-100%-0.00-5.55-PASS_≤13



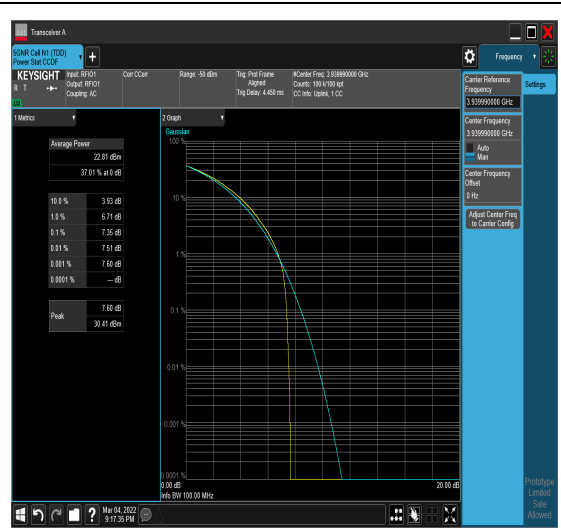
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
H-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-21
6@0-6.32-100%-0.00-6.32-PASS_≤13



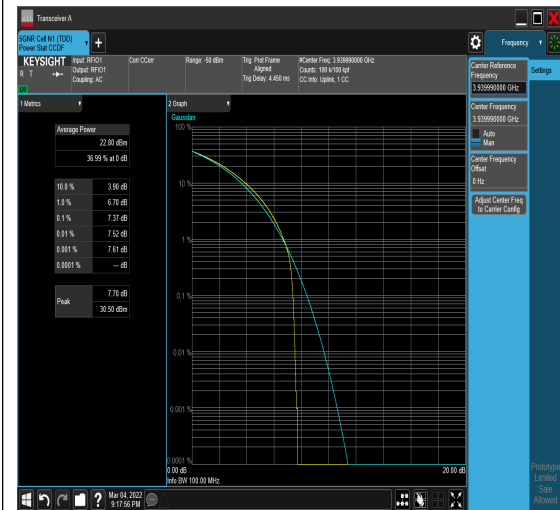
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
H-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-21
6@0-6.48-100%-0.00-6.48-PASS_≤13



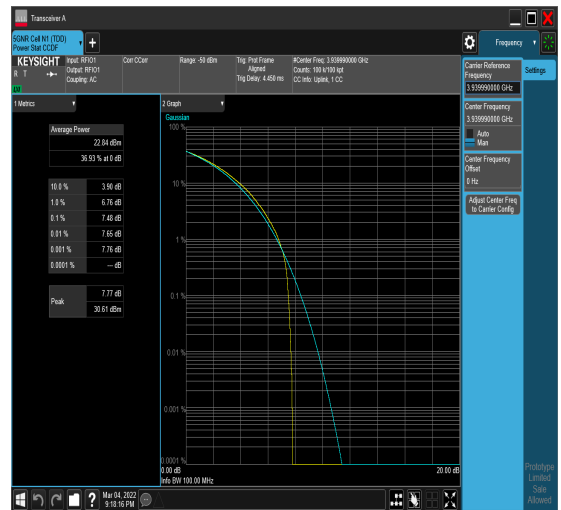
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
H-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-2
16@0-6.99-100%-0.00-6.99-PASS-≤13



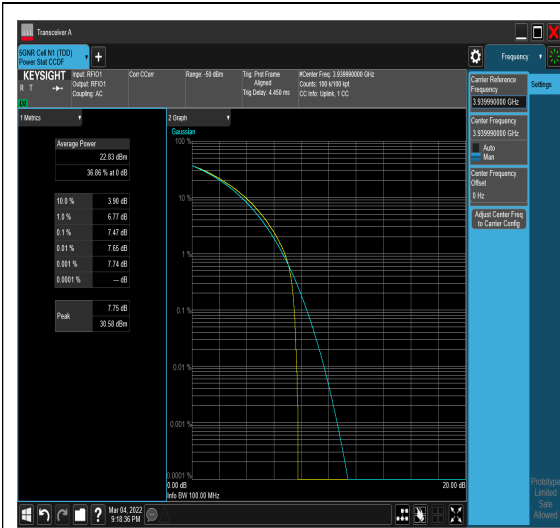
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
H-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-217
@0-7.35-100%-0.00-7.35-PASS-≤13



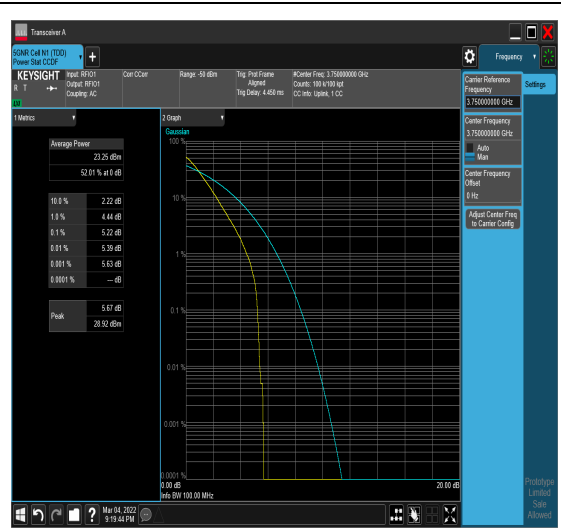
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
H-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-217
@0-7.37-100%-0.00-7.37-PASS-≤13



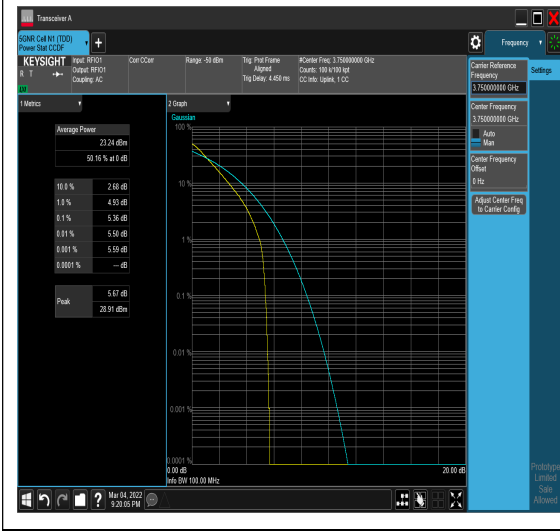
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+
H-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-217
@0-7.48-100%-0.00-7.48-PASS-≤13



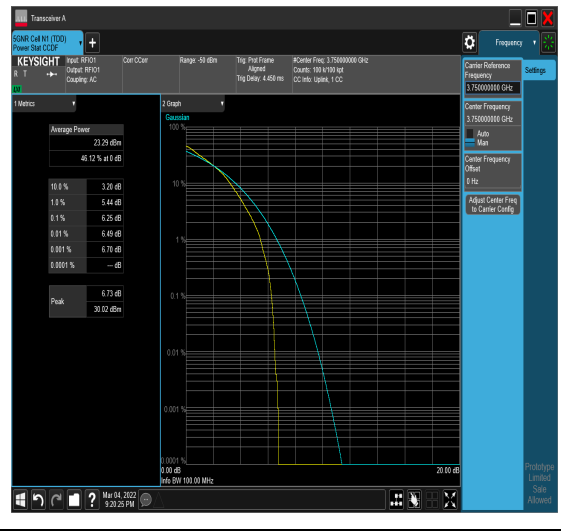
1-DC_2A_n77A-3700-3980-PC3-30-5+80-M+H-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-217@0-7.47-100%-0.00-7.47-PASS-≤13



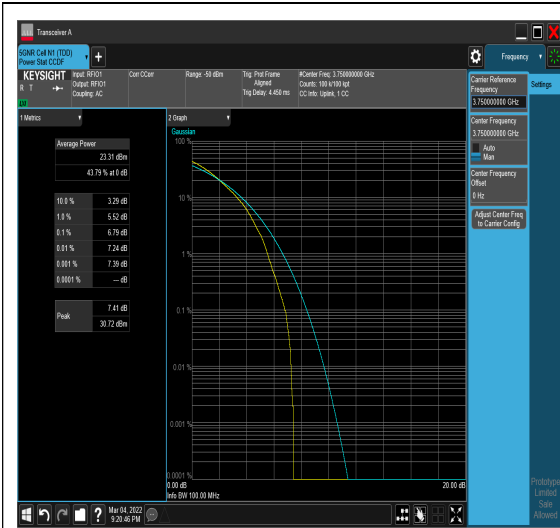
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M+L-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-270@0-5.22-100%-0.00-5.22-PASS-≤13



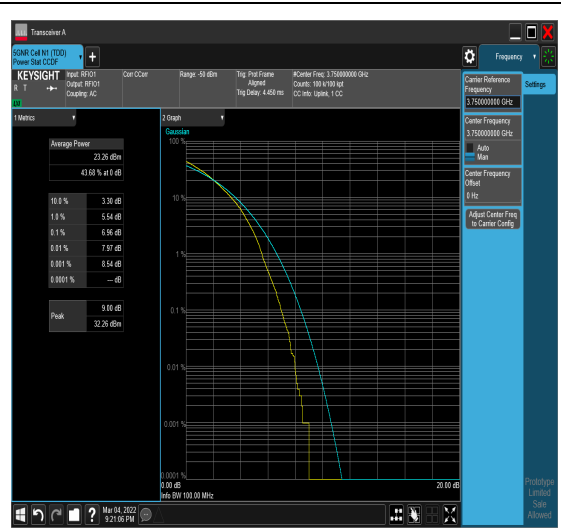
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M+L-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-270@0-5.36-100%-0.00-5.36-PASS-≤13



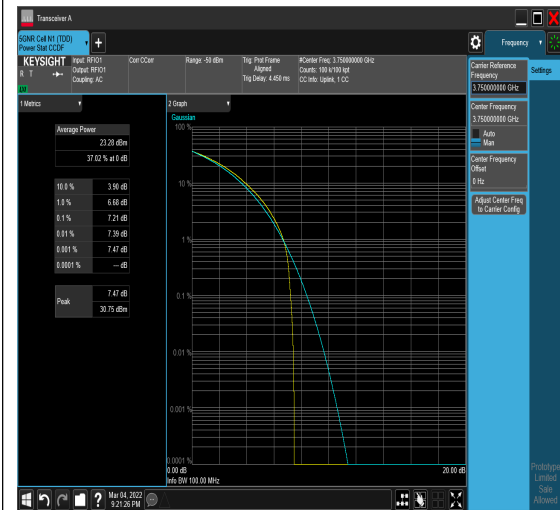
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M+L-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-270@0-6.25-100%-0.00-6.25-PASS-≤13



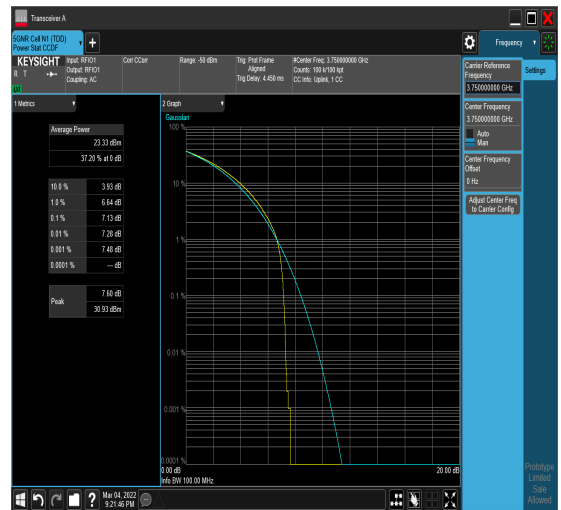
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+L-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-2
70@0-6.79-100%-0.00-6.79-PASS-≤13



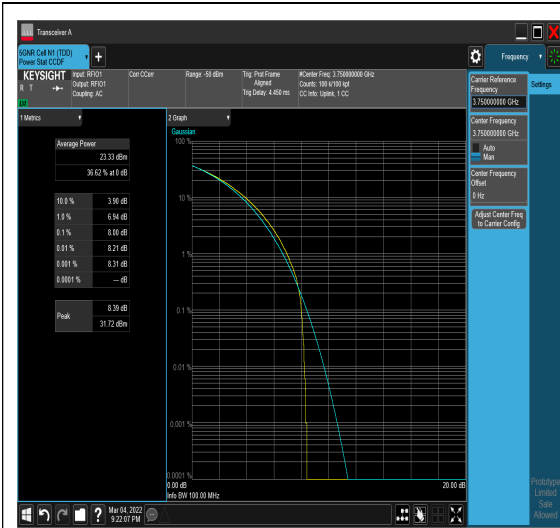
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+L-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-
270@0-6.96-100%-0.00-6.96-PASS-≤13



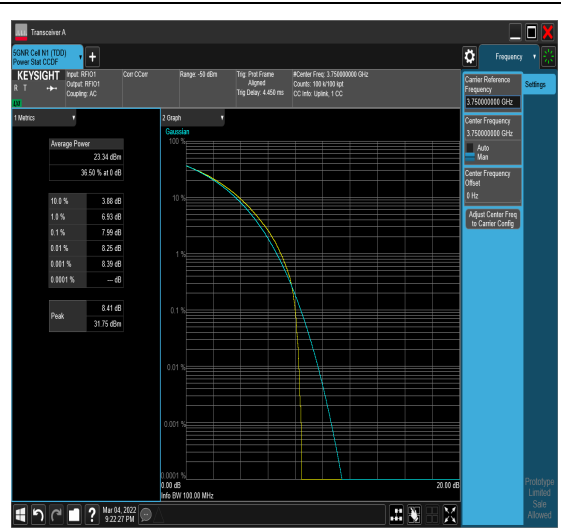
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+L-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-273
@0-7.21-100%-0.00-7.21-PASS-≤13



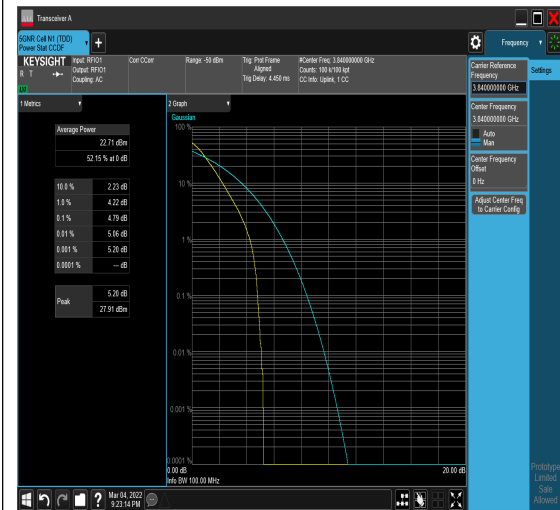
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+L-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-27
3@0-7.13-100%-0.00-7.13-PASS-≤13



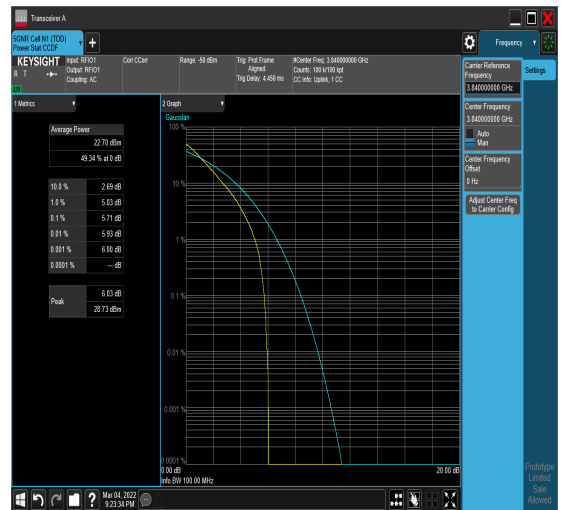
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+L-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-27
3@0-8.00-100%-0.00-8.00-PASS-≤13



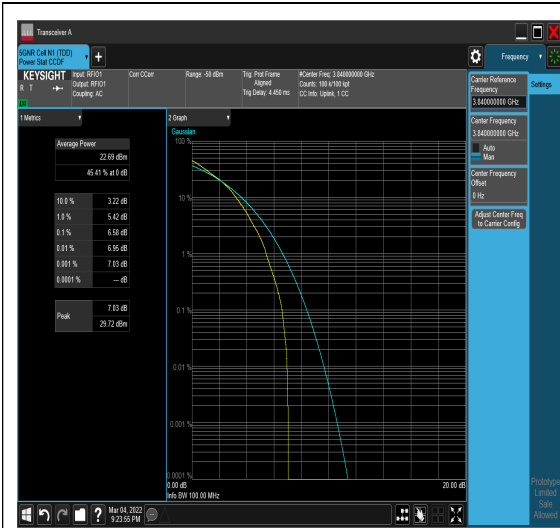
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+L-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-2
73@0-7.99-100%-0.00-7.99-PASS-≤13



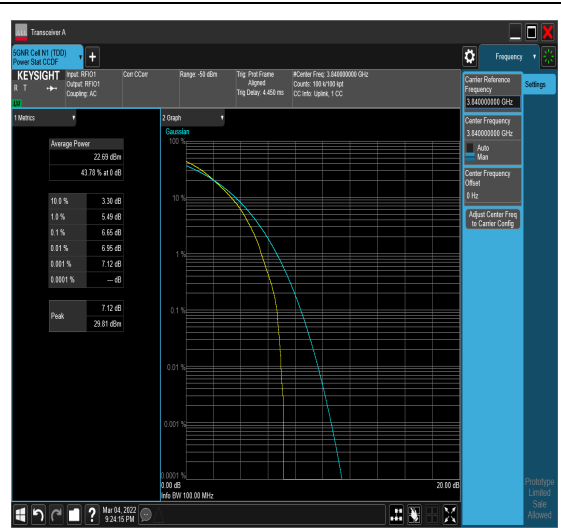
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+M-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full
-270@0-4.79-100%-0.00-4.79-PASS-≤13



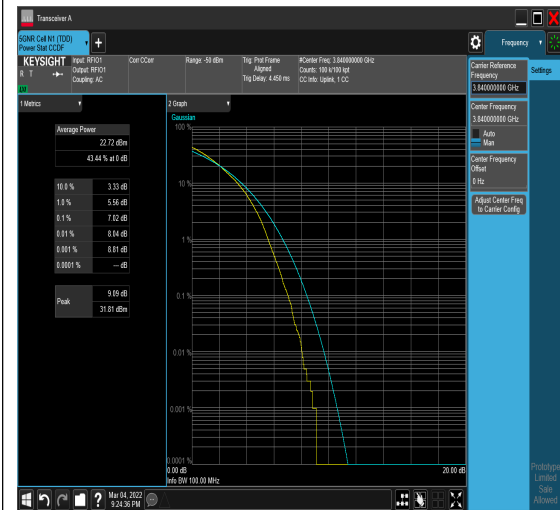
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+M-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-27
0@0-5.71-100%-0.00-5.71-PASS-≤13



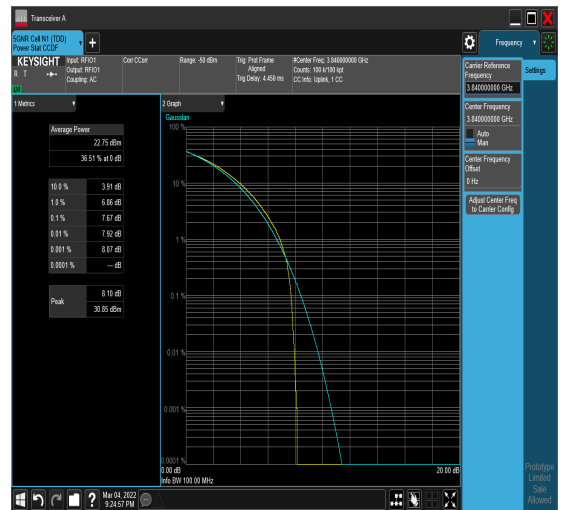
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+M-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-2
70@0-6.58-100%-0.00-6.58-PASS-≤13



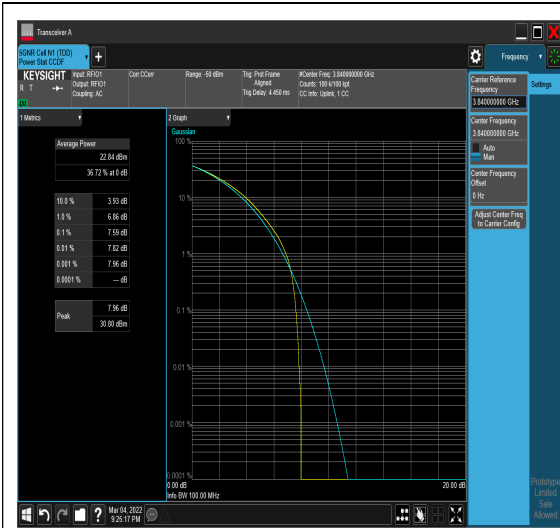
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+M-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-2
70@0-6.65-100%-0.00-6.65-PASS-≤13



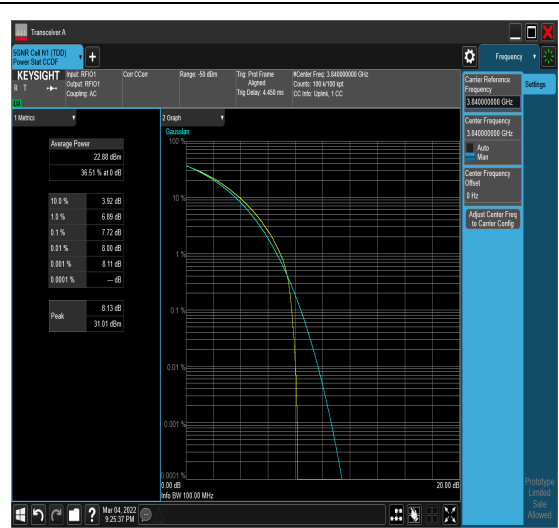
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+M-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-
270@0-7.02-100%-0.00-7.02-PASS-≤13



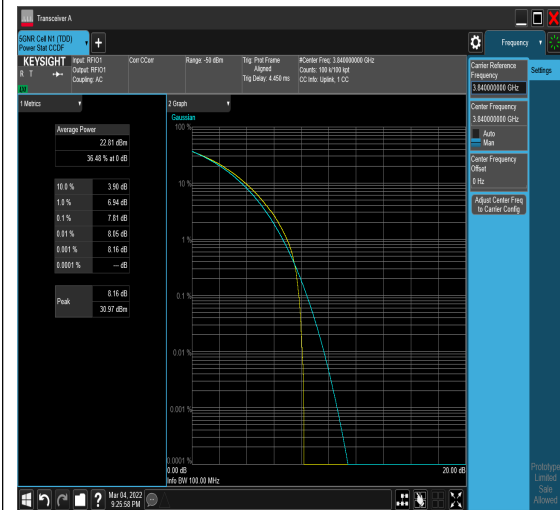
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+M-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-273
@0-7.67-100%-0.00-7.67-PASS-≤13



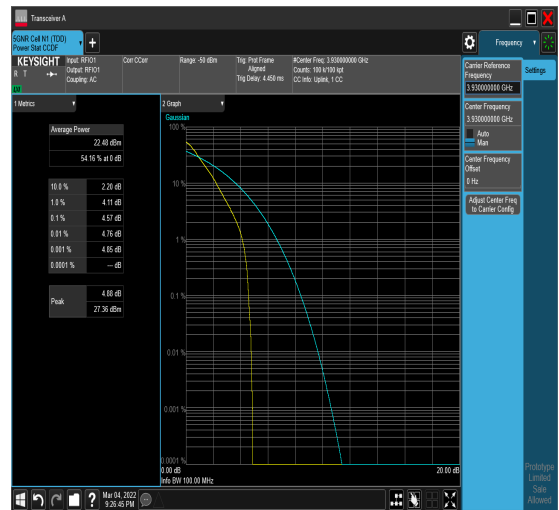
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+M-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-27
3@0-7.59-100%-0.00-7.59-PASS-≤13



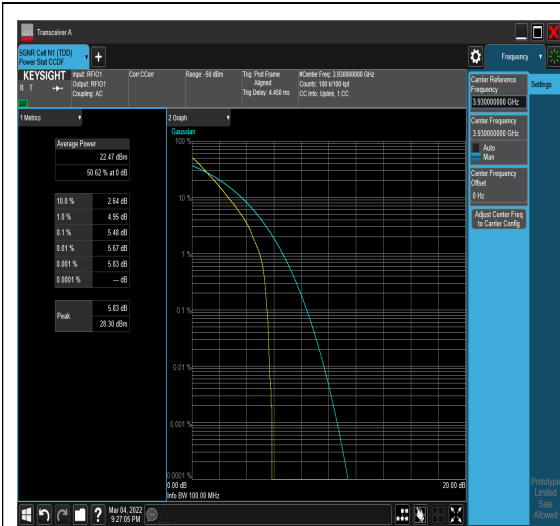
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+M-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-27
3@0-7.72-100%-0.00-7.72-PASS-≤13



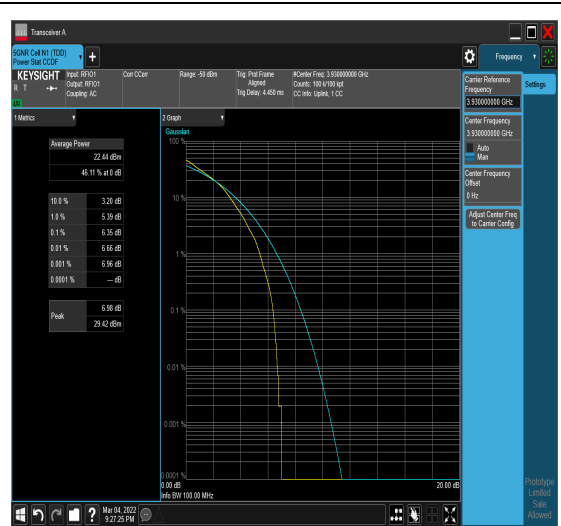
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+M-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-2
73@0-7.81-100%-0.00-7.81-PASS-≤13



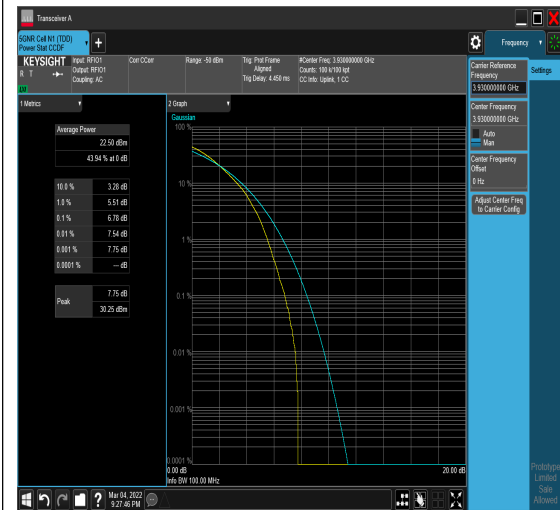
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full
-270@0-4.57-100%-0.00-4.57-PASS-≤13



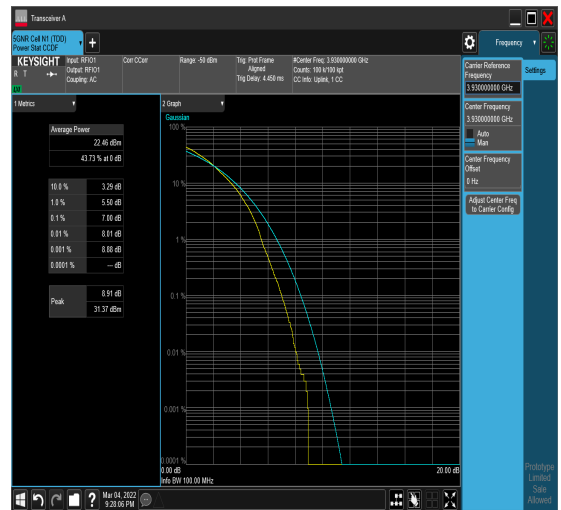
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M +H-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-27 @0-5.48-100%-0.00-5.48-PASS-≤13



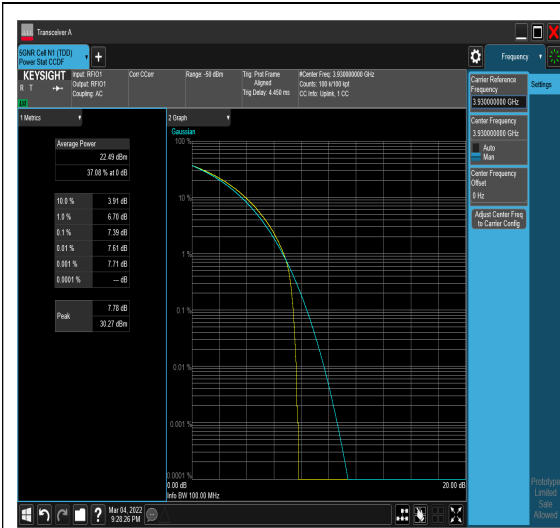
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M +H-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-27 @0-6.35-100%-0.00-6.35-PASS-≤13



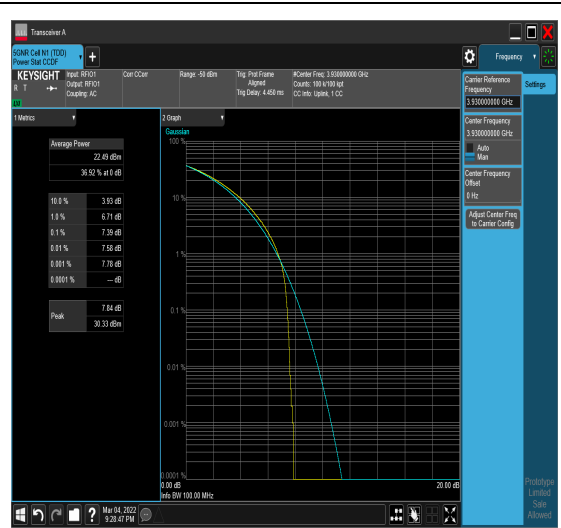
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M +H-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-27 @0-6.78-100%-0.00-6.78-PASS-≤13



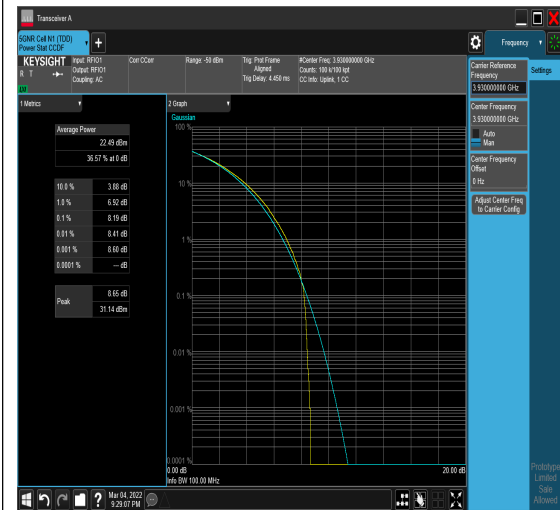
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M +H-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-270 @0-7.00-100%-0.00-7.00-PASS-≤13



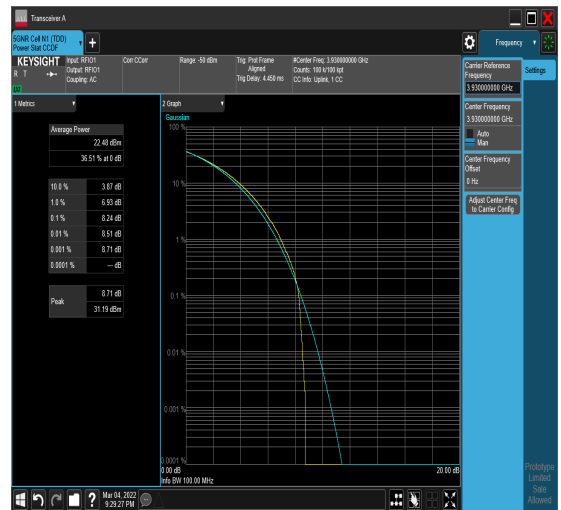
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-273
@0-7.39-100%-0.00-7.39-PASS-≤13



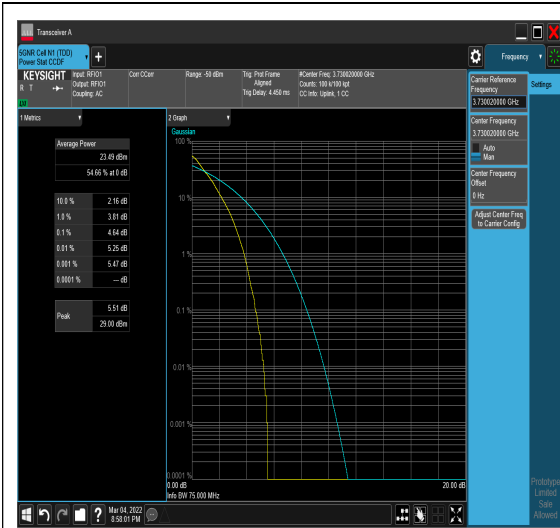
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-27
3@0-7.39-100%-0.00-7.39-PASS-≤13



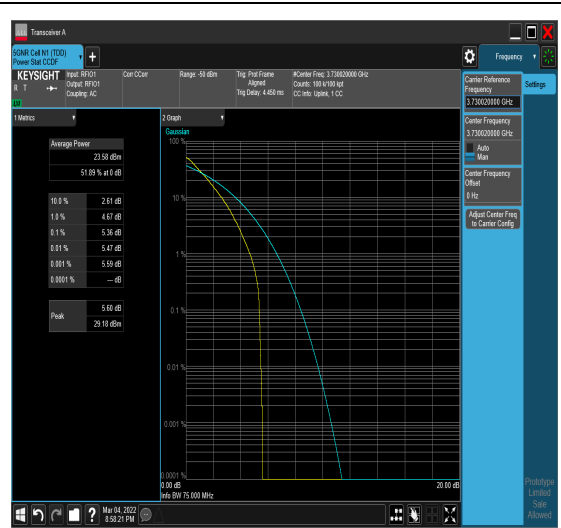
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-27
3@0-8.19-100%-0.00-8.19-PASS-≤13



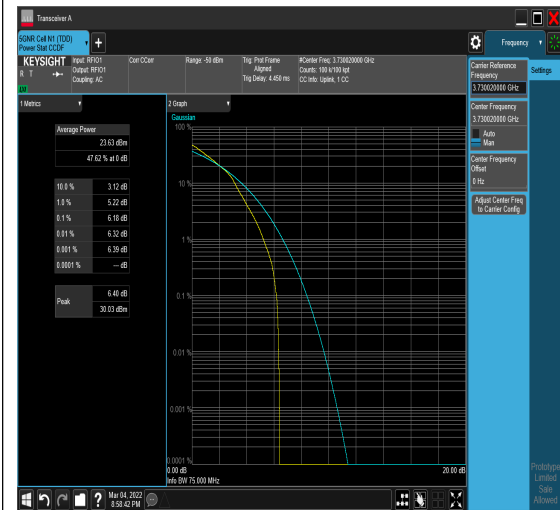
1-DC_2A_n77A-3700-3980-PC3-30-5+100-M
+H-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-2
73@0-8.24-100%-0.00-8.24-PASS-≤13



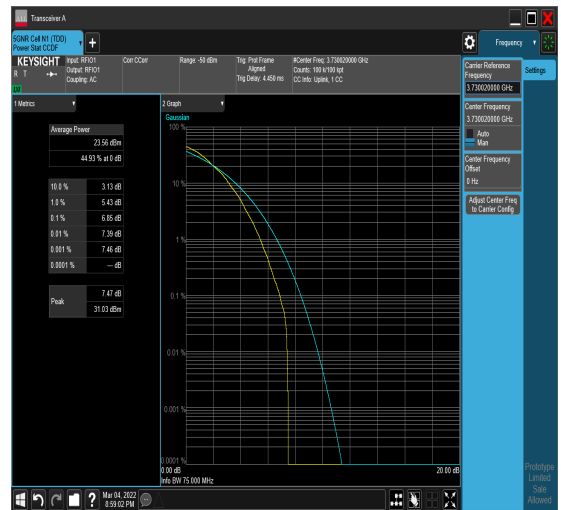
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+L-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-162@0-4.64-100%-0.00-4.64-PASS-≤13



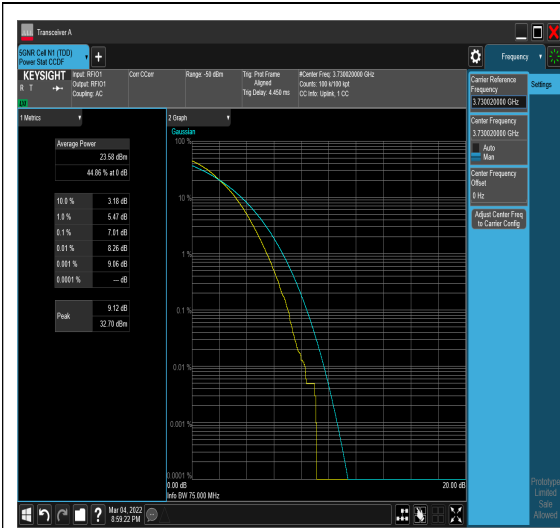
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+L-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-162@0-5.36-100%-0.00-5.36-PASS-≤13



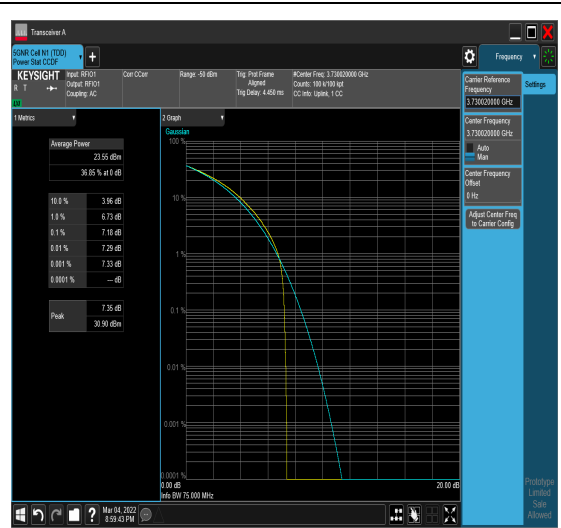
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+L-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-162@0-6.18-100%-0.00-6.18-PASS-≤13



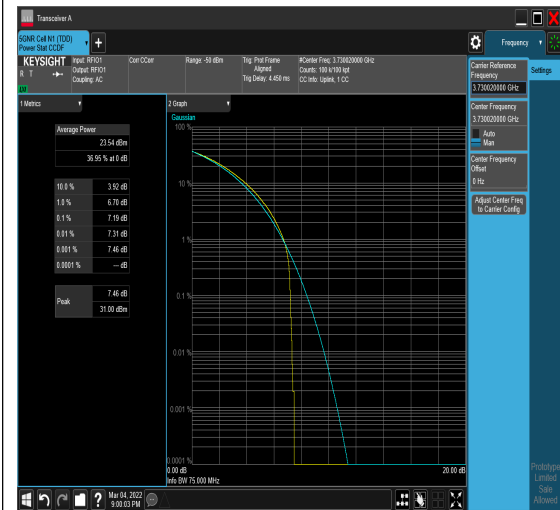
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+L-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-162@0-6.85-100%-0.00-6.85-PASS-≤13



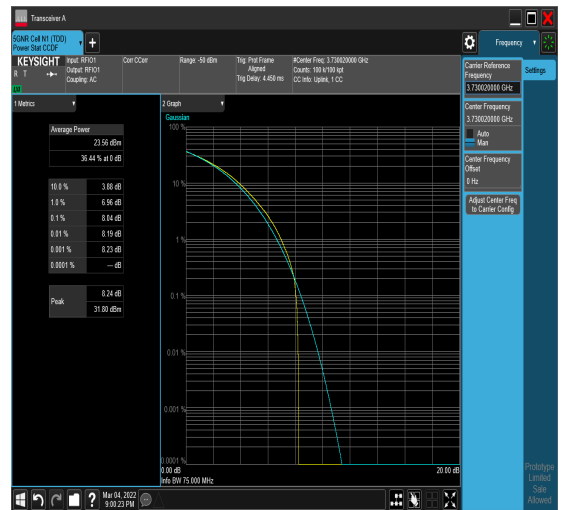
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-1
62@0-7.01-100%-0.00-7.01-PASS-≤13



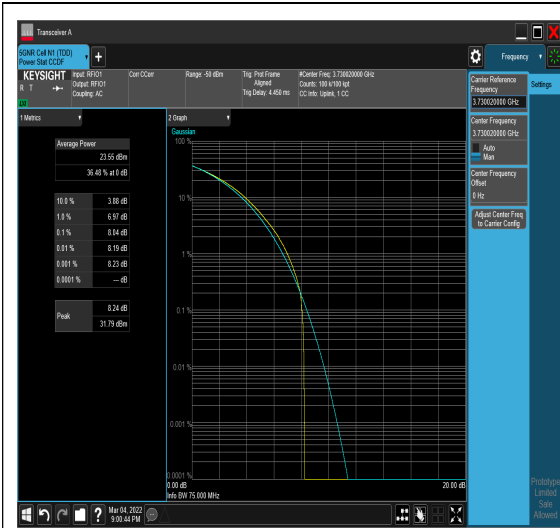
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-162
@0-7.18-100%-0.00-7.18-PASS-≤13



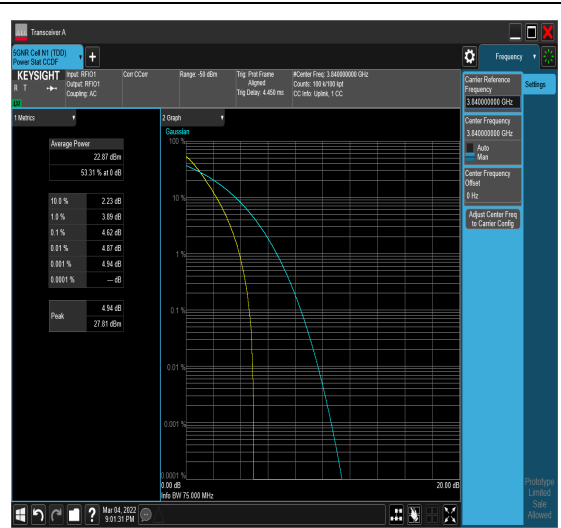
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-162
@0-7.19-100%-0.00-7.19-PASS-≤13



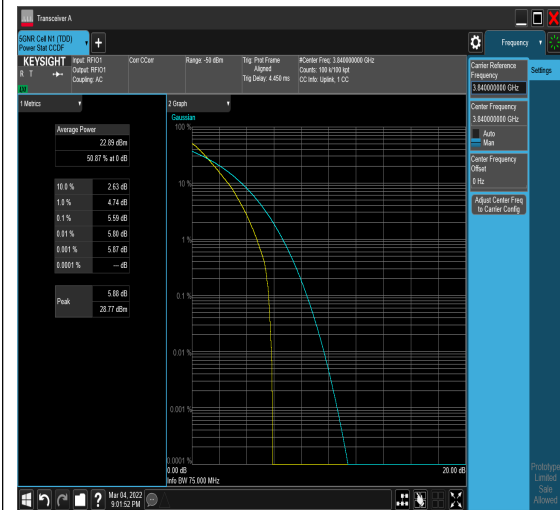
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-162
@0-8.04-100%-0.00-8.04-PASS-≤13



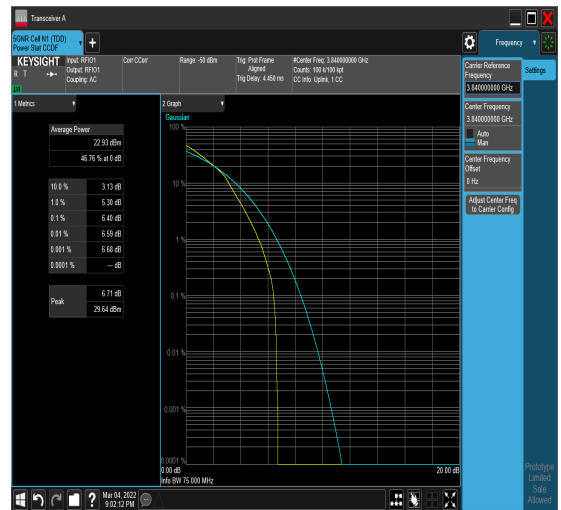
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
L-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-16
2@0-8.04-100%-0.00-8.04-PASS-≤13



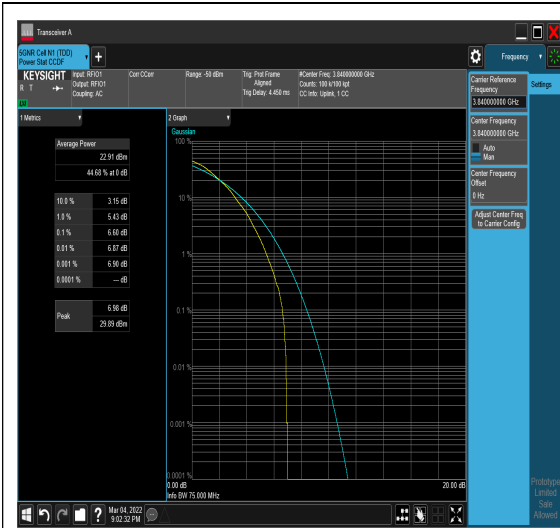
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
M-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
162@0-4.62-100%-0.00-4.62-PASS-≤13



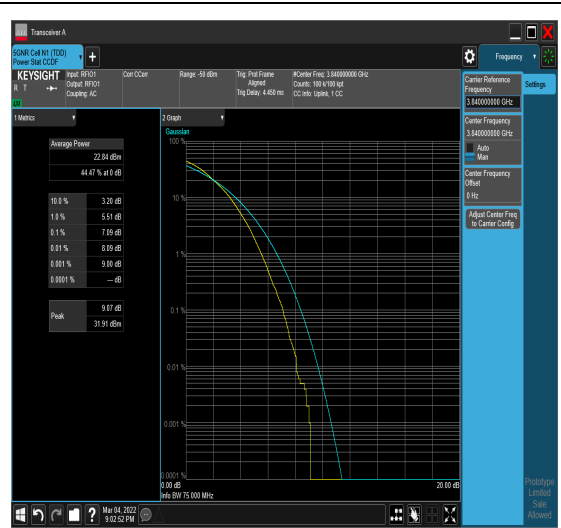
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
M-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-162
@0-5.59-100%-0.00-5.59-PASS-≤13



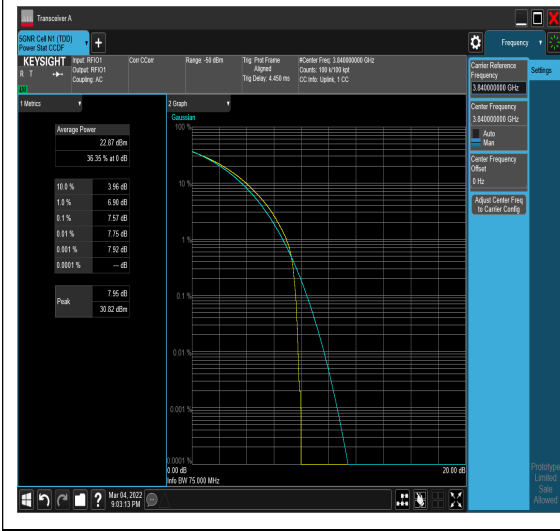
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
M-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-16
2@0-6.40-100%-0.00-6.40-PASS-≤13



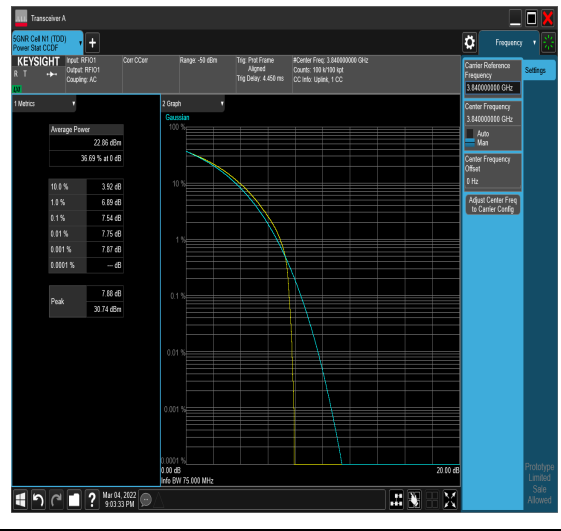
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+M-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-16
 2@0-6.60-100%-0.00-6.60-PASS-≤13



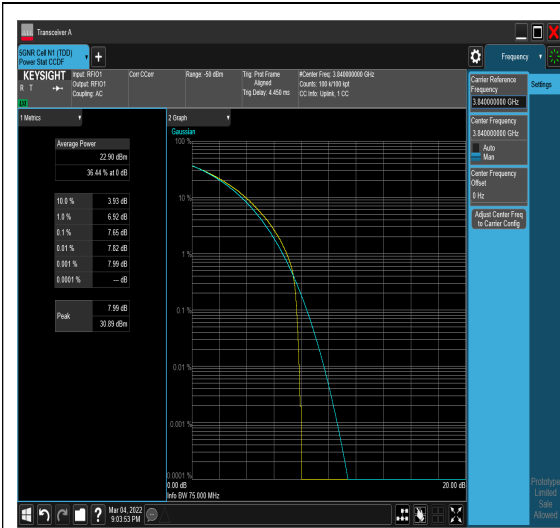
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+M-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-162@0-7.09-100%-0.00-7.09-PASS-≤13



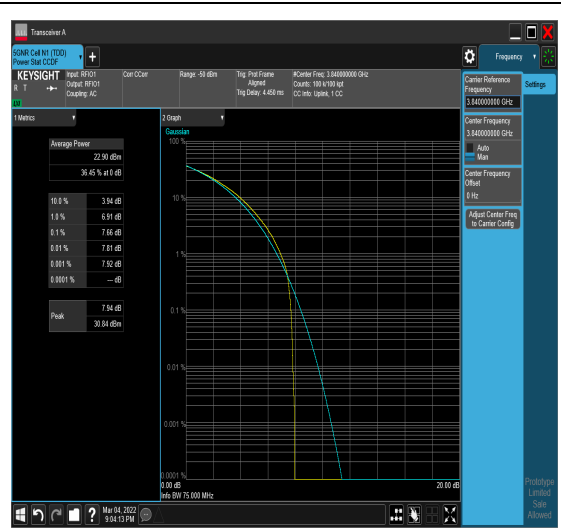
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+M-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-162
 @0-7.57-100%-0.00-7.57-PASS-≤13



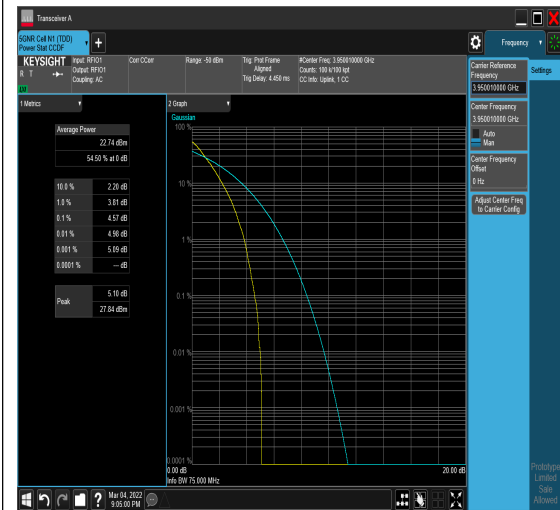
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+M-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-162
 @0-7.54-100%-0.00-7.54-PASS-≤13



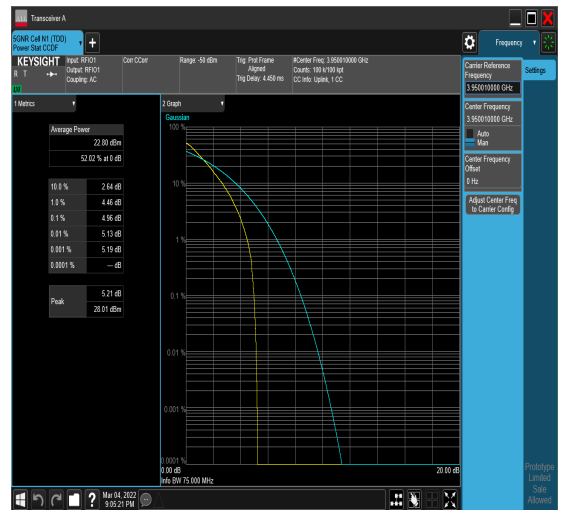
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
M-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-162
@0-7.65-100%-0.00-7.65-PASS-≤13



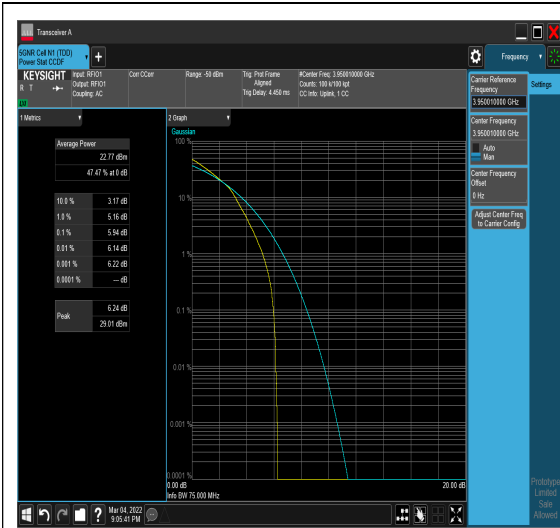
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
M-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-16
2@0-7.66-100%-0.00-7.66-PASS-≤13



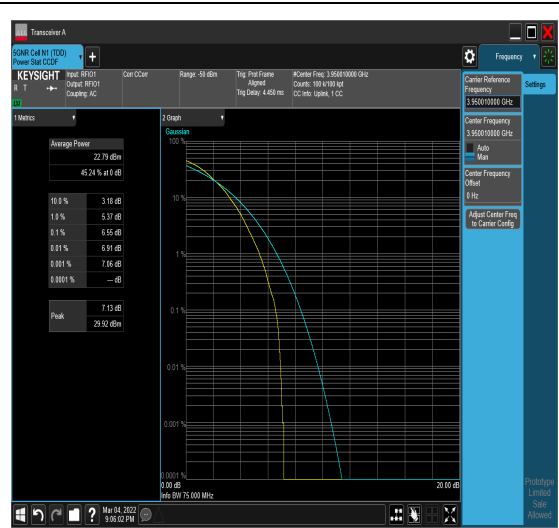
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
H-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
162@0-4.57-100%-0.00-4.57-PASS-≤13



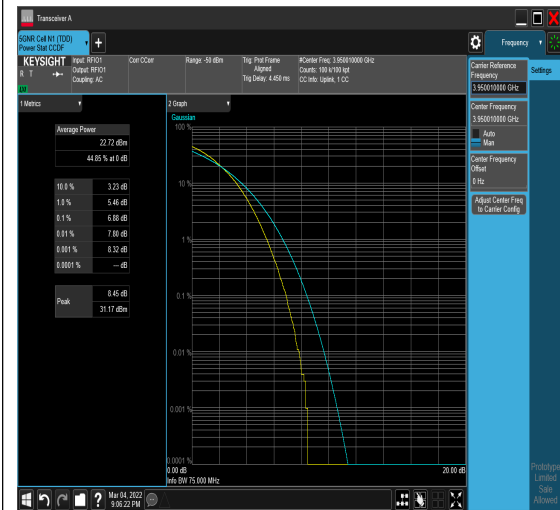
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
H-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-162
@0-4.96-100%-0.00-4.96-PASS-≤13



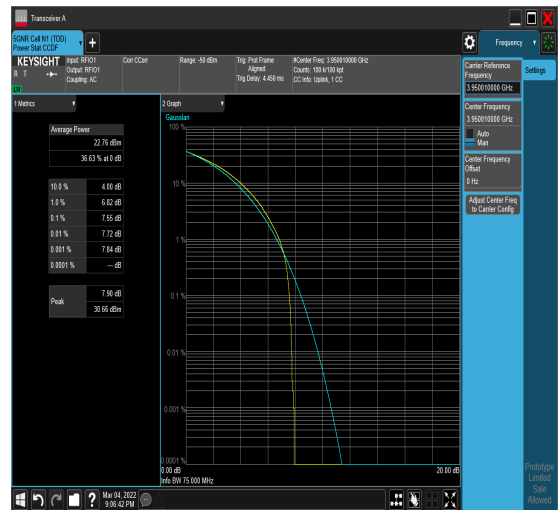
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
H-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-16
2@0-5.94-100%-0.00-5.94-PASS-≤13



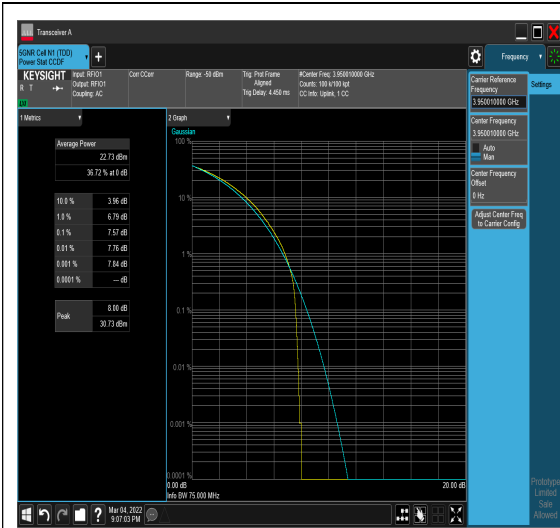
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
H-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-16
2@0-6.55-100%-0.00-6.55-PASS-≤13



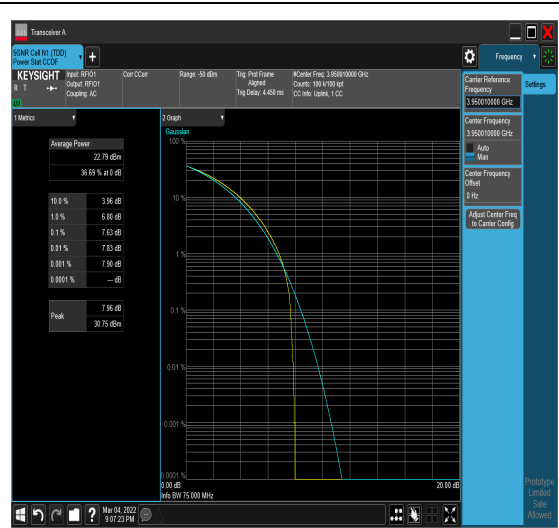
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
H-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-162
62@0-6.88-100%-0.00-6.88-PASS-≤13



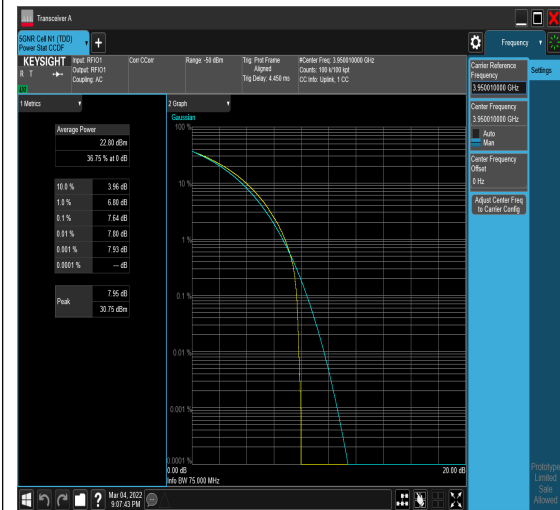
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
H-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-162
@0-7.55-100%-0.00-7.55-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
H-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-162
@0-7.57-100%-0.00-7.57-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
H-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-162
@0-7.63-100%-0.00-7.63-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
H-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-16
2@0-7.64-100%-0.00-7.64-PASS-≤13

Peak-to-Average Ratio(PAPR)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	DutyCycle	Factor	Result	Limit	Verdict
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	100%	0.00	7.14	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.68	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.37	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+L	Outer_Full	100%	0.00	8.02	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+L	Outer_Full	100%	0.00	8.09	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	100%	0.00	8.04	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	100%	0.00	8.50	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+L	Outer_Full	100%	0.00	8.68	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+L	Outer_Full	100%	0.00	8.43	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	100%	0.00	7.27	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.43	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.18	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+M	Outer_Full	100%	0.00	7.63	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+M	Outer_Full	100%	0.00	8.13	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	100%	0.00	8.45	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	100%	0.00	8.52	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+M	Outer_Full	100%	0.00	8.84	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+M	Outer_Full	100%	0.00	8.48	≤13	PASS

DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	100%	0.00	7.30	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.09	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.49	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+H	Outer_Full	100%	0.00	8.17	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-256QAM	M+H	Outer_Full	100%	0.00	7.96	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	100%	0.00	8.66	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	100%	0.00	8.30	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+H	Outer_Full	100%	0.00	8.17	≤13	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+H	Outer_Full	100%	0.00	8.90	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+L	Outer_Full	100%	0.00	8.10	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	100%	0.00	7.74	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.09	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+L	Outer_Full	100%	0.00	8.58	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+L	Outer_Full	100%	0.00	8.50	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	100%	0.00	8.89	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	100%	0.00	8.30	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+L	Outer_Full	100%	0.00	8.56	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+L	Outer_Full	100%	0.00	8.75	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+M	Outer_Full	100%	0.00	9.71	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	100%	0.00	7.93	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.14	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+M	Outer_Full	100%	0.00	8.00	≤13	PASS

DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+M	Outer_Full	100%	0.00	7.96	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	100%	0.00	8.21	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	100%	0.00	8.80	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+M	Outer_Full	100%	0.00	8.30	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+M	Outer_Full	100%	0.00	8.76	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+H	Outer_Full	100%	0.00	8.00	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.17	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.97	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+H	Outer_Full	100%	0.00	7.79	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-256QAM	M+H	Outer_Full	100%	0.00	8.53	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	100%	0.00	8.83	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	100%	0.00	8.35	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+H	Outer_Full	100%	0.00	9.08	≤13	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+H	Outer_Full	100%	0.00	8.30	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+L	Outer_Full	100%	0.00	8.34	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.14	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.73	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+L	Outer_Full	100%	0.00	8.10	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+L	Outer_Full	100%	0.00	8.01	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	100%	0.00	8.23	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	100%	0.00	9.09	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+L	Outer_Full	100%	0.00	8.81	≤13	PASS

DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+L	Outer_Full	100%	0.00	8.33	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+M	Outer_Full	100%	0.00	8.43	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.17	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.64	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+M	Outer_Full	100%	0.00	8.84	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+M	Outer_Full	100%	0.00	8.40	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	100%	0.00	8.48	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	100%	0.00	7.93	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+M	Outer_Full	100%	0.00	8.89	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+M	Outer_Full	100%	0.00	8.55	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+H	Outer_Full	100%	0.00	8.78	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.75	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.66	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+H	Outer_Full	100%	0.00	7.77	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-256QAM	M+H	Outer_Full	100%	0.00	8.62	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	100%	0.00	8.93	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	100%	0.00	8.25	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+H	Outer_Full	100%	0.00	8.33	≤13	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+H	Outer_Full	100%	0.00	8.65	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+L	Outer_Full	100%	0.00	8.69	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.68	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	100%	0.00	8.31	≤13	PASS

DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+L	Outer_Full	100%	0.00	8.84	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+L	Outer_Full	100%	0.00	8.68	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+L	Outer_Full	100%	0.00	8.89	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+L	Outer_Full	100%	0.00	8.71	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+L	Outer_Full	100%	0.00	8.54	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+L	Outer_Full	100%	0.00	8.56	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+M	Outer_Full	100%	0.00	8.60	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.31	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.98	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+M	Outer_Full	100%	0.00	8.25	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+M	Outer_Full	100%	0.00	7.50	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+M	Outer_Full	100%	0.00	9.22	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+M	Outer_Full	100%	0.00	8.03	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+M	Outer_Full	100%	0.00	8.58	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+M	Outer_Full	100%	0.00	8.61	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+H	Outer_Full	100%	0.00	7.95	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	100%	0.00	8.75	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+H	Outer_Full	100%	0.00	7.89	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+H	Outer_Full	100%	0.00	8.14	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-256QAM	M+H	Outer_Full	100%	0.00	8.46	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+H	Outer_Full	100%	0.00	8.98	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+H	Outer_Full	100%	0.00	9.07	≤13	PASS

DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+H	Outer_Full	100%	0.00	8.81	≤13	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+H	Outer_Full	100%	0.00	8.20	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+L	Outer_Full	100%	0.00	9.41	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	100%	0.00	8.06	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	100%	0.00	7.97	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+L	Outer_Full	100%	0.00	8.19	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+L	Outer_Full	100%	0.00	8.59	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	100%	0.00	8.58	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	100%	0.00	8.76	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+L	Outer_Full	100%	0.00	8.99	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+L	Outer_Full	100%	0.00	8.59	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+M	Outer_Full	100%	0.00	7.97	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	100%	0.00	8.22	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	100%	0.00	8.51	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+M	Outer_Full	100%	0.00	9.54	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+M	Outer_Full	100%	0.00	7.94	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	100%	0.00	9.42	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	100%	0.00	8.49	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+M	Outer_Full	100%	0.00	8.17	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+M	Outer_Full	100%	0.00	8.51	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+H	Outer_Full	100%	0.00	8.39	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	100%	0.00	7.91	≤13	PASS

DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	100%	0.00	8.94	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+H	Outer_Full	100%	0.00	9.08	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-256QAM	M+H	Outer_Full	100%	0.00	7.79	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	100%	0.00	8.62	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	100%	0.00	8.99	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+H	Outer_Full	100%	0.00	8.23	≤13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+H	Outer_Full	100%	0.00	9.11	≤13	PASS

Test Graphs



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-5
@0-7.14-100%-0.00-7.14-PASS-≤13

1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-50
@0-7.68-100%-0.00-7.68-PASS-≤13

1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-50
@0-8.37-100%-0.00-8.37-PASS-≤13

1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-50
@0-8.02-100%-0.00-8.02-PASS-≤13