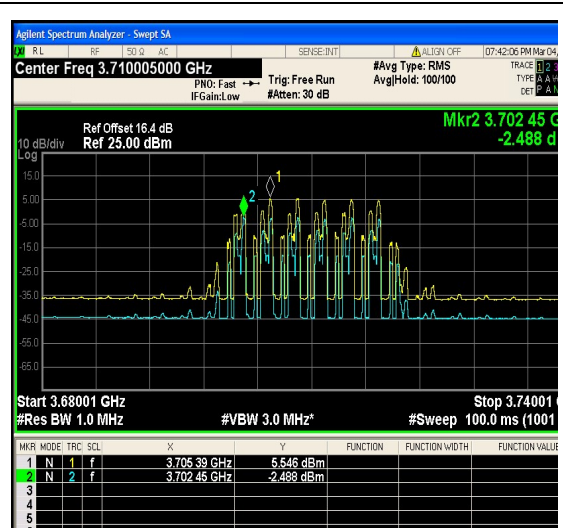
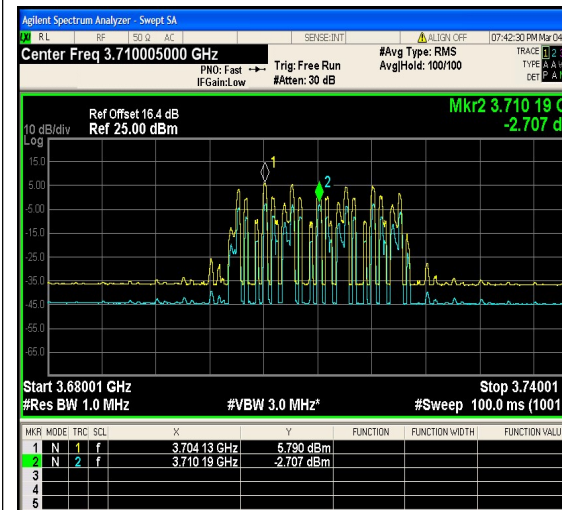


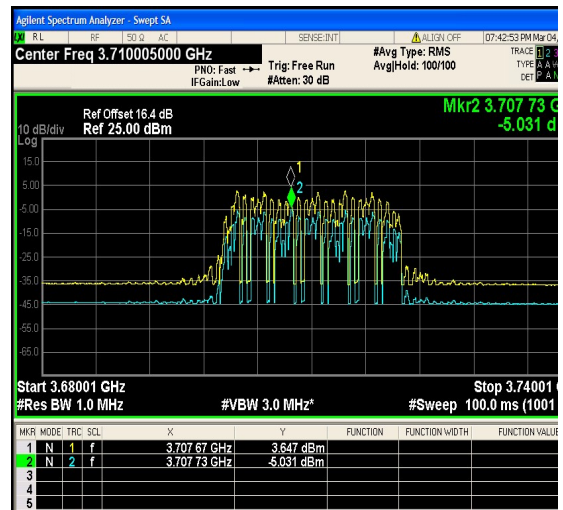
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-5
@0-8.09-100%-0.00-8.09-PASS-≤13



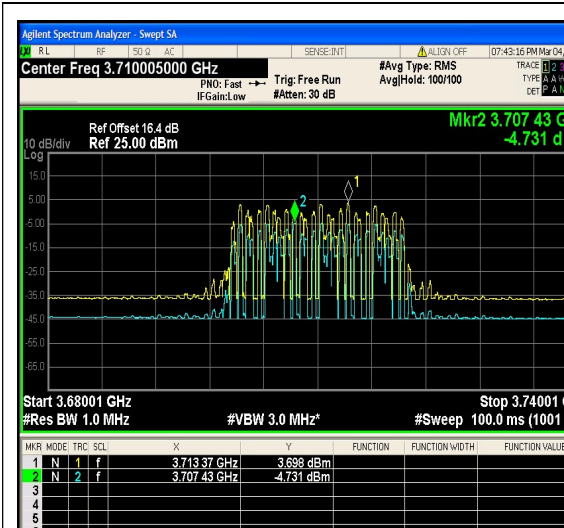
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-51@
0-8.04-100%-0.00-8.04-PASS-≤13



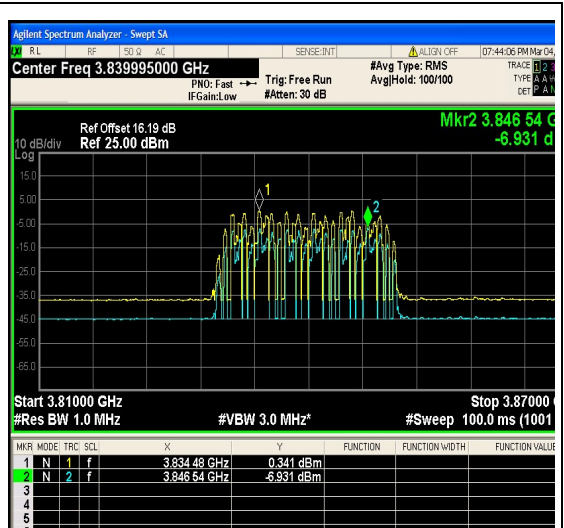
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-51
@0-8.50-100%-0.00-8.50-PASS-≤13



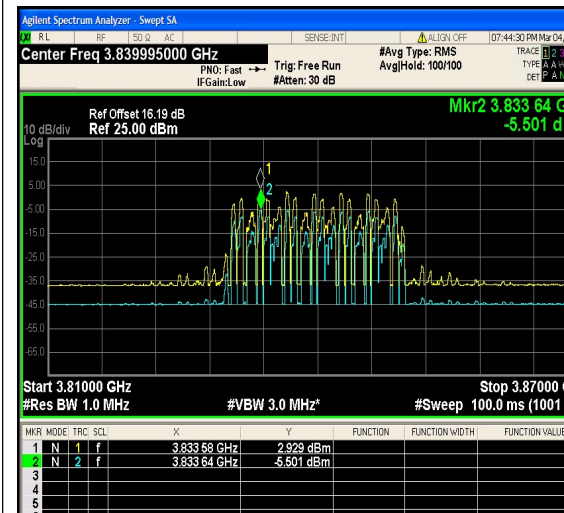
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L-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-51
@0-8.68-100%-0.00-8.68-PASS-≤13



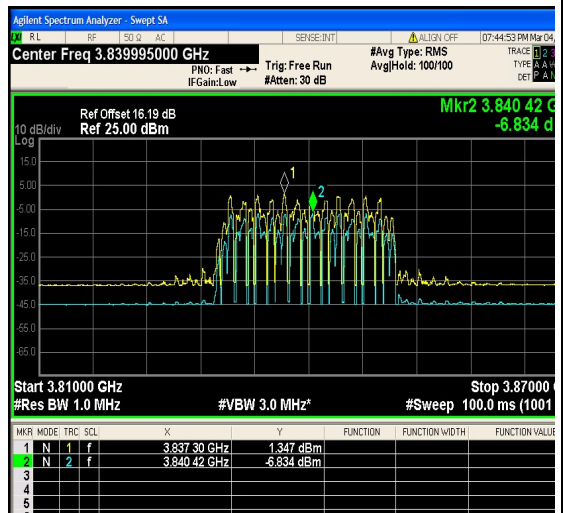
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 L-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-51
 @0-8.43-100%-0.00-8.43-PASS-≤13



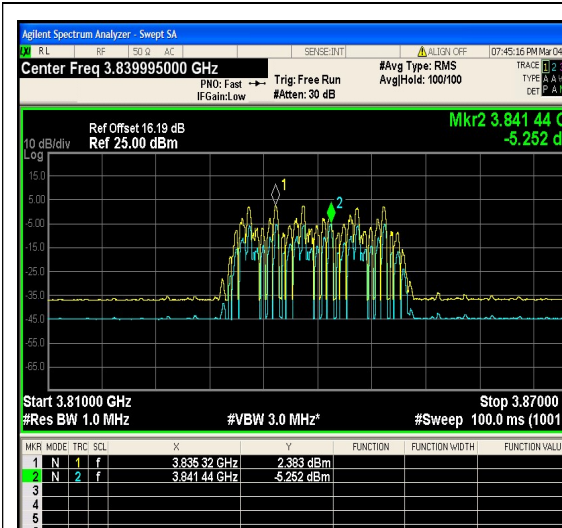
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 M-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
 50@0-7.27-100%-0.00-7.27-PASS-≤13



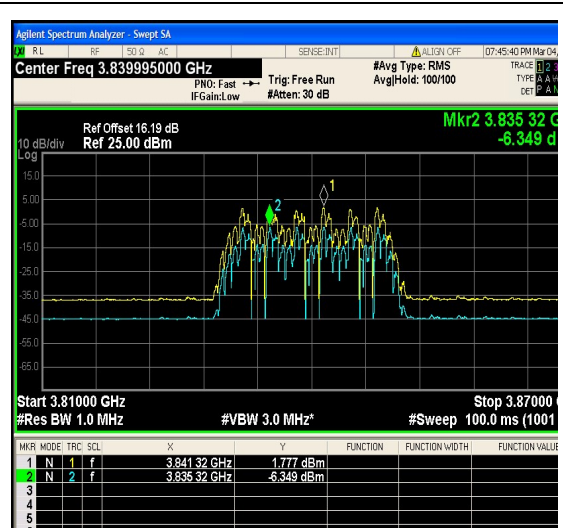
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 @0-8.43-100%-0.00-8.43-PASS-≤13



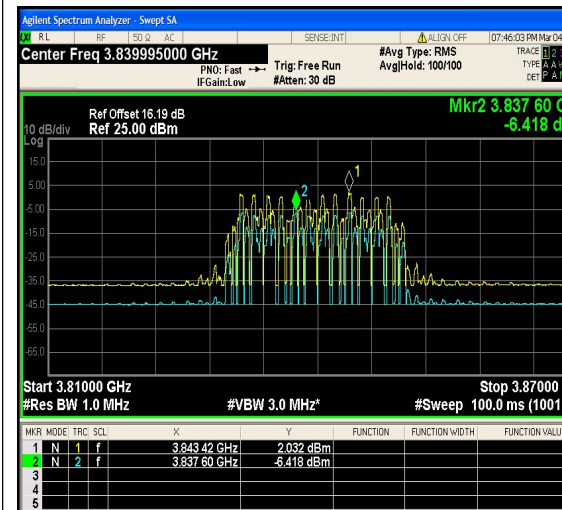
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 M-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-50
 @0-8.18-100%-0.00-8.18-PASS-≤13



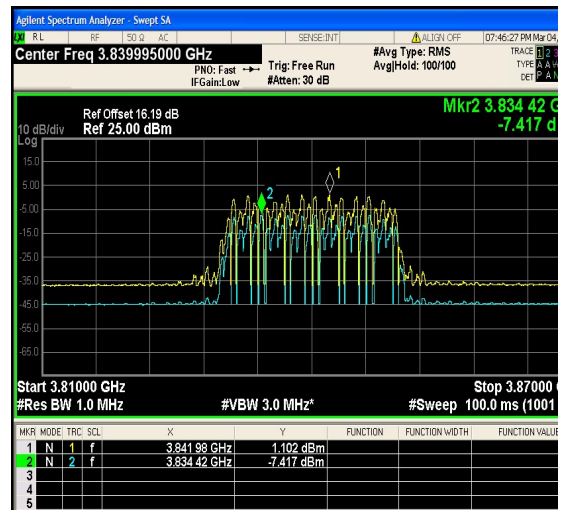
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 M-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-50
 @0-7.63-100%-0.00-7.63-PASS-≤13



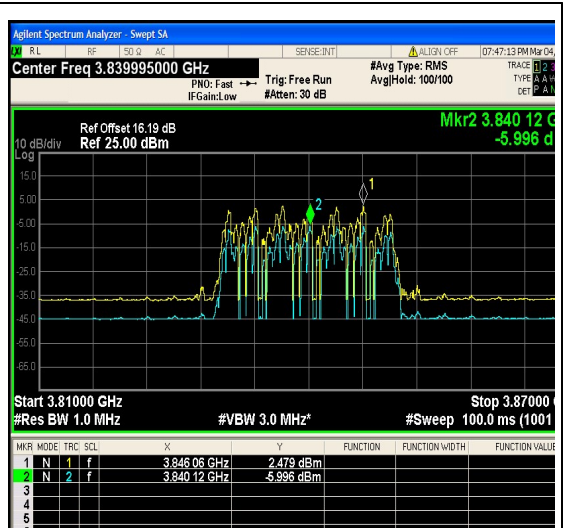
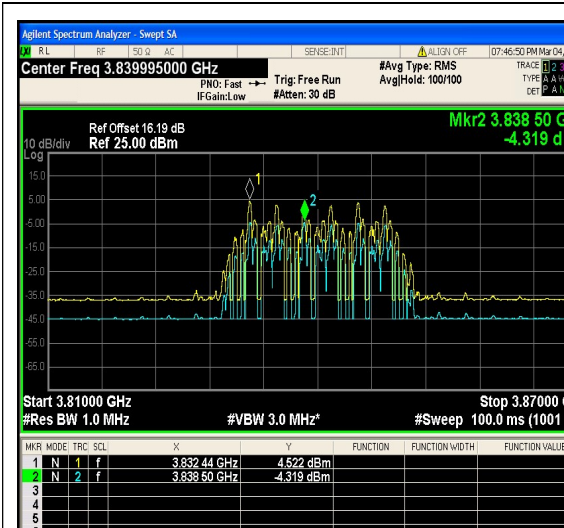
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 M-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-5
 @0-8.13-100%-0.00-8.13-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 M-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-51@
 0-8.45-100%-0.00-8.45-PASS-≤13

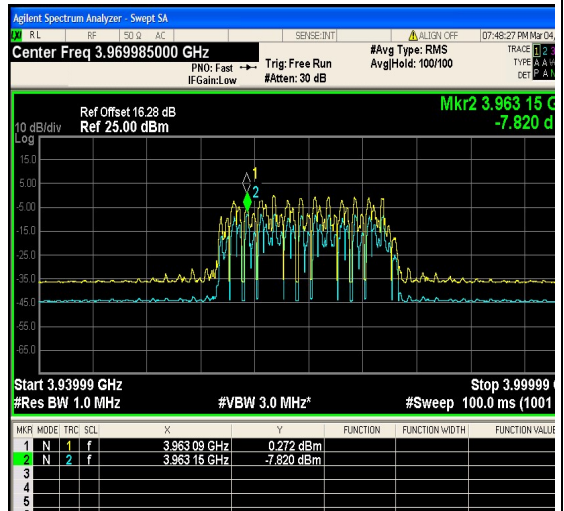
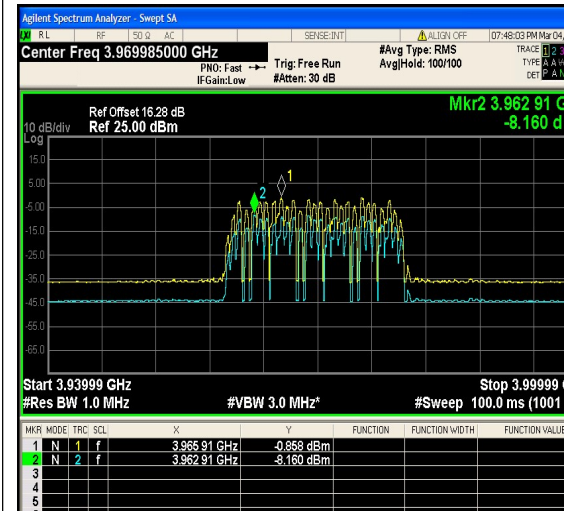


1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 M-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-51
 @0-8.52-100%-0.00-8.52-PASS-≤13



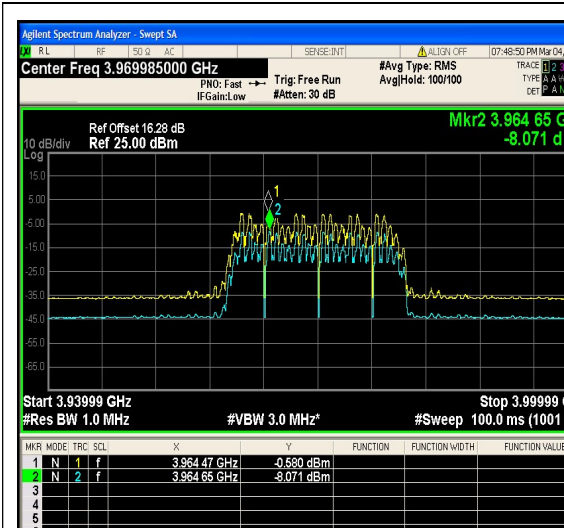
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
M-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-51
@0-8.84-100%-0.00-8.84-PASS-≤13

1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
M-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-51
@0-8.48-100%-0.00-8.48-PASS-≤13

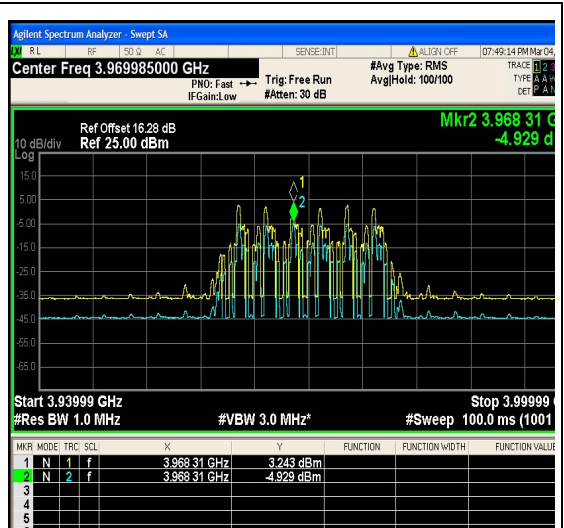


1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
50@0-7.30-100%-0.00-7.30-PASS-≤13

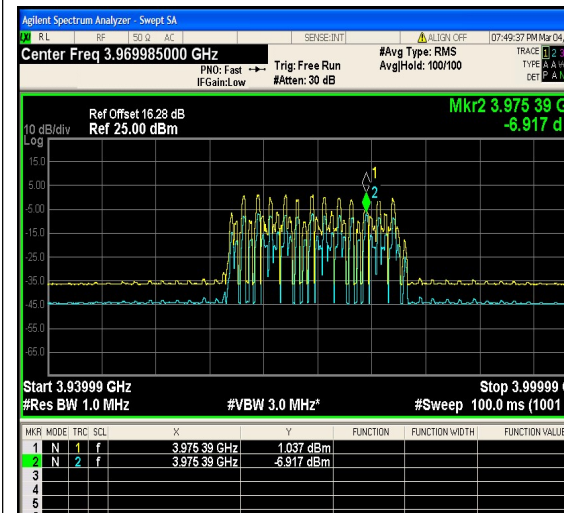
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@0-8.09-100%-0.00-8.09-PASS-≤13



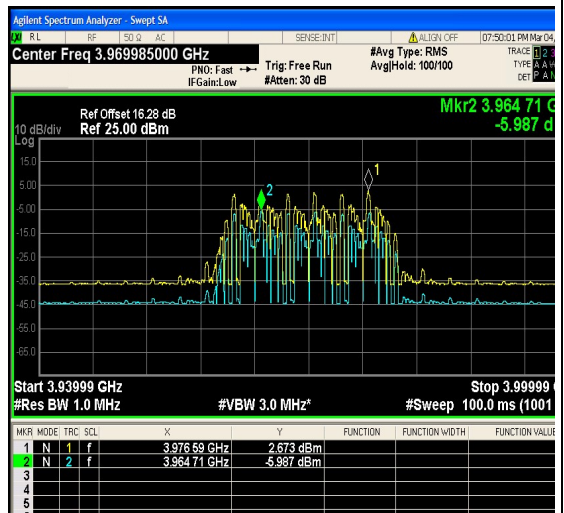
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 @0-7.49-100%-0.00-7.49-PASS-≤13



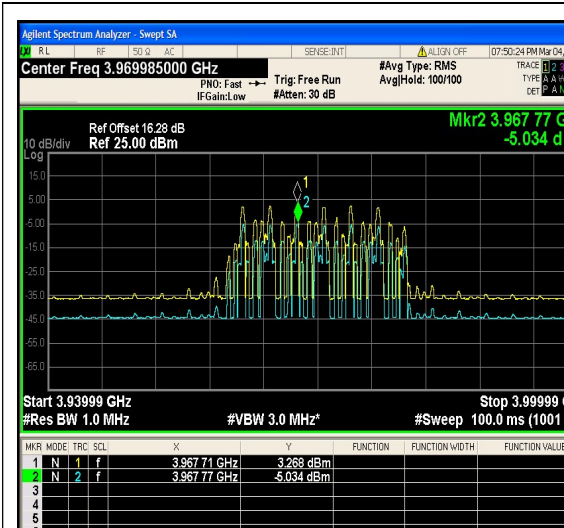
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 H-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-50
 @0-8.17-100%-0.00-8.17-PASS-≤13



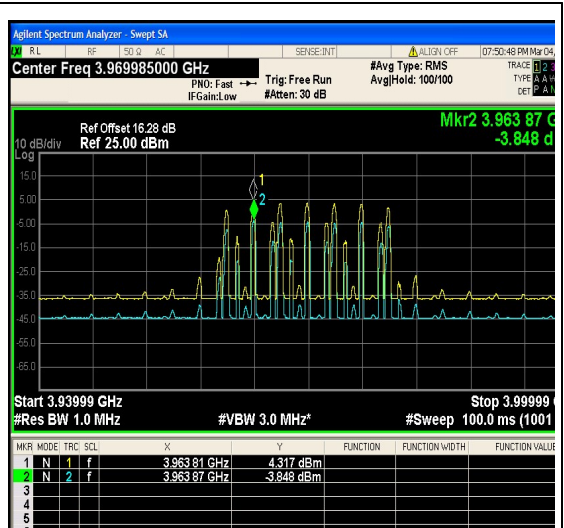
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 H-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-5
 0@0-7.96-100%-0.00-7.96-PASS-≤13



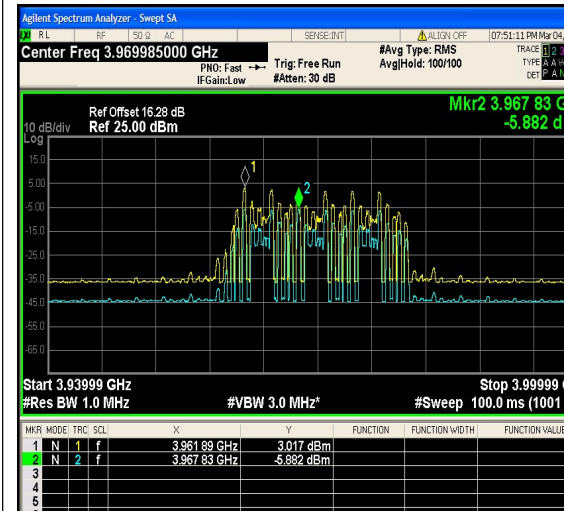
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 H-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-51@
 0-8.66-100%-0.00-8.66-PASS-≤13



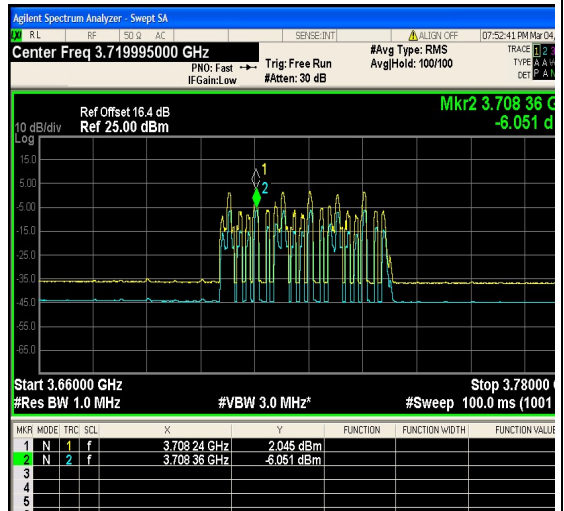
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-51
@0-8.30-100%-0.00-8.30-PASS-≤13



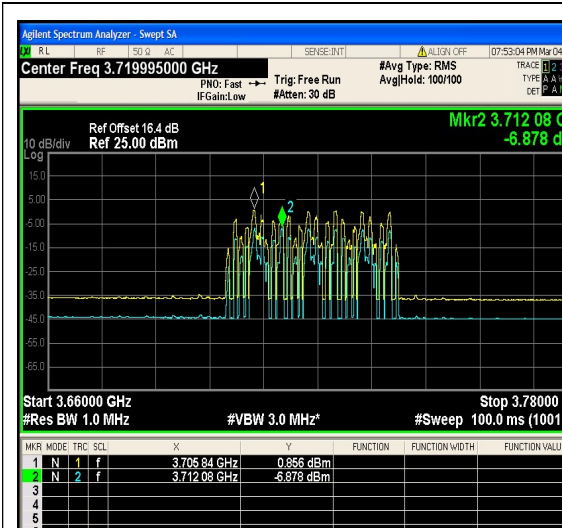
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H-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-51
@0-8.17-100%-0.00-8.17-PASS-≤13



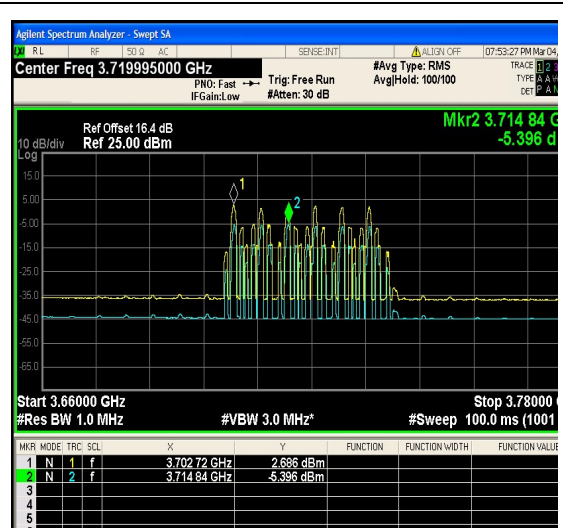
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
H-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-51
@0-8.90-100%-0.00-8.90-PASS-≤13



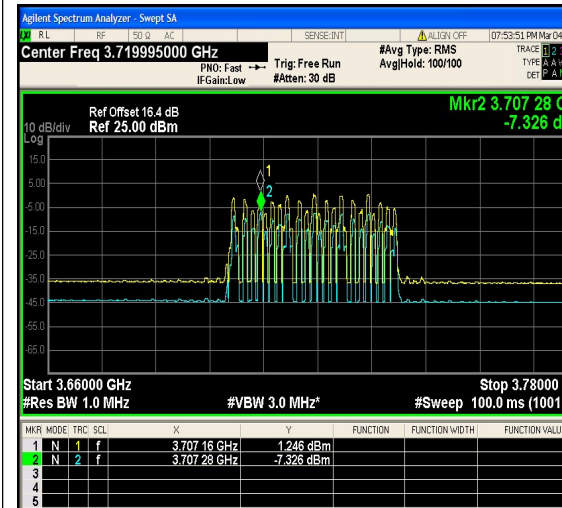
1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
L-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-1
00@0-8.10-100%-0.00-8.10-PASS-≤13



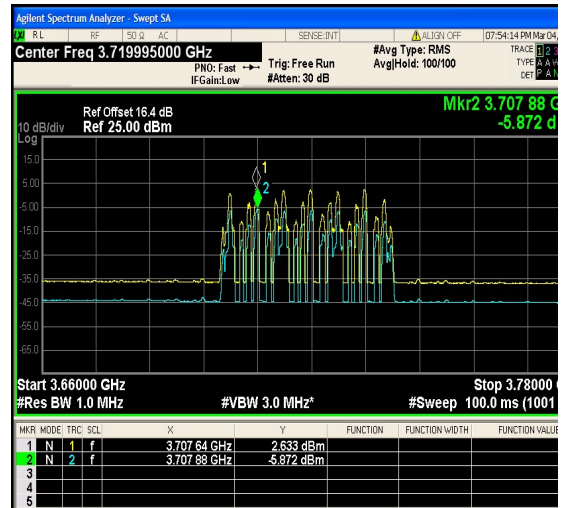
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 L-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-100
 @0-7.74-100%-0.00-7.74-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-10
 @0-8.09-100%-0.00-8.09-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-10
 @0-8.58-100%-0.00-8.58-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+40-M+
 L-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-1
 @0-8.50-100%-0.00-8.50-PASS-≤13