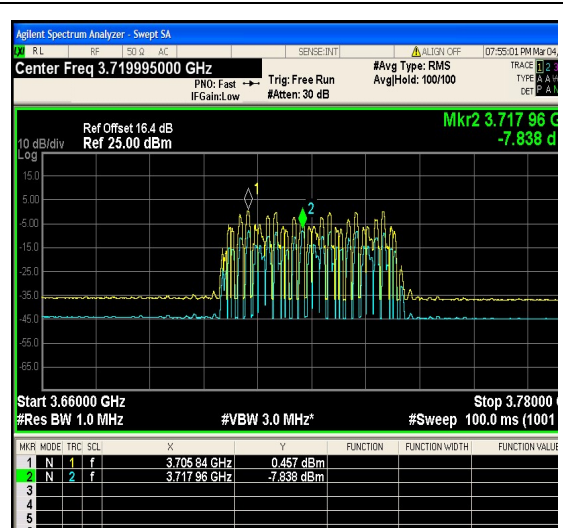
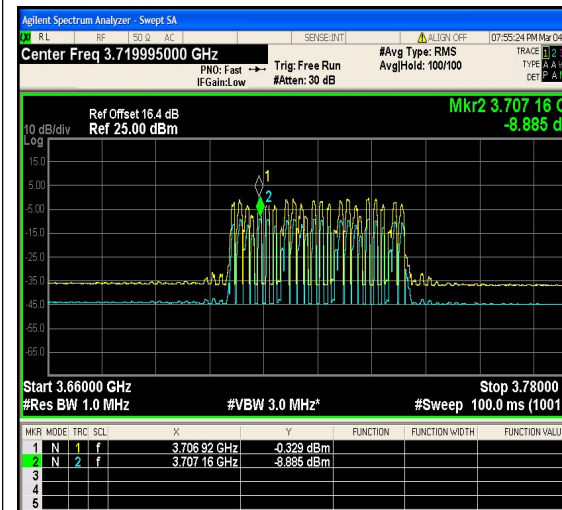


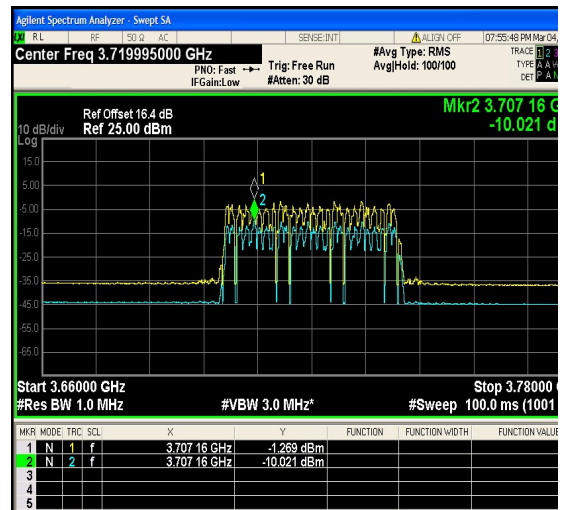
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
 L-6-N/A-N/A-N/A-CP-QPSK-Outer\_Full-106  
 @0-8.89-100%-0.00-8.89-PASS-≤13



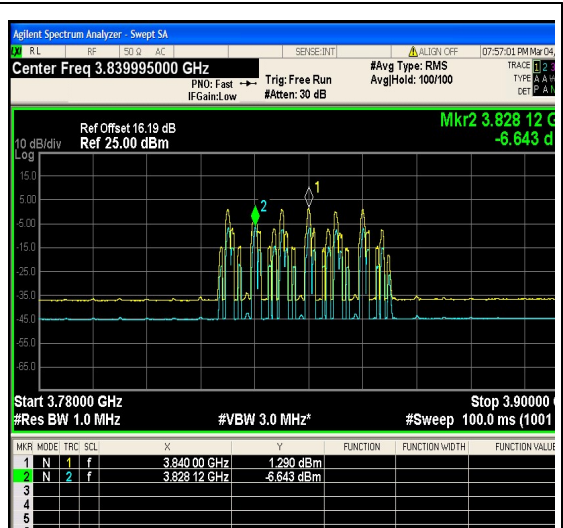
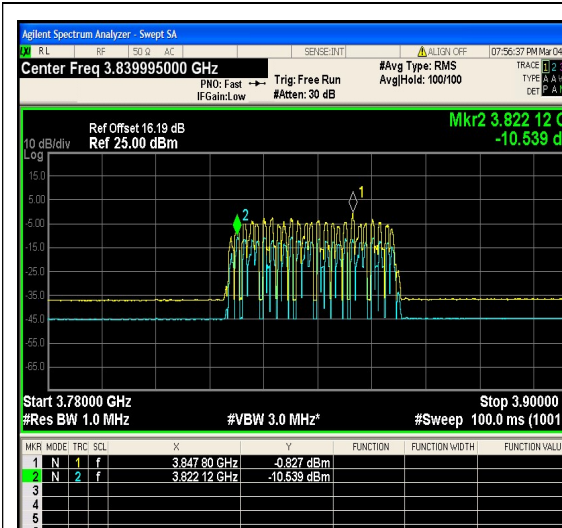
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
 L-7-N/A-N/A-N/A-CP-16QAM-Outer\_Full-106  
 @0-8.30-100%-0.00-8.30-PASS-≤13



1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
 L-8-N/A-N/A-N/A-CP-64QAM-Outer\_Full-106  
 @0-8.56-100%-0.00-8.56-PASS-≤13

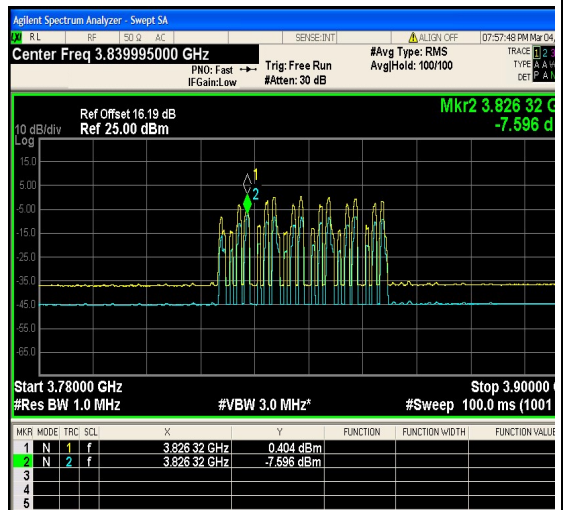
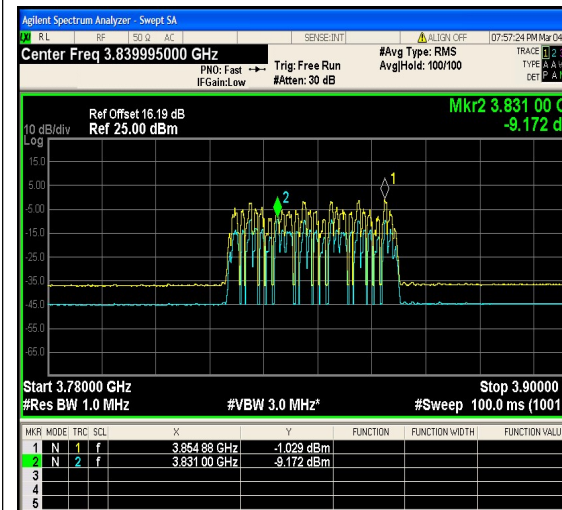


1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
 L-9-N/A-N/A-N/A-CP-256QAM-Outer\_Full-10  
 6@0-8.75-100%-0.00-8.75-PASS-≤13



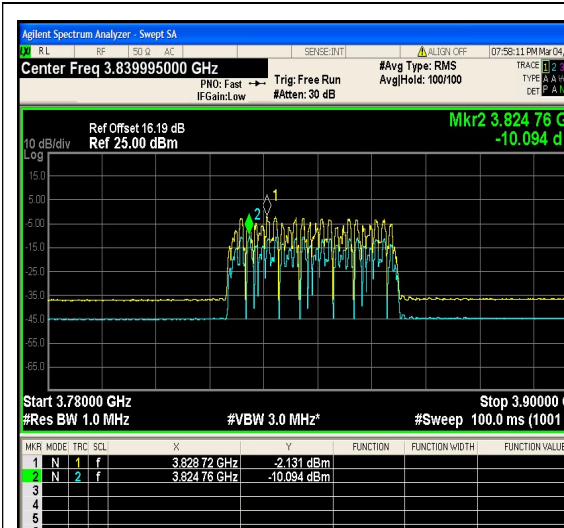
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
M-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer\_Full-  
100@0-9.71-100%-0.00-9.71-PASS-≤13

1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
M-2-N/A-N/A-N/A-DFT-QPSK-Outer\_Full-100  
@0-7.93-100%-0.00-7.93-PASS-≤13

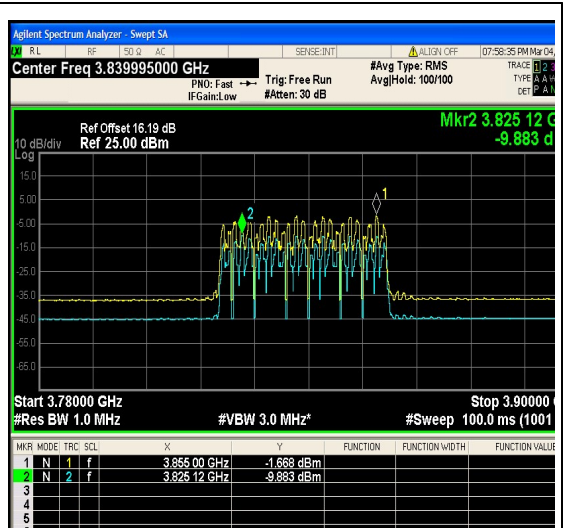


1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
M-3-N/A-N/A-N/A-DFT-16QAM-Outer\_Full-10  
0@0-8.14-100%-0.00-8.14-PASS-≤13

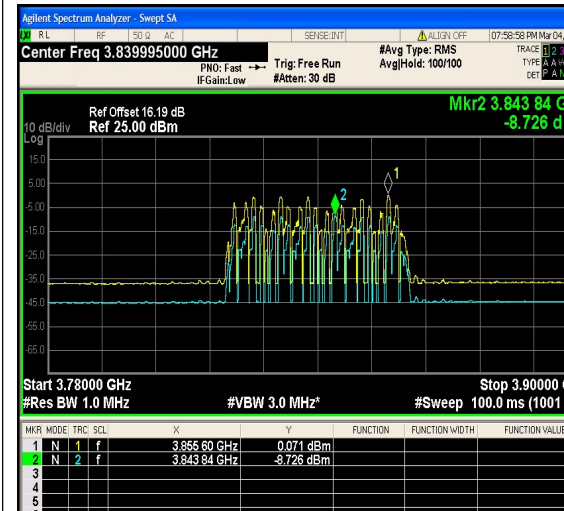
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0@0-8.00-100%-0.00-8.00-PASS-≤13



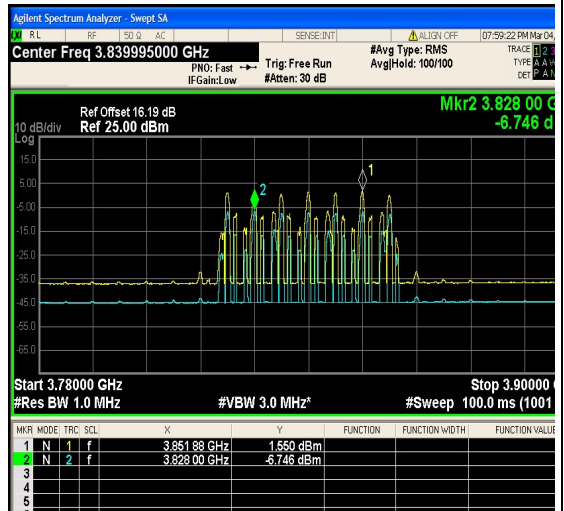
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 M-5-N/A-N/A-N/A-DFT-256QAM-Outer\_Full-1  
 00@0-7.96-100%-0.00-7.96-PASS-≤13



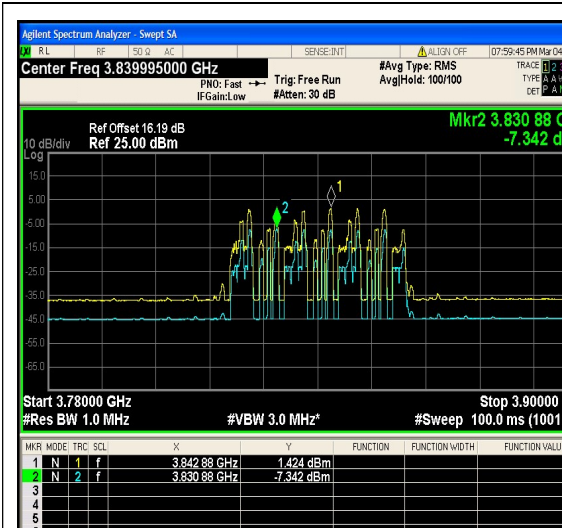
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
 M-6-N/A-N/A-N/A-CP-QPSK-Outer\_Full-106  
 @0-8.21-100%-0.00-8.21-PASS-≤13



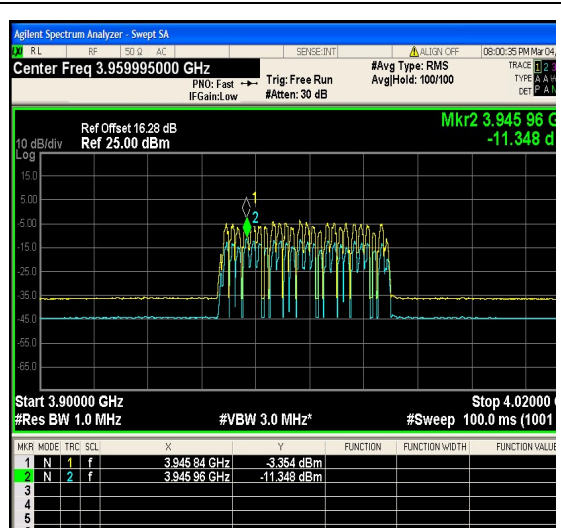
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
 M-7-N/A-N/A-N/A-CP-16QAM-Outer\_Full-106  
 @0-8.80-100%-0.00-8.80-PASS-≤13



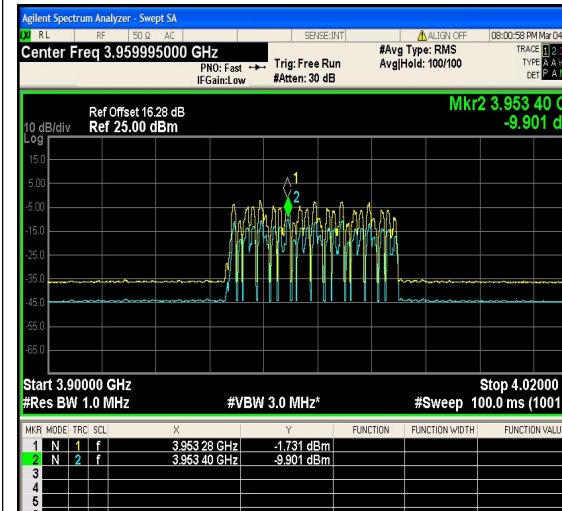
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
 M-8-N/A-N/A-N/A-CP-64QAM-Outer\_Full-106  
 @0-8.30-100%-0.00-8.30-PASS-≤13



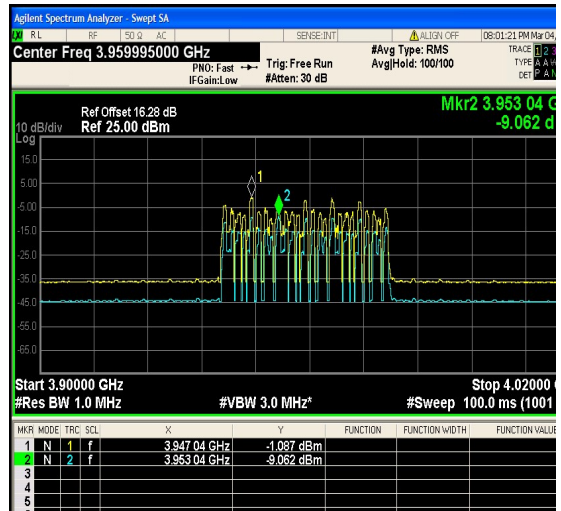
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
 M-9-N/A-N/A-N/A-CP-256QAM-Outer\_Full-10  
 6@0-8.76-100%-0.00-8.76-PASS-≤13



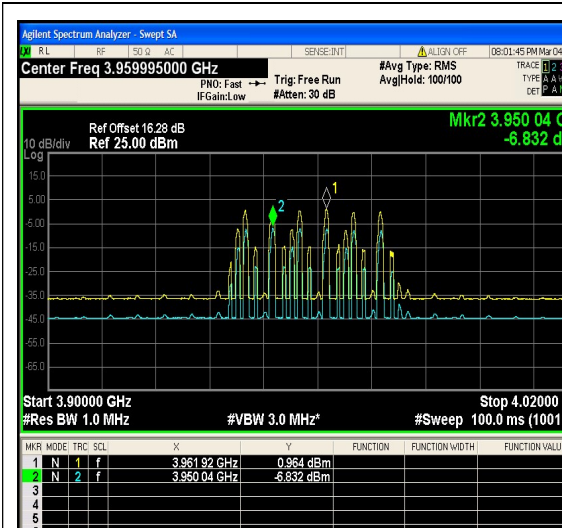
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
 H-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer\_Full-  
 100@0-8.00-100%-0.00-8.00-PASS-≤13



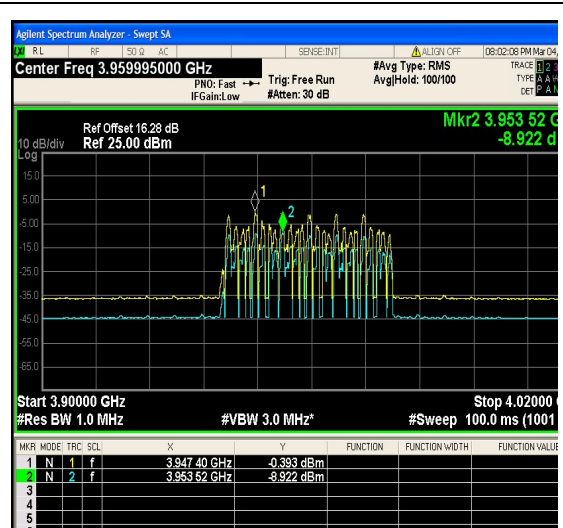
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 0@0-8.17-100%-0.00-8.17-PASS-≤13



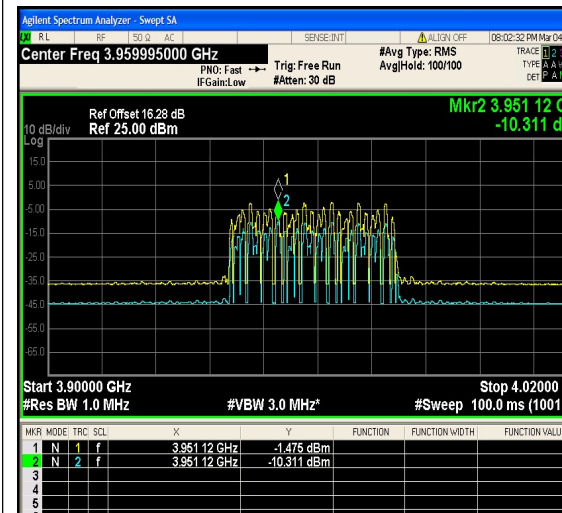
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 H-3-N/A-N/A-N/A-DFT-16QAM-Outer\_Full-10  
 0@0-7.97-100%-0.00-7.97-PASS-≤13



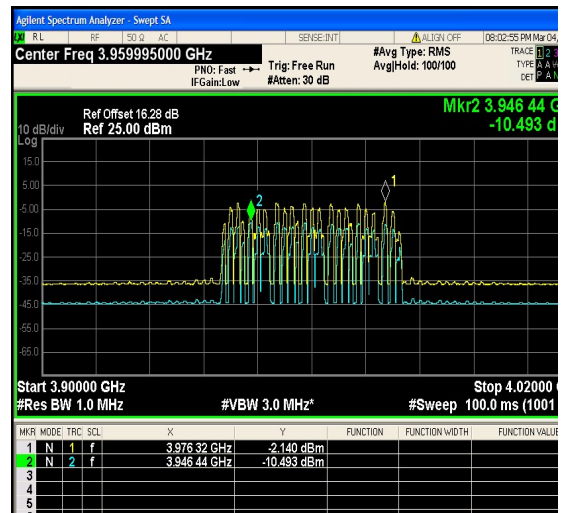
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H-4-N/A-N/A-N/A-DFT-64QAM-Outer\_Full-10  
@0-7.79-100%-0.00-7.79-PASS-≤13



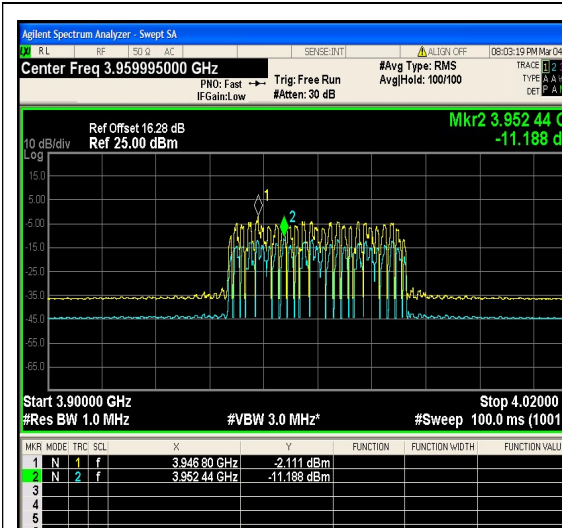
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
H-5-N/A-N/A-N/A-DFT-256QAM-Outer\_Full-1  
@0-8.53-100%-0.00-8.53-PASS-≤13



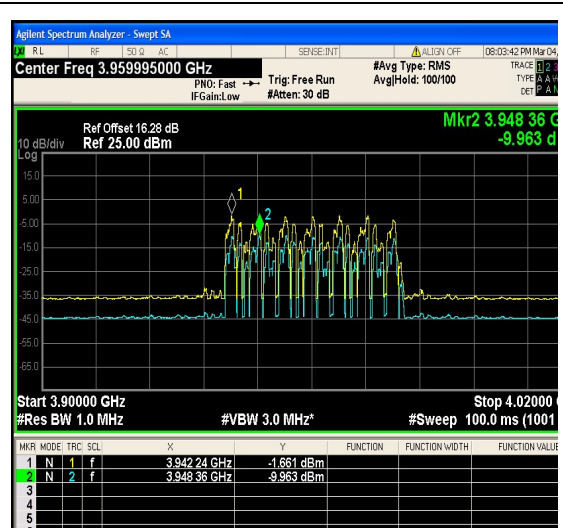
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
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@0-8.83-100%-0.00-8.83-PASS-≤13



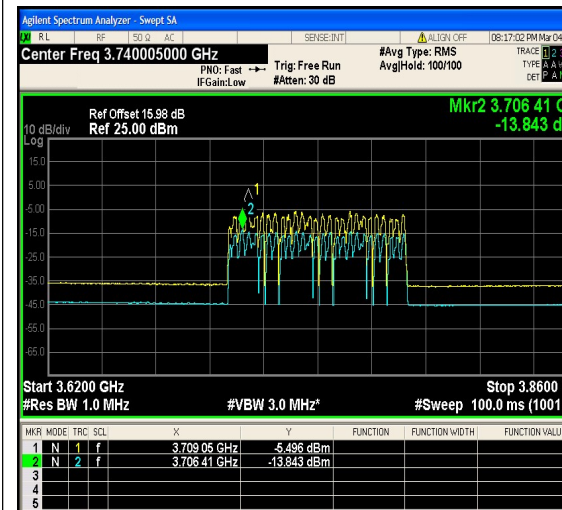
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
H-7-N/A-N/A-N/A-CP-16QAM-Outer\_Full-106  
@0-8.35-100%-0.00-8.35-PASS-≤13



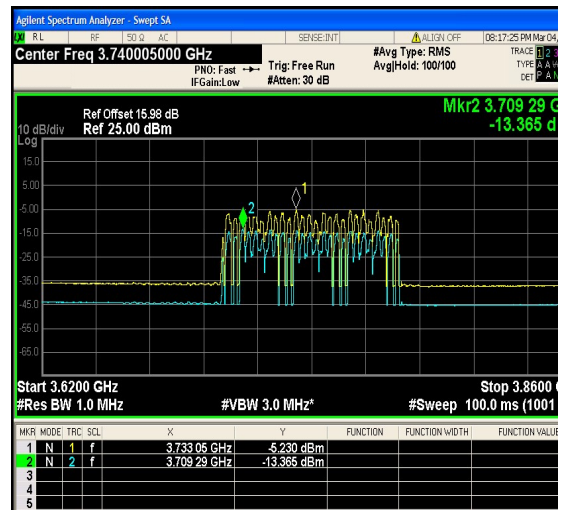
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
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 @0-9.08-100%-0.00-9.08-PASS-≤13



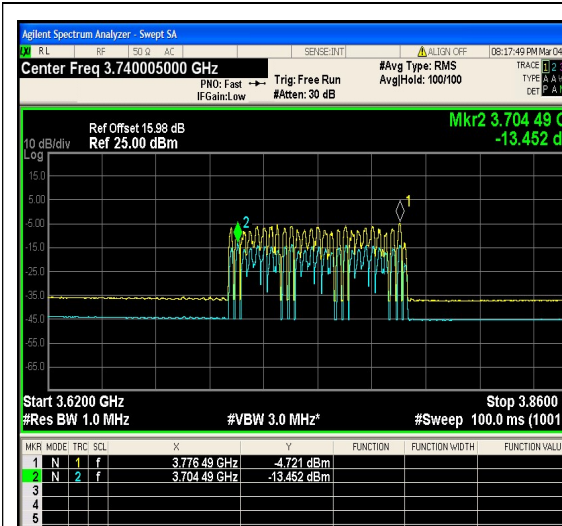
1-DC\_2A\_n77A-3700-3980-PC3-30-5+40-M+  
 H-9-N/A-N/A-N/A-CP-256QAM-Outer\_Full-10  
 6@0-8.30-100%-0.00-8.30-PASS-≤13



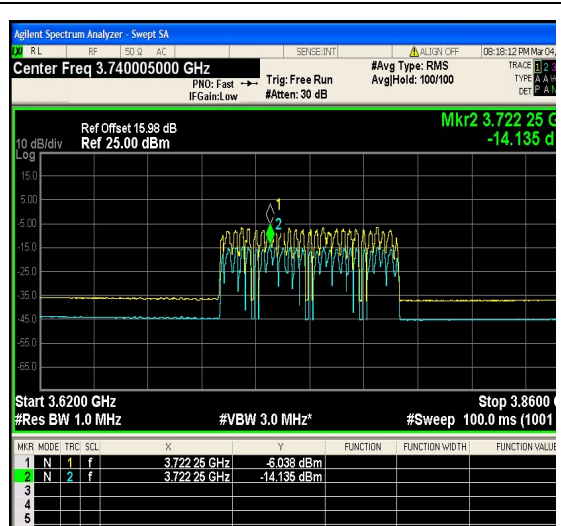
1-DC\_2A\_n77A-3700-3980-PC3-30-5+80-M+  
 L-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer\_Full-2  
 16@0-8.34-100%-0.00-8.34-PASS-≤13



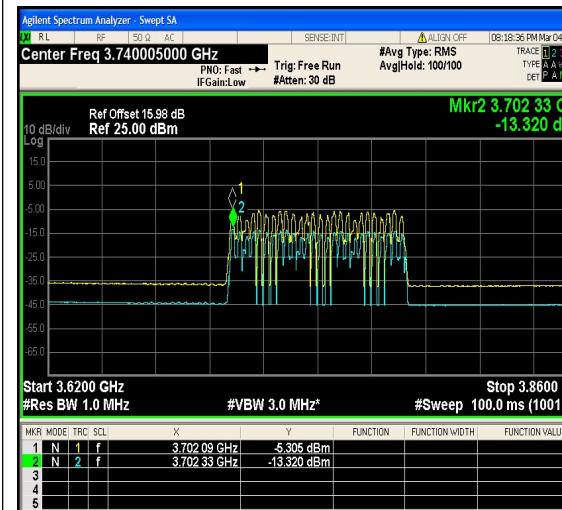
1-DC\_2A\_n77A-3700-3980-PC3-30-5+80-M+  
 L-2-N/A-N/A-N/A-DFT-QPSK-Outer\_Full-216  
 @0-8.14-100%-0.00-8.14-PASS-≤13



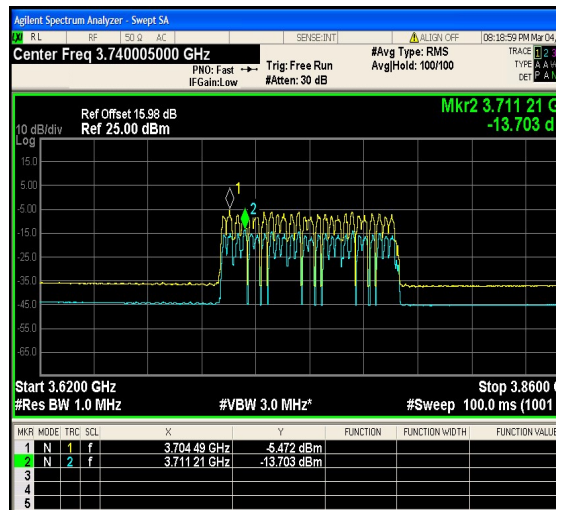
1-DC\_2A\_n77A-3700-3980-PC3-30-5+80-M+  
 L-3-N/A-N/A-N/A-DFT-16QAM-Outer\_Full-21  
 6@0-8.73-100%-0.00-8.73-PASS-≤13



1-DC\_2A\_n77A-3700-3980-PC3-30-5+80-M+  
 L-4-N/A-N/A-N/A-DFT-64QAM-Outer\_Full-21  
 6@0-8.10-100%-0.00-8.10-PASS-≤13



1-DC\_2A\_n77A-3700-3980-PC3-30-5+80-M+  
 L-5-N/A-N/A-N/A-DFT-256QAM-Outer\_Full-2  
 16@0-8.01-100%-0.00-8.01-PASS-≤13



1-DC\_2A\_n77A-3700-3980-PC3-30-5+80-M+  
 L-6-N/A-N/A-N/A-CP-QPSK-Outer\_Full-217  
 @0-8.23-100%-0.00-8.23-PASS-≤13