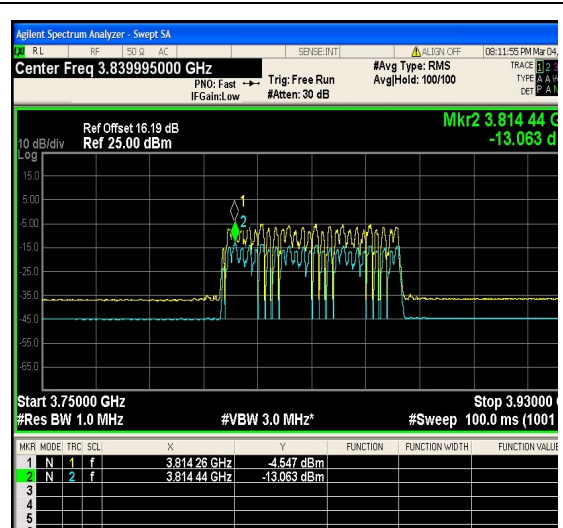
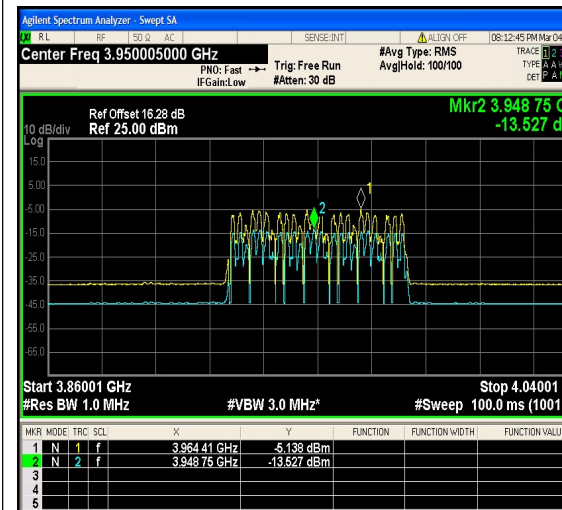


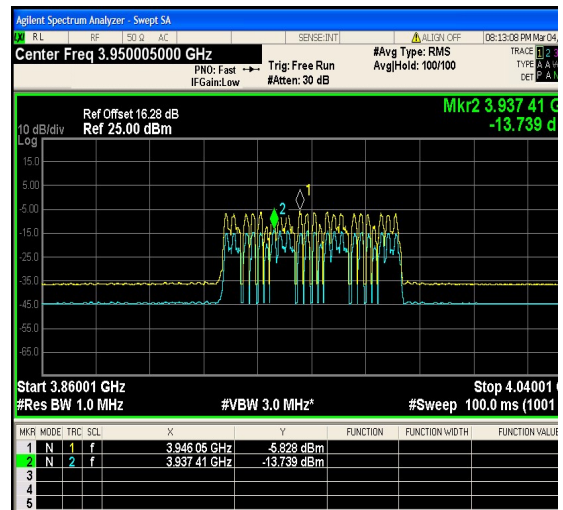
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 M-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-162
 @0-8.17-100%-0.00-8.17-PASS-≤13



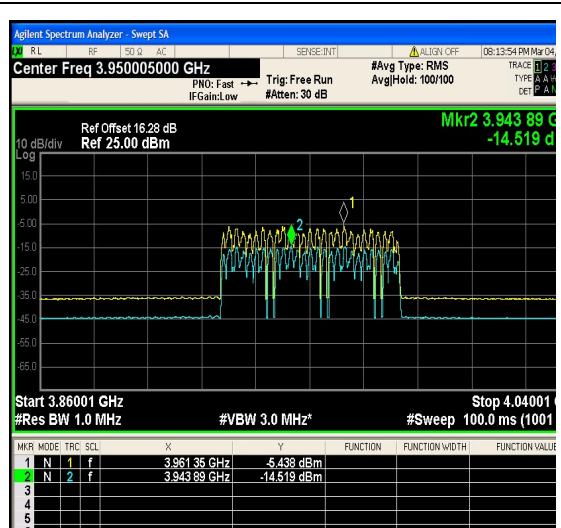
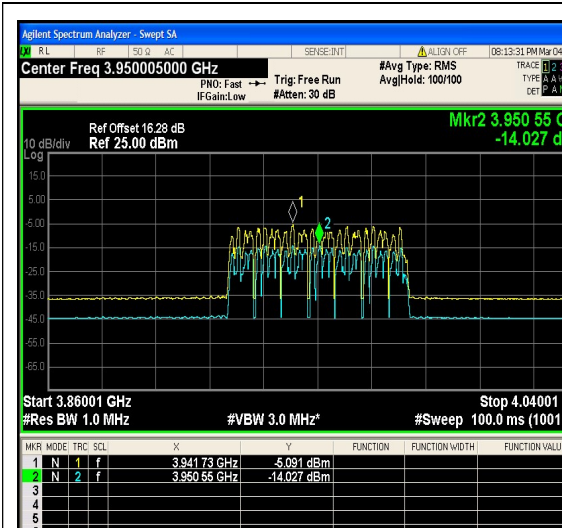
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 M-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-16
 2@0-8.51-100%-0.00-8.51-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-1-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
 162@0-8.39-100%-0.00-8.39-PASS-≤13

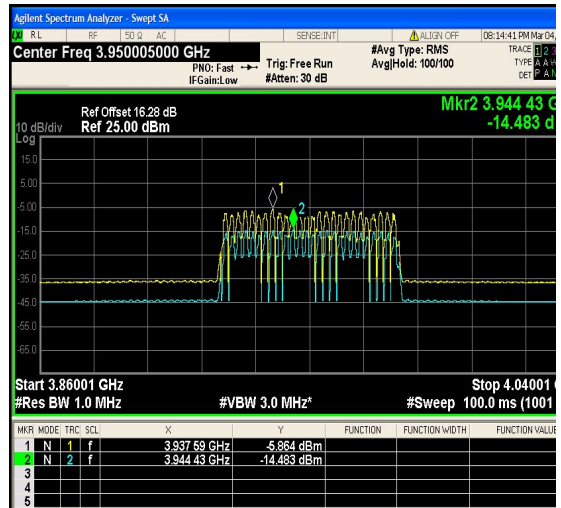
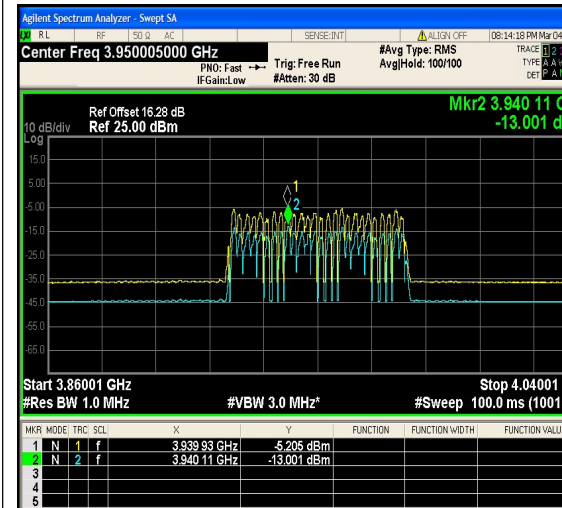


1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-2-N/A-N/A-N/A-DFT-QPSK-Outer_Full-162
 @0-7.91-100%-0.00-7.91-PASS-≤13



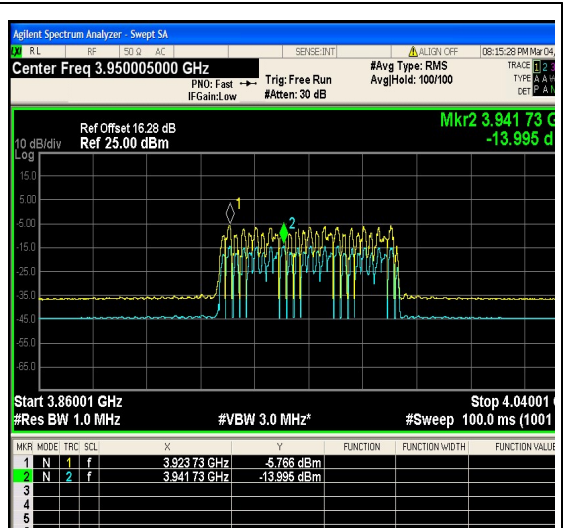
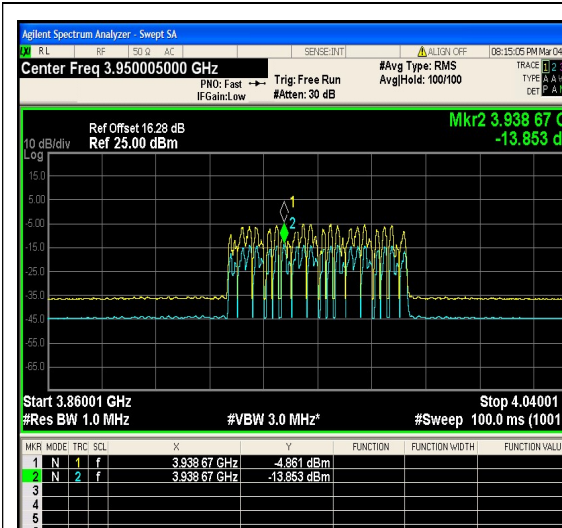
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-16
 2@0-8.94-100%-0.00-8.94-PASS-≤13

1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-16
 2@0-9.08-100%-0.00-9.08-PASS-≤13



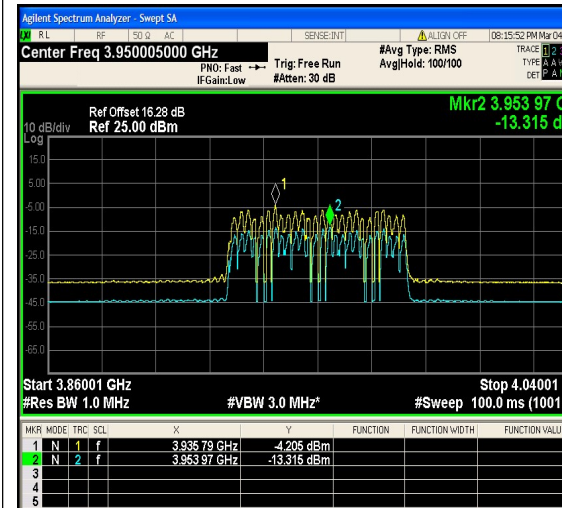
1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-5-N/A-N/A-N/A-DFT-256QAM-Outer_Full-1
 62@0-7.79-100%-0.00-7.79-PASS-≤13

1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
 H-6-N/A-N/A-N/A-CP-QPSK-Outer_Full-162
 @0-8.62-100%-0.00-8.62-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
H-7-N/A-N/A-N/A-CP-16QAM-Outer_Full-162
@0-8.99-100%-0.00-8.99-PASS-≤13

1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
H-8-N/A-N/A-N/A-CP-64QAM-Outer_Full-162
@0-8.23-100%-0.00-8.23-PASS-≤13



1-DC_2A_n77A-3700-3980-PC3-30-5+60-M+
H-9-N/A-N/A-N/A-CP-256QAM-Outer_Full-16
2@0-9.11-100%-0.00-9.11-PASS-≤13

Appendix C: 26dB Bandwidth and Occupied Bandwidth for

NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	17.847	19.19	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+L	Outer_Full	17.802	18.89	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	17.865	19.07	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+L	Outer_Full	17.828	18.84	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+L	Outer_Full	17.864	19.31	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+L	Outer_Full	18.208	19.59	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+L	Outer_Full	18.196	19.31	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+L	Outer_Full	18.201	19.36	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	17.846	19.13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+M	Outer_Full	17.834	19.00	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	17.831	19.02	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+M	Outer_Full	17.811	19.02	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+M	Outer_Full	17.868	19.11	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+M	Outer_Full	18.177	19.21	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+M	Outer_Full	18.183	19.46	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+M	Outer_Full	18.226	19.24	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	17.880	19.26	PASS

DC_2A_n77A-3700-3980	30	5+20	DFT-PI2BPSK	M+H	Outer_Full	17.814	18.98	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	17.815	18.93	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-64QAM	M+H	Outer_Full	17.813	19.09	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-QPSK	M+H	Outer_Full	17.865	19.12	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-16QAM	M+H	Outer_Full	18.209	19.48	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-64QAM	M+H	Outer_Full	18.226	19.38	PASS
DC_2A_n77A-3700-3980	30	5+20	CP-256QAM	M+H	Outer_Full	18.218	19.41	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	35.699	37.35	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+L	Outer_Full	35.656	37.24	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	35.736	37.41	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+L	Outer_Full	35.687	37.44	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+L	Outer_Full	35.710	37.33	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+L	Outer_Full	37.789	39.55	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+L	Outer_Full	37.766	39.72	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+L	Outer_Full	37.753	39.39	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	35.756	37.30	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+M	Outer_Full	35.725	37.41	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	35.805	36.98	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+M	Outer_Full	35.726	37.35	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+M	Outer_Full	35.721	37.35	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+M	Outer_Full	37.826	39.42	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+M	Outer_Full	37.824	39.62	PASS

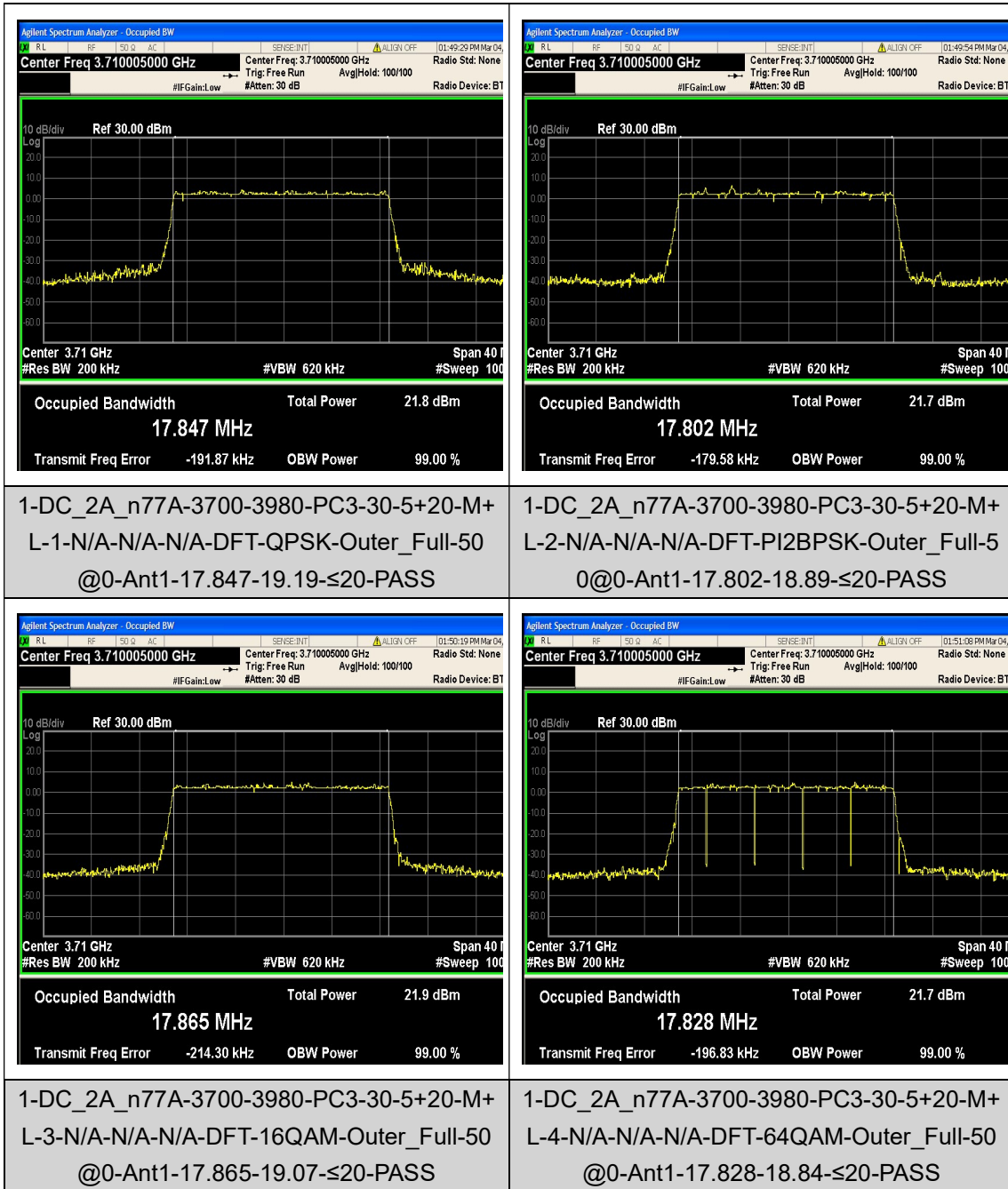
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+M	Outer_Full	37.794	39.41	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	35.577	37.47	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-PI2BPSK	M+H	Outer_Full	35.699	37.13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	35.760	37.25	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-64QAM	M+H	Outer_Full	35.712	37.43	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-QPSK	M+H	Outer_Full	35.662	37.43	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-16QAM	M+H	Outer_Full	37.649	39.41	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-64QAM	M+H	Outer_Full	37.782	39.57	PASS
DC_2A_n77A-3700-3980	30	5+40	CP-256QAM	M+H	Outer_Full	37.775	39.52	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	77.135	79.71	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+L	Outer_Full	77.068	79.63	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	77.150	79.85	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+L	Outer_Full	77.089	79.78	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+L	Outer_Full	76.981	79.88	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+L	Outer_Full	77.295	79.76	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+L	Outer_Full	77.446	80.13	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+L	Outer_Full	77.495	80.25	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	77.168	79.69	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+M	Outer_Full	77.067	79.84	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	77.148	79.79	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+M	Outer_Full	77.100	79.86	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+M	Outer_Full	76.710	79.69	PASS

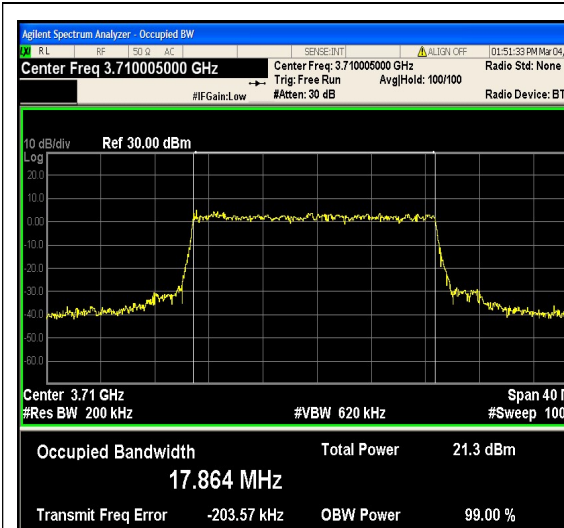
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+M	Outer_Full	77.445	79.98	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+M	Outer_Full	77.391	80.23	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+M	Outer_Full	77.612	80.01	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	77.009	79.66	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-PI2BPSK	M+H	Outer_Full	76.917	79.77	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	77.203	79.73	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-64QAM	M+H	Outer_Full	77.034	79.93	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-QPSK	M+H	Outer_Full	77.158	79.90	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-16QAM	M+H	Outer_Full	77.292	80.14	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-64QAM	M+H	Outer_Full	77.100	80.35	PASS
DC_2A_n77A-3700-3980	30	5+80	CP-256QAM	M+H	Outer_Full	77.403	80.35	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+L	Outer_Full	96.259	99.49	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+L	Outer_Full	96.191	99.69	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+L	Outer_Full	96.318	99.65	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM	M+L	Outer_Full	96.153	99.60	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+L	Outer_Full	96.401	99.53	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+L	Outer_Full	97.270	100.8	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+L	Outer_Full	97.263	100.7	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM	M+L	Outer_Full	97.251	100.6	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+M	Outer_Full	96.239	99.48	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+M	Outer_Full	96.193	99.62	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM	M+M	Outer_Full	96.202	99.47	PASS

DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM M	M+M	Outer_Full	96.392	99.76	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+M	Outer_Full	96.222	99.47	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+M	Outer_Full	97.352	100.6	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+M	Outer_Full	97.183	100.5	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM M	M+M	Outer_Full	97.220	100.6	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-QPSK	M+H	Outer_Full	96.156	99.61	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-PI2BPSK	M+H	Outer_Full	96.486	99.56	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-16QAM M	M+H	Outer_Full	96.207	99.68	PASS
DC_2A_n77A-3700-3980	30	5+100	DFT-64QAM M	M+H	Outer_Full	96.224	99.69	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-QPSK	M+H	Outer_Full	96.487	99.59	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-16QAM	M+H	Outer_Full	97.342	100.7	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-64QAM	M+H	Outer_Full	97.275	100.6	PASS
DC_2A_n77A-3700-3980	30	5+100	CP-256QAM M	M+H	Outer_Full	97.119	100.5	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	57.908	59.95	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+L	Outer_Full	57.737	59.88	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM M	M+L	Outer_Full	57.736	60.22	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM M	M+L	Outer_Full	57.845	60.29	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+L	Outer_Full	57.872	59.80	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+L	Outer_Full	57.817	59.70	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+L	Outer_Full	57.716	60.08	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM M	M+L	Outer_Full	57.821	60.05	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	57.884	60.00	PASS

DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+M	Outer_Full	57.752	60.04	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	57.807	60.21	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+M	Outer_Full	57.956	60.23	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+M	Outer_Full	57.495	60.19	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+M	Outer_Full	57.803	59.79	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+M	Outer_Full	57.824	60.01	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+M	Outer_Full	57.874	60.05	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	57.918	60.10	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-PI2BPSK	M+H	Outer_Full	57.676	59.97	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	57.709	60.22	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-64QAM	M+H	Outer_Full	57.682	60.13	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-QPSK	M+H	Outer_Full	57.774	59.99	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-16QAM	M+H	Outer_Full	57.624	60.07	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-64QAM	M+H	Outer_Full	57.793	59.79	PASS
DC_2A_n77A-3700-3980	30	5+60	CP-256QAM	M+H	Outer_Full	57.733	60.12	PASS

Test Graphs

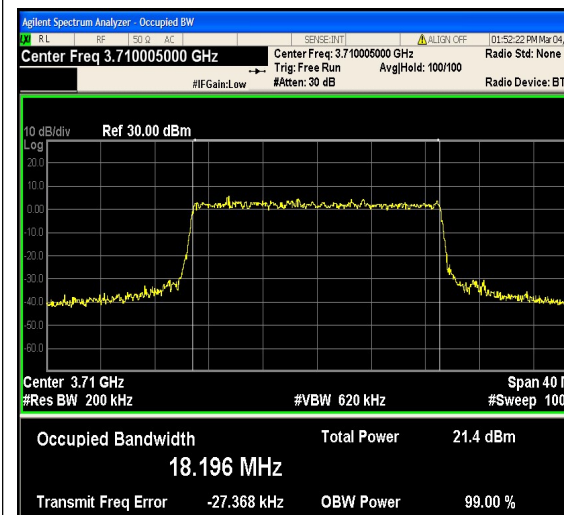




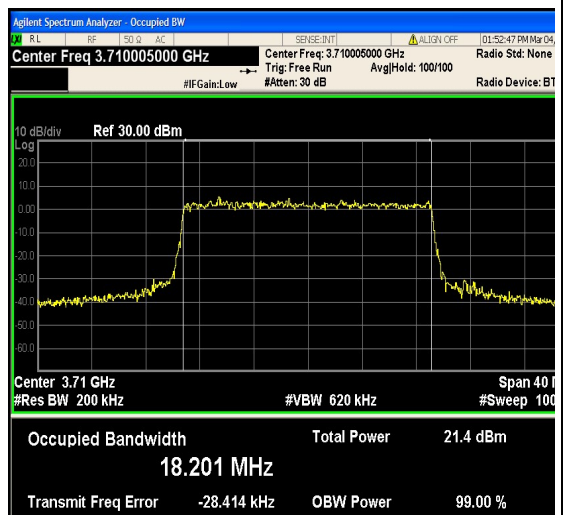
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-5-N/A-N/A-N/A-CP-QPSK-Outer_Full-51@
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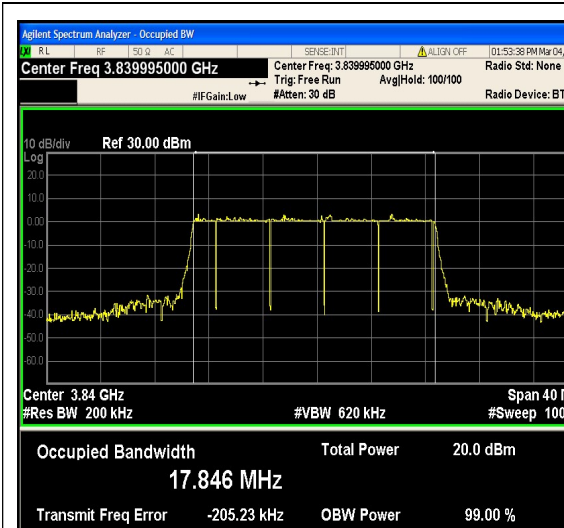
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L-6-N/A-N/A-N/A-CP-16QAM-Outer_Full-51
@0-Ant1-18.208-19.59-≤20-PASS



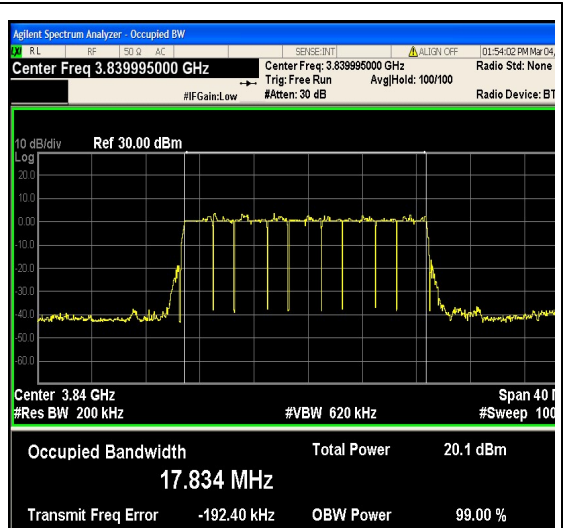
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
L-7-N/A-N/A-N/A-CP-64QAM-Outer_Full-51
@0-Ant1-18.196-19.31-≤20-PASS



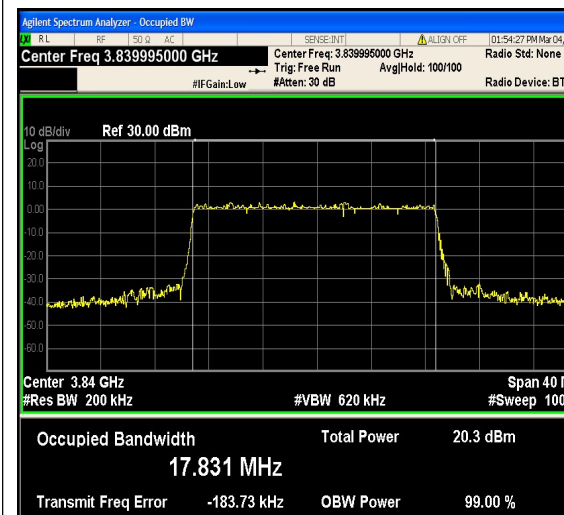
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L-8-N/A-N/A-N/A-CP-256QAM-Outer_Full-51
@0-Ant1-18.201-19.36-≤20-PASS



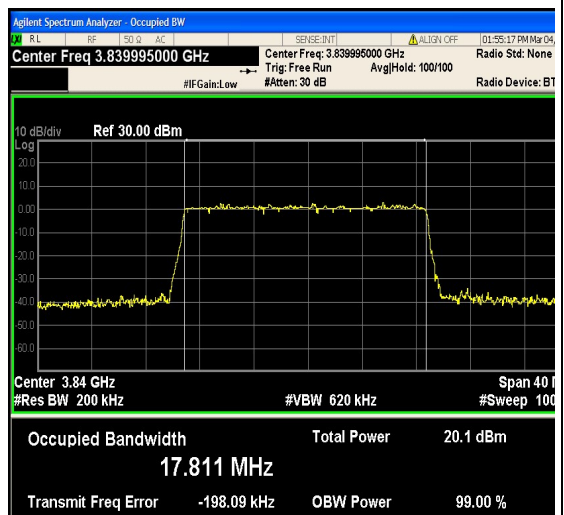
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 @0-Ant1-17.846-19.13-≤20-PASS



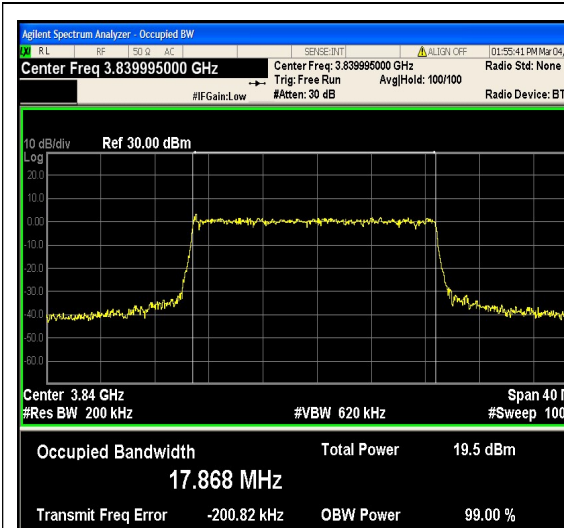
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 M-2-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
 50@0-Ant1-17.834-19.00-≤20-PASS



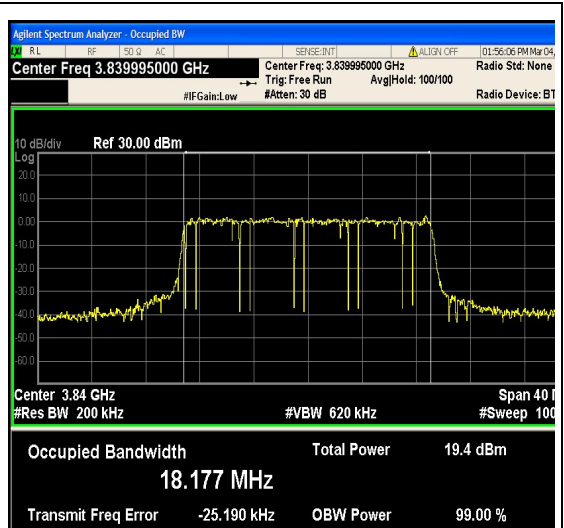
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 M-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-50
 @0-Ant1-17.831-19.02-≤20-PASS



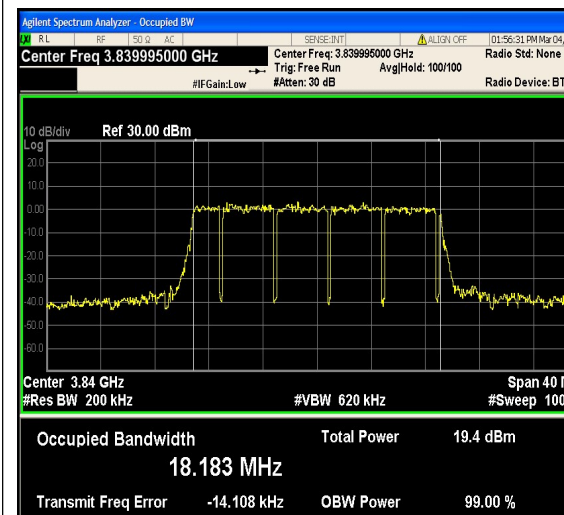
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 M-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-50
 @0-Ant1-17.811-19.02-≤20-PASS



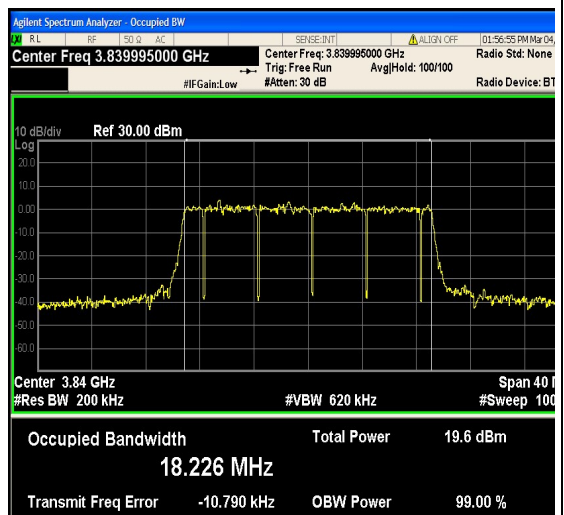
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 M-5-N/A-N/A-N/A-CP-QPSK-Outer_Full-51@
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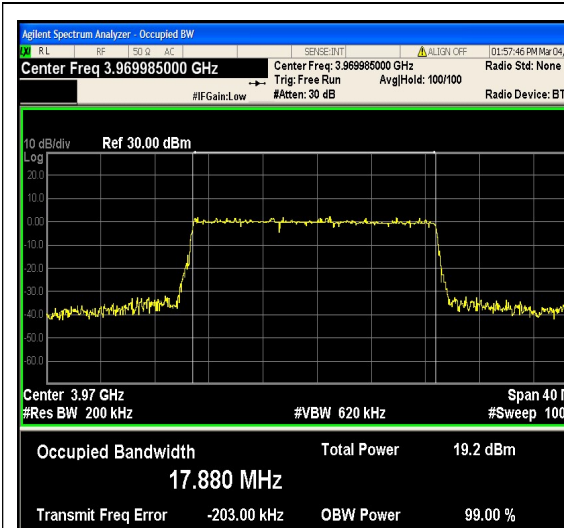
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 M-6-N/A-N/A-N/A-CP-16QAM-Outer_Full-51
 @0-Ant1-18.177-19.21-≤20-PASS



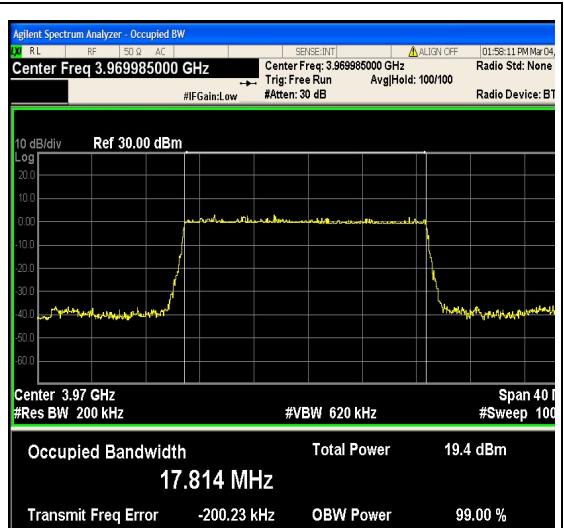
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 M-7-N/A-N/A-N/A-CP-64QAM-Outer_Full-51
 @0-Ant1-18.183-19.46-≤20-PASS



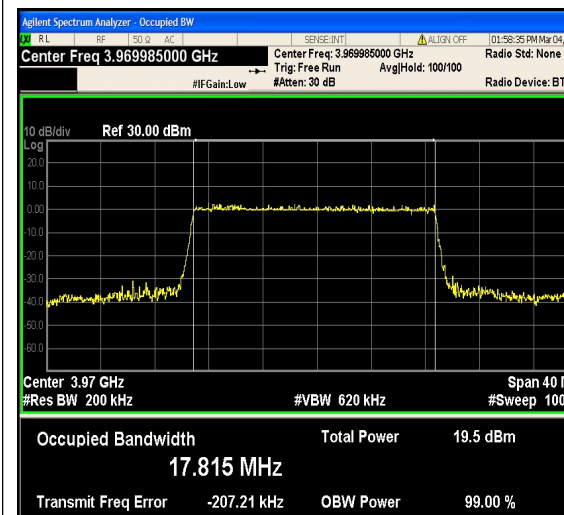
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 M-8-N/A-N/A-N/A-CP-256QAM-Outer_Full-51
 @0-Ant1-18.226-19.24-≤20-PASS



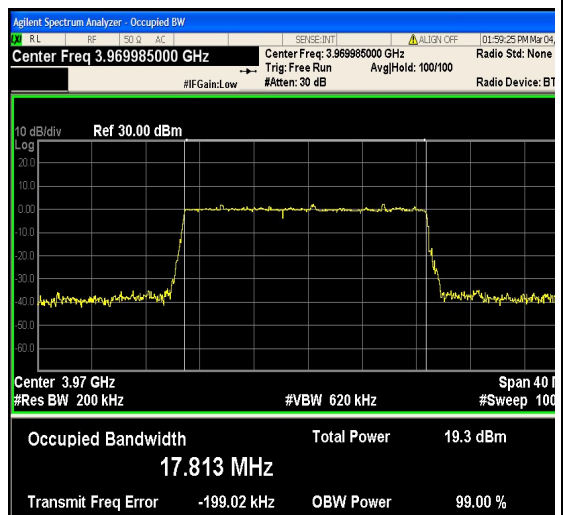
1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 H-1-N/A-N/A-N/A-DFT-QPSK-Outer_Full-50
 @0-Ant1-17.880-19.26-≤20-PASS



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 H-2-N/A-N/A-N/A-DFT-PI2BPSK-Outer_Full-
 50@0-Ant1-17.814-18.98-≤20-PASS



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 H-3-N/A-N/A-N/A-DFT-16QAM-Outer_Full-50
 @0-Ant1-17.815-18.93-≤20-PASS



1-DC_2A_n77A-3700-3980-PC3-30-5+20-M+
 H-4-N/A-N/A-N/A-DFT-64QAM-Outer_Full-50
 @0-Ant1-17.813-19.09-≤20-PASS