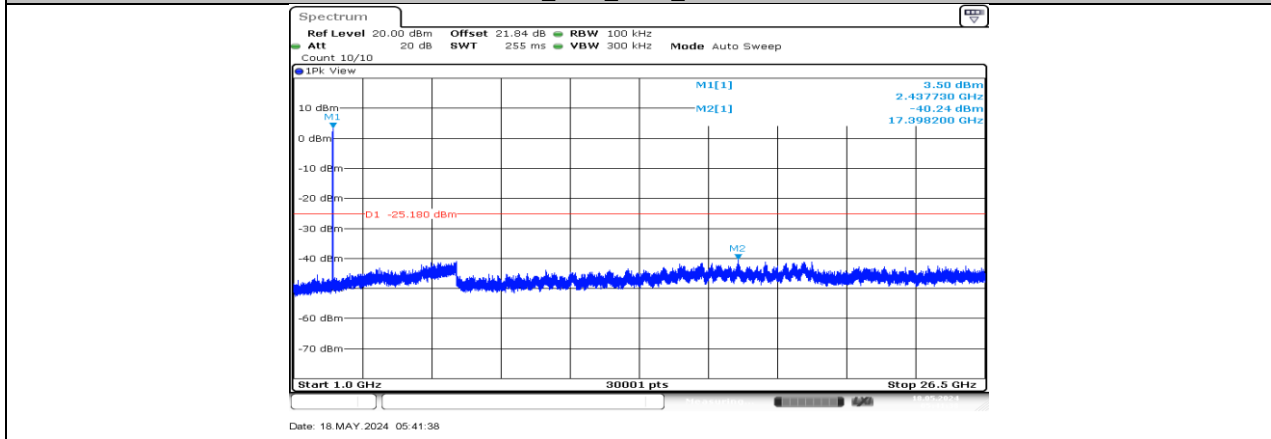
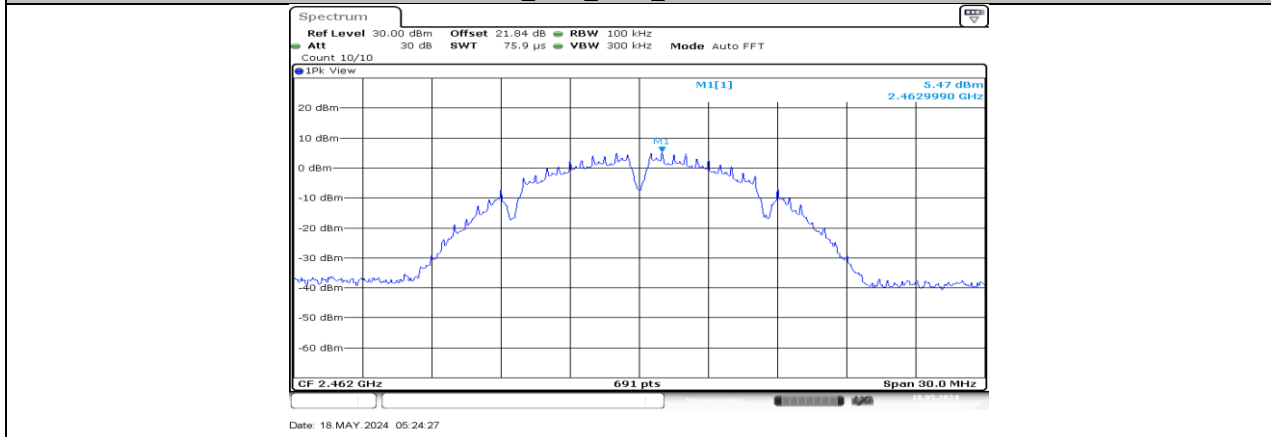


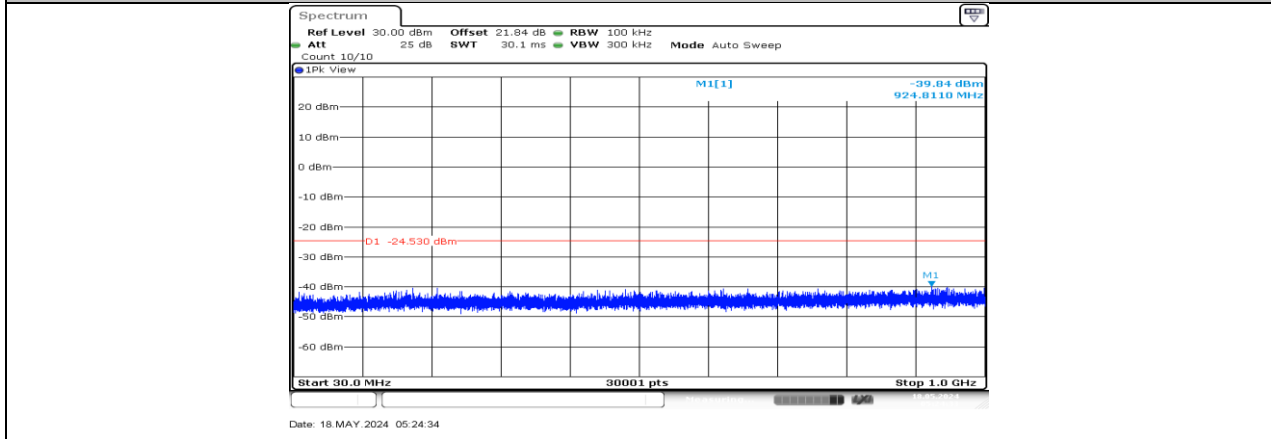
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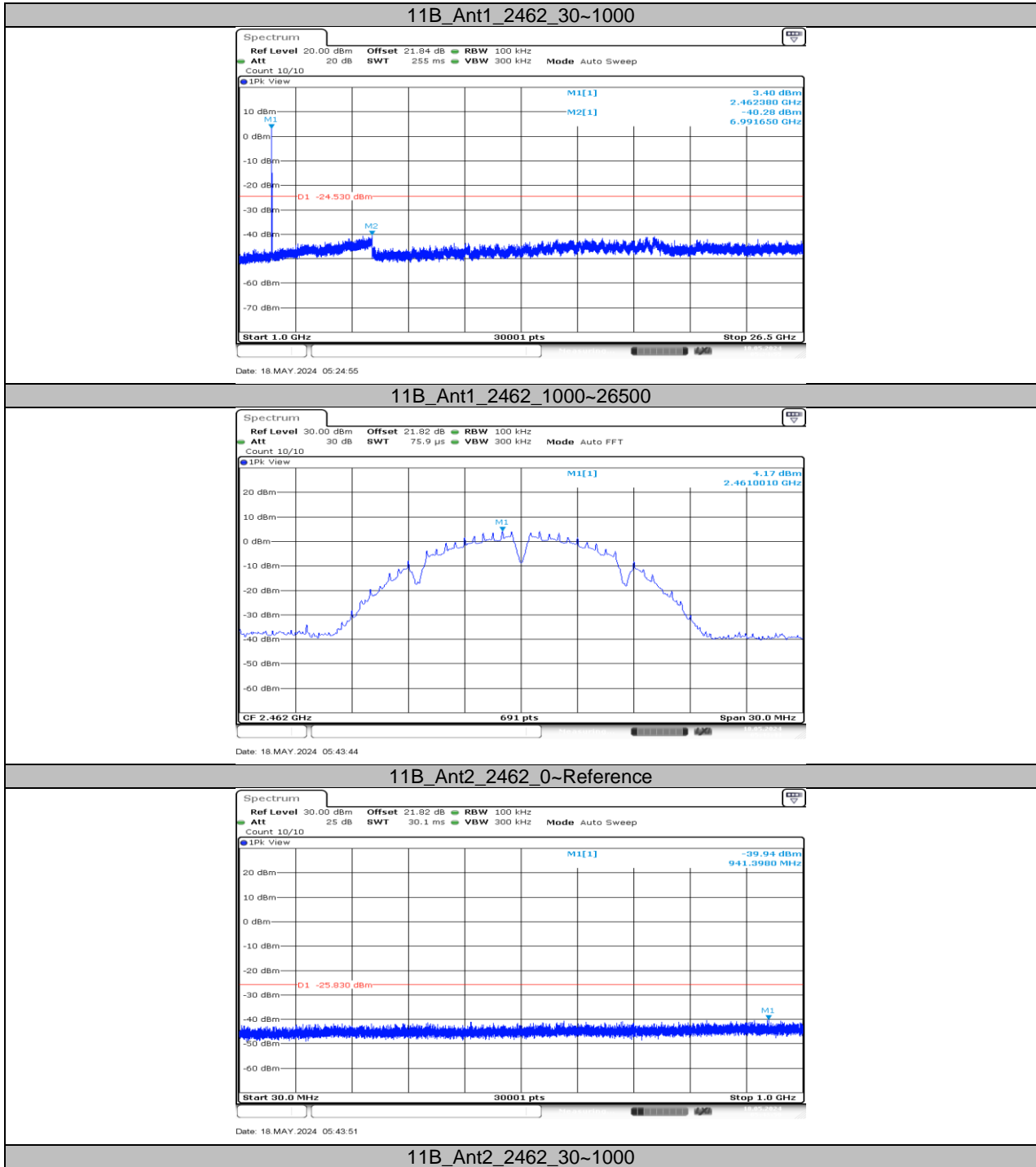


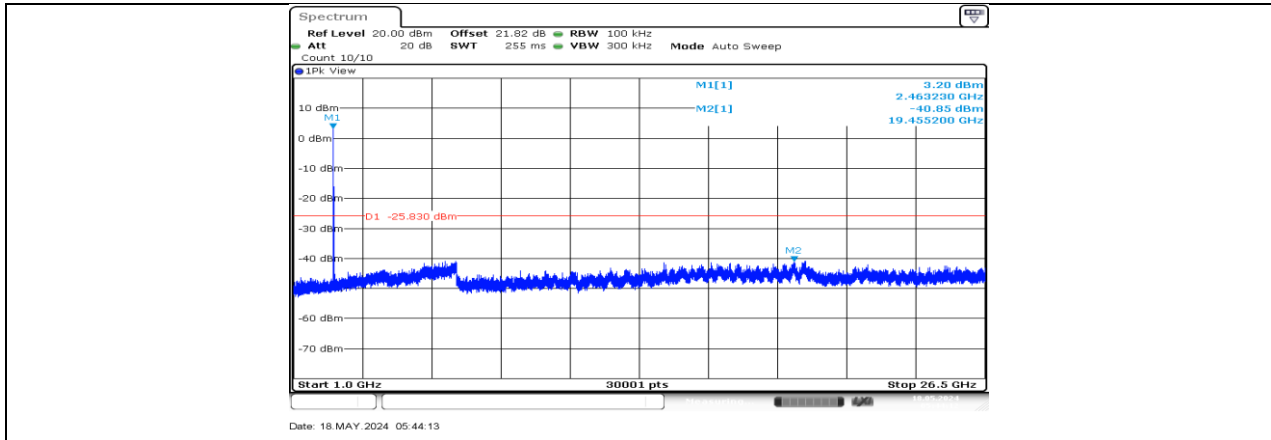
11B_Ant2_2437_1000~26500



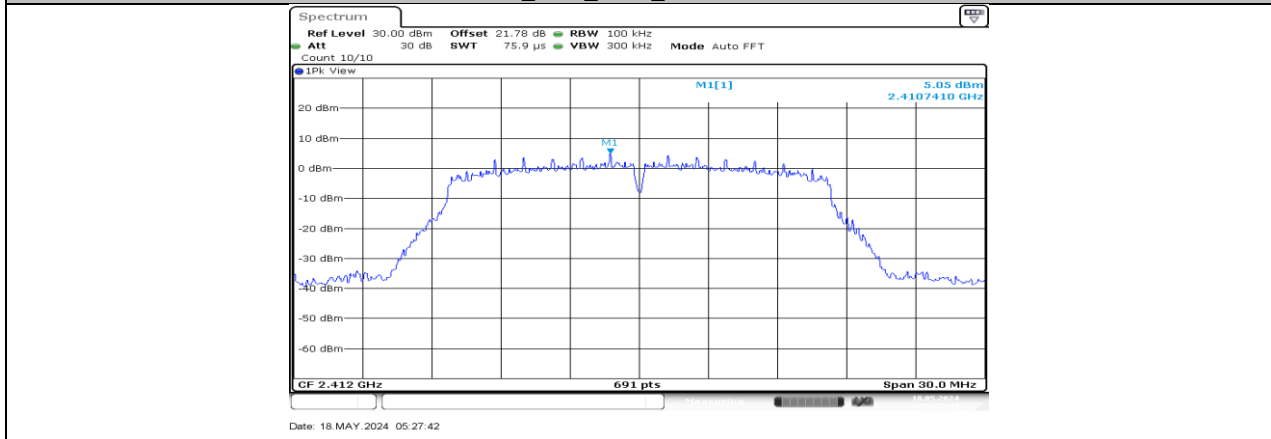
11B_Ant1_2462_0~Reference



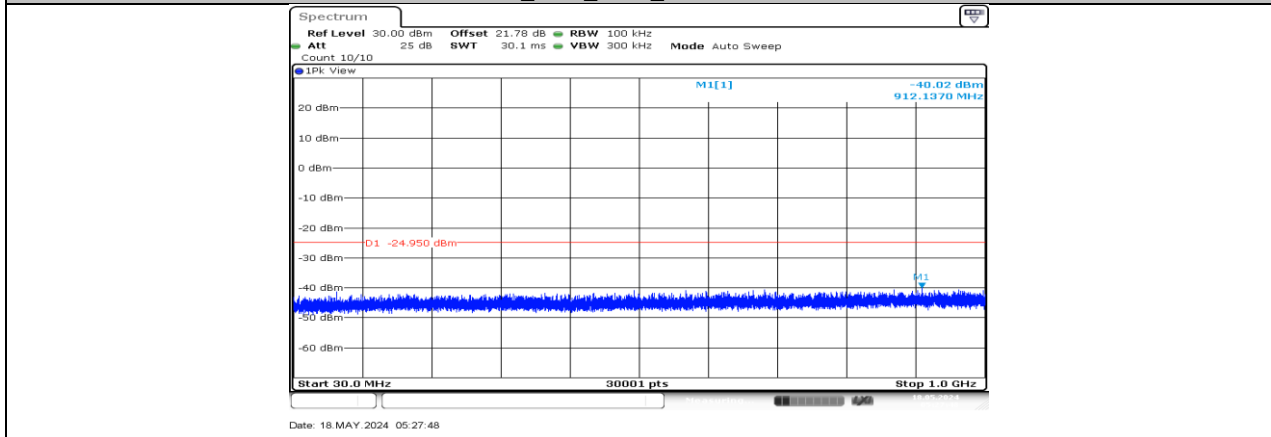




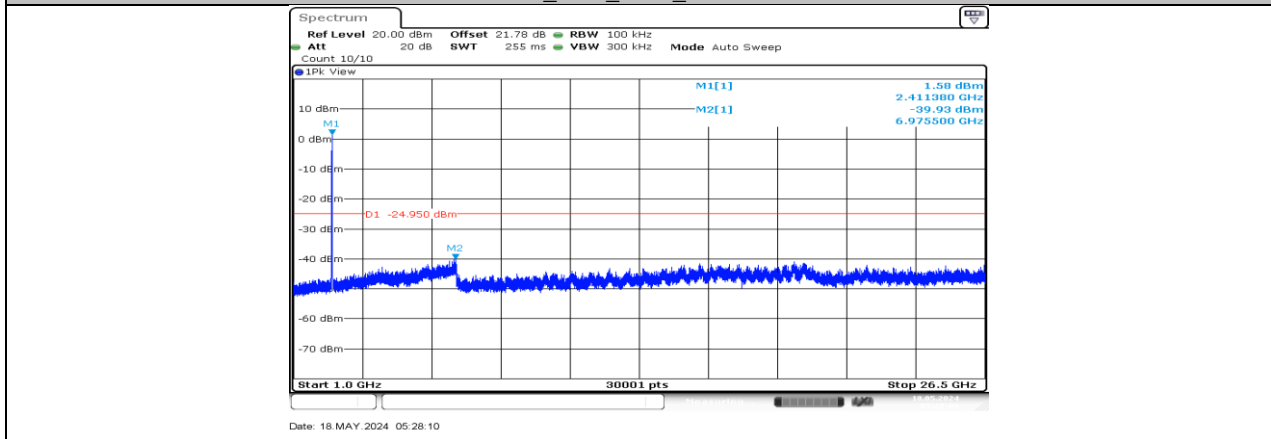
11B_Ant2_2462_1000~26500

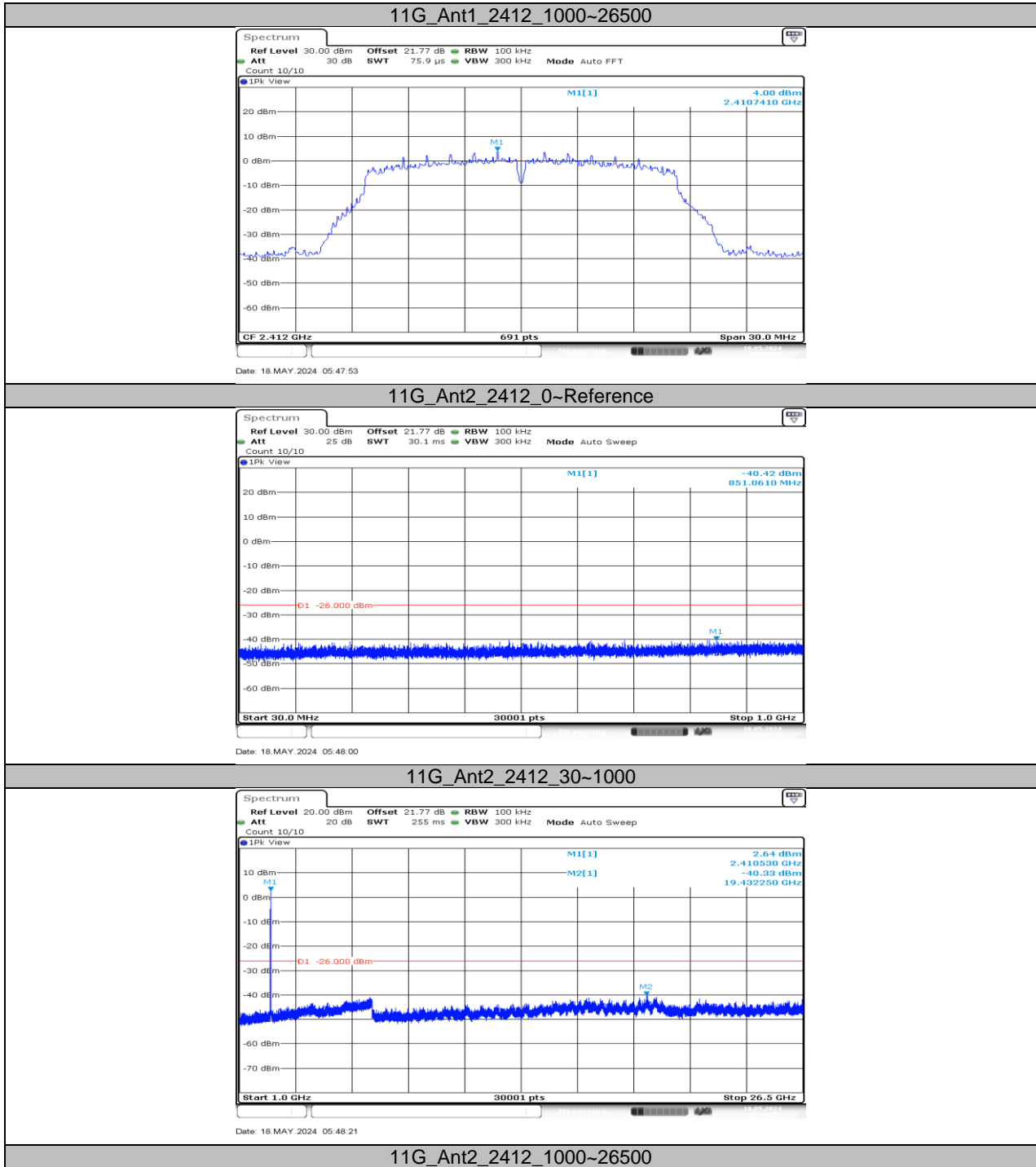


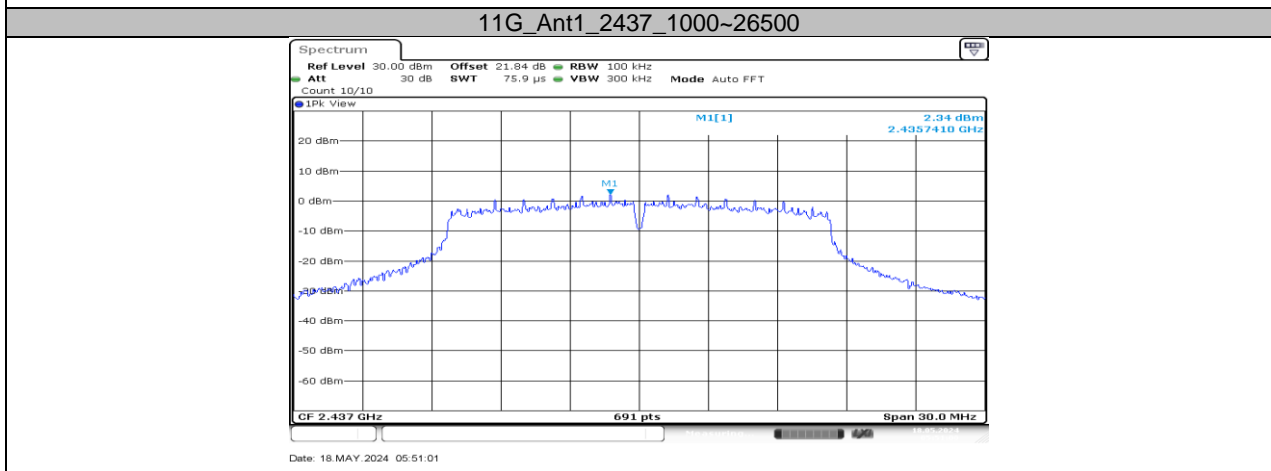
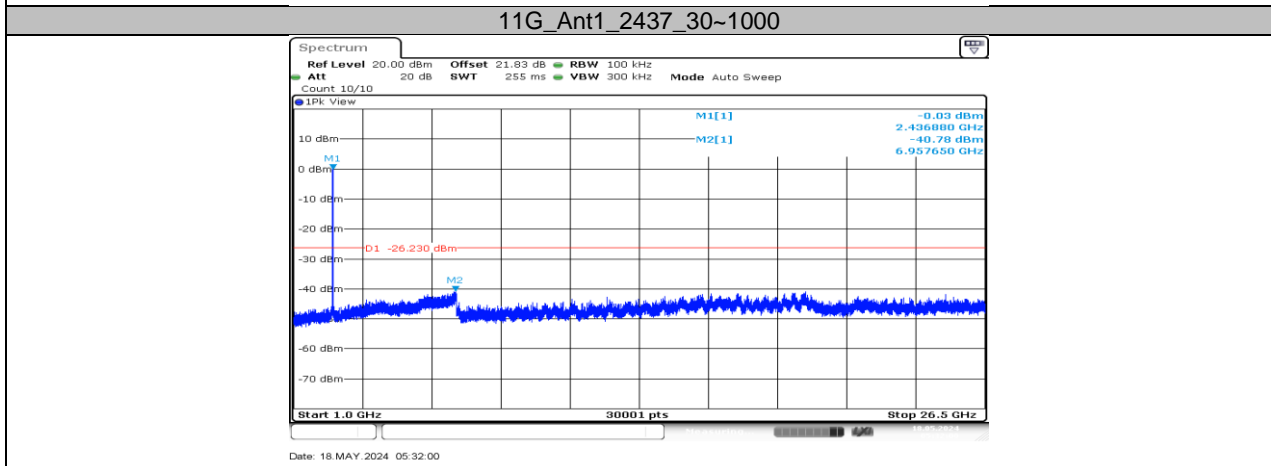
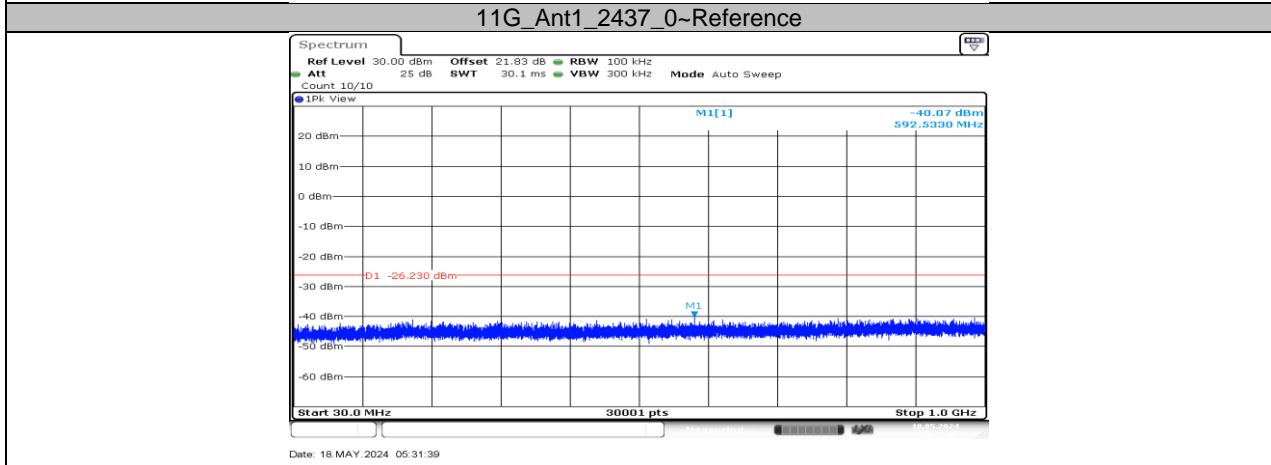
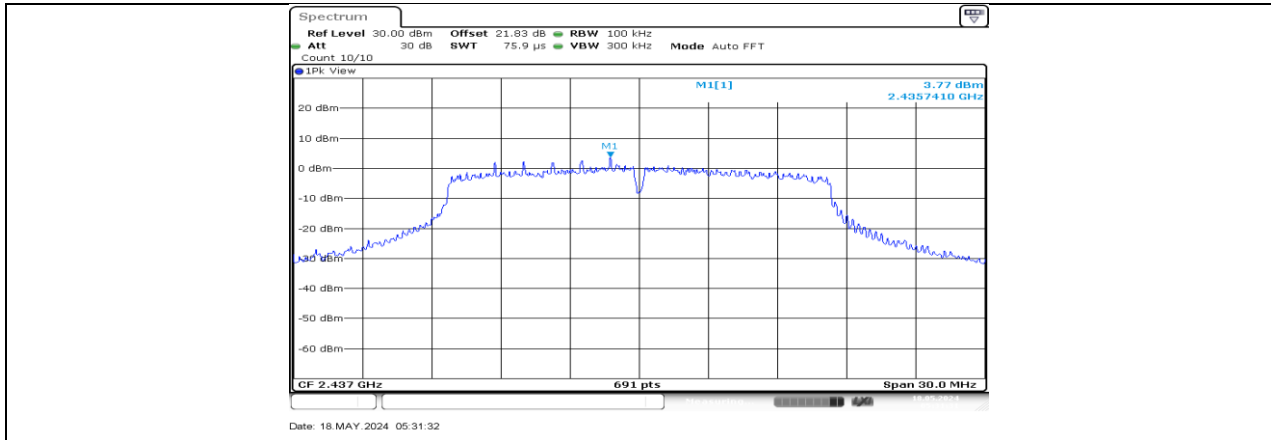
11G_Ant1_2412_0~Reference

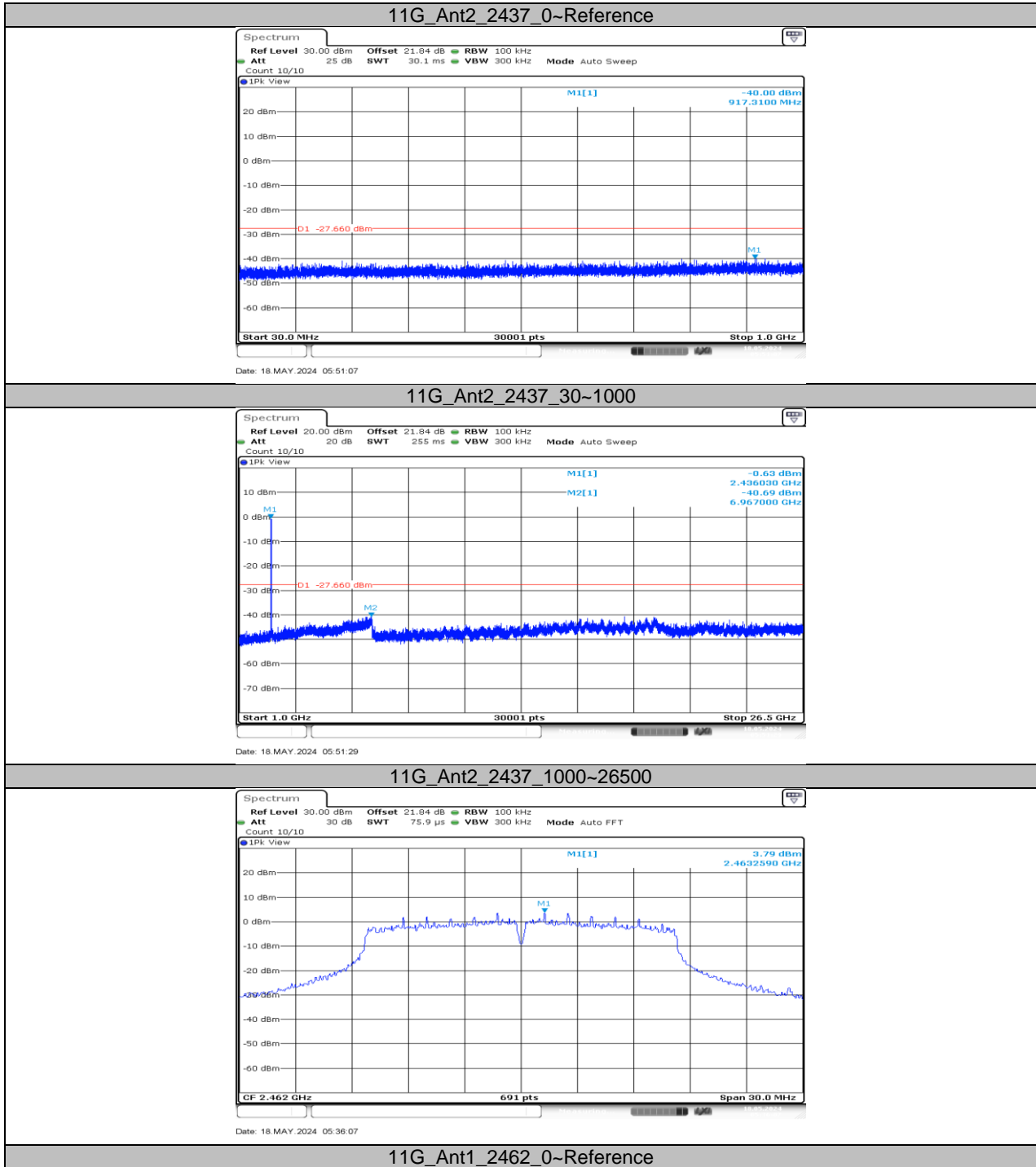


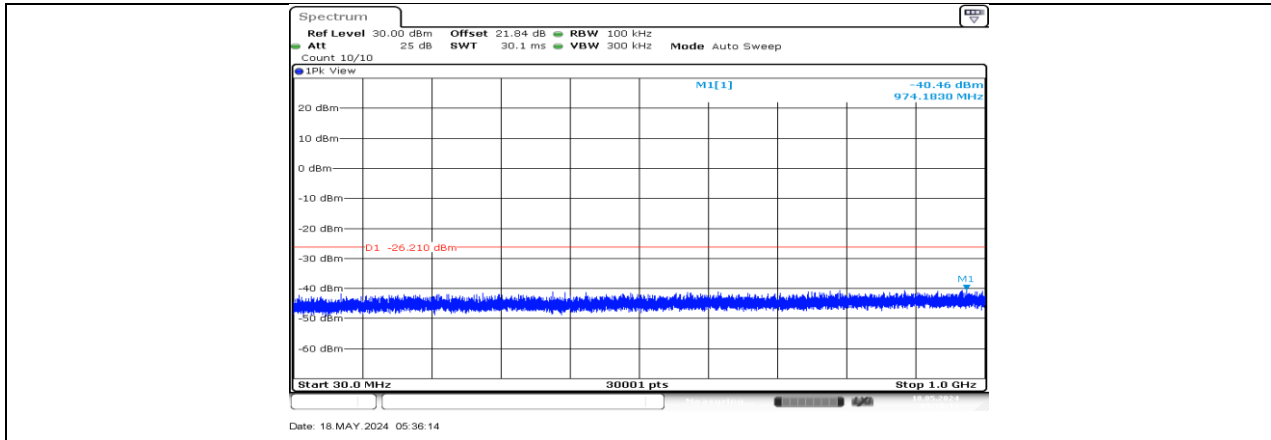
11G_Ant1_2412_30~1000





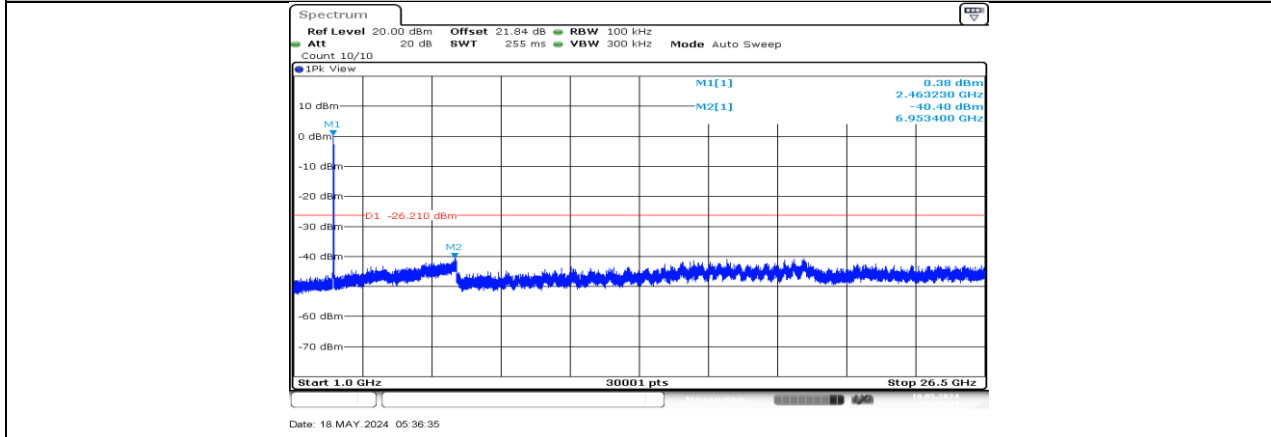






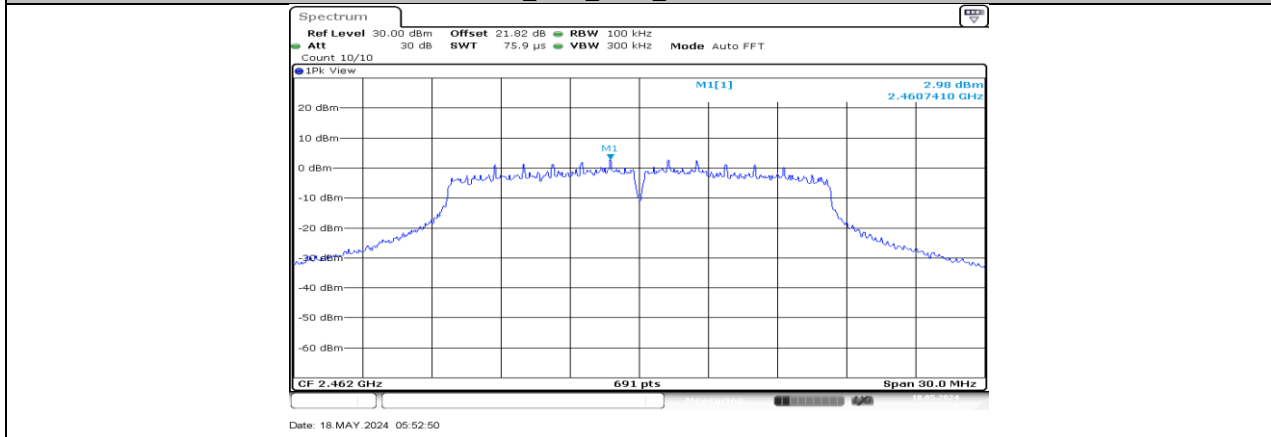
Date: 18.MAY.2024 05:36:14

11G_Ant1_2462_30~1000



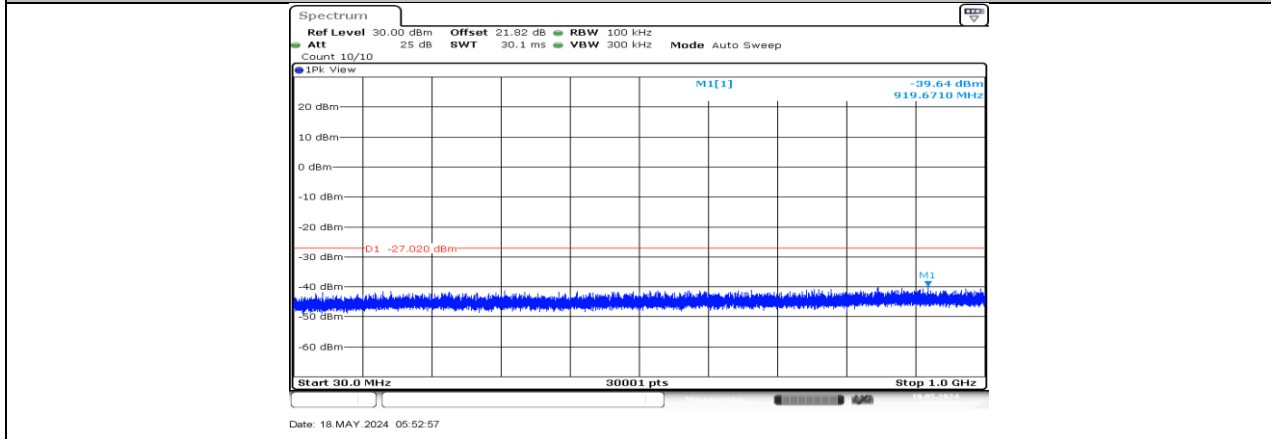
Date: 18.MAY.2024 05:36:35

11G_Ant1_2462_1000~26500

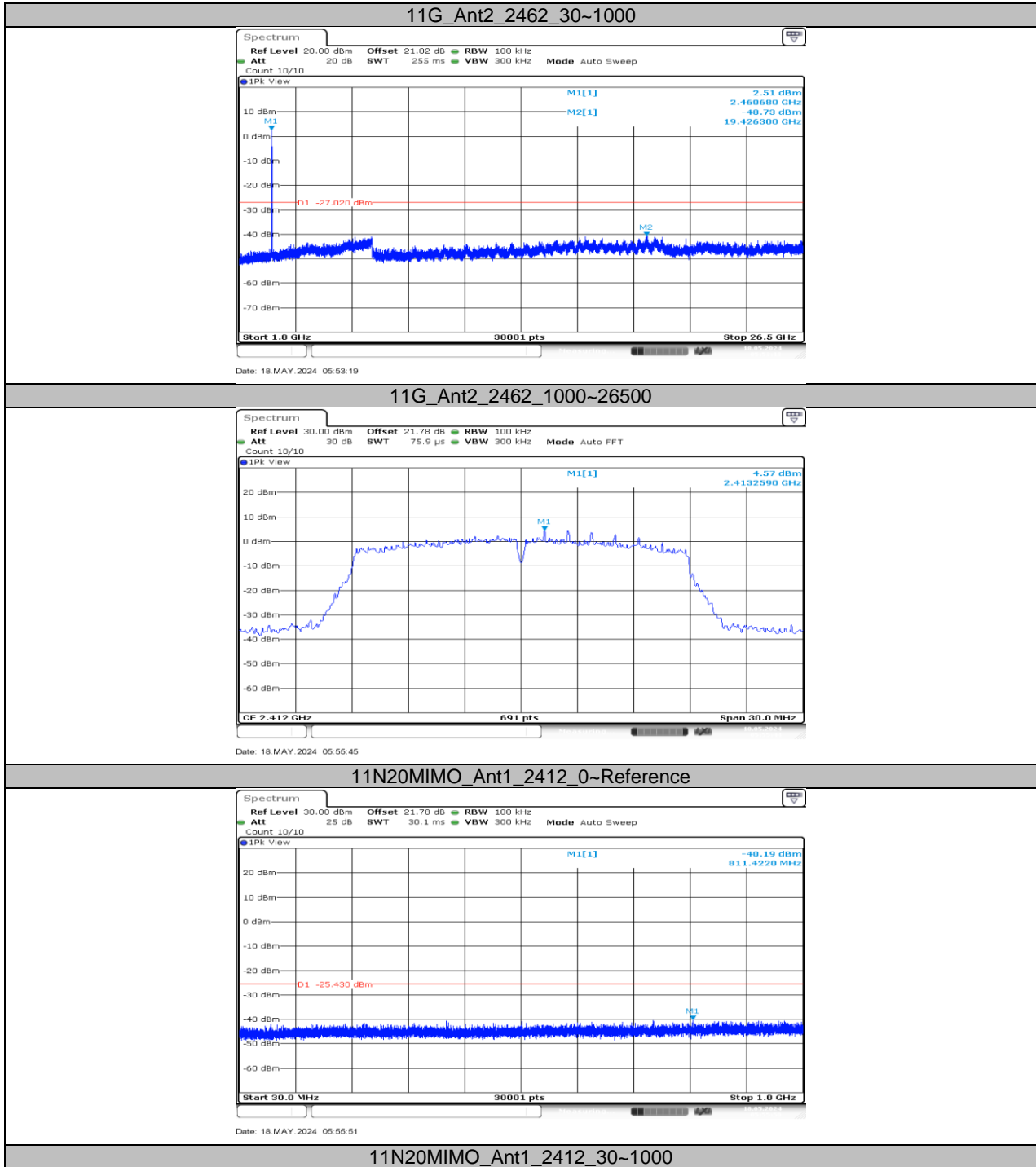


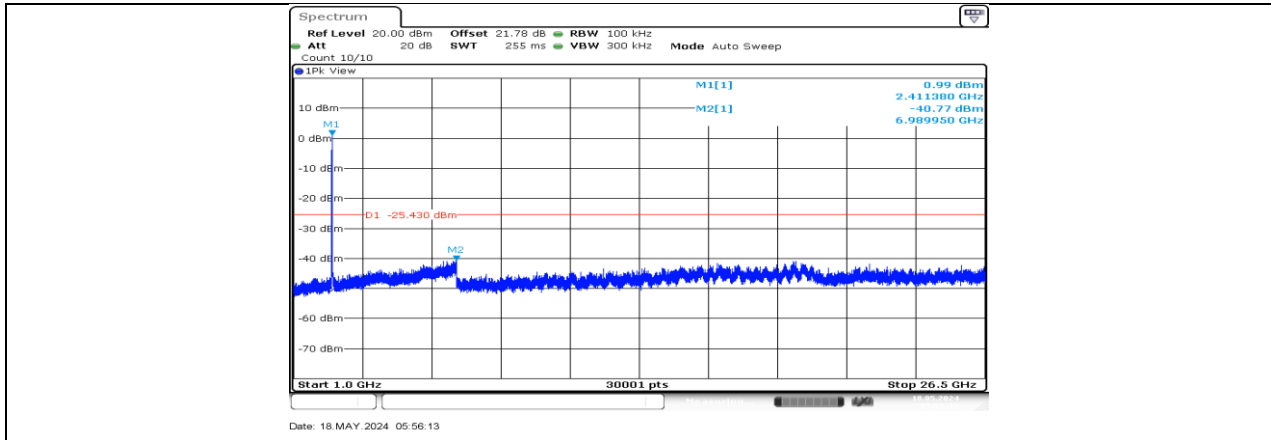
Date: 18.MAY.2024 05:52:50

11G_Ant2_2462_0~Reference

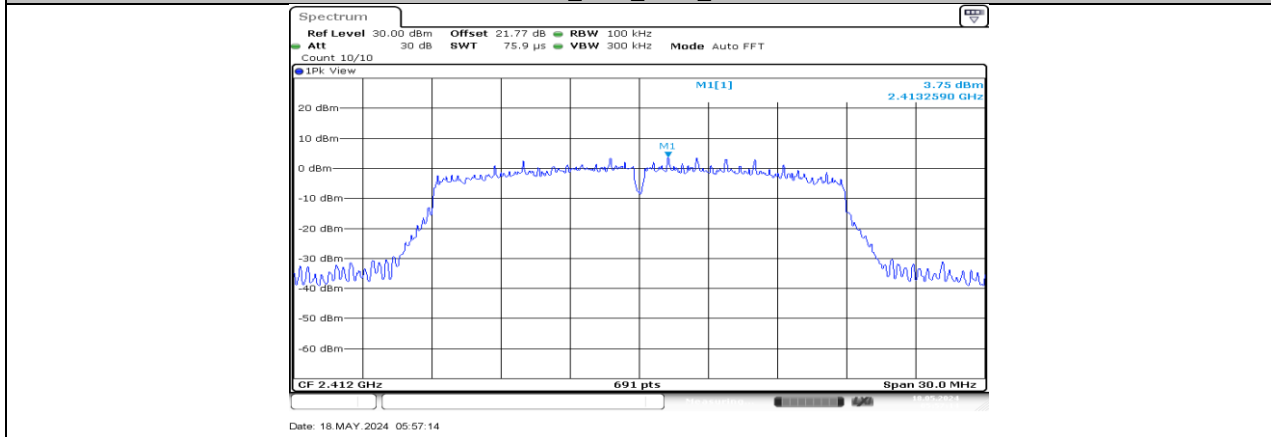


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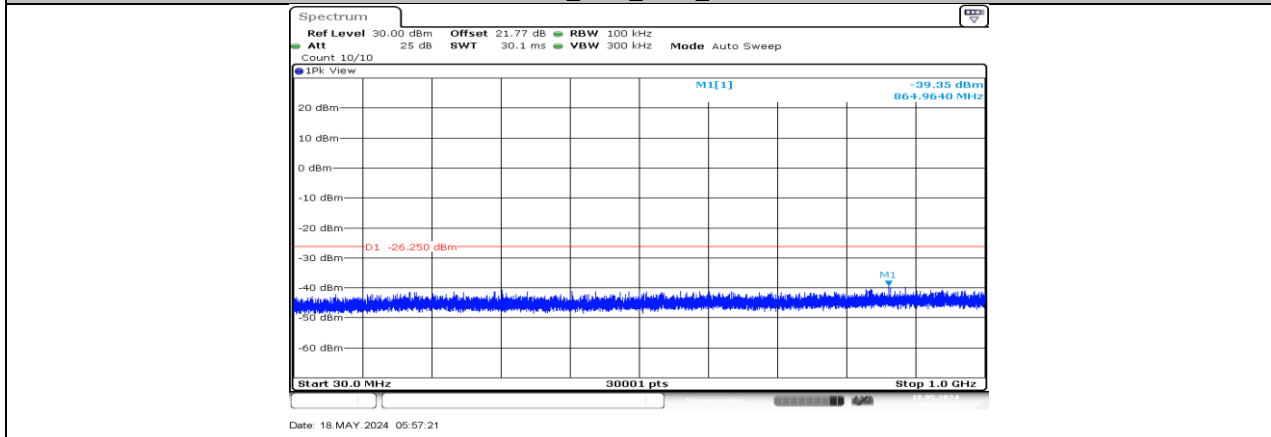




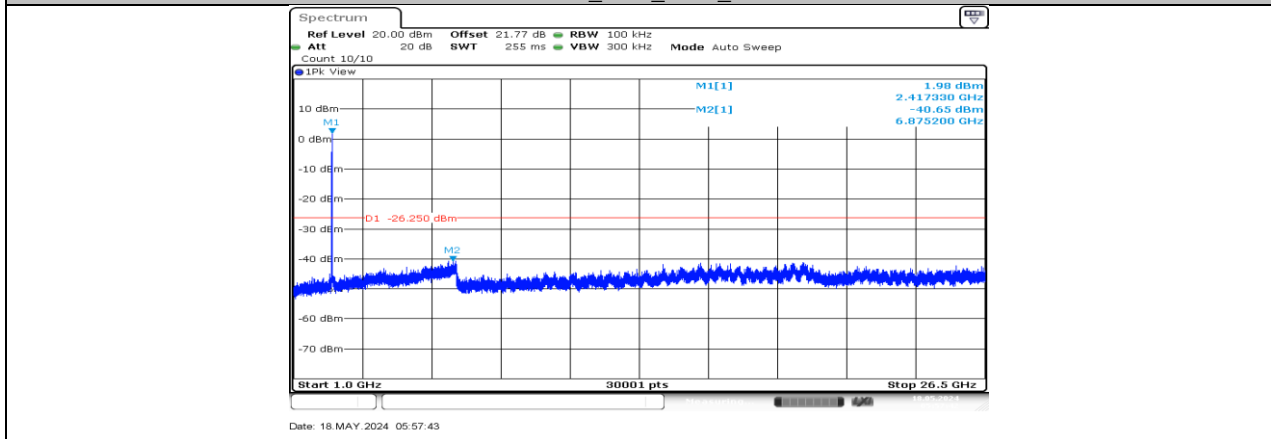
11N20MIMO_Ant1_2412_1000~26500

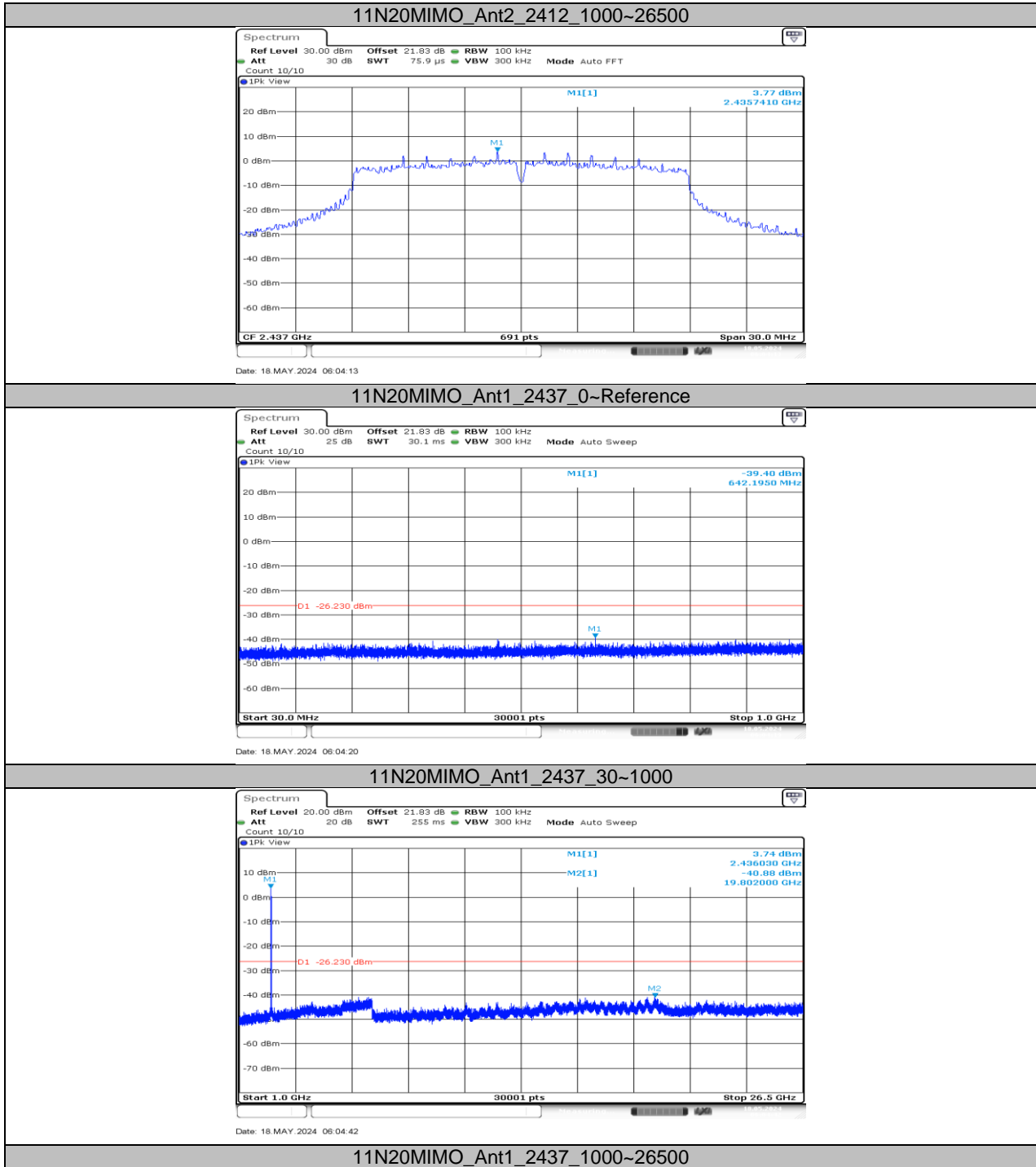


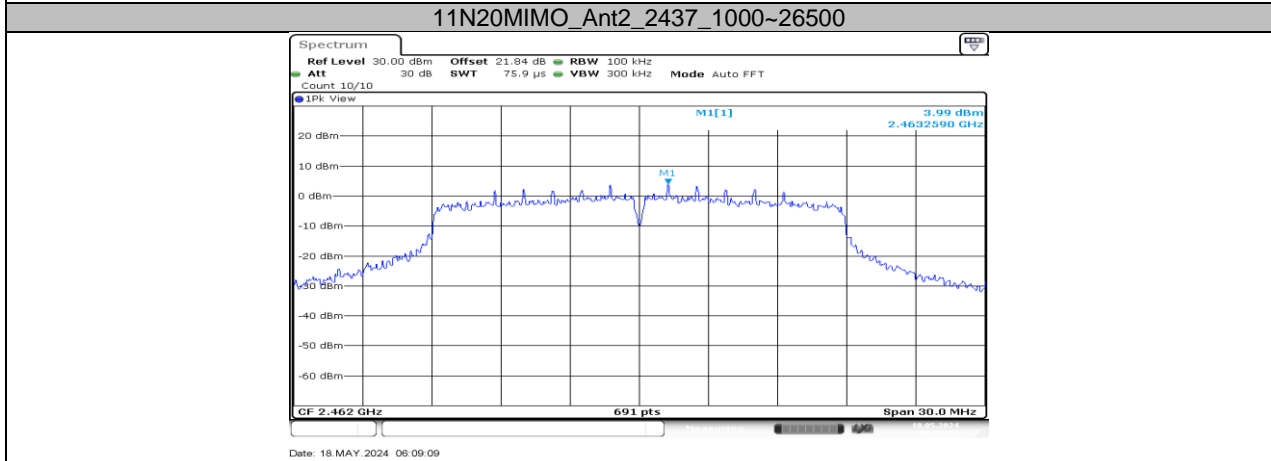
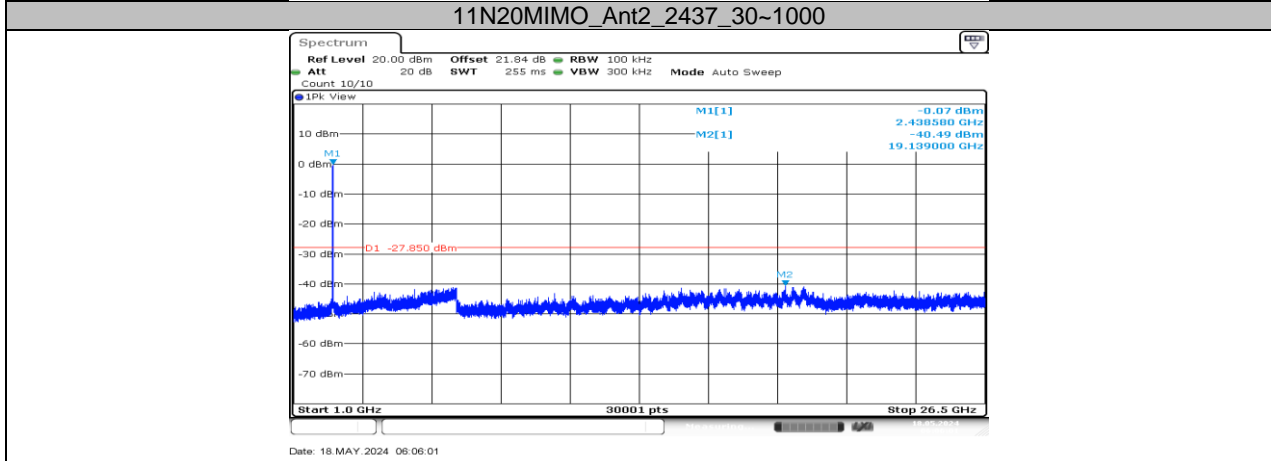
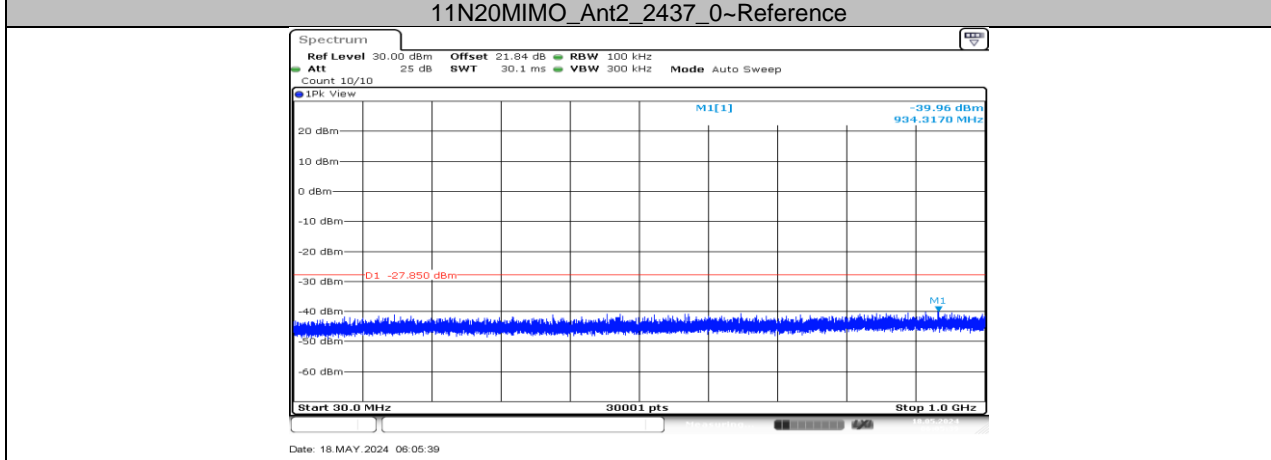
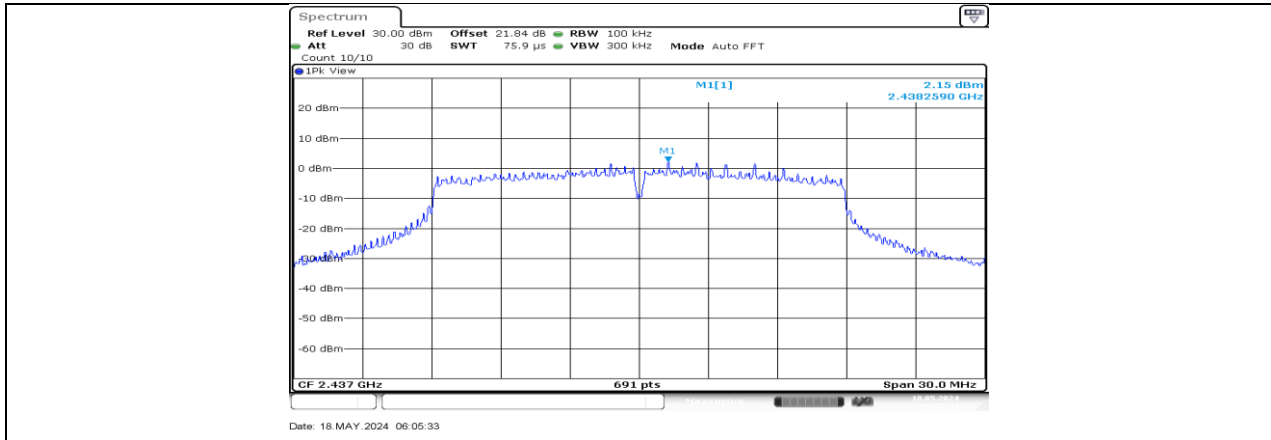
11N20MIMO_Ant2_2412_0~Reference

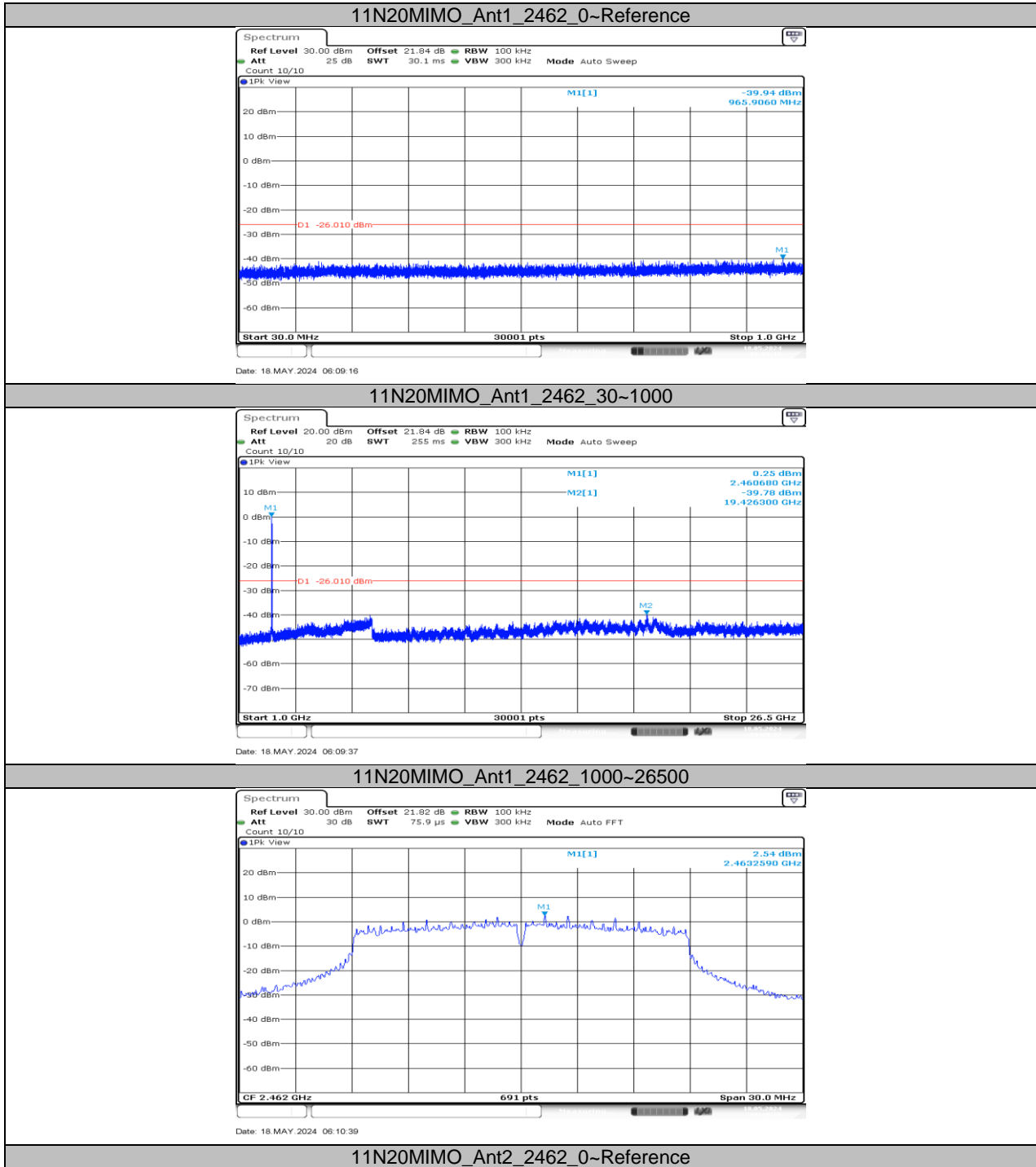


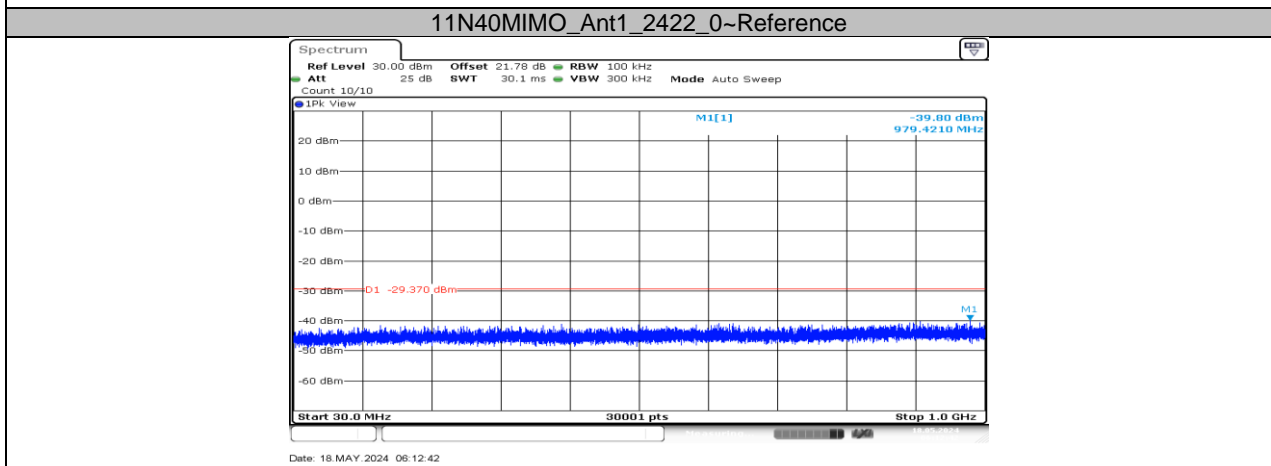
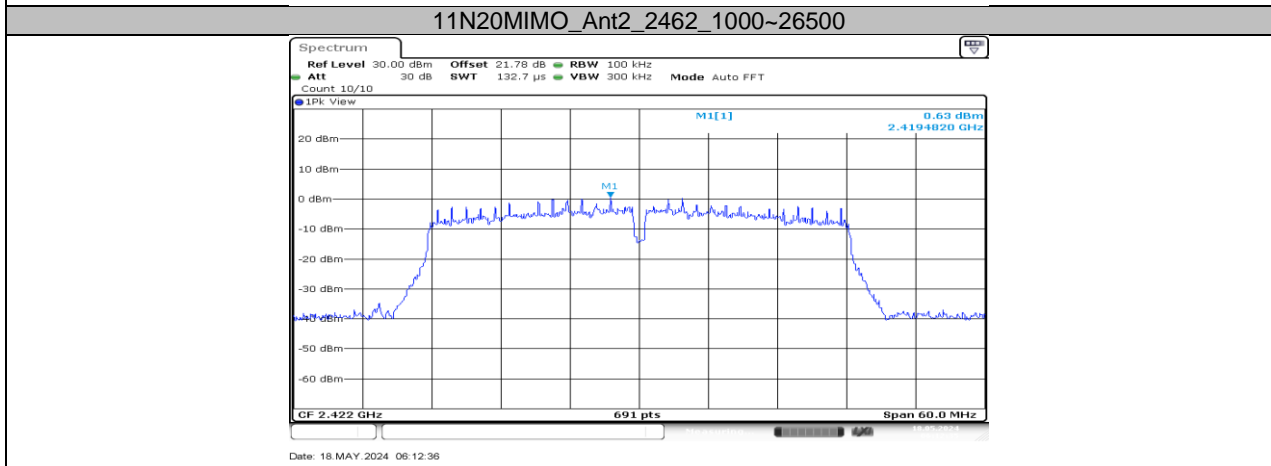
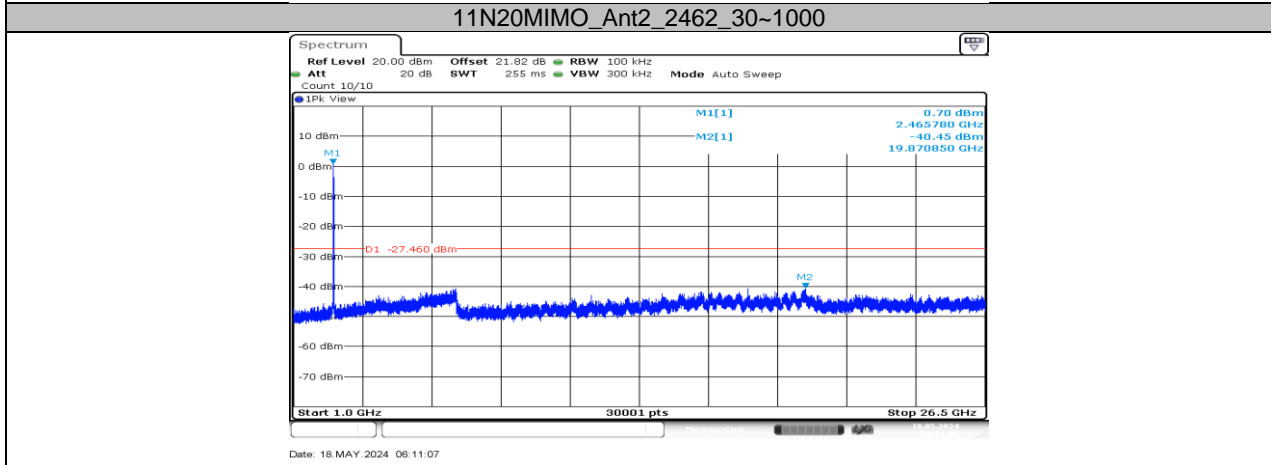
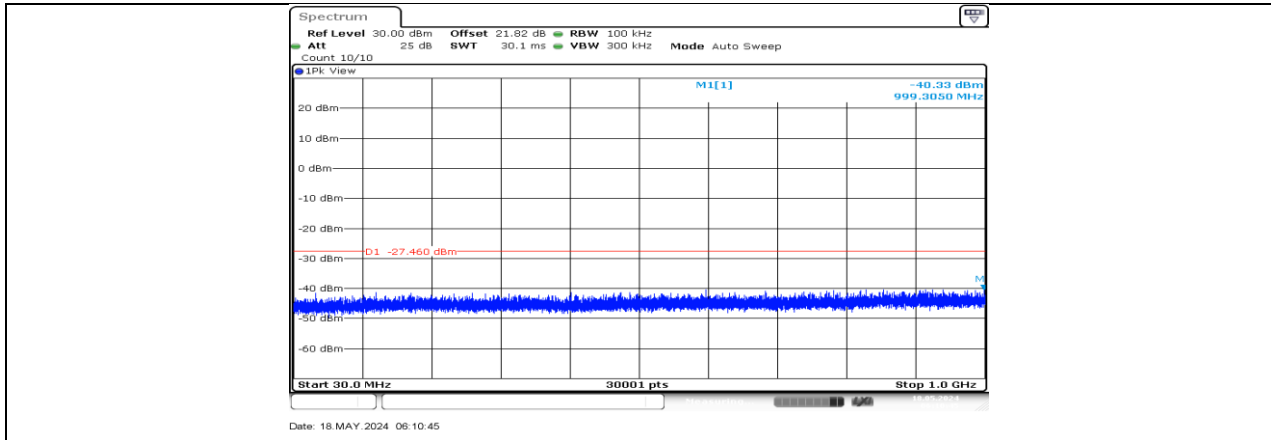
11N20MIMO_Ant2_2412_30~1000

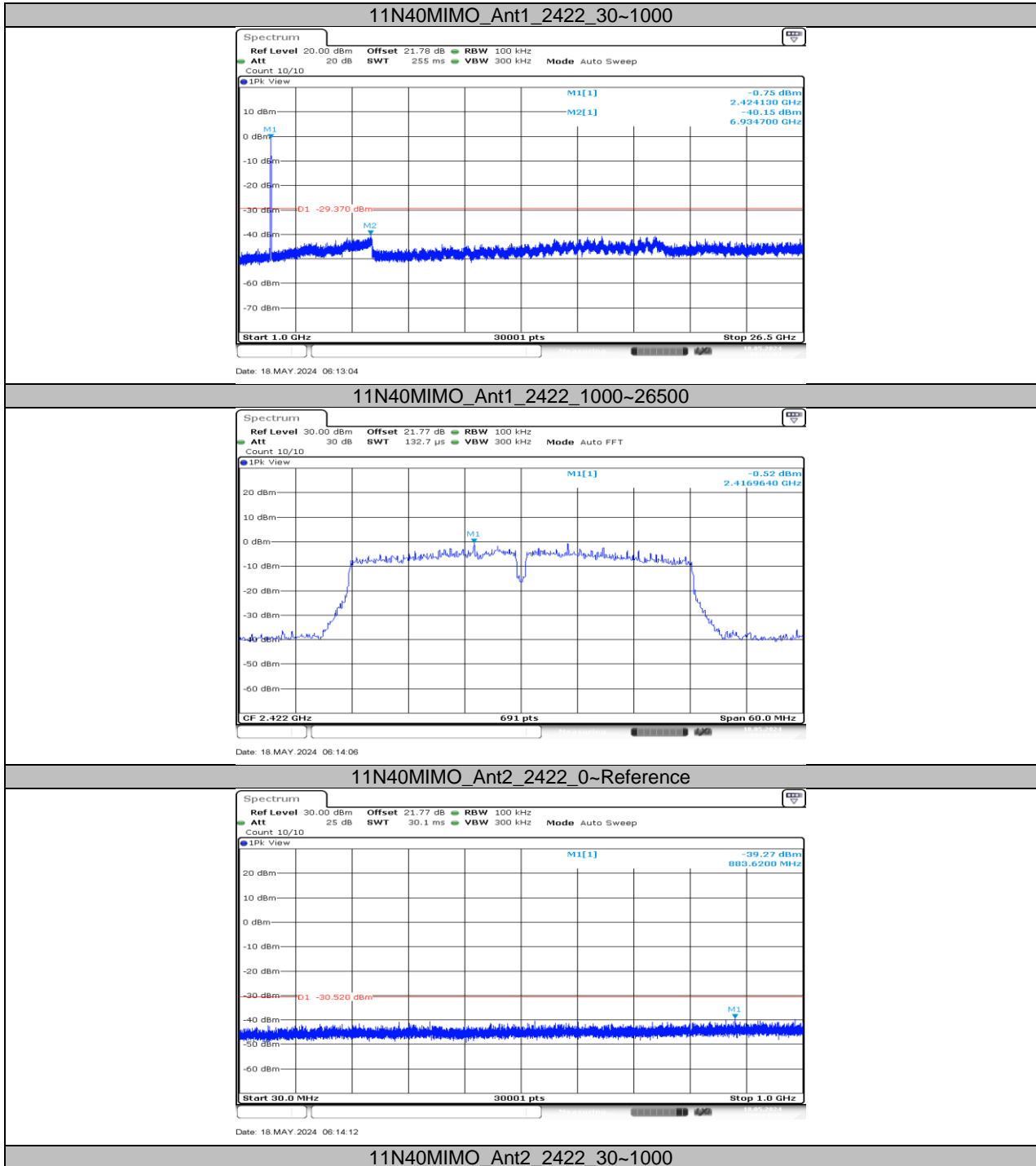


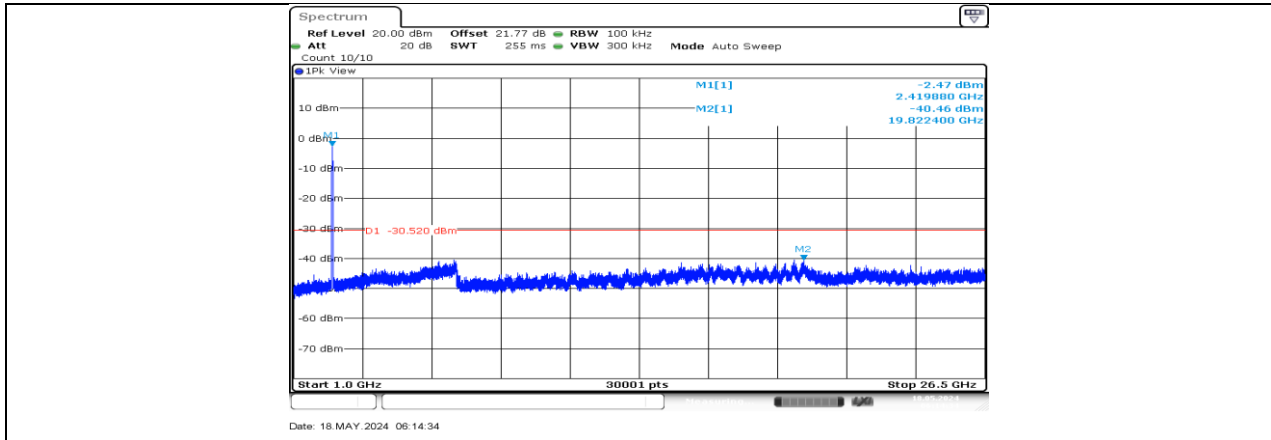




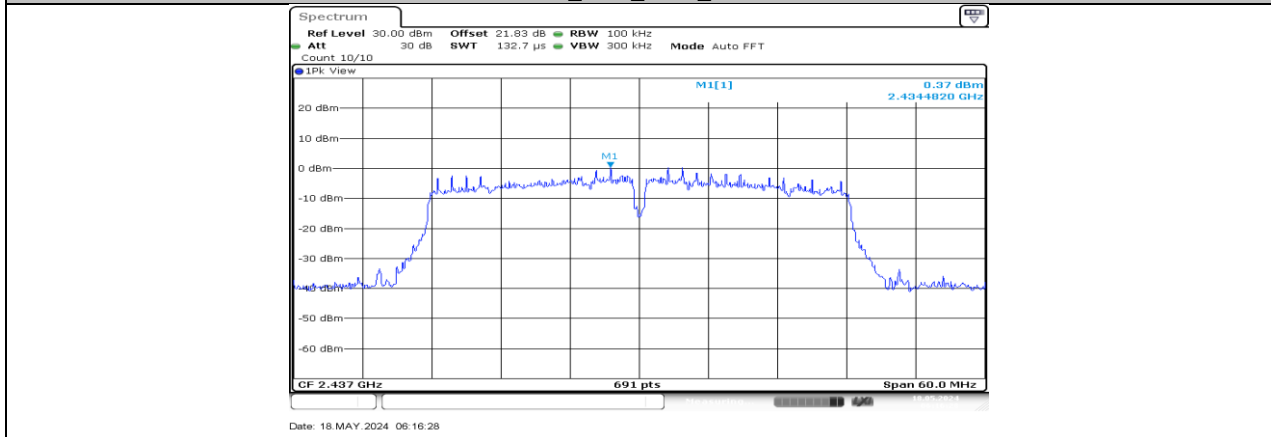




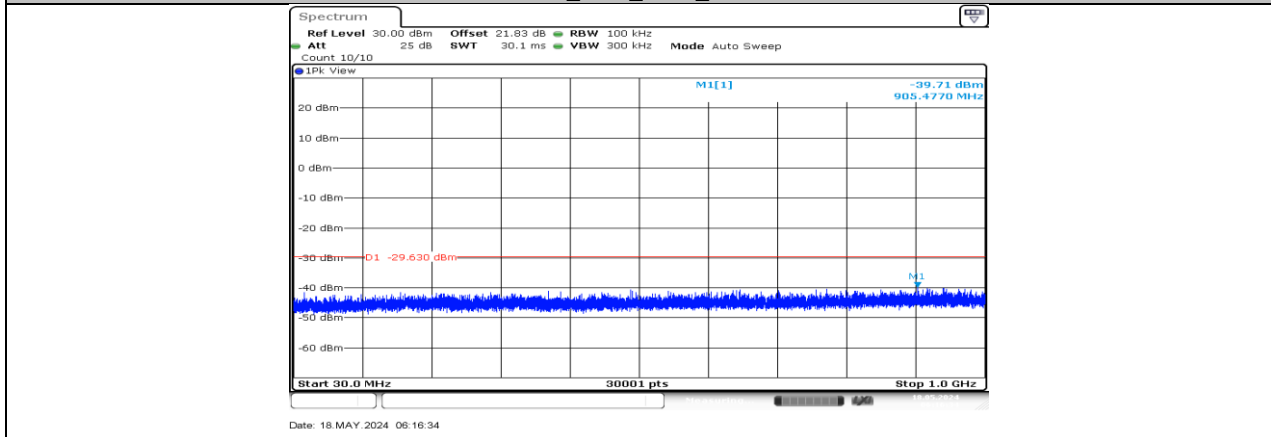




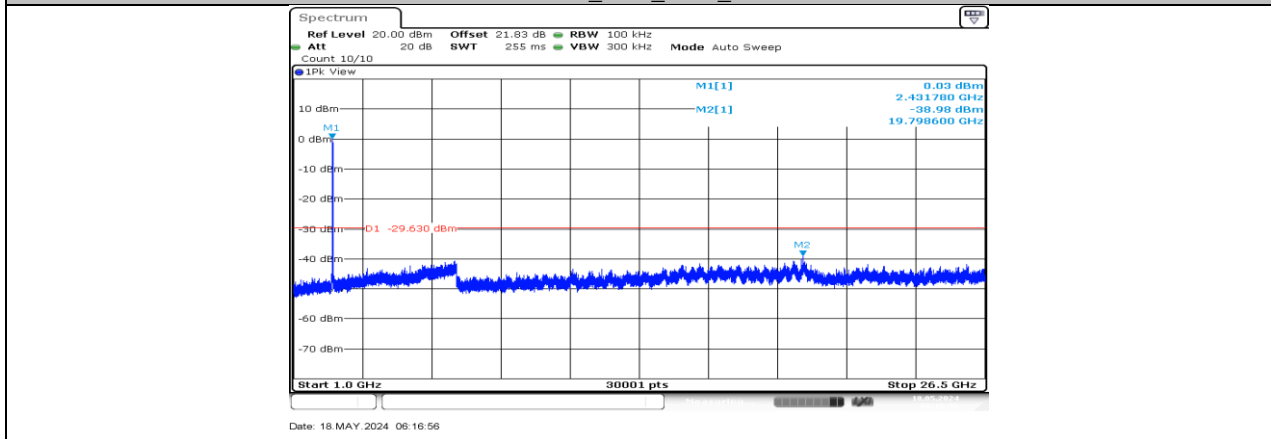
11N40MIMO_Ant2_2422_1000~26500

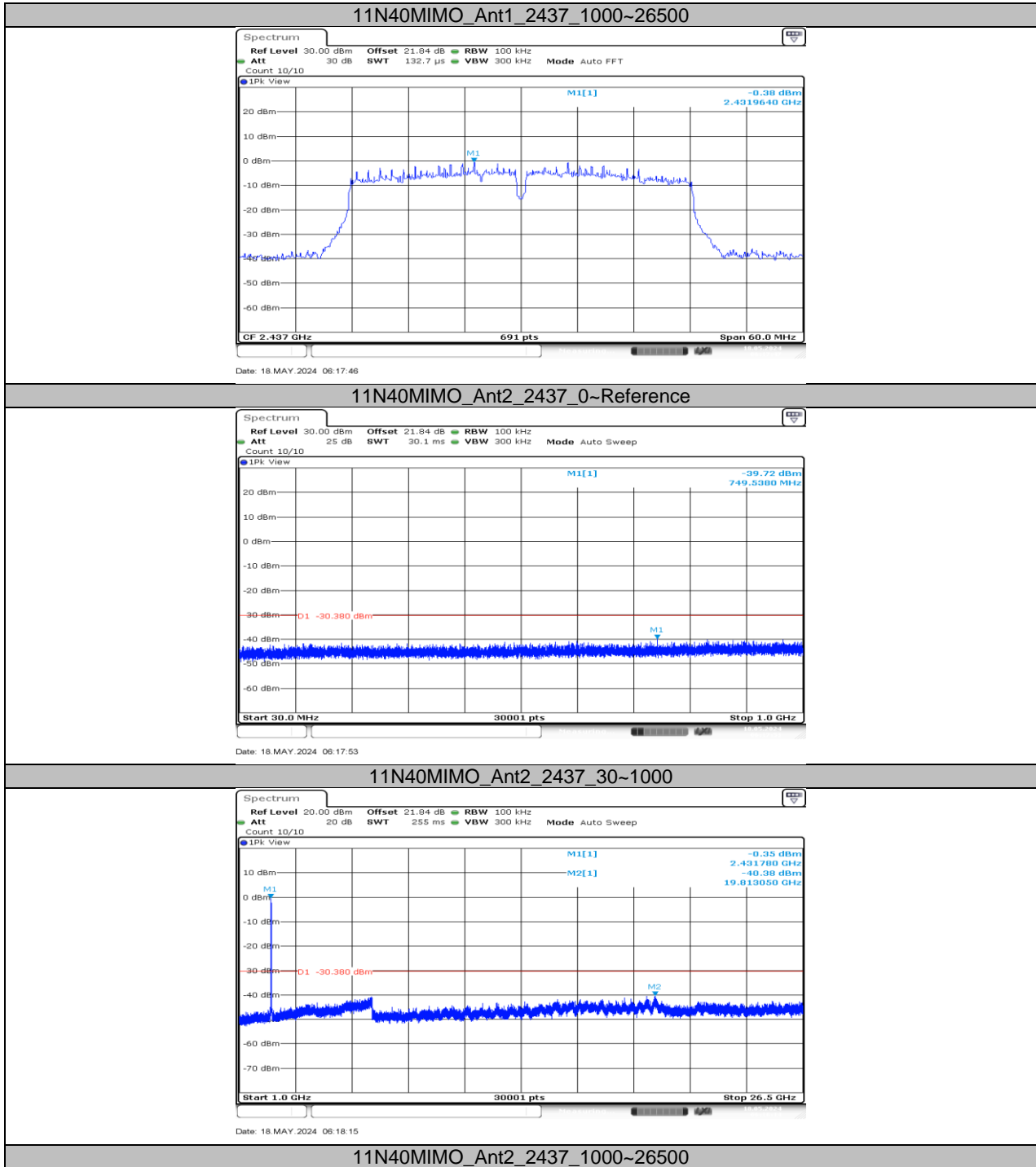


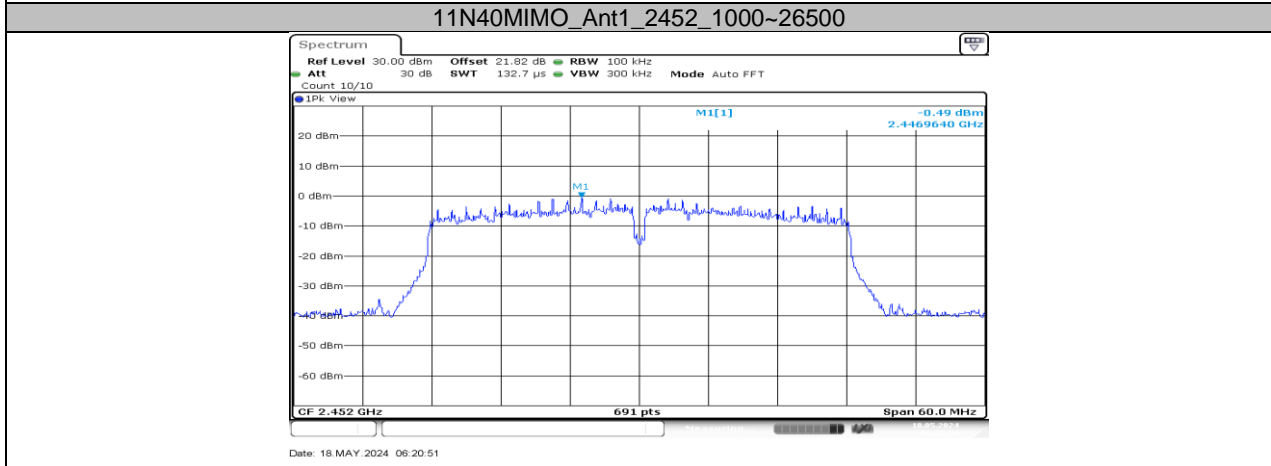
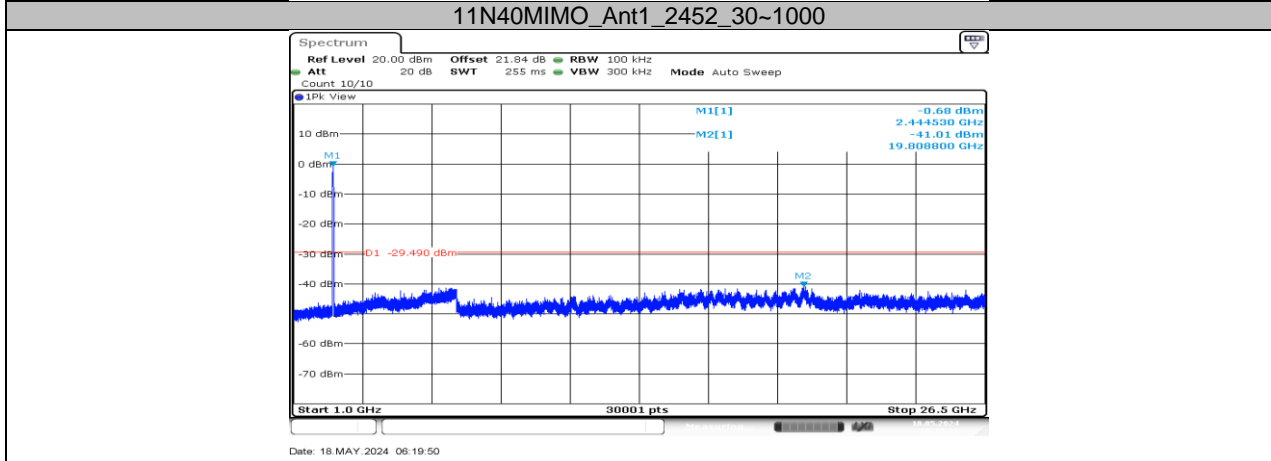
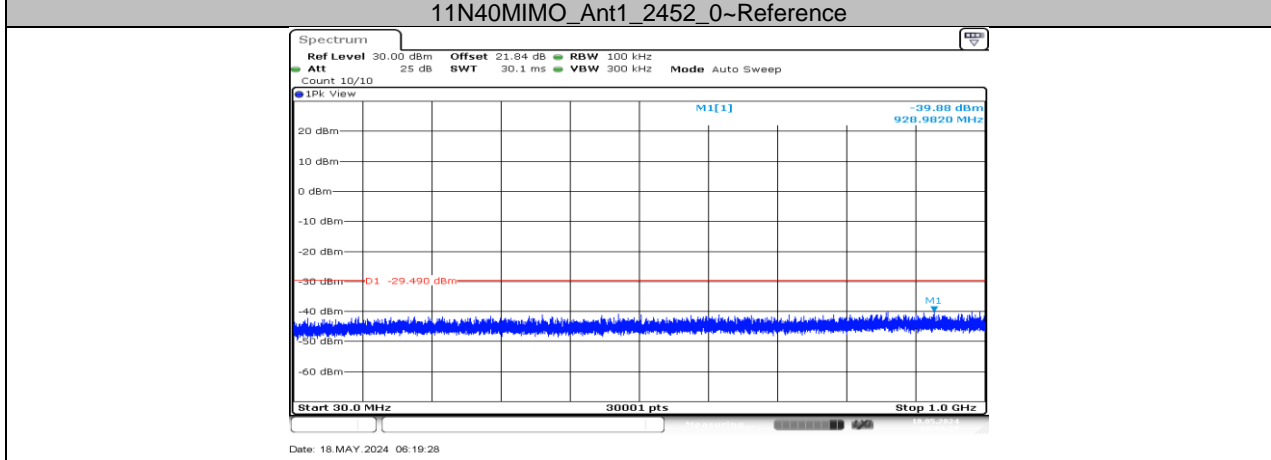
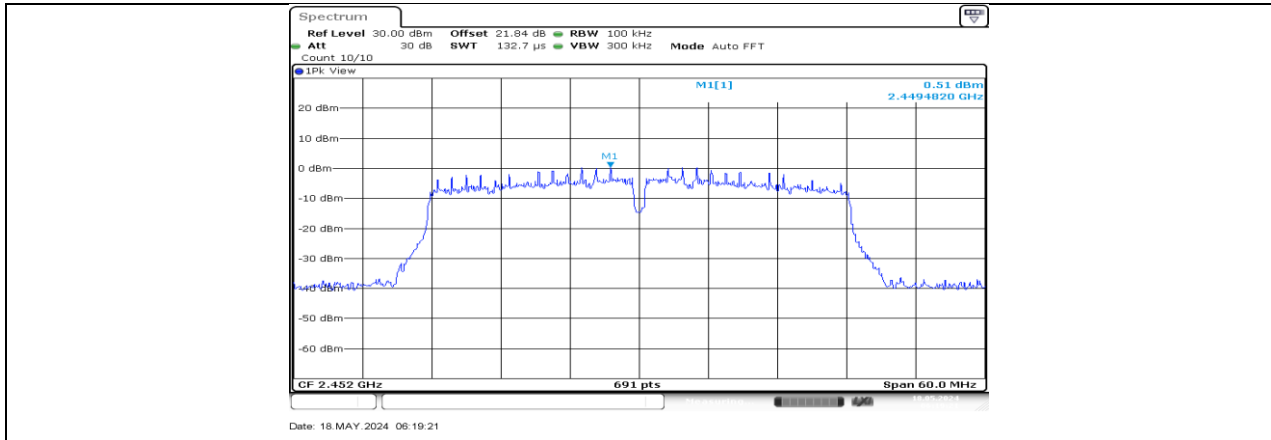
11N40MIMO_Ant1_2437_0~Reference

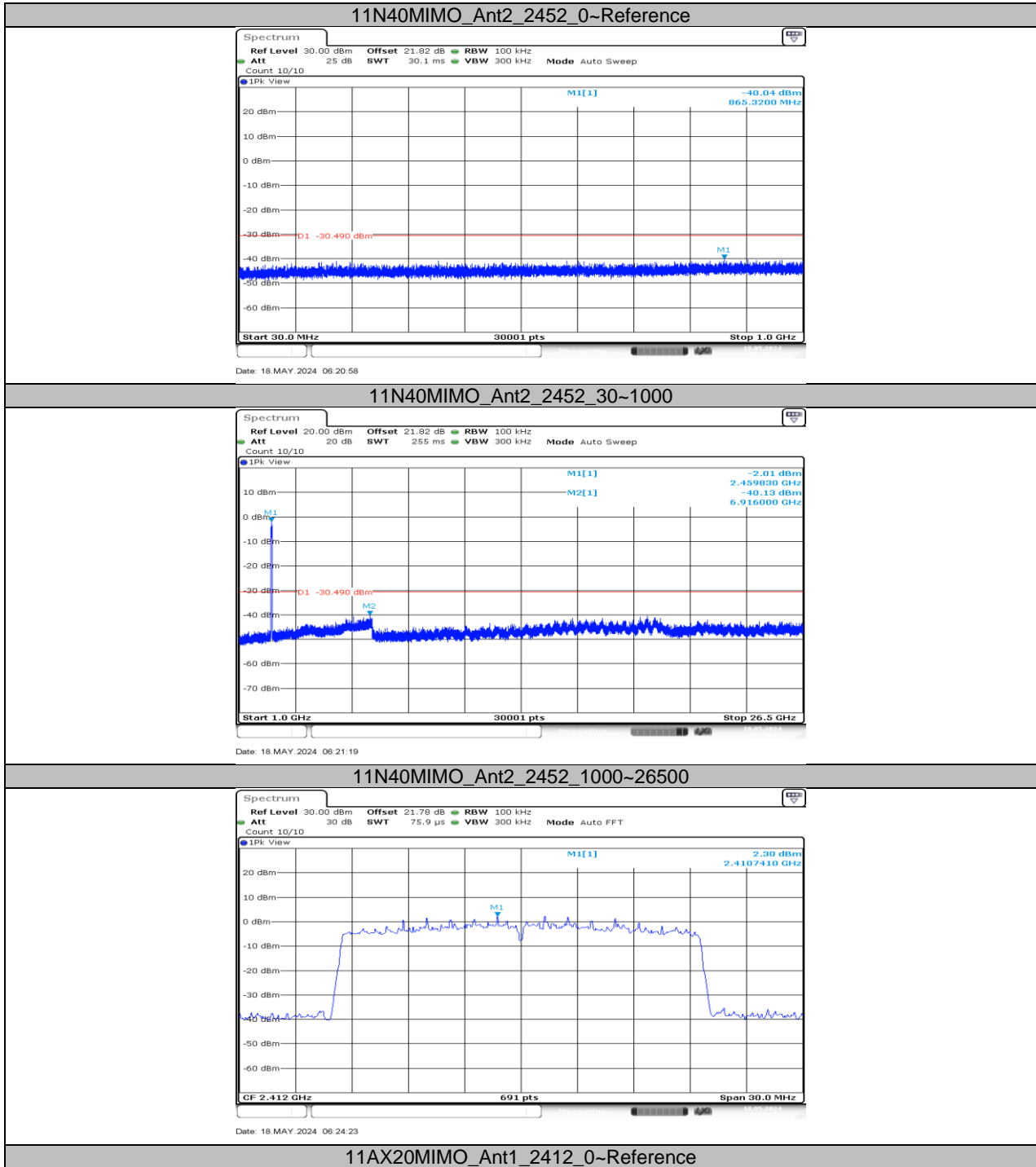


11N40MIMO_Ant1_2437_30~1000

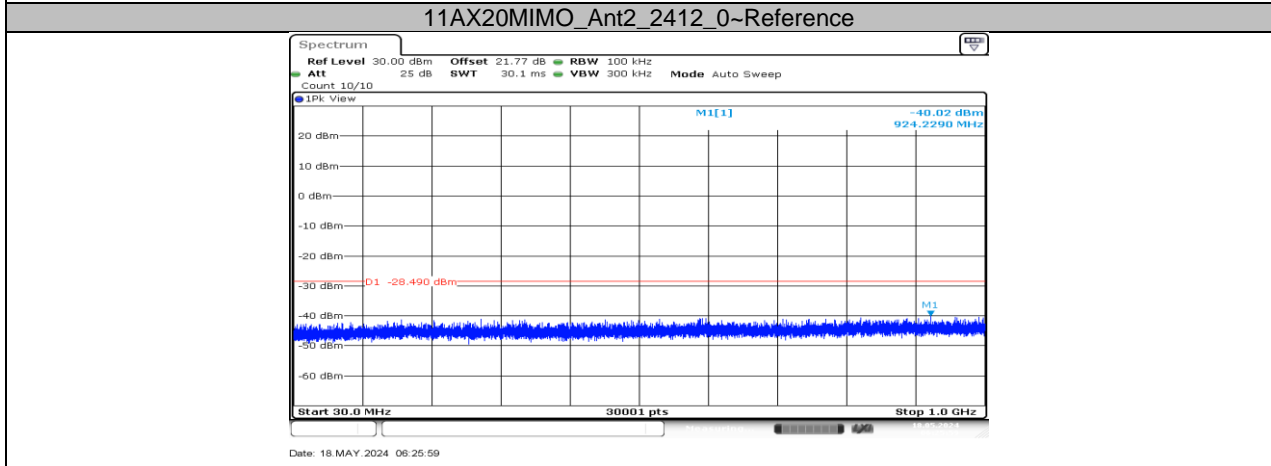
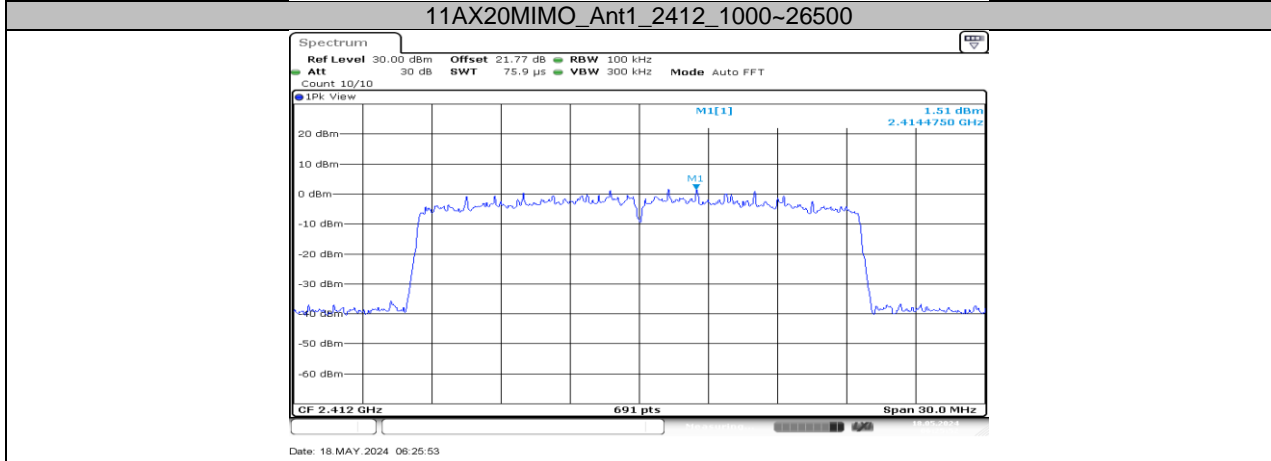
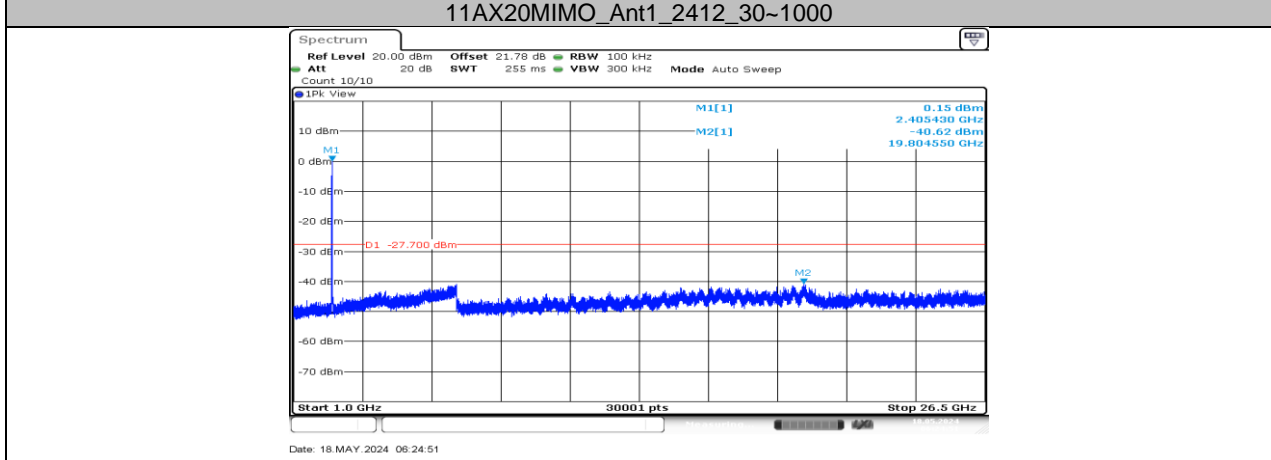
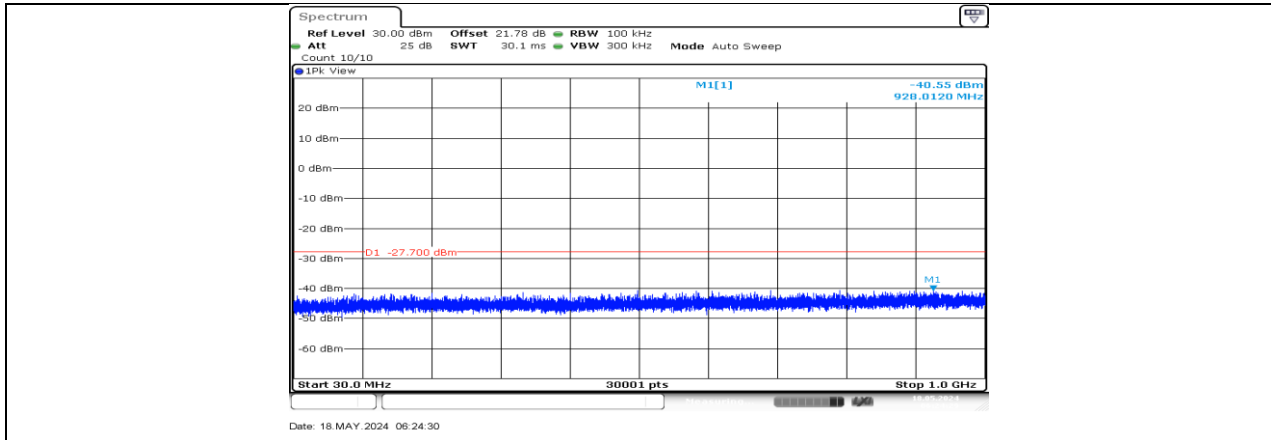


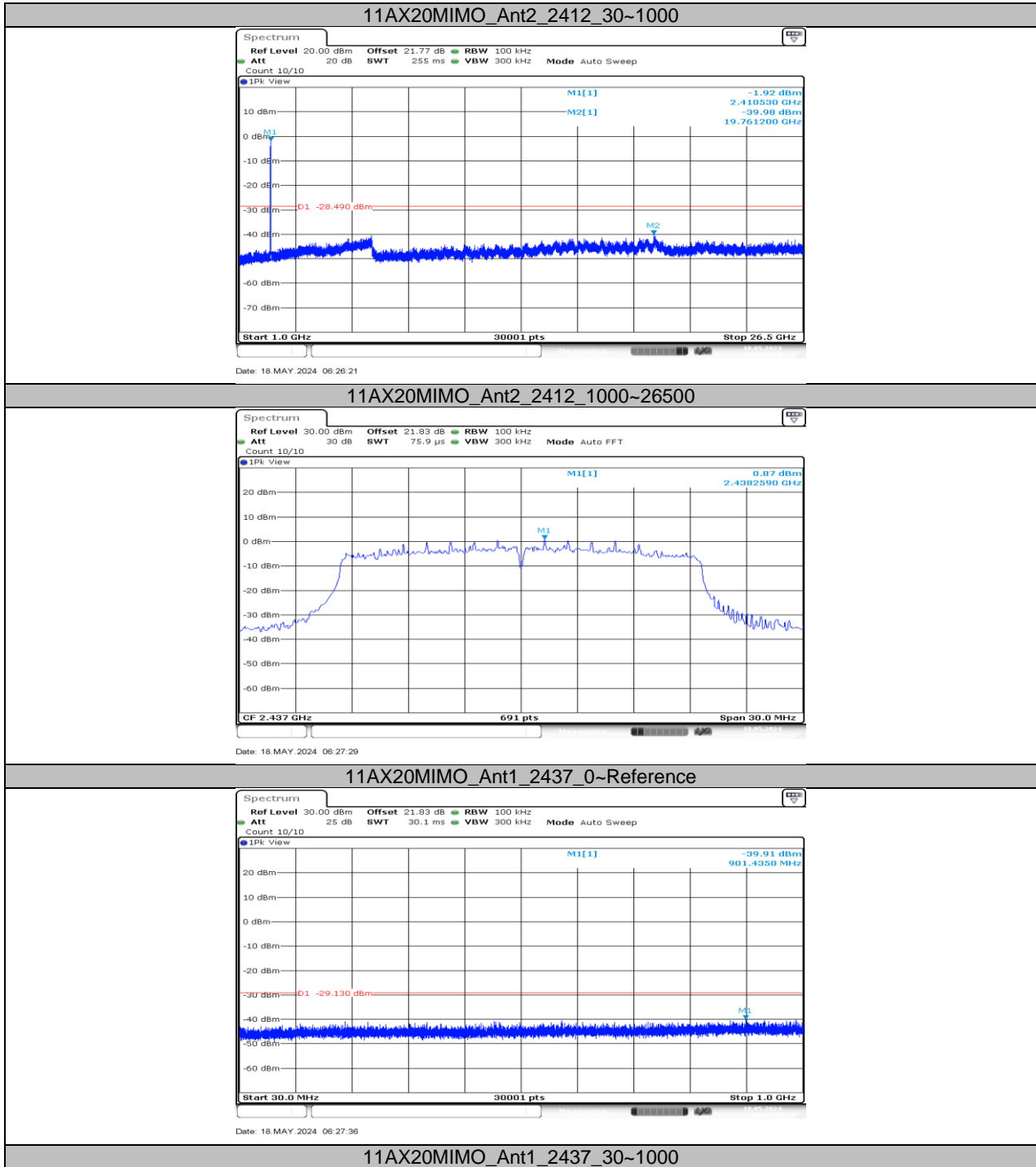


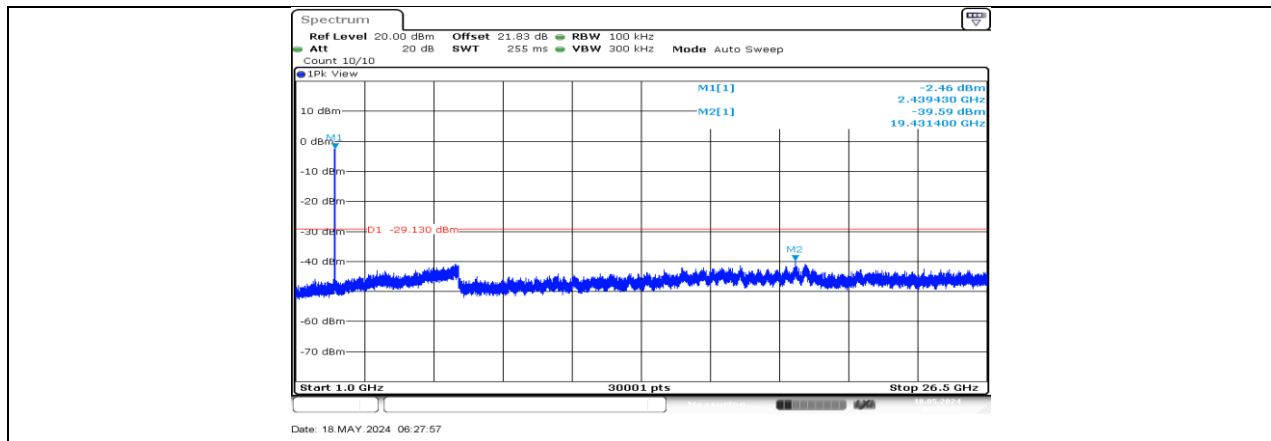




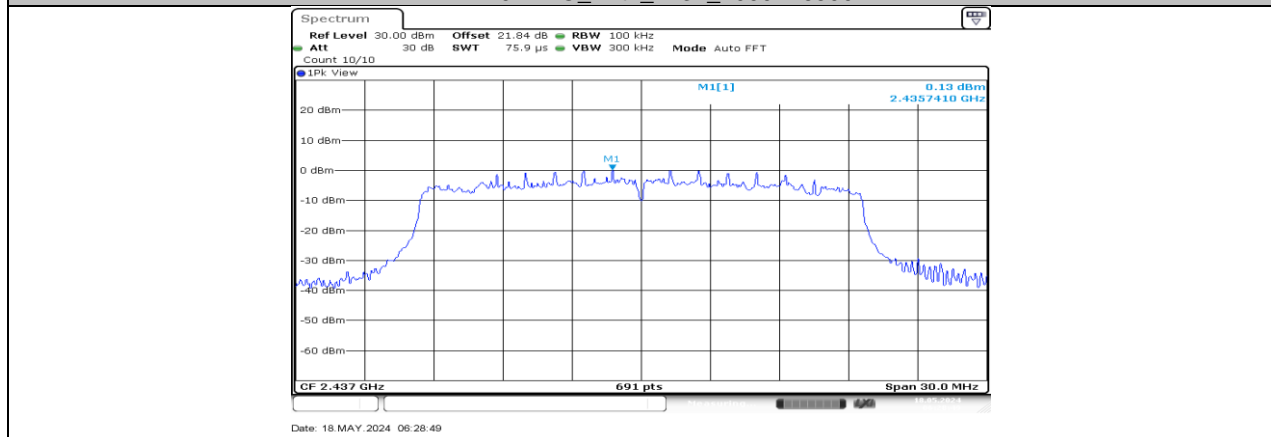
11AX20MIMO_Ant1_2412_0~Reference



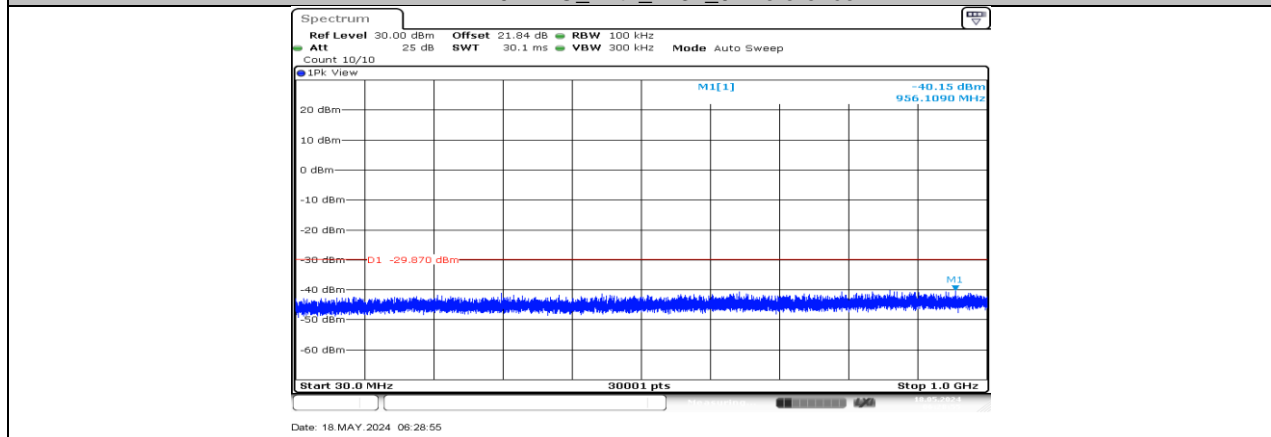




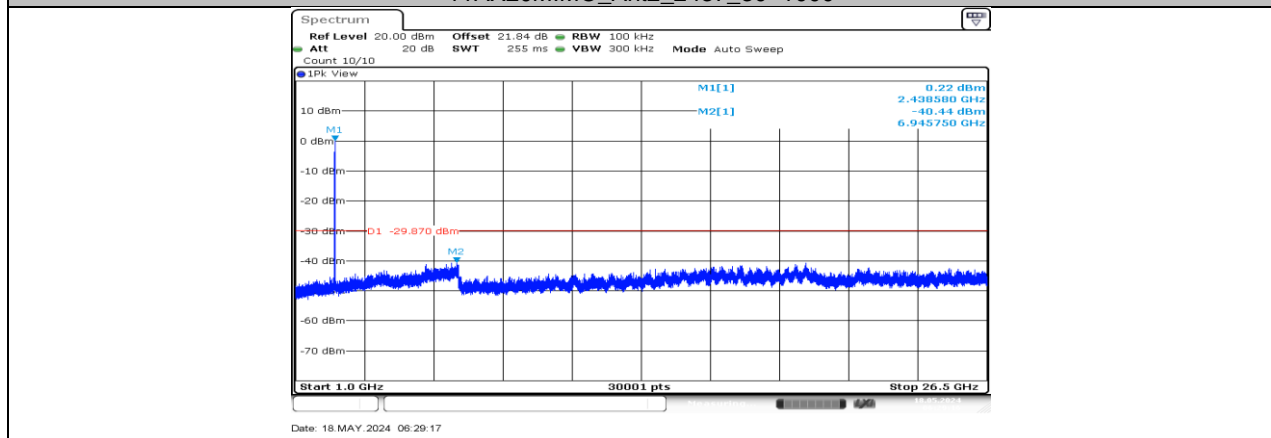
11AX20MIMO_Ant1_2437_1000~26500

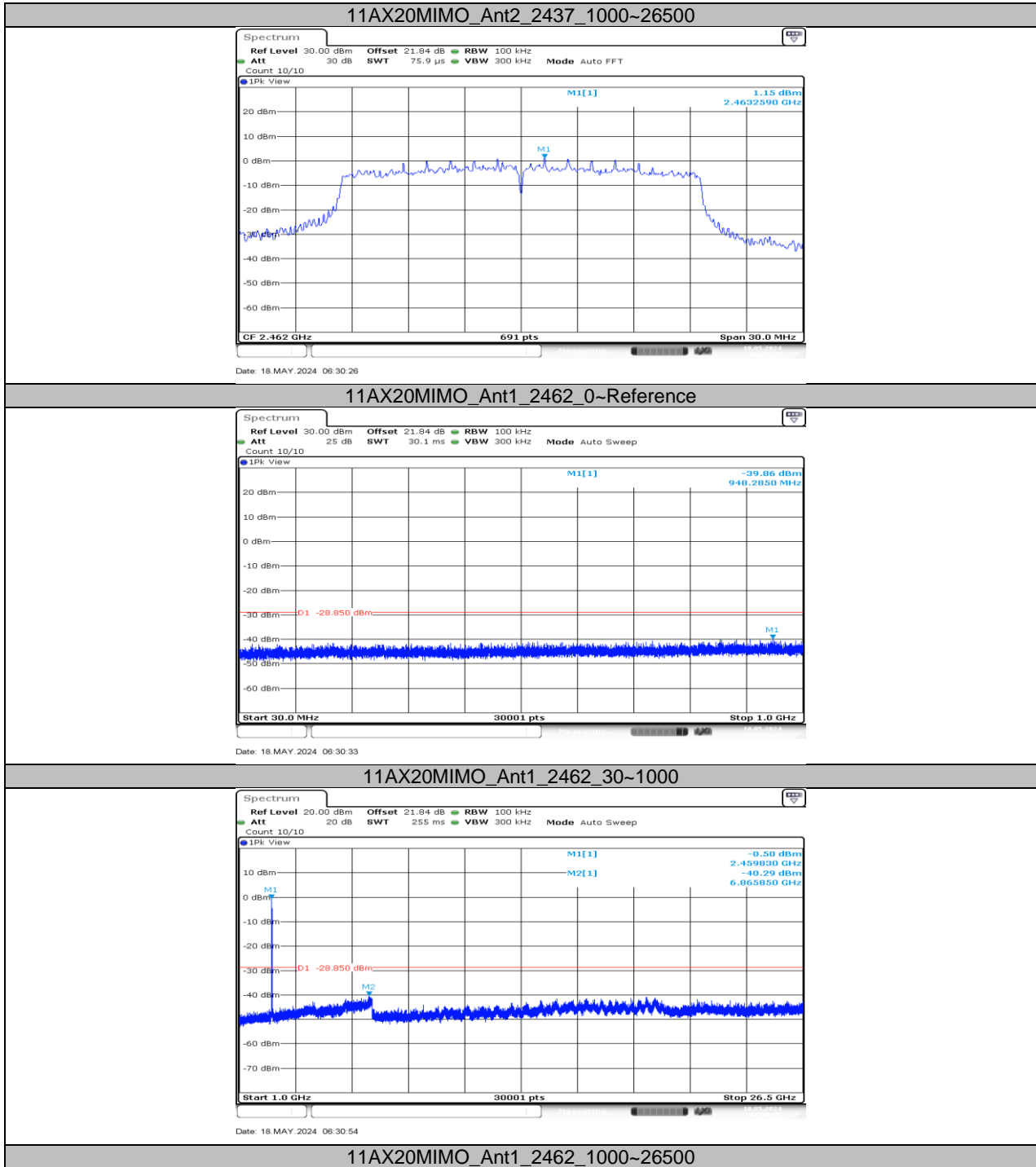


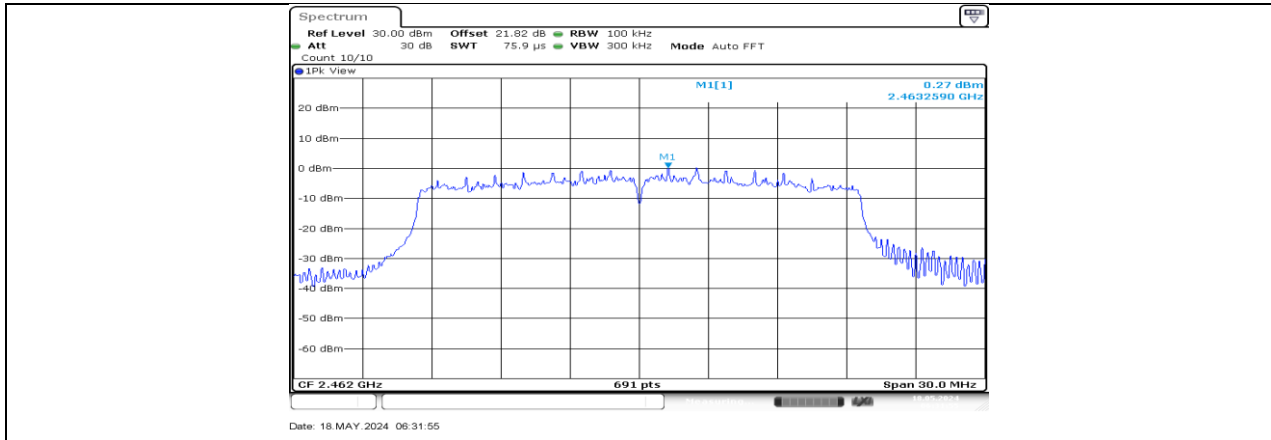
11AX20MIMO_Ant2_2437_0~Reference



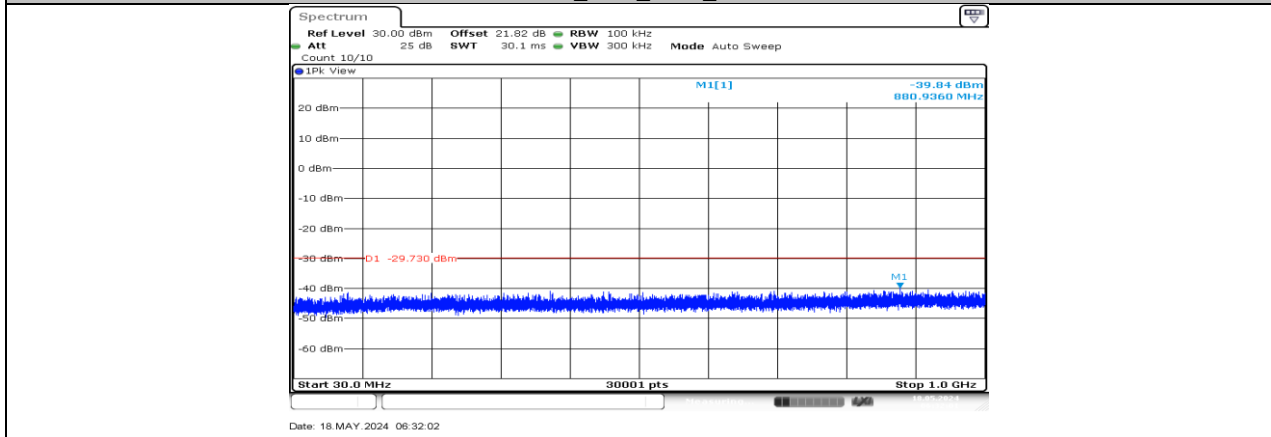
11AX20MIMO_Ant2_2437_30~1000



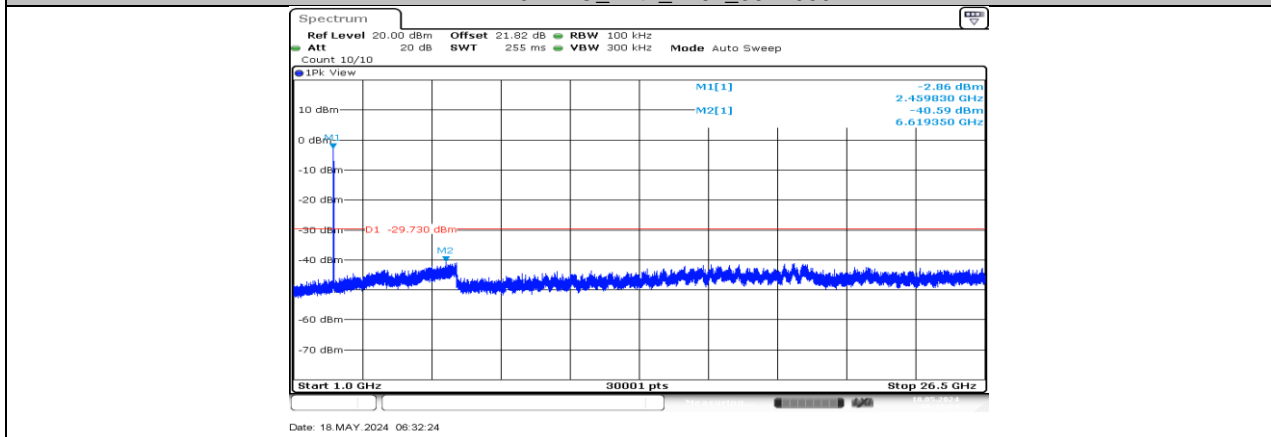




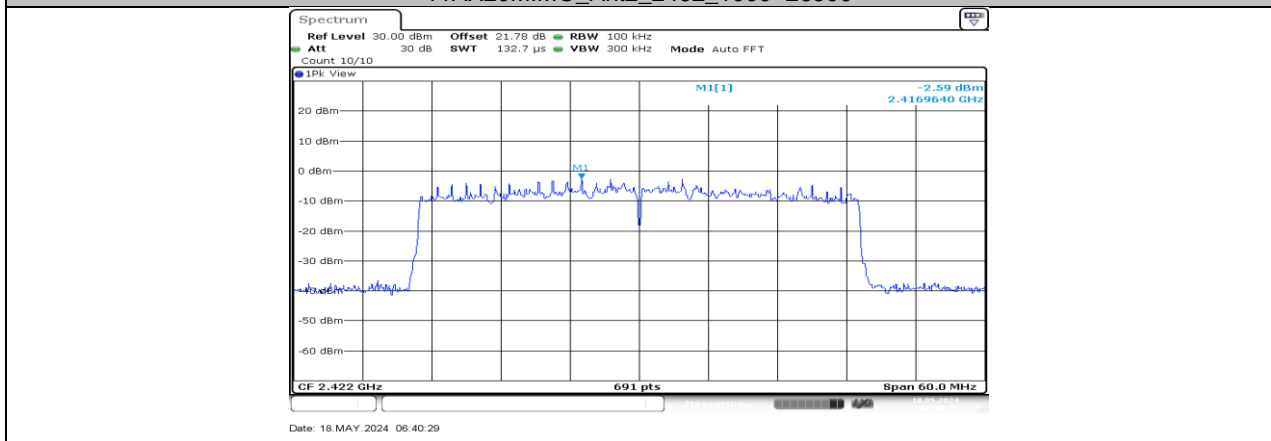
11AX20MIMO_Ant2_2462_0-Reference

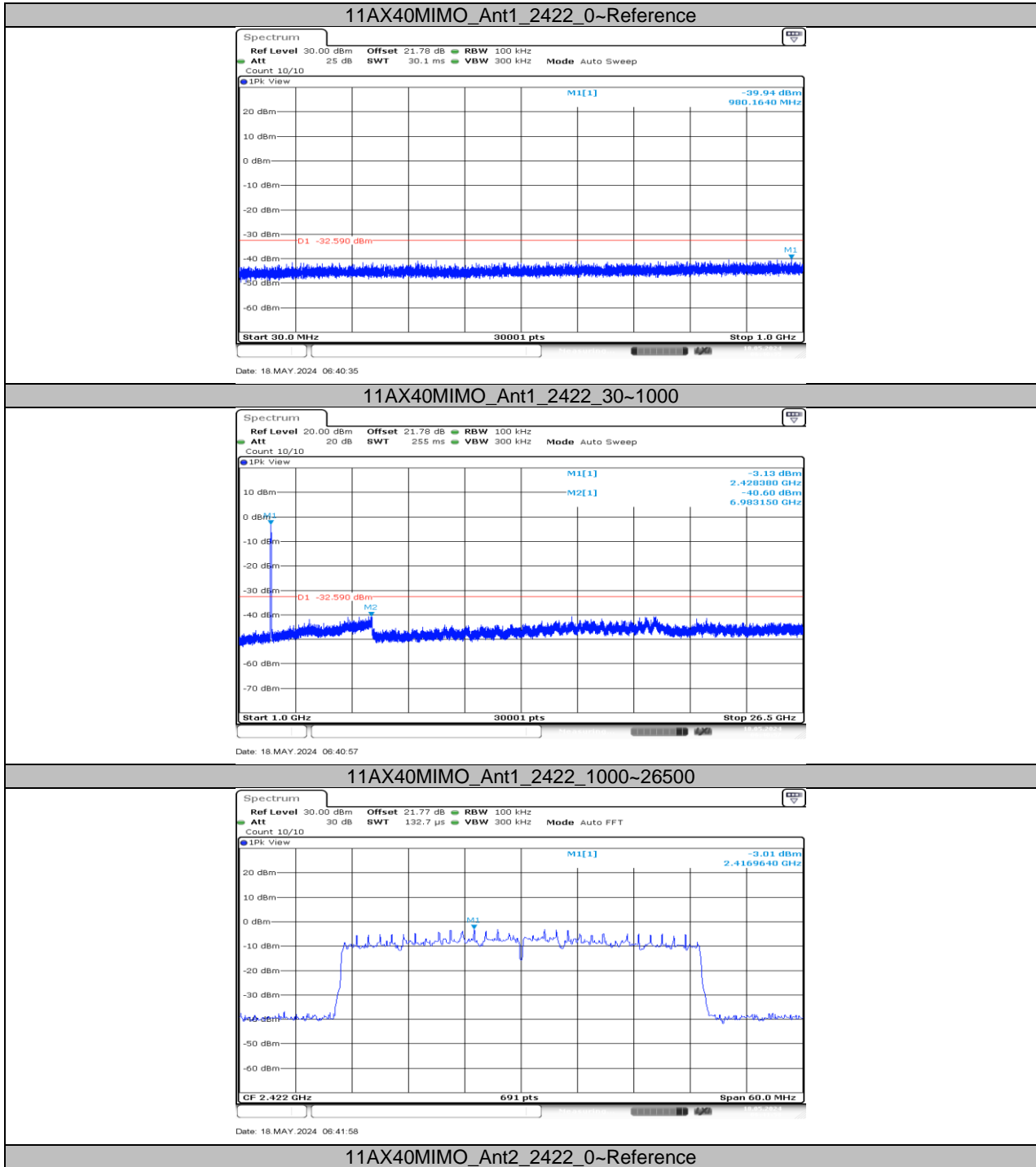


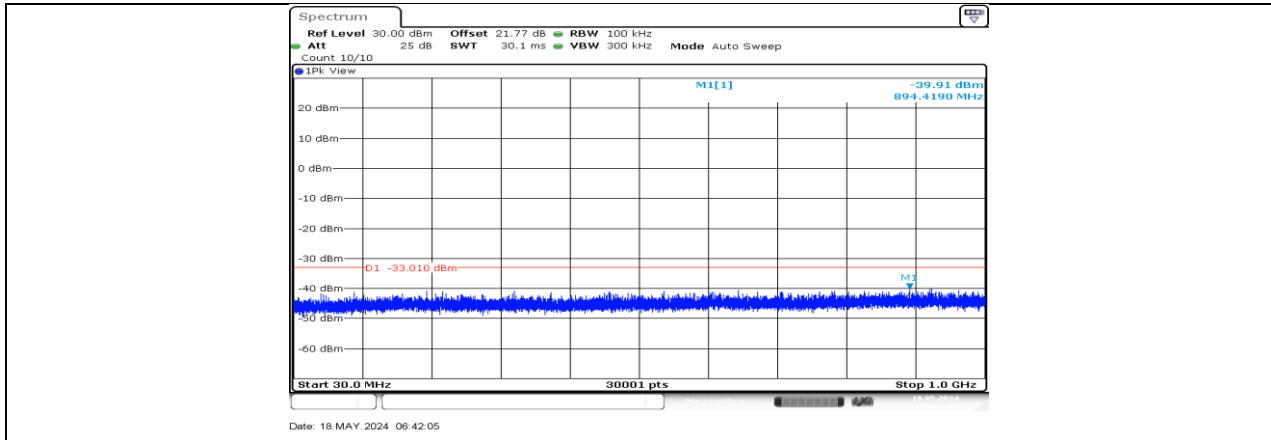
11AX20MIMO_Ant2_2462_30-1000



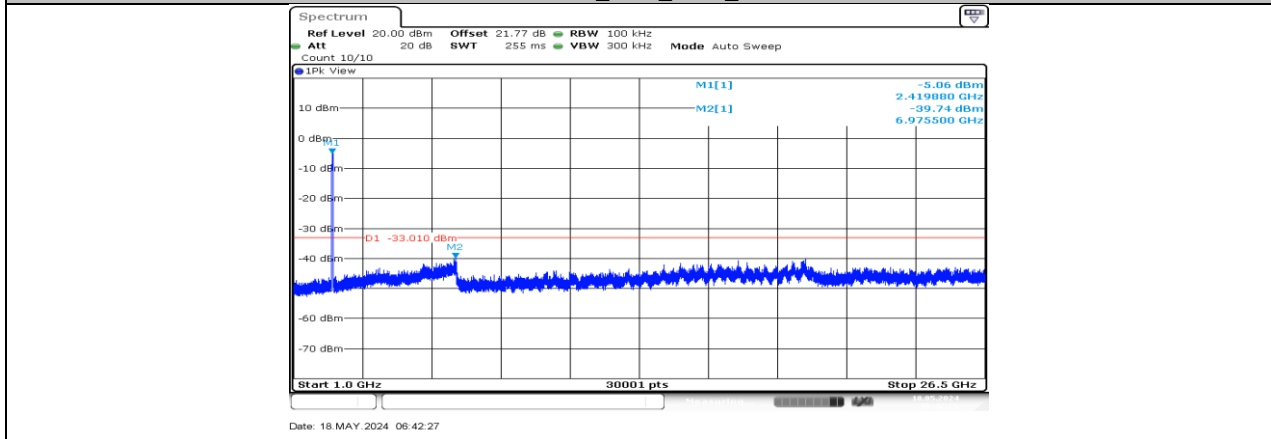
11AX20MIMO_Ant2_2462_1000-26500



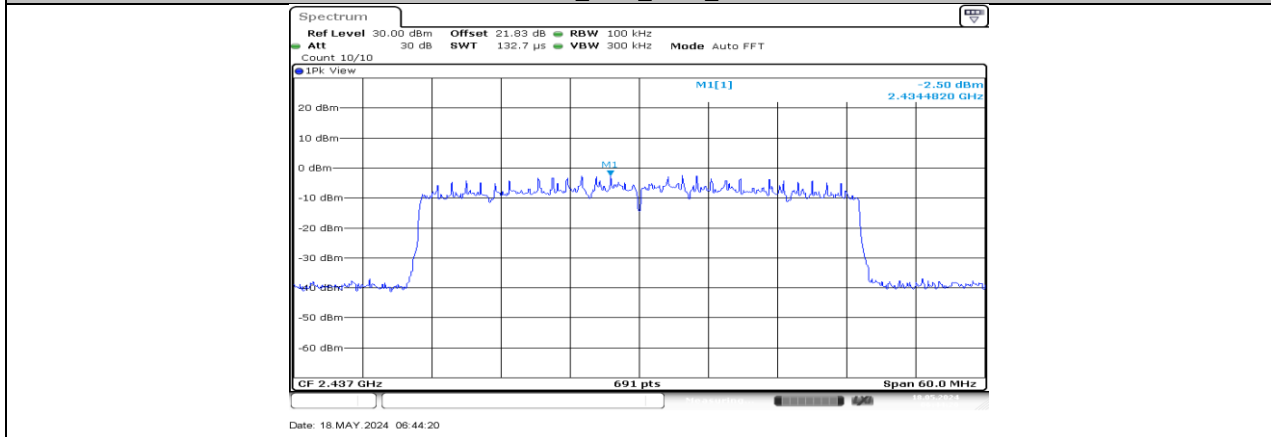




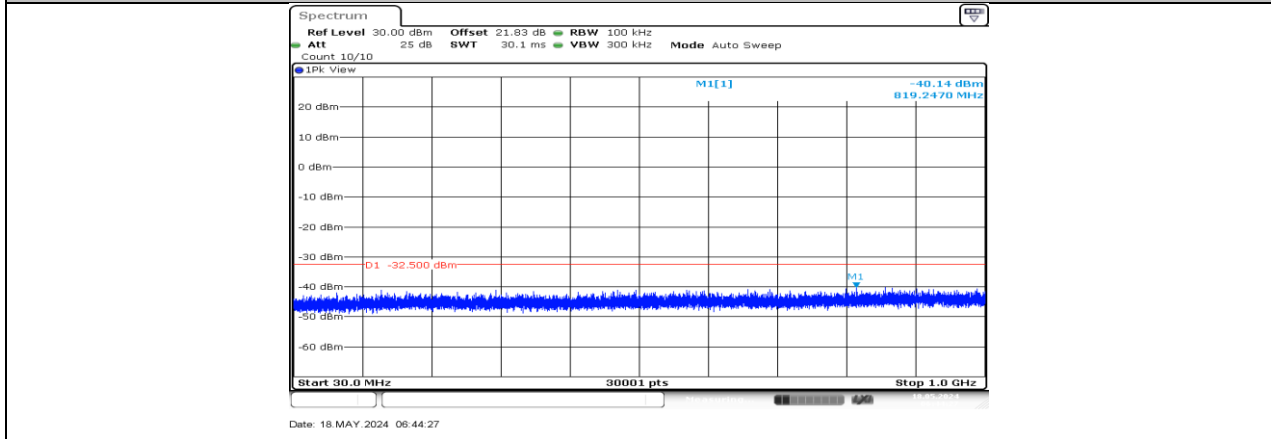
11AX40MIMO_Ant2_2422_30~1000

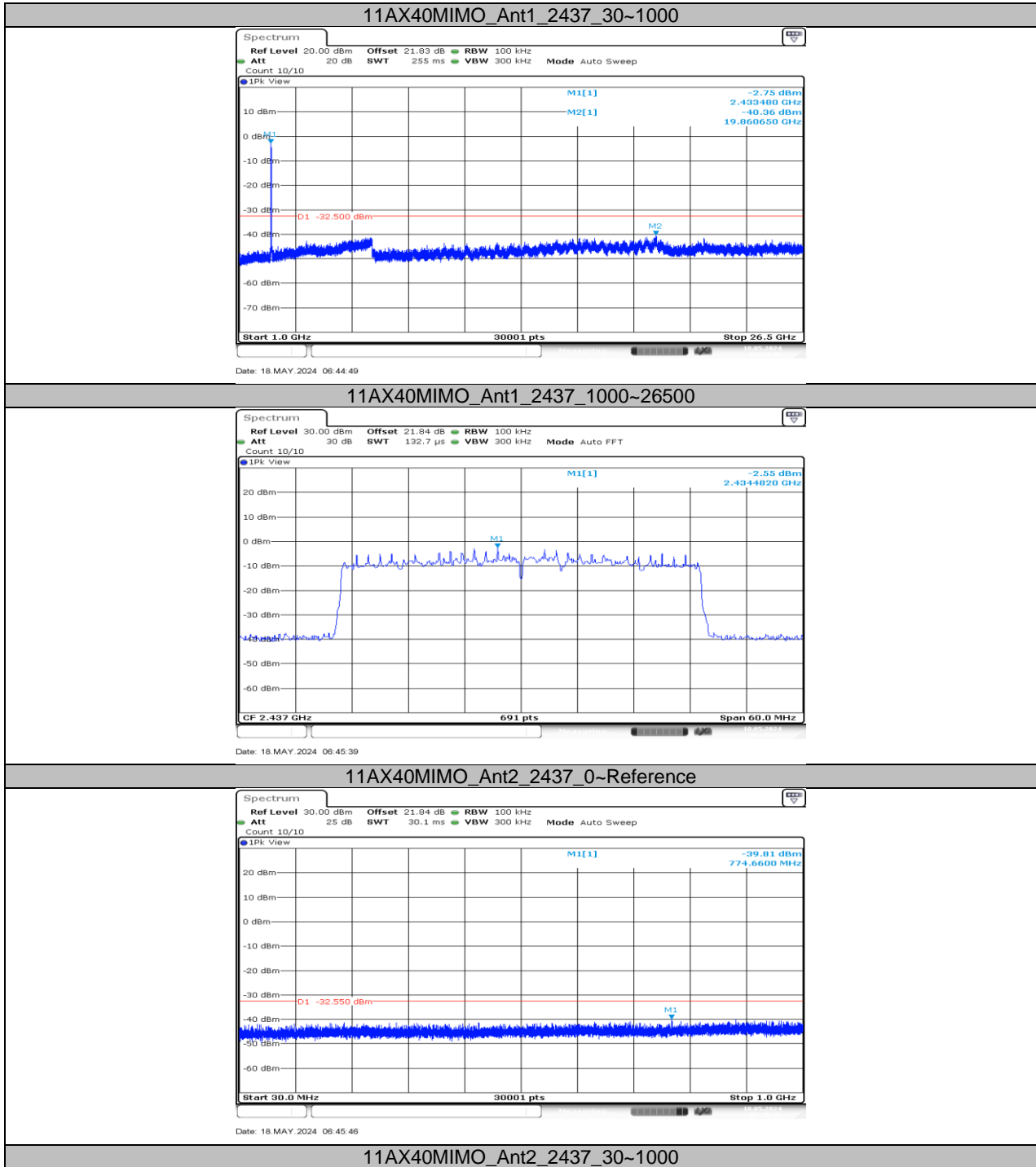


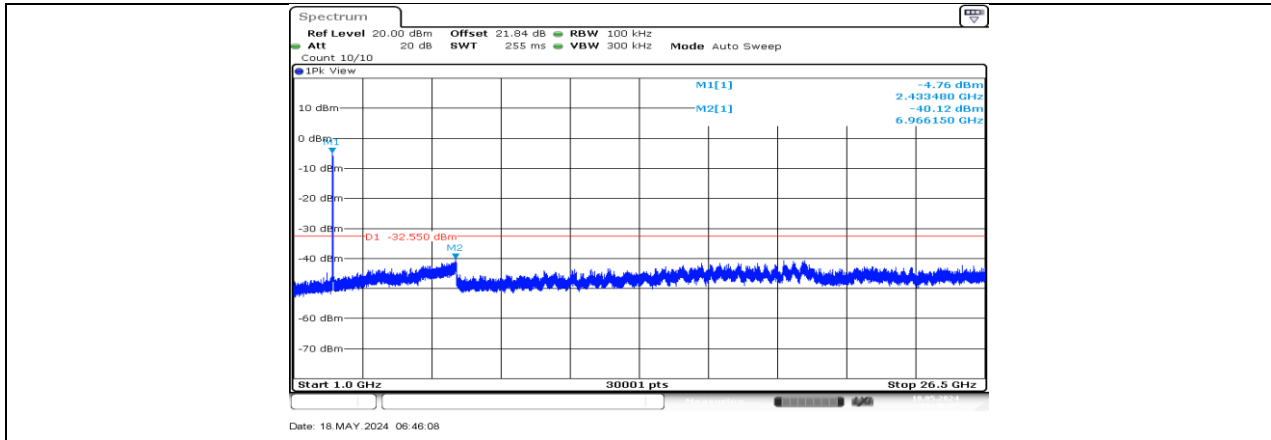
11AX40MIMO_Ant2_2422_1000~26500



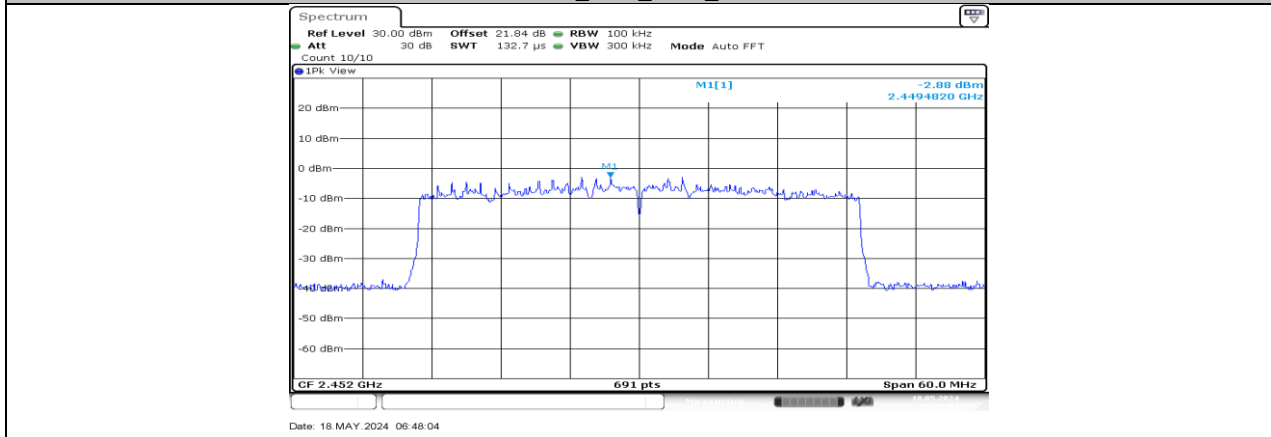
11AX40MIMO_Ant1_2437_0~Reference



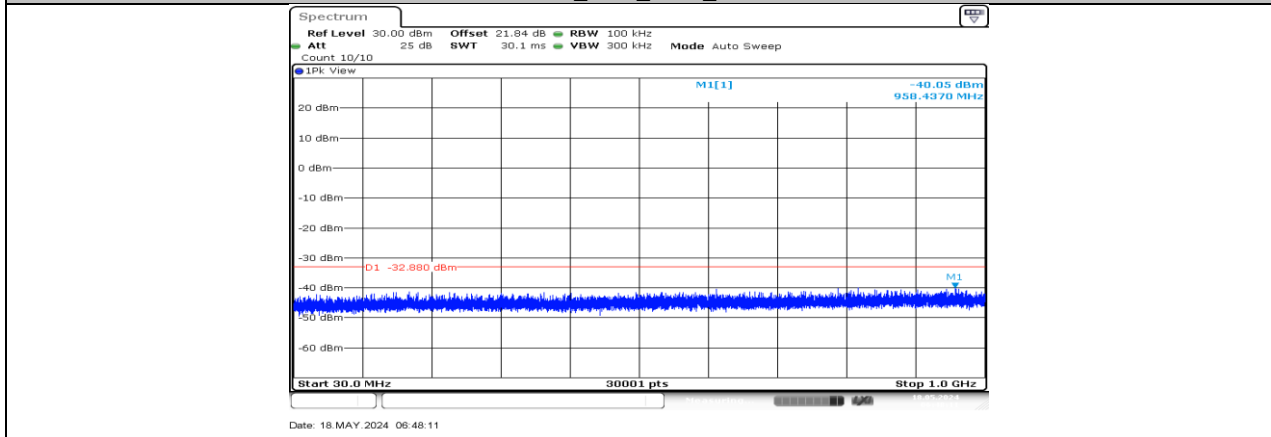




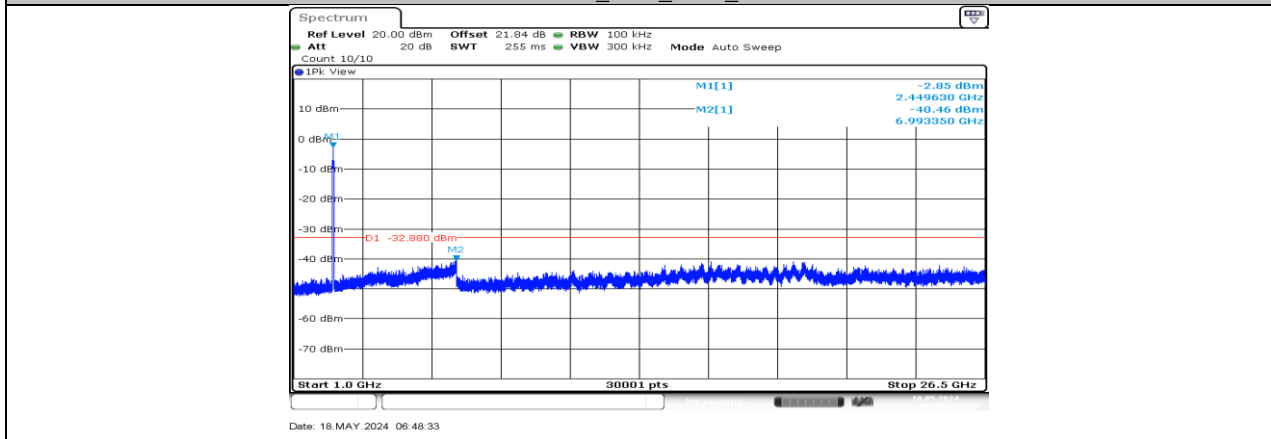
11AX40MIMO_Ant2_2437_1000~26500

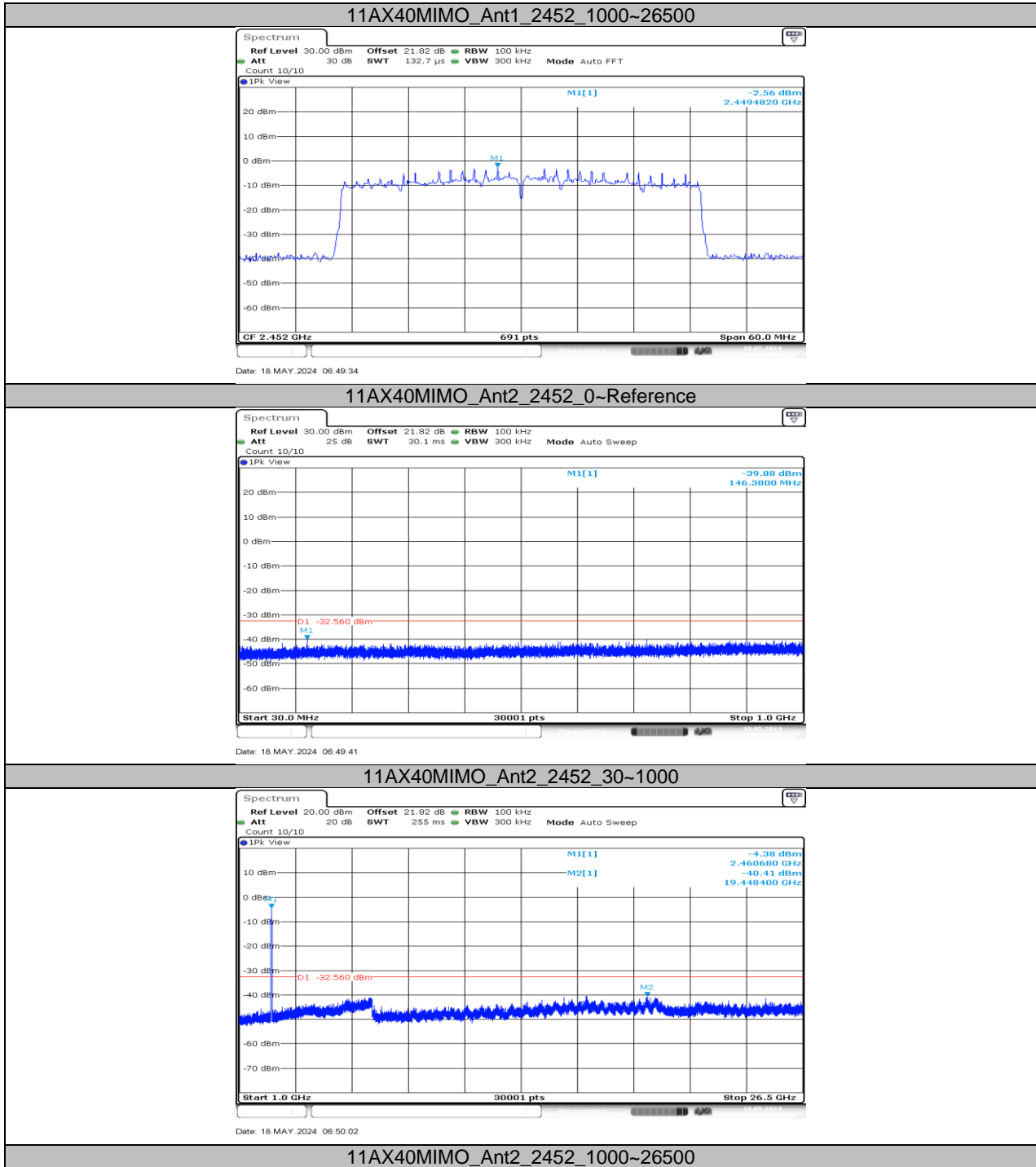


11AX40MIMO_Ant1_2452_0~Reference



11AX40MIMO_Ant1_2452_30~1000





11.7. APPENDIX G: DUTY CYCLE

11.7.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11B	8.37	8.99	0.9310	93.10	0.31	0.12	1
11G	1.39	2.00	0.6950	69.50	1.58	0.72	1
11N20MIMO	1.29	1.92	0.6719	67.19	1.73	0.78	1
11N40MIMO	0.64	1.27	0.5039	50.39	2.98	1.56	2
11AX20MIMO	1.02	1.65	0.6182	61.82	2.09	0.98	1
11AX40MIMO	0.54	1.17	0.4615	46.15	3.36	1.85	2

Note:

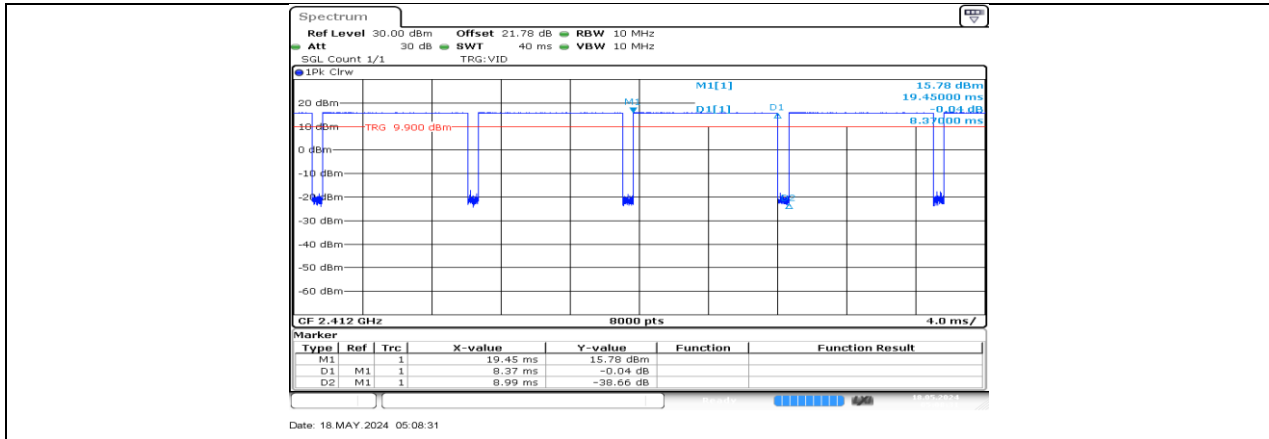
Duty Cycle Correction Factor=10log (1/x).

Where: x is Duty Cycle (Linear)

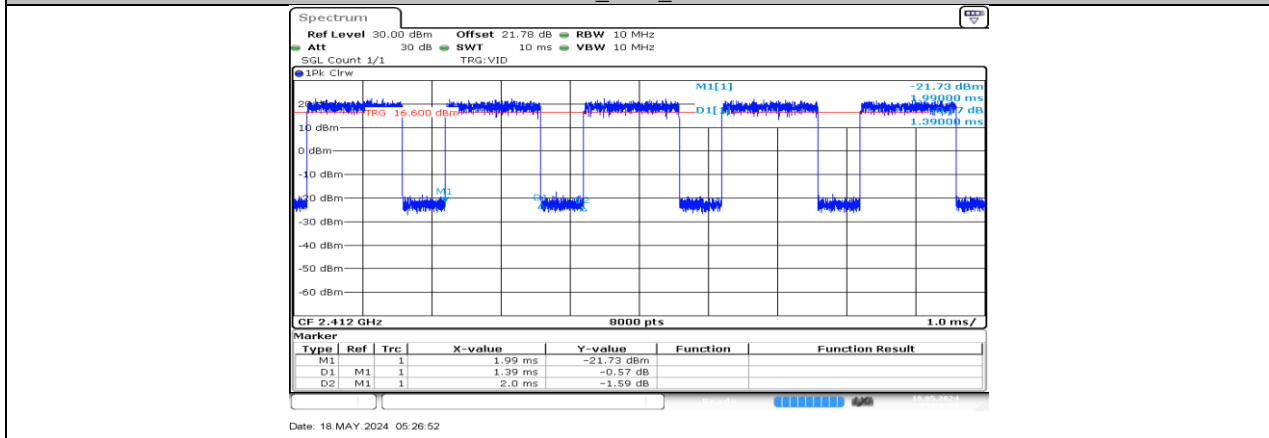
Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

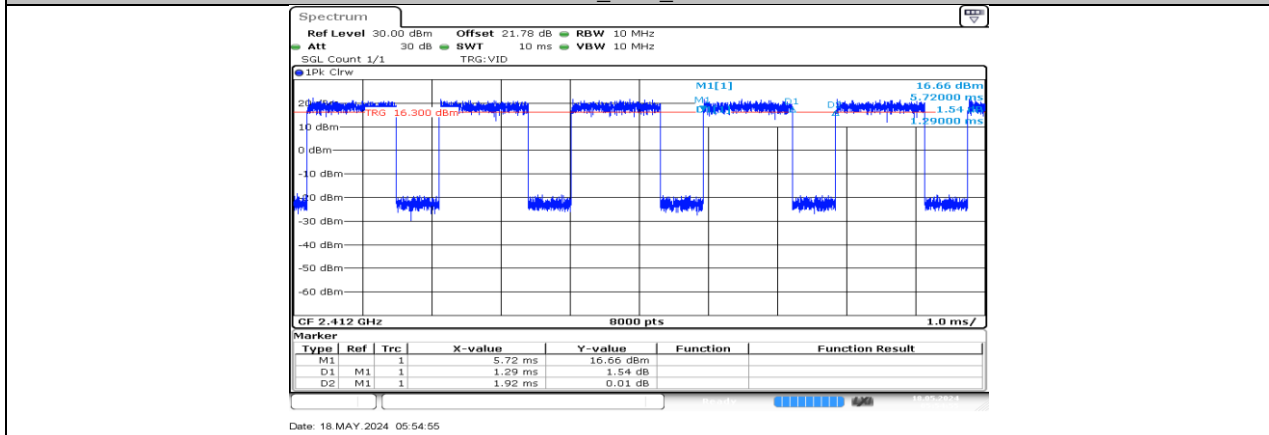
11.7.2. Test Graphs



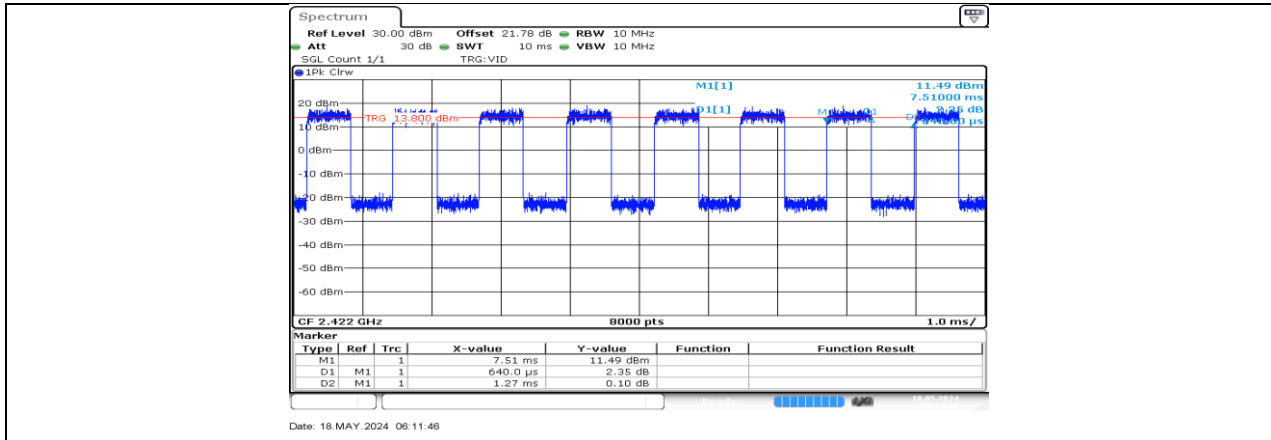
11B_Ant1_2412



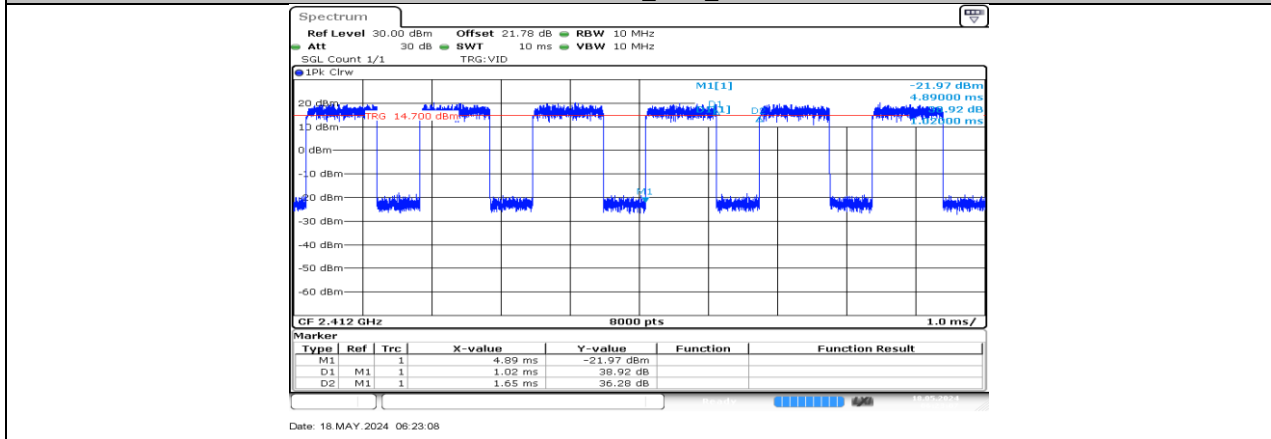
11G_Ant1_2412



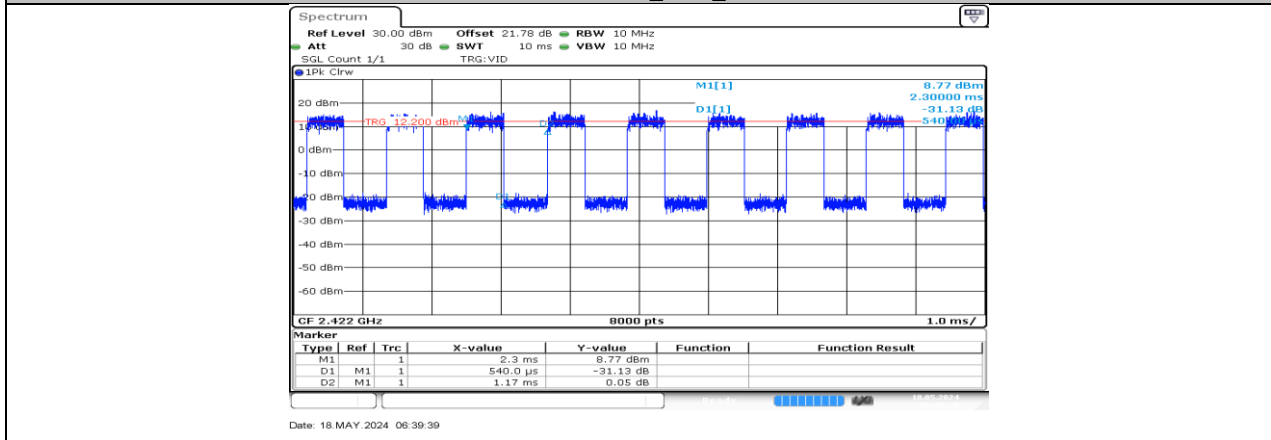
11N20MIMO_Ant1_2412



11N40MIMO_Ant1_2422



11A20MIMO_Ant1_2412



11A40MIMO_Ant1_2422

END OF REPORT