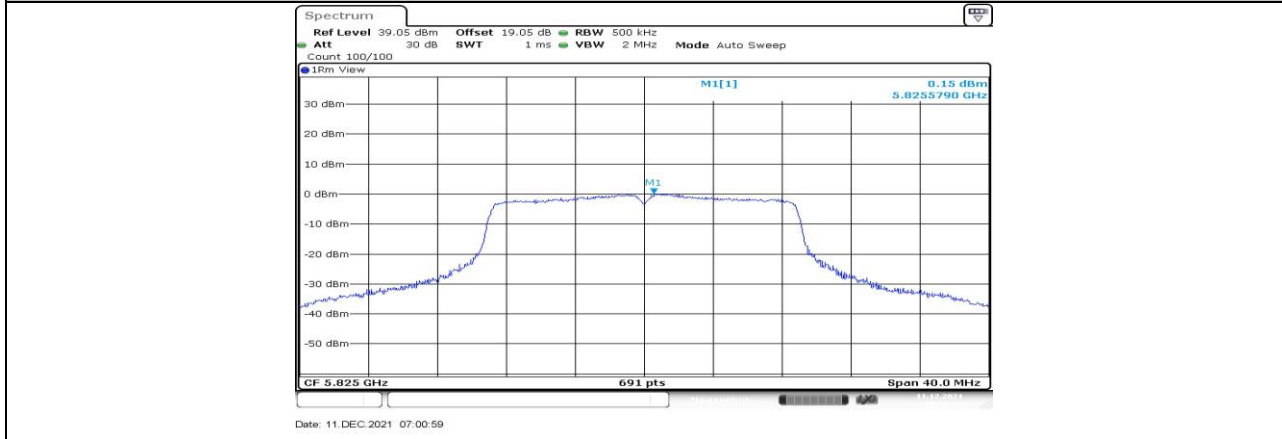


11AC20MIMO_Ant1_5825



11AC20MIMO_Ant2_5825



11AC40MIMO_Ant1_5190



11AC40MIMO_Ant2_5190



11AC40MIMO_Ant1_5230



11AC40MIMO_Ant2_5230



11AC40MIMO_Ant1_5270



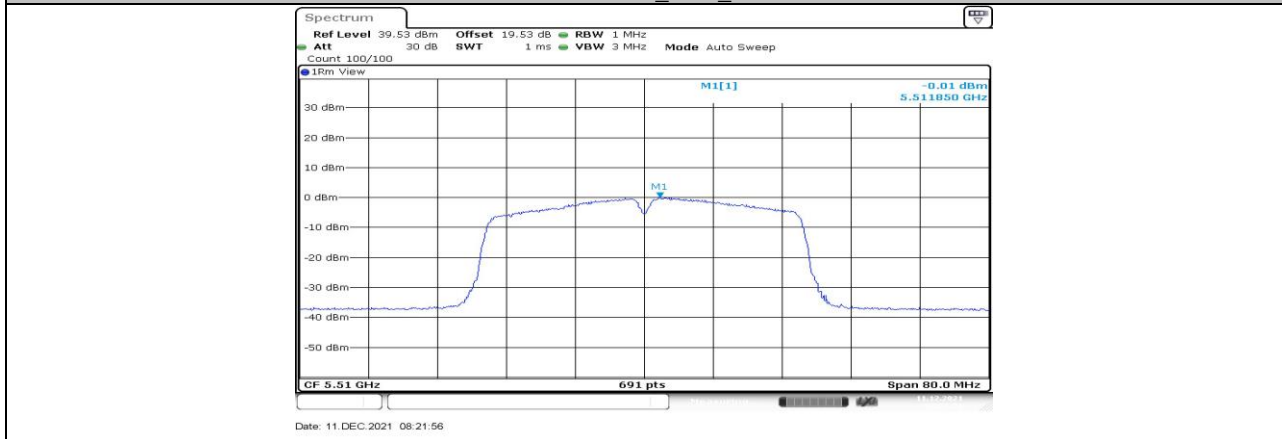
11AC40MIMO_Ant2_5270



11AC40MIMO_Ant1_5310



11AC40MIMO_Ant2_5310



11AC40MIMO_Ant1_5510



11AC40MIMO_Ant2_5510



11AC40MIMO_Ant1_5550



11AC40MIMO_Ant2_5550



11AC40MIMO_Ant1_5670



11AC40MIMO_Ant2_5670



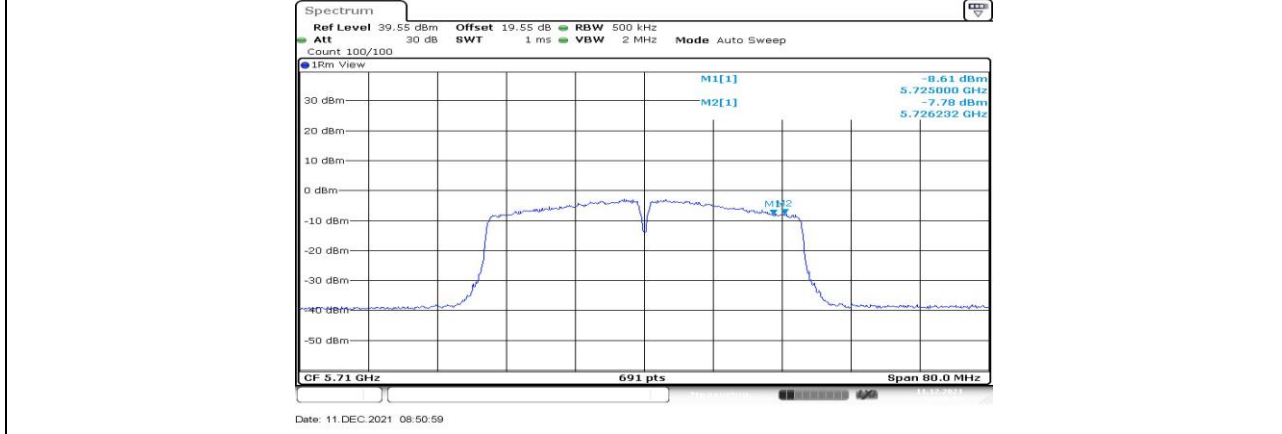
11AC40MIMO_Ant1_5710_UNII-2C



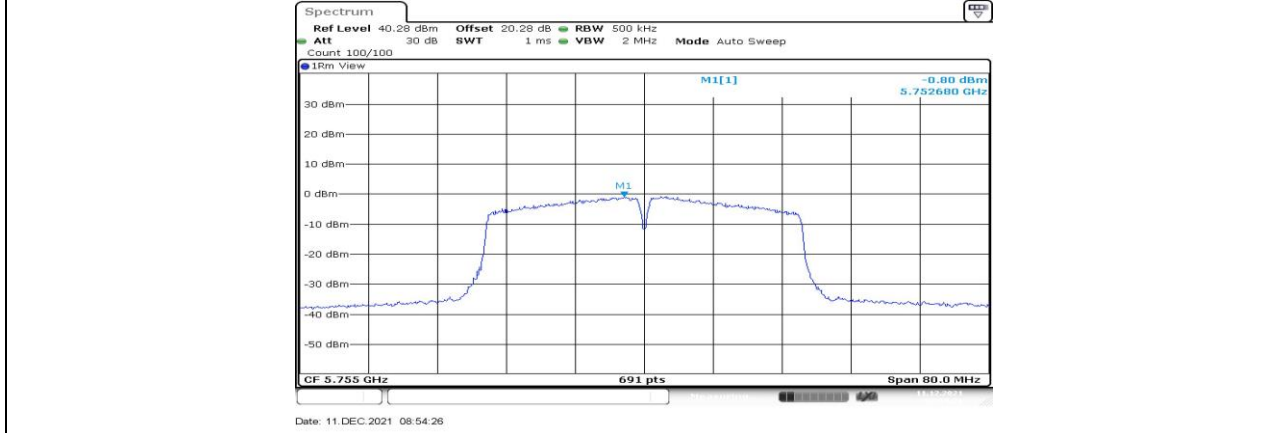
11AC40MIMO_Ant2_5710_UNII-2C



11AC40MIMO_Ant1_5710_UNII-3



11AC40MIMO_Ant2_5710_UNII-3



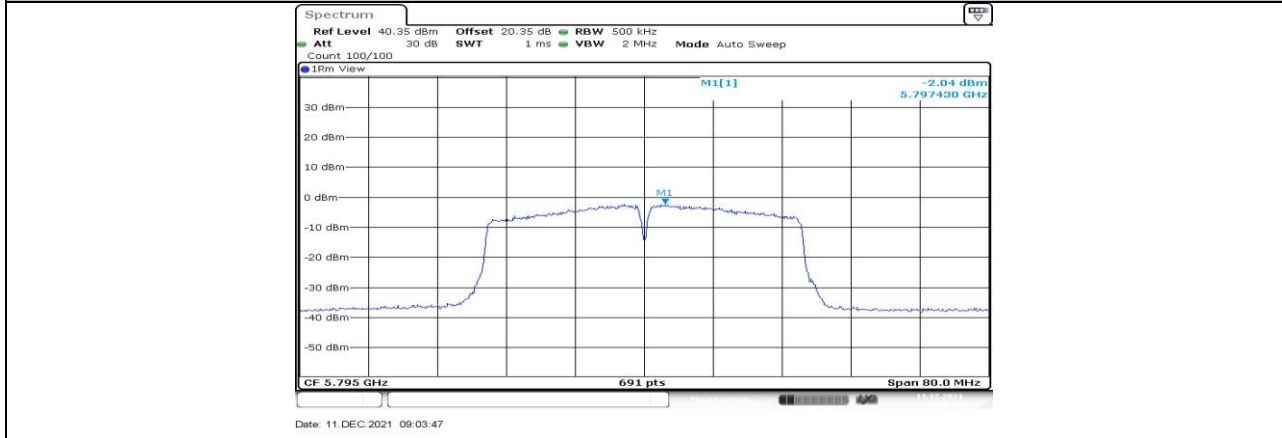
11AC40MIMO_Ant1_5755



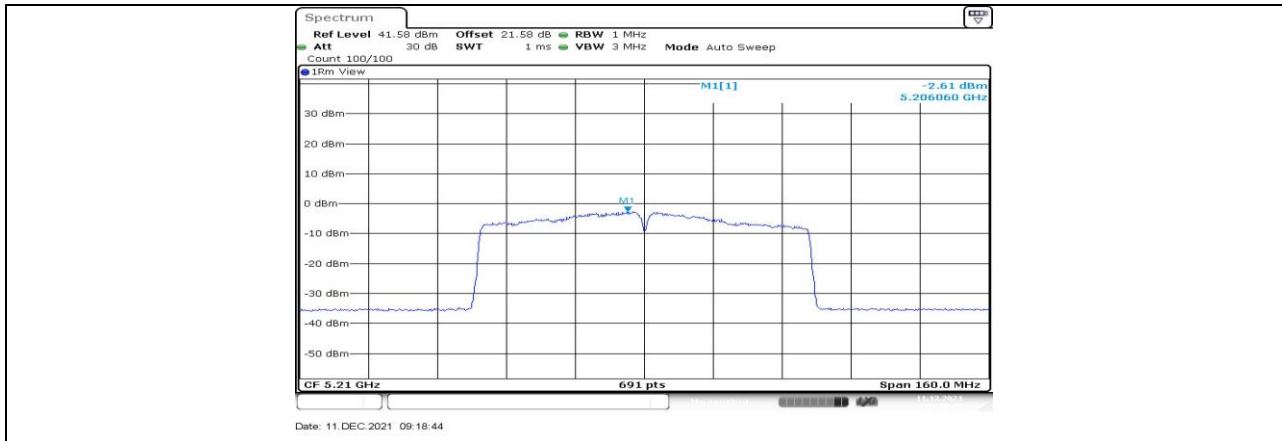
11AC40MIMO_Ant2_5755



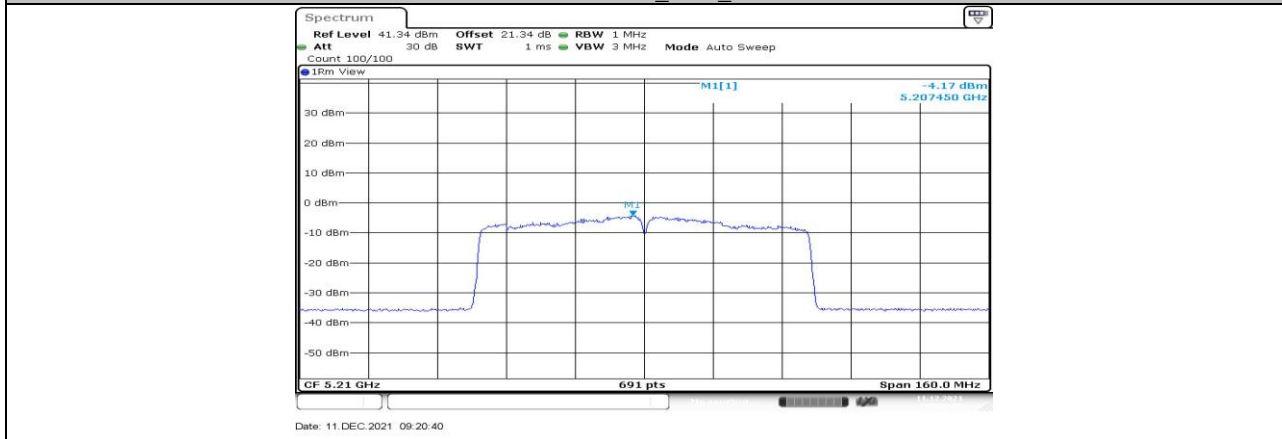
11AC40MIMO_Ant1_5795



11AC40MIMO_Ant2_5795



11AC80MIMO_Ant1_5210



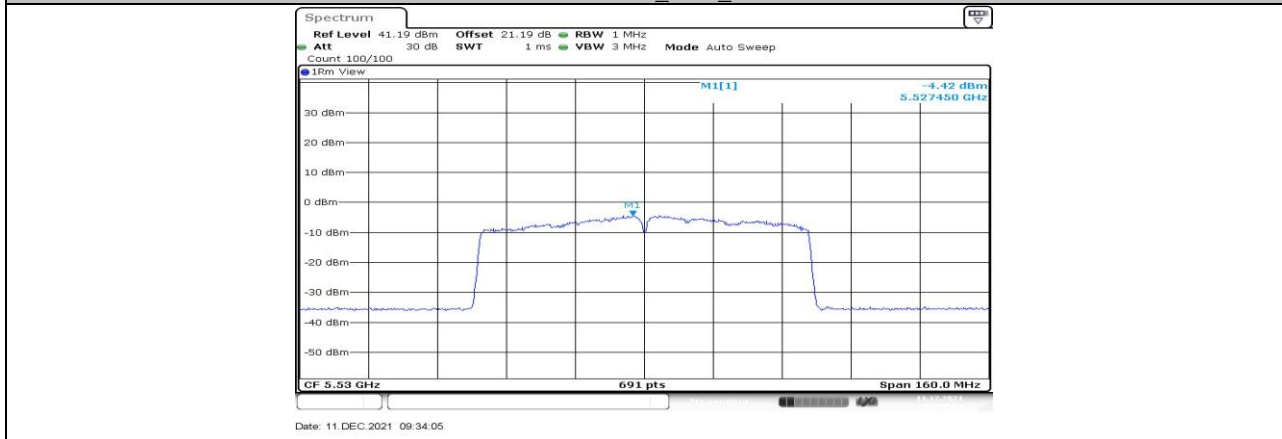
11AC80MIMO_Ant2_5210



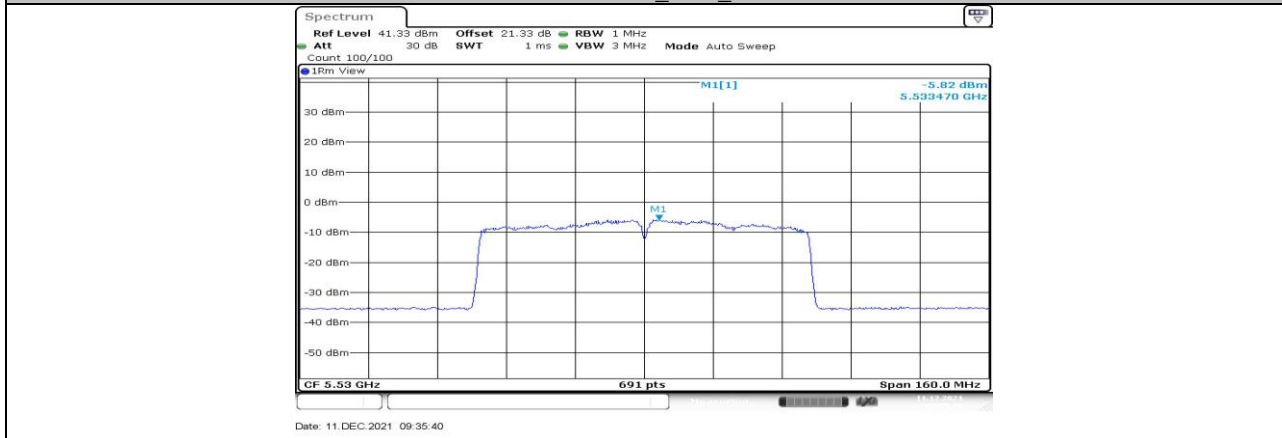
11AC80MIMO_Ant1_5290



11AC80MIMO_Ant2_5290



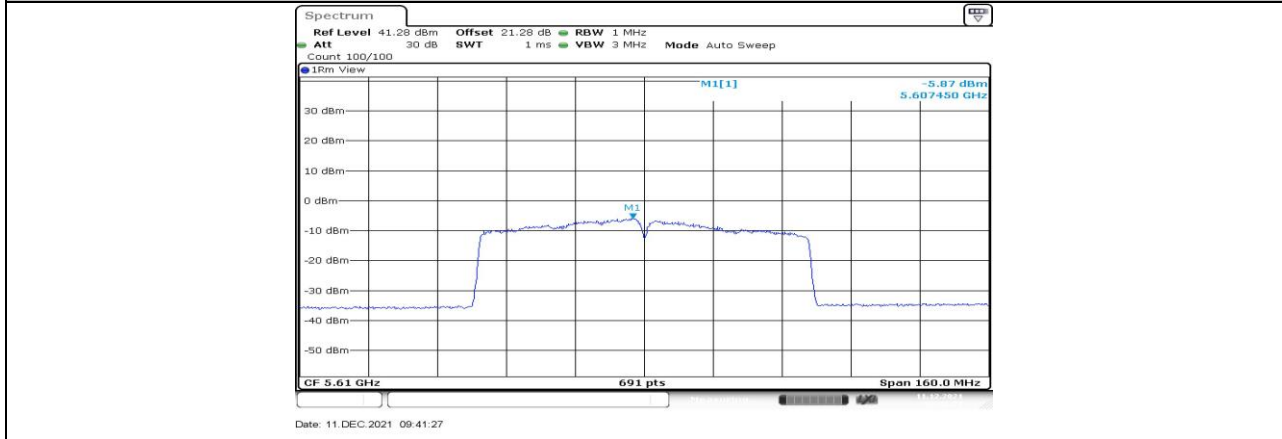
11AC80MIMO_Ant1_5530



11AC80MIMO_Ant2_5530



11AC80MIMO_Ant1_5610



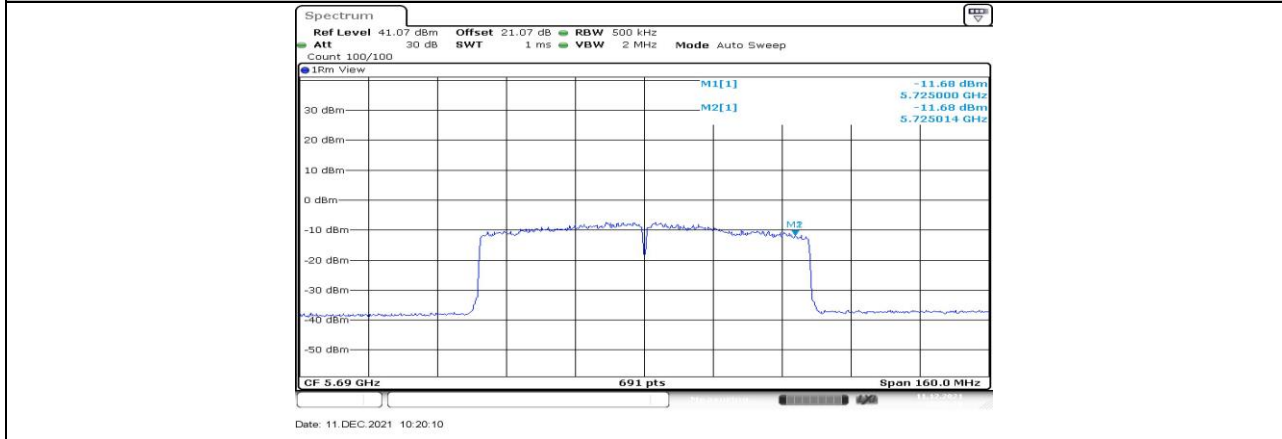
11AC80MIMO_Ant2_5610



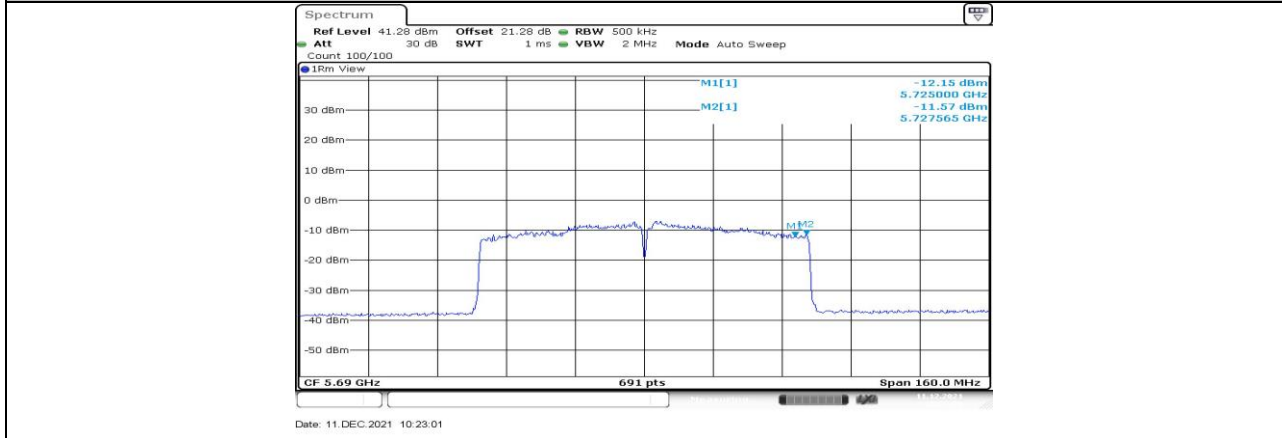
11AC80MIMO_Ant1_5690_UNII-2C



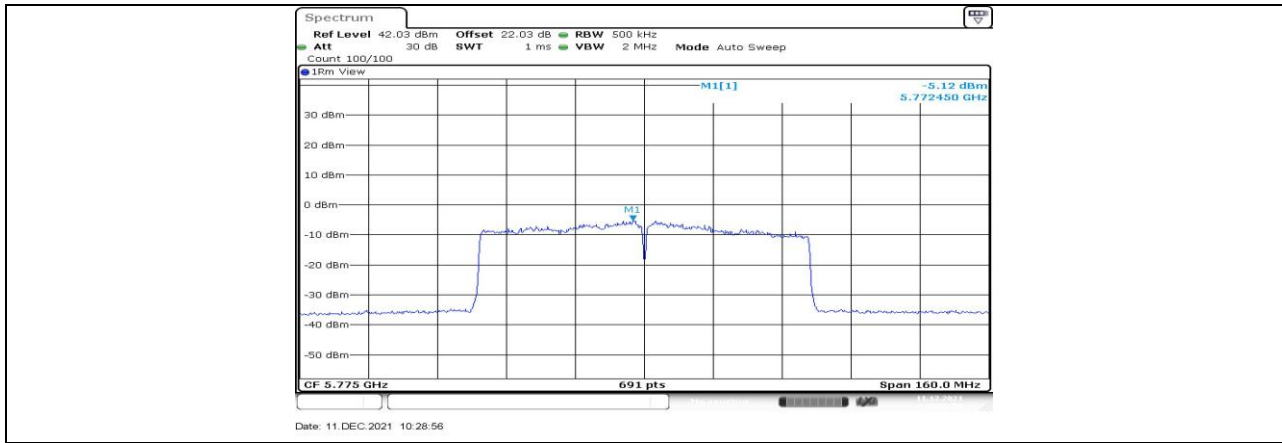
11AC80MIMO_Ant2_5690_UNII-2C



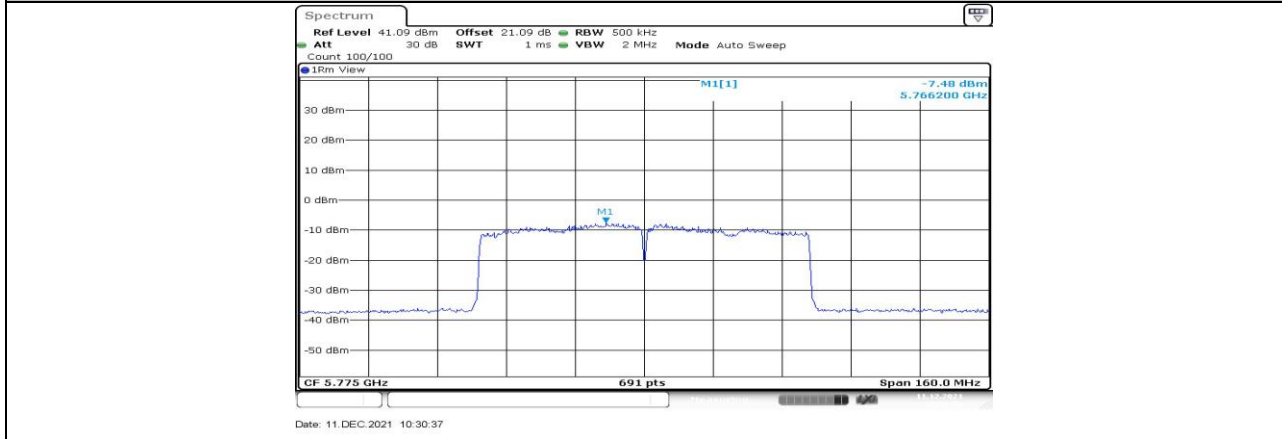
11AC80MIMO_Ant1_5690_UNII-3



11AC80MIMO_Ant2_5690_UNII-3



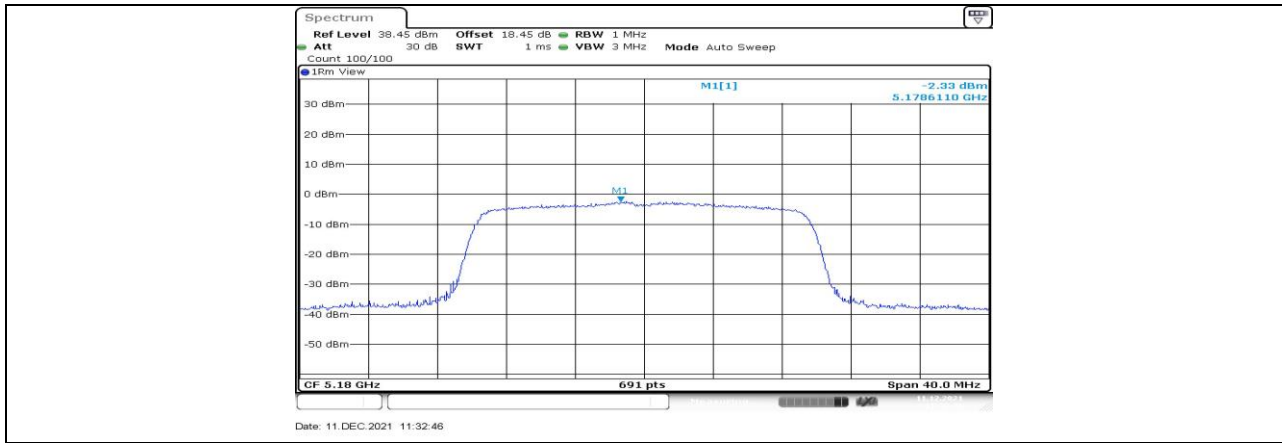
11AC80MIMO_Ant1_5775



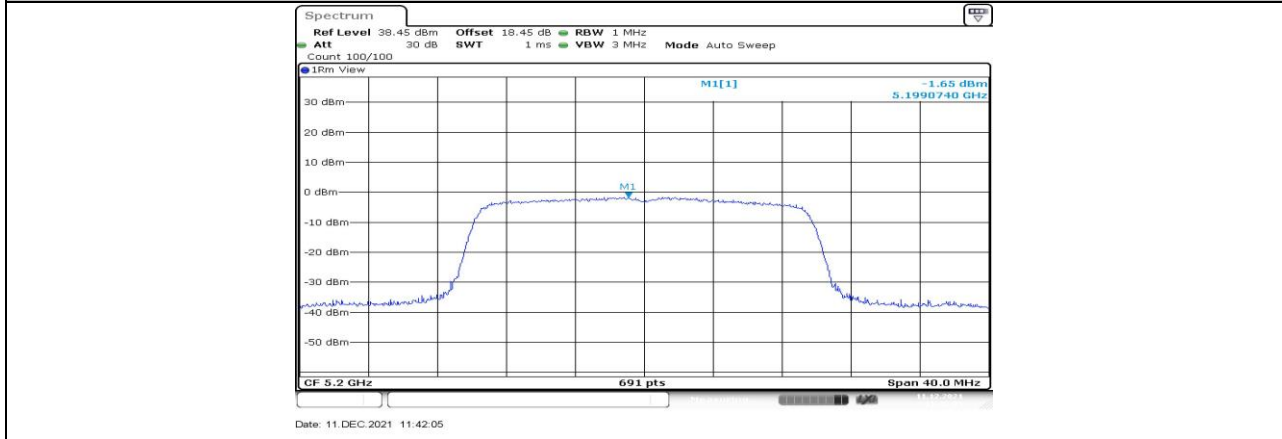
11AC80MIMO_Ant2_5775



11AX20MIMO_Ant1_5180



11AX20MIMO_Ant2_5180



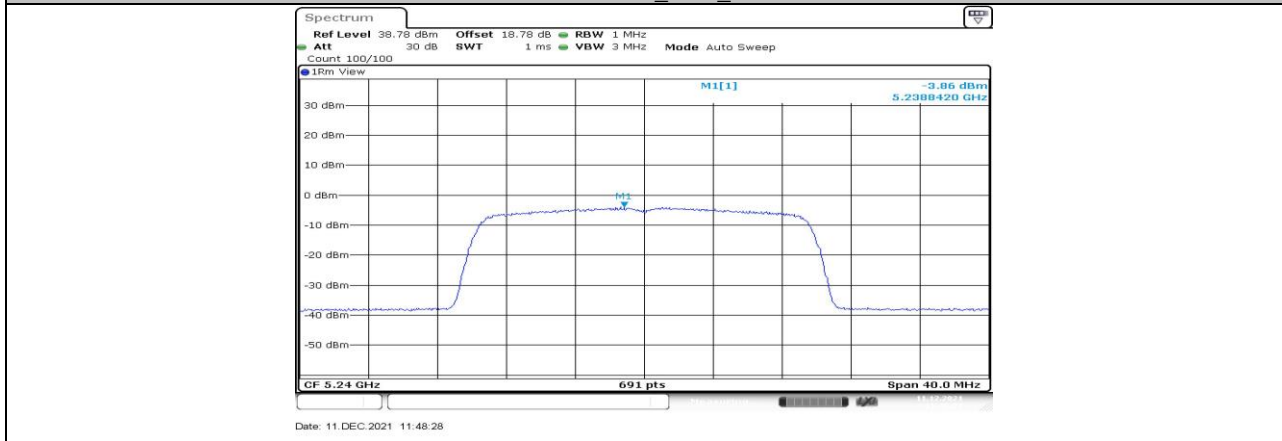
11AX20MIMO_Ant1_5200



11AX20MIMO_Ant2_5200



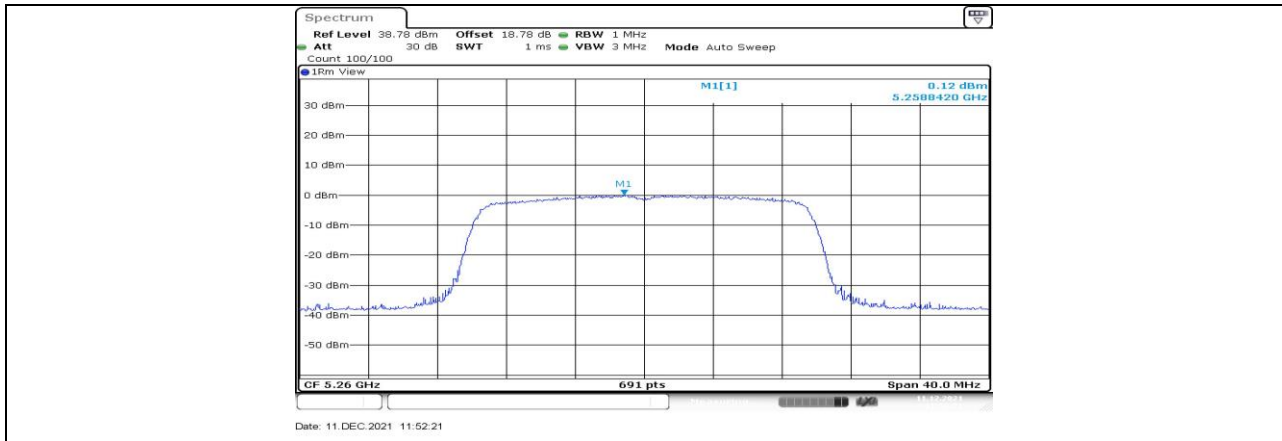
11AX20MIMO_Ant1_5240



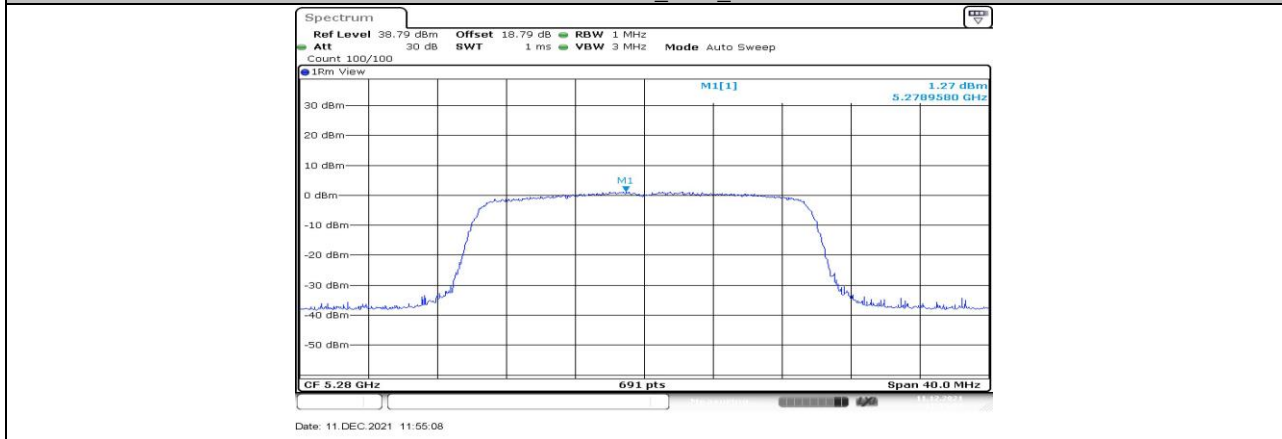
11AX20MIMO_Ant2_5240



11AX20MIMO_Ant1_5260



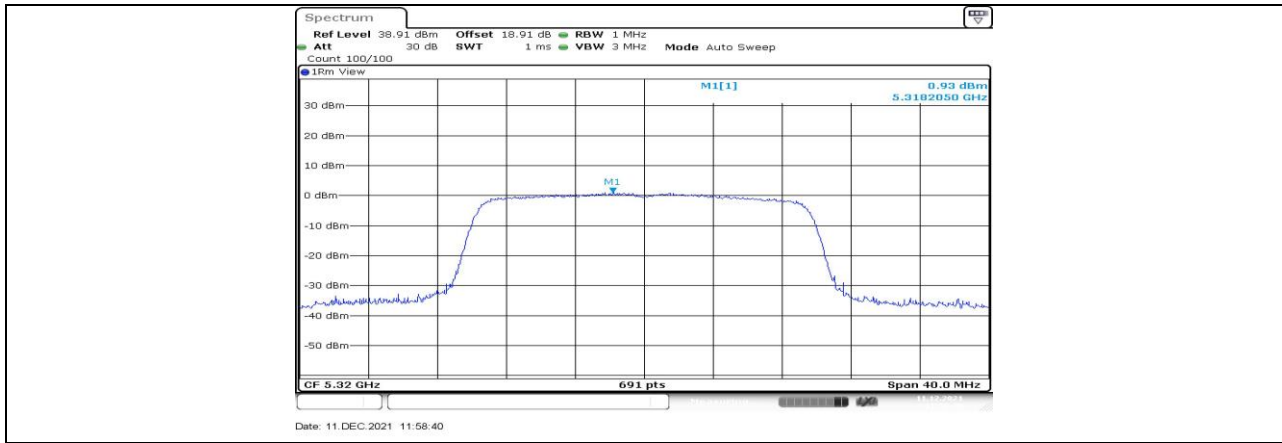
11AX20MIMO_Ant2_5260



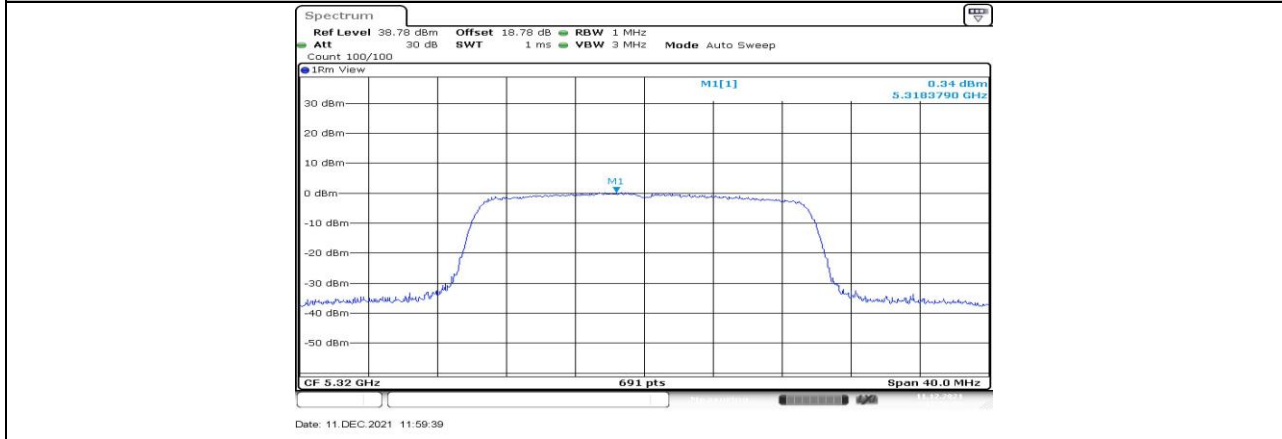
11AX20MIMO_Ant1_5280



11AX20MIMO_Ant2_5280



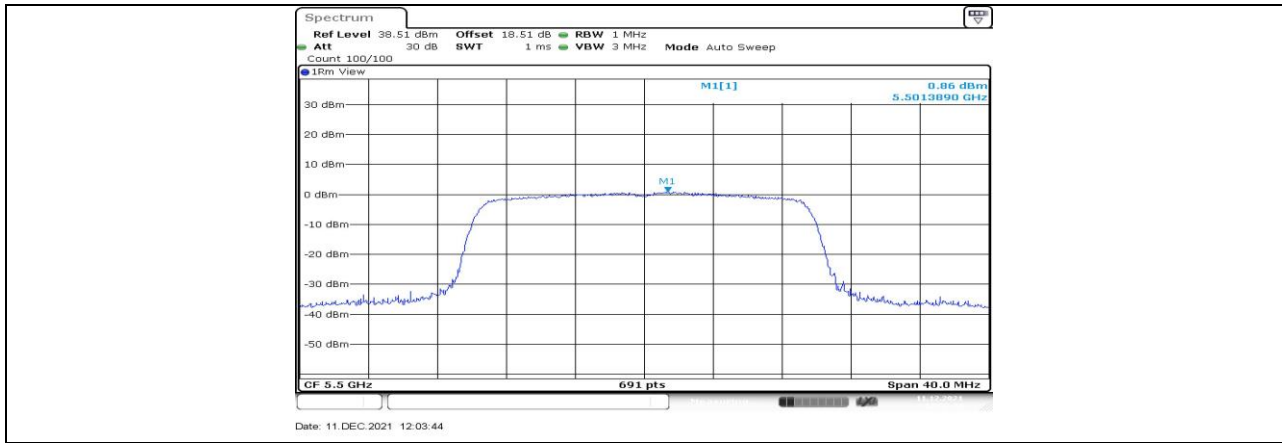
11AX20MIMO_Ant1_5320



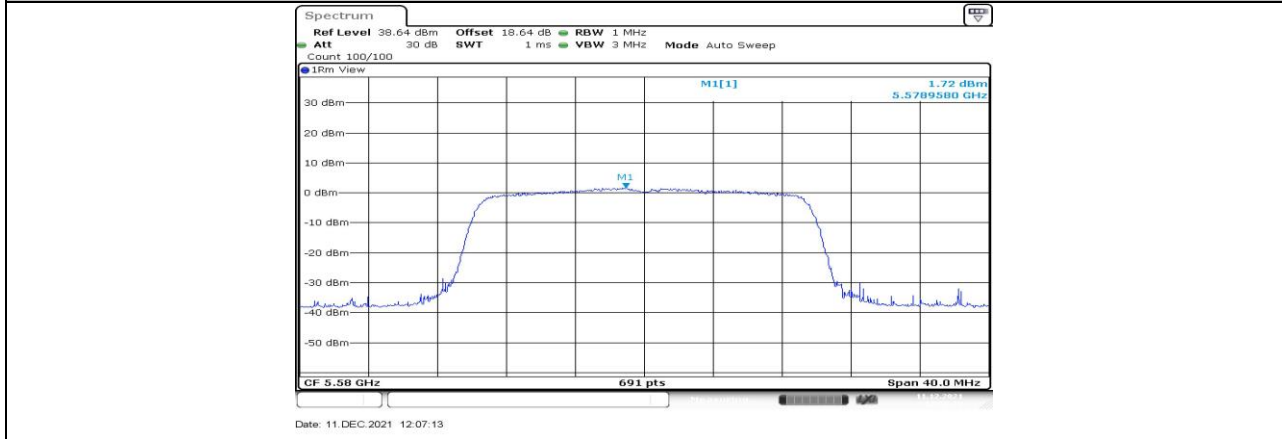
11AX20MIMO_Ant2_5320



11AX20MIMO_Ant1_5500



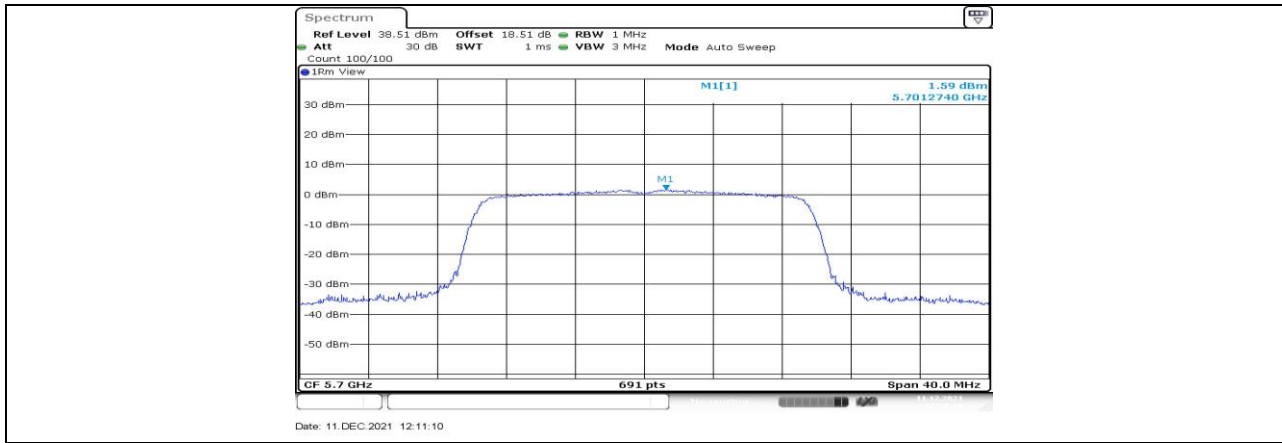
11AX20MIMO_Ant2_5500



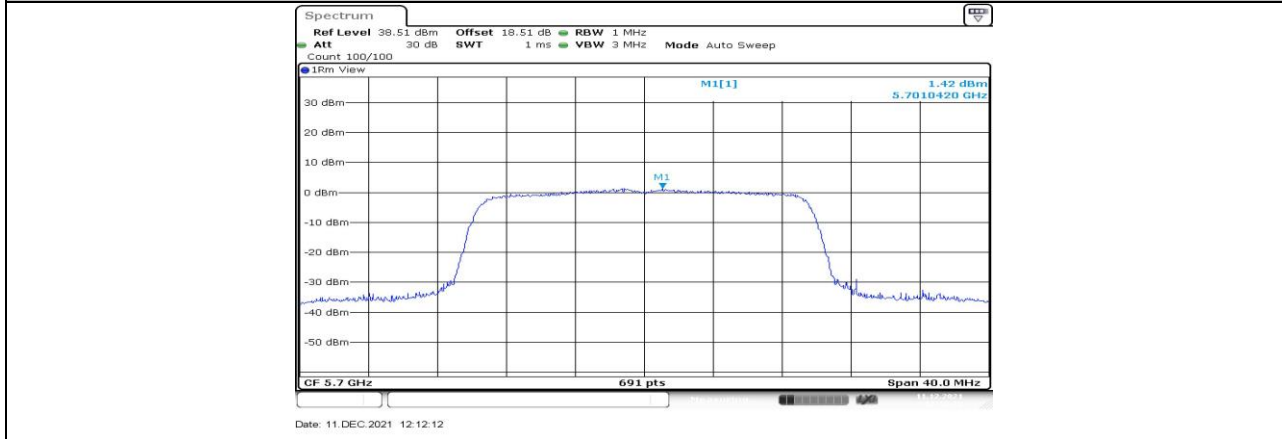
11AX20MIMO_Ant1_5580



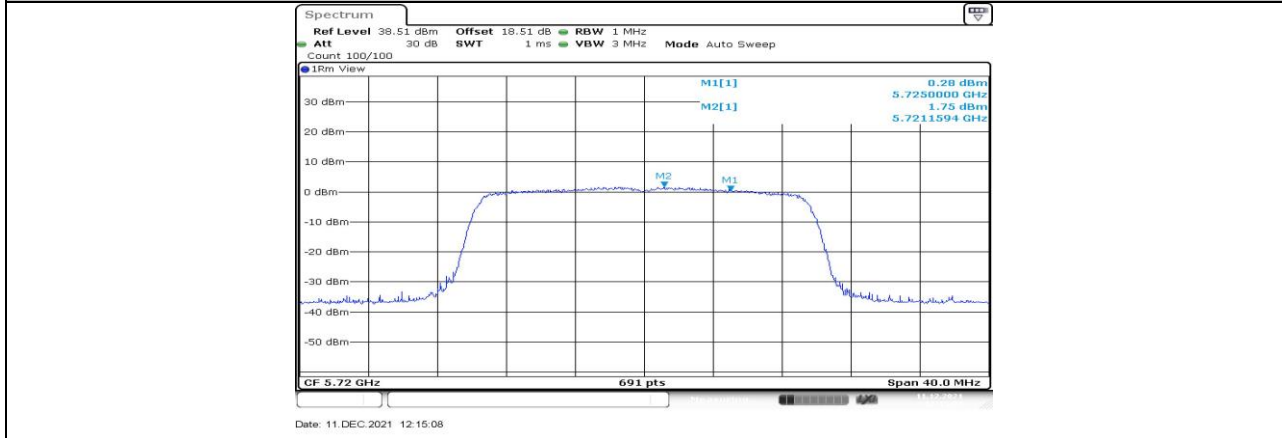
11AX20MIMO_Ant2_5580



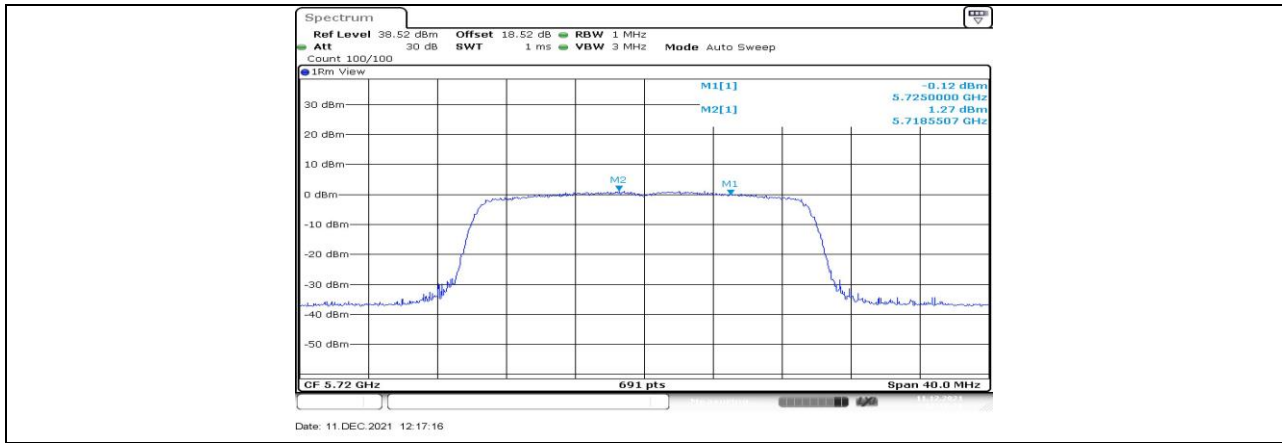
11AX20MIMO_Ant1_5700



11AX20MIMO_Ant2_5700

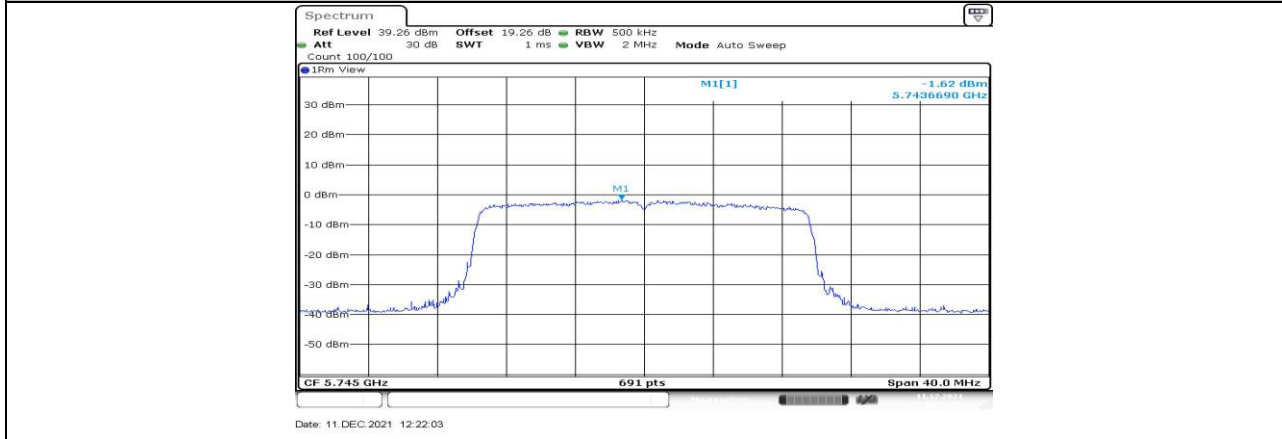


11AX20MIMO_Ant1_5720_UNII-2C





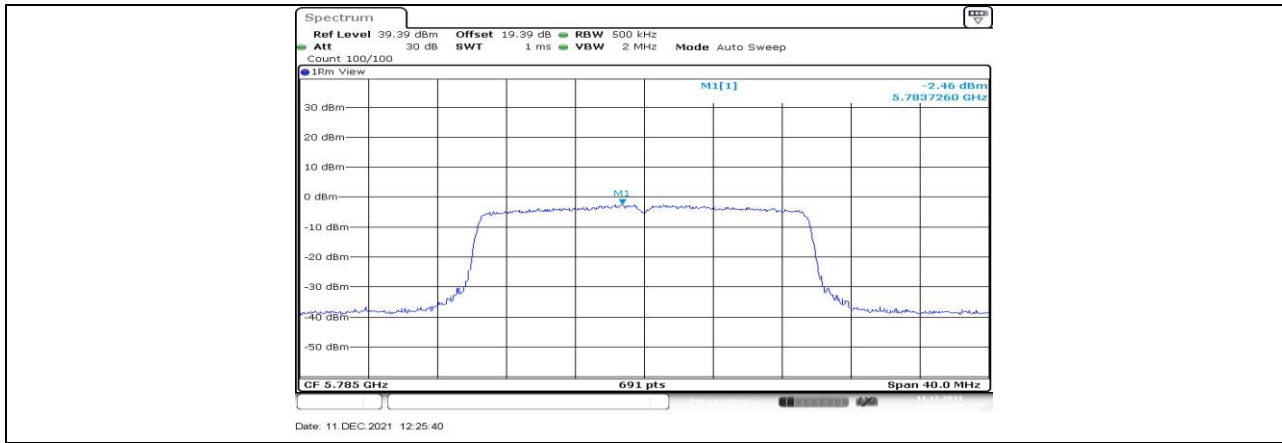
11AX20MIMO_Ant1_5745



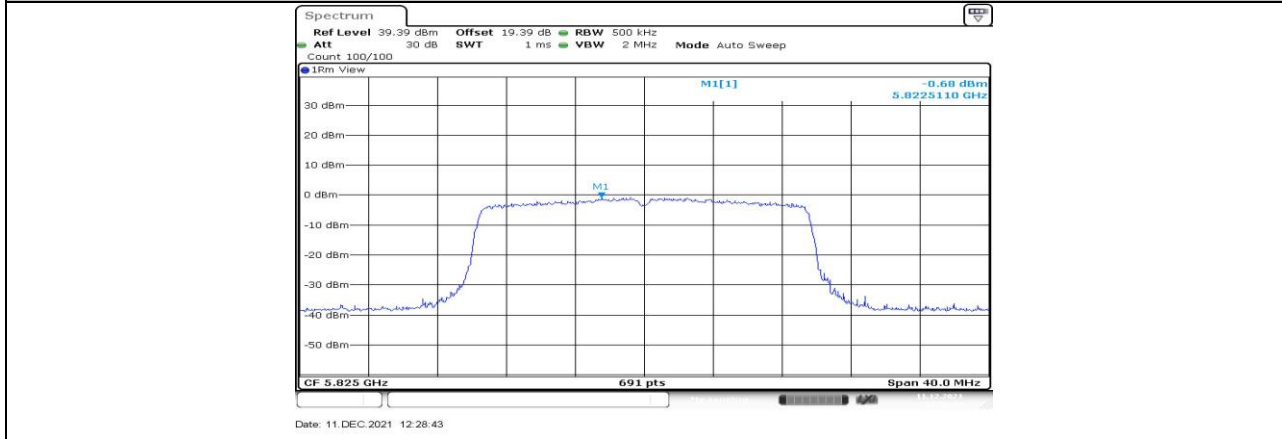
11AX20MIMO_Ant2_5745



11AX20MIMO_Ant1_5785



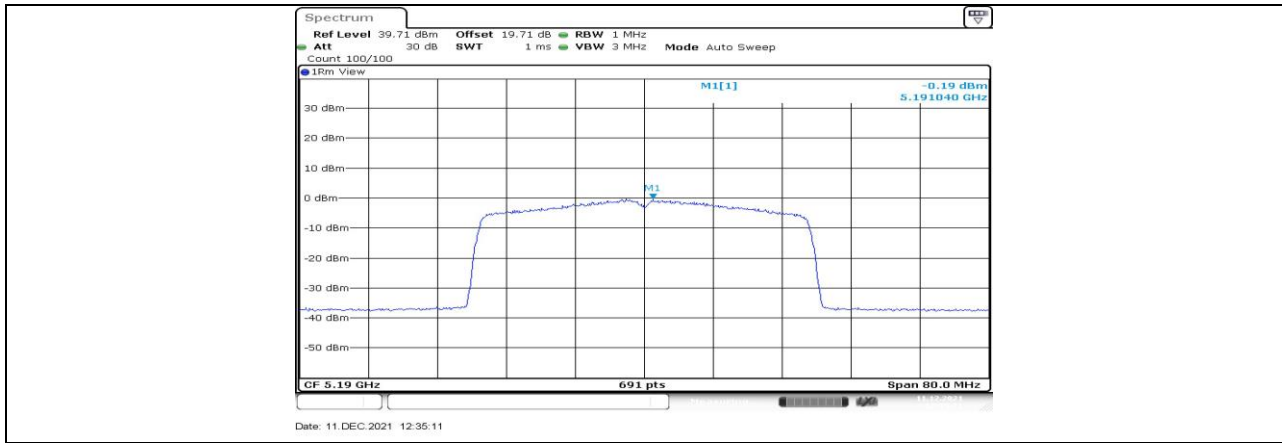
11AX20MIMO_Ant2_5785



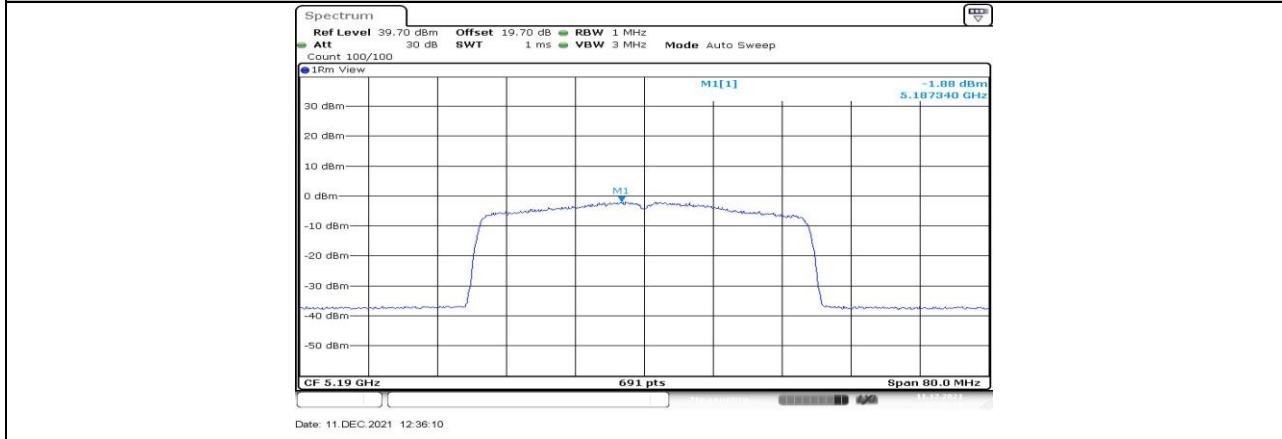
11AX20MIMO_Ant1_5825



11AX20MIMO_Ant2_5825



11AX40MIMO_Ant1_5190



11AX40MIMO_Ant2_5190



11AX40MIMO_Ant1_5230



11AX40MIMO_Ant2_5230



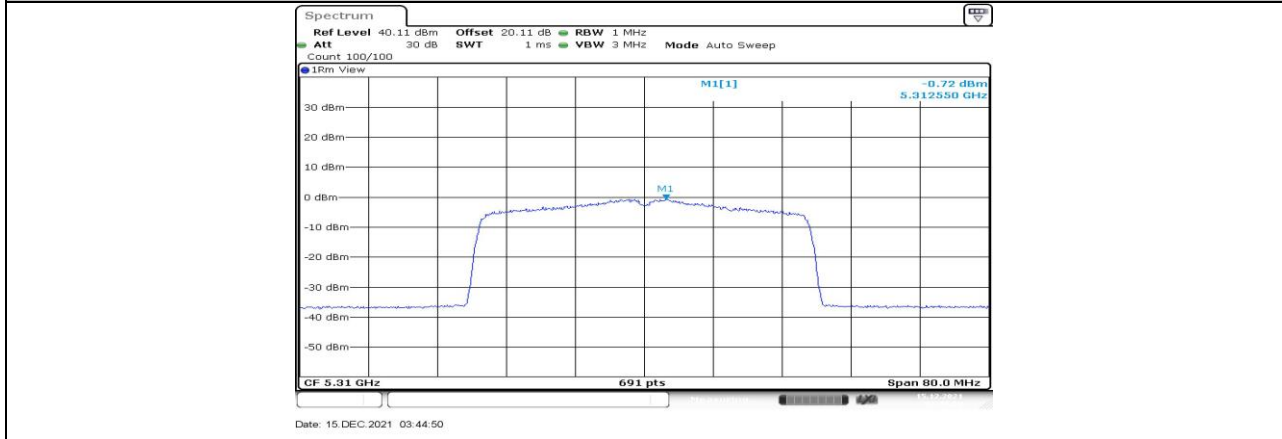
11AX40MIMO_Ant1_5270



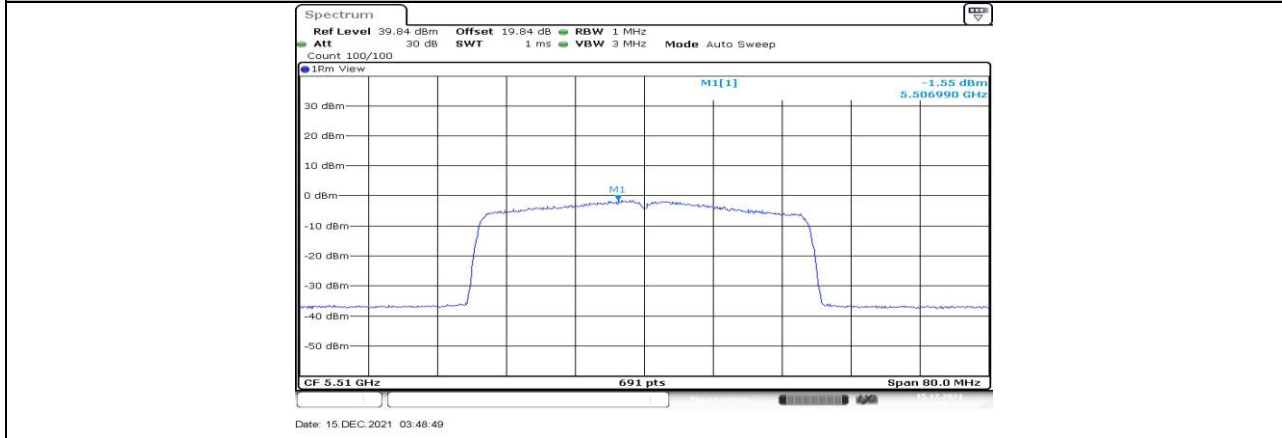
11AX40MIMO_Ant2_5270



11AX40MIMO_Ant1_5310



11AX40MIMO_Ant2_5310



11AX40MIMO_Ant1_5510



11AX40MIMO_Ant2_5510



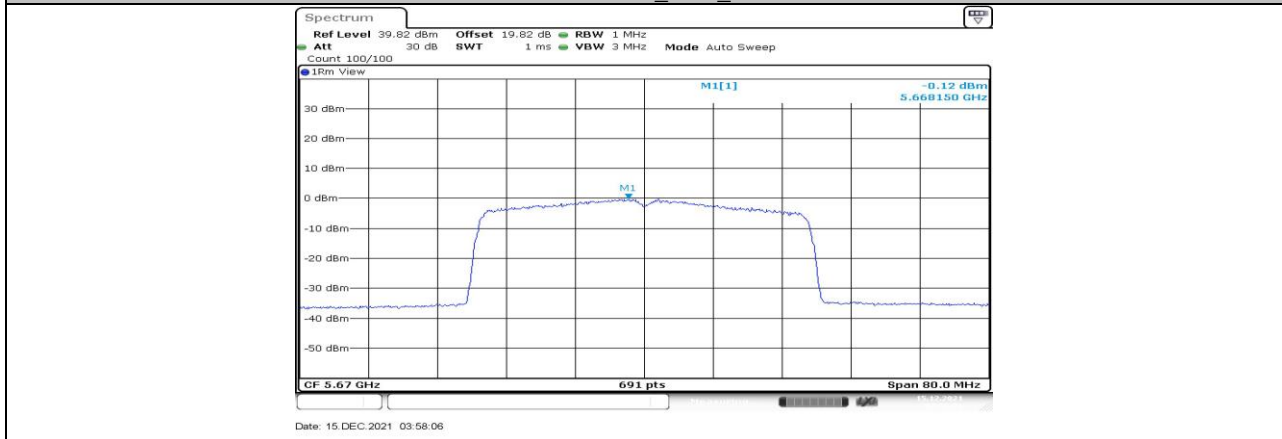
11AX40MIMO_Ant1_5550



11AX40MIMO_Ant2_5550



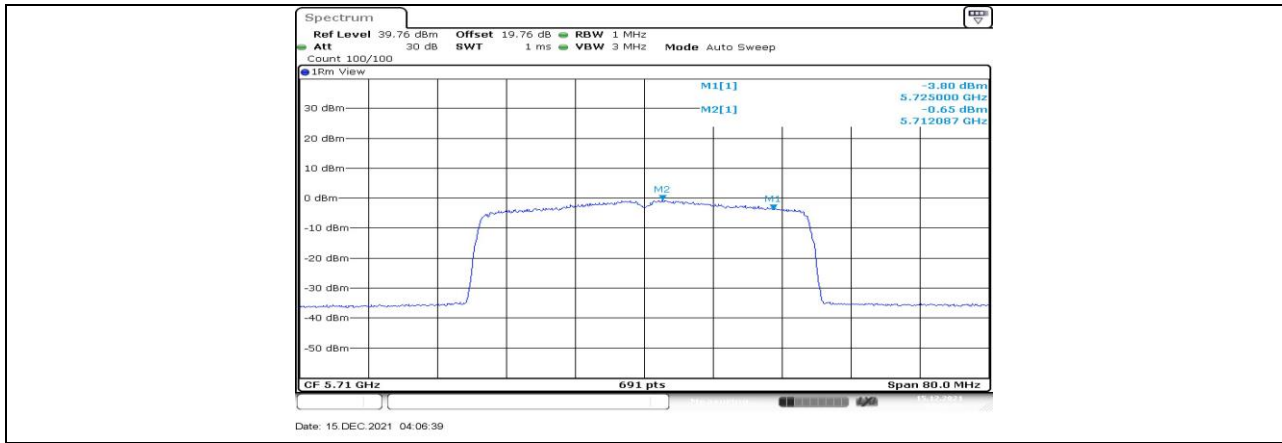
11AX40MIMO_Ant1_5670



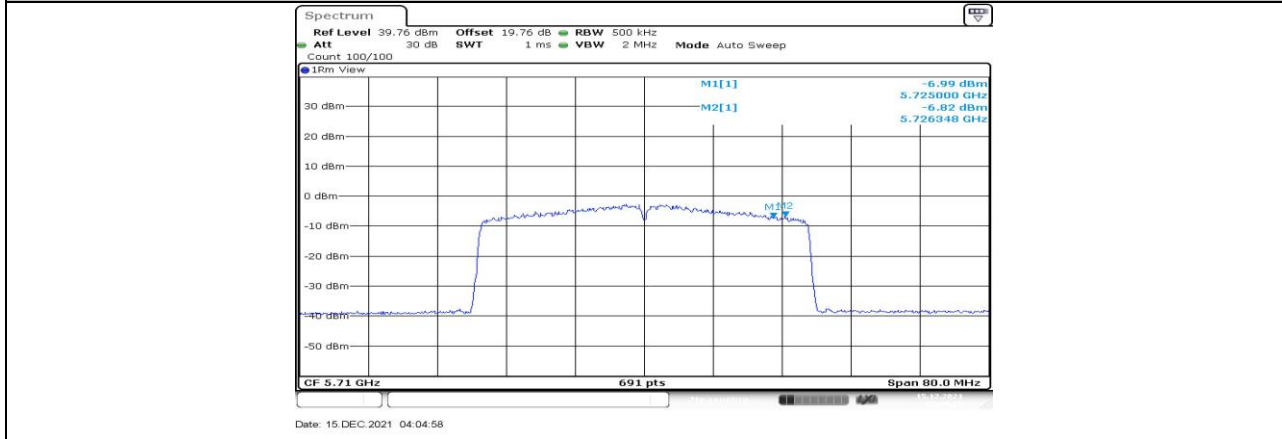
11AX40MIMO_Ant2_5670



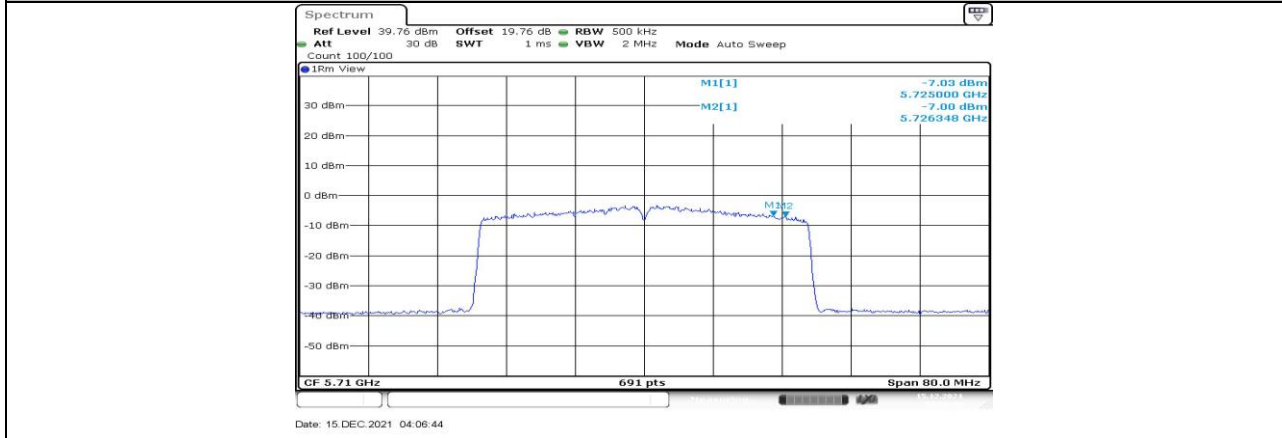
11AX40MIMO_Ant1_5710_UNII-2C



11AX40MIMO_Ant2_5710_UNII-2C



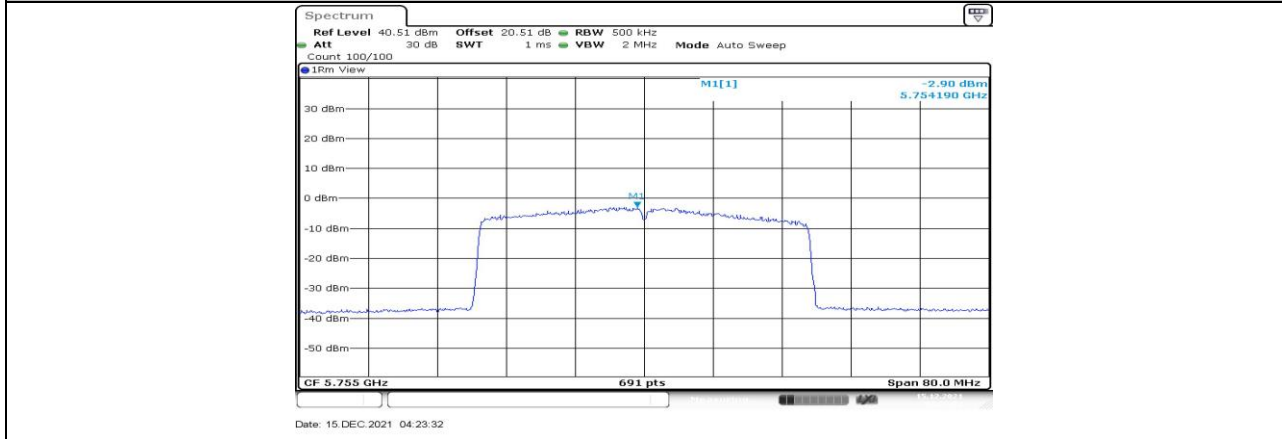
11AX40MIMO_Ant1_5710_UNII-3



11AX40MIMO_Ant2_5710_UNII-3



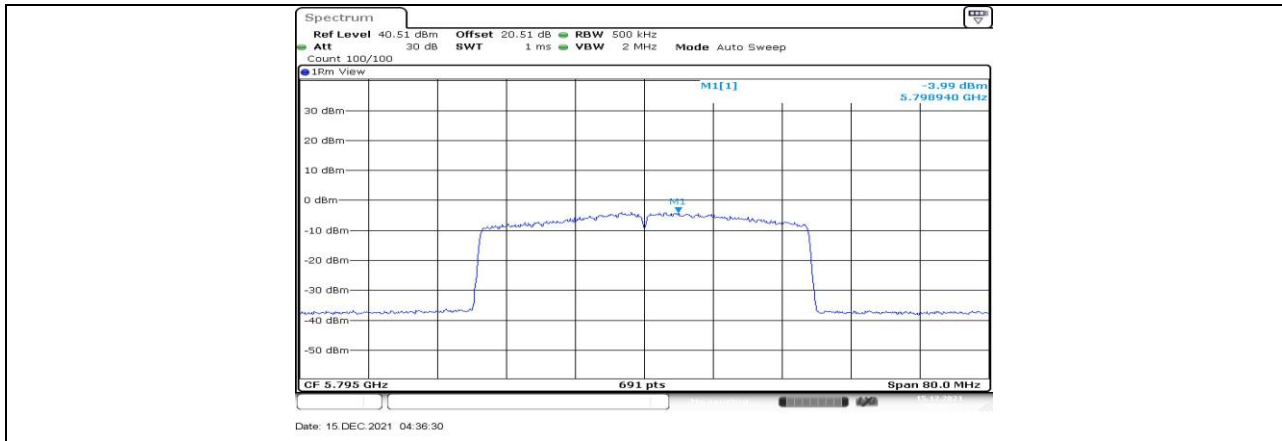
11AX40MIMO_Ant1_5755



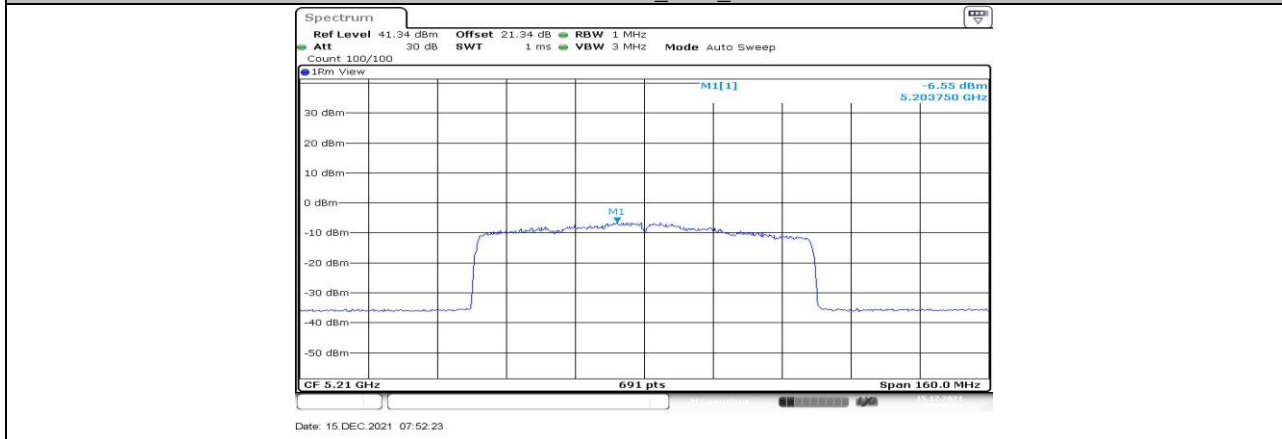
11AX40MIMO_Ant2_5755



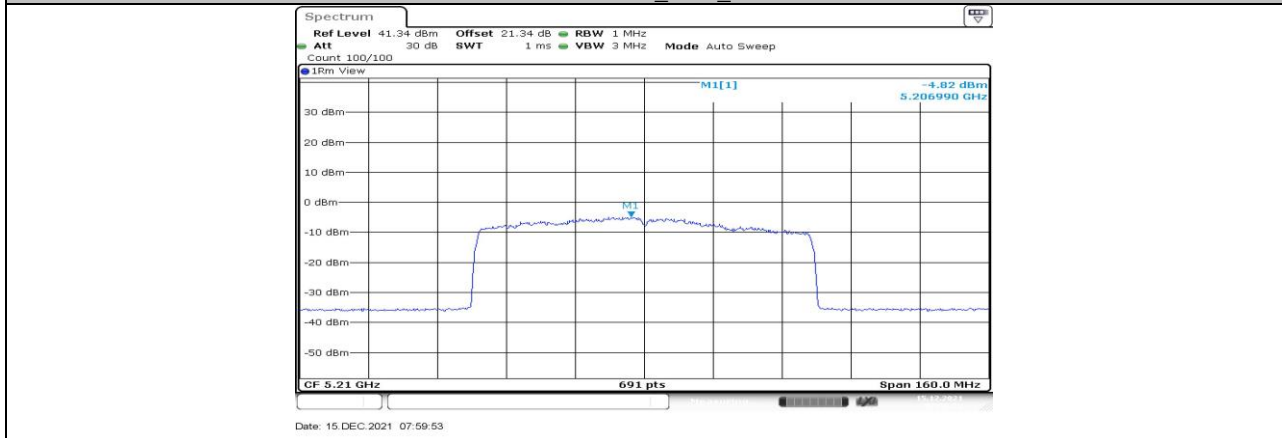
11AX40MIMO_Ant1_5795



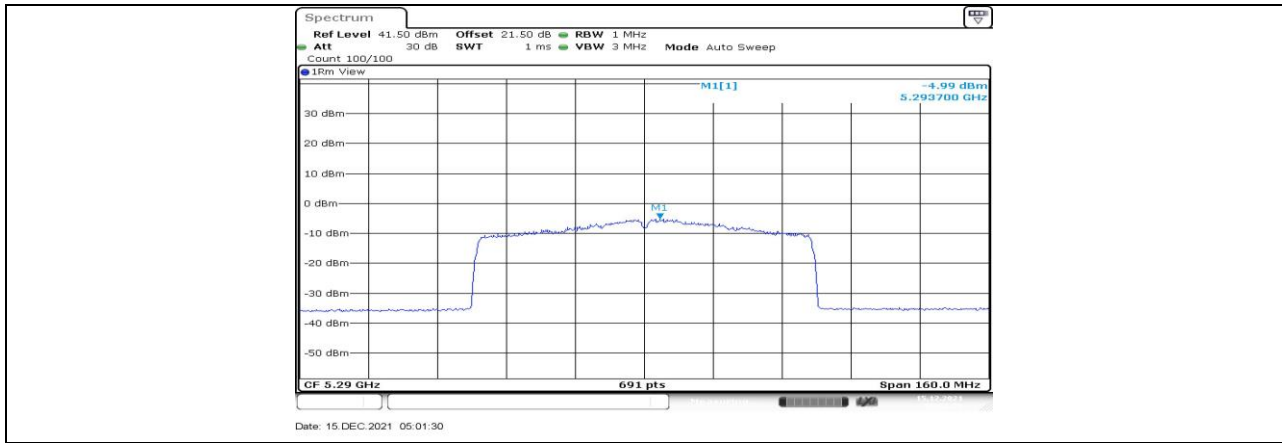
11AX40MIMO_Ant2_5795



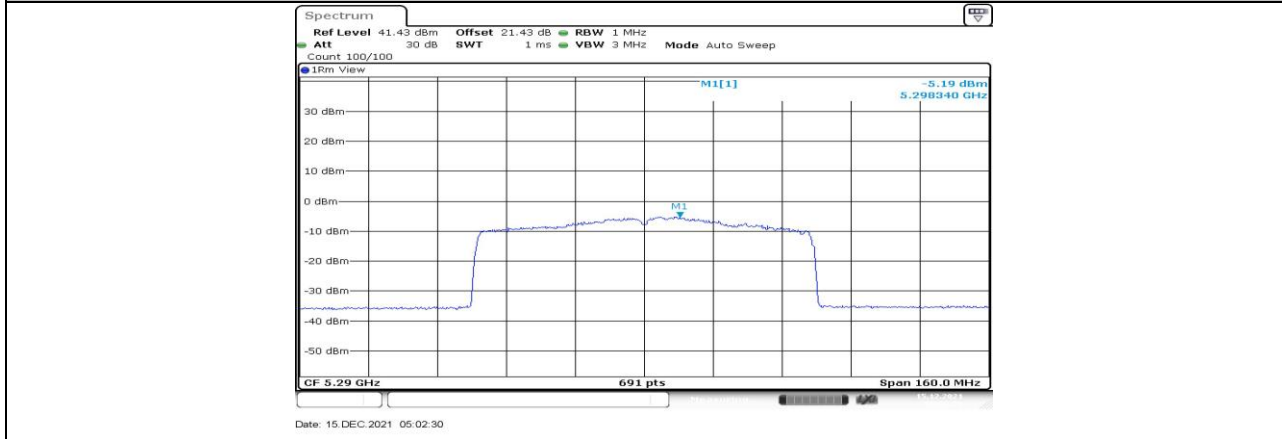
11AX80MIMO_Ant1_5210



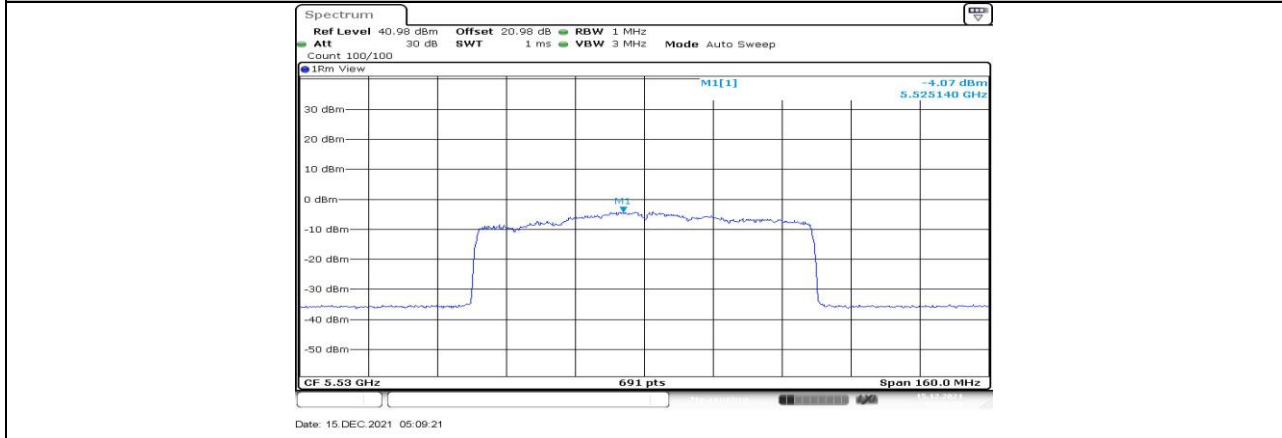
11AX80MIMO_Ant2_5210



11AX80MIMO_Ant1_5290



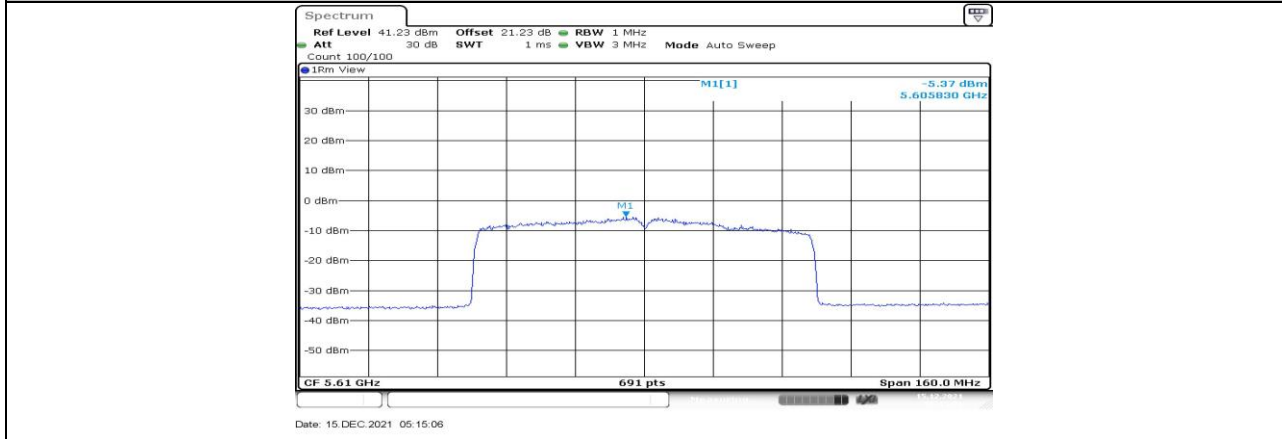
11AX80MIMO_Ant2_5290



11AX80MIMO_Ant1_5530



11AX80MIMO_Ant2_5530



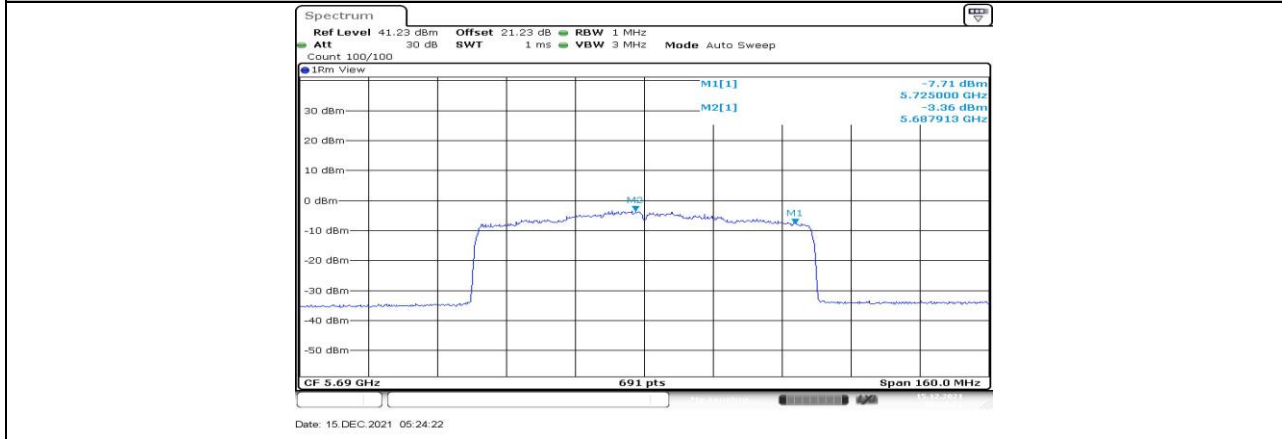
11AX80MIMO_Ant1_5610



11AX80MIMO_Ant2_5610



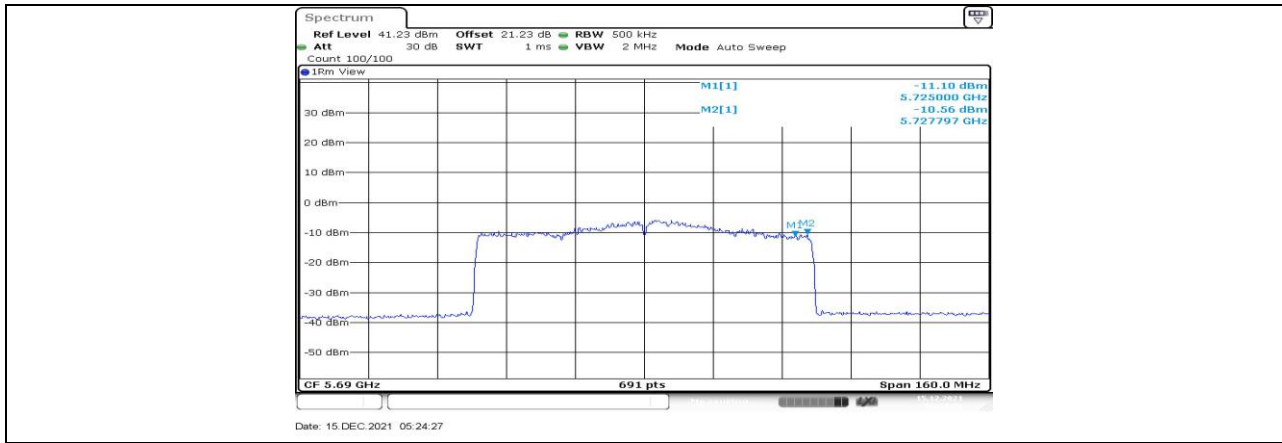
11AX80MIMO_Ant1_5690_UNII-2C



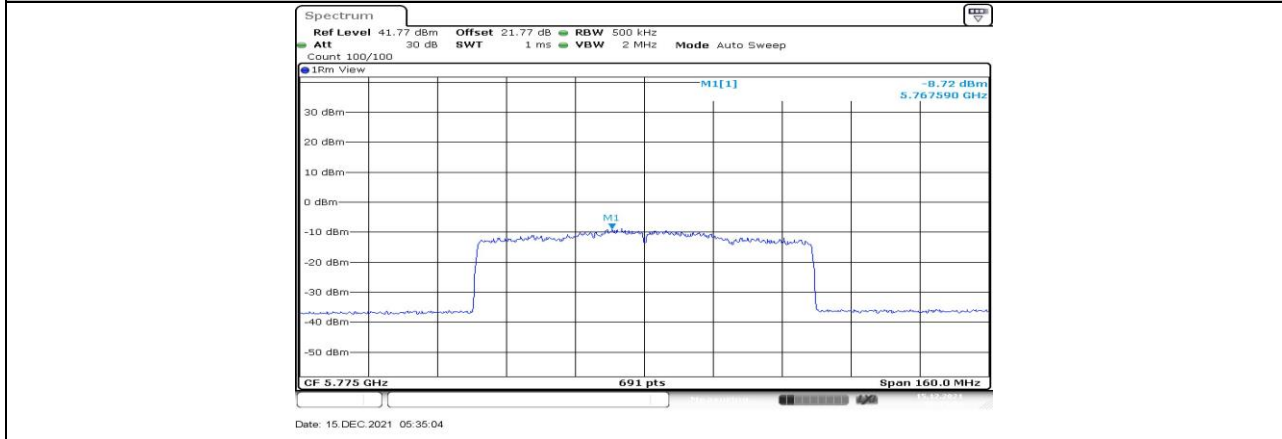
11AX80MIMO_Ant2_5690_UNII-2C



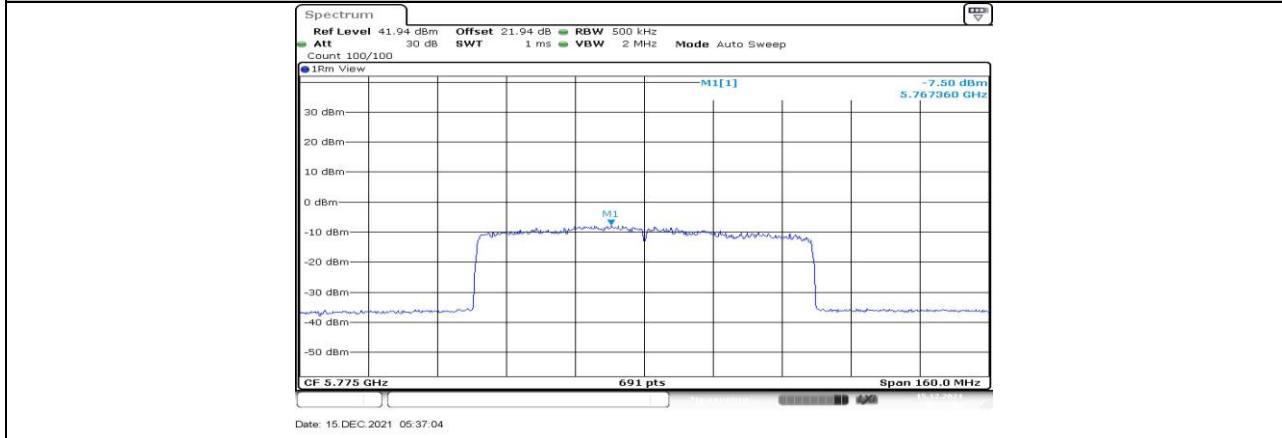
11AX80MIMO_Ant1_5690_UNII-3



11AX80MIMO_Ant2_5690_UNII-3



11AX80MIMO_Ant1_5775



11AX80MIMO_Ant2_5775



12.6. Appendix D: Duty Cycle

12.6.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11A	1.37	1.81	0.7569	75.69	1.21	0.73	1
11AC20MIMO	0.68	1.06	0.6415	64.15	1.93	1.47	2
11AC40MIMO	0.35	0.73	0.4795	47.95	3.19	2.86	3
11AC80MIMO	0.18	0.57	0.3158	31.58	5.01	5.56	6
11AX20MIMO	0.55	0.91	0.6044	60.44	2.19	1.82	2
11AX40MIMO	0.31	0.69	0.4493	44.93	3.47	3.23	4
11AX80MIMO	0.19	0.57	0.3333	33.33	4.77	5.26	6

Note:

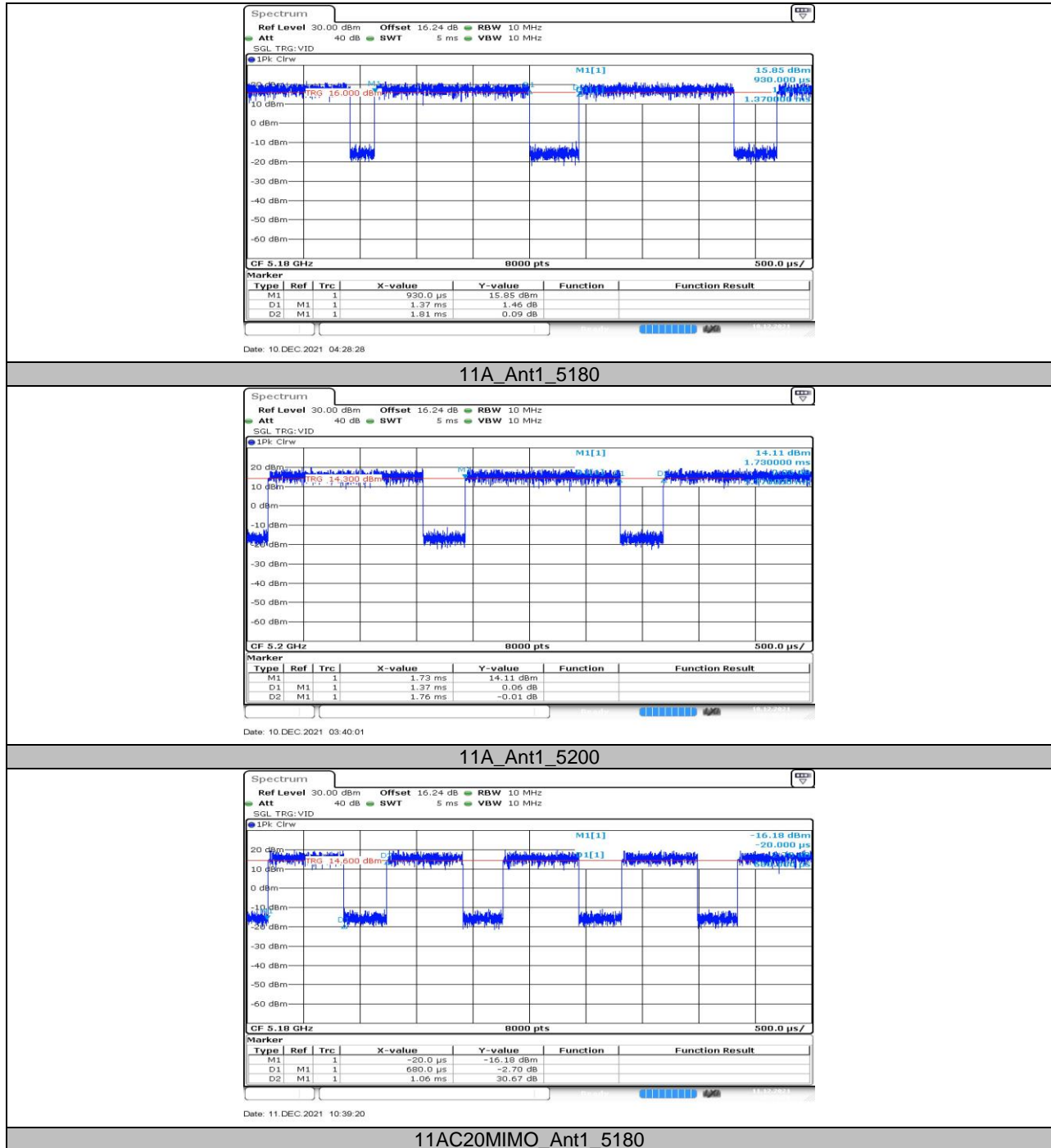
Duty Cycle Correction Factor= $10\log(1/x)$.

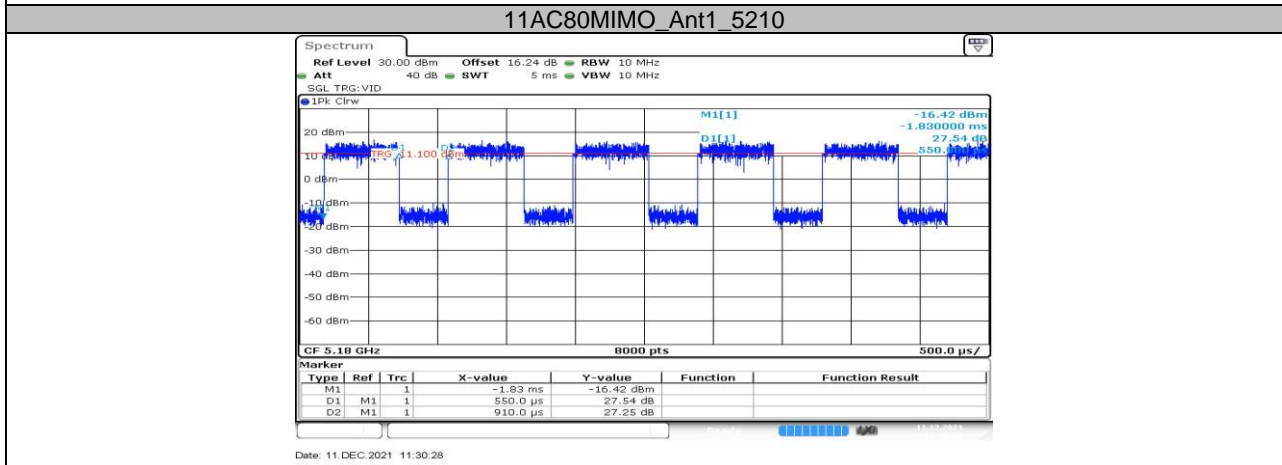
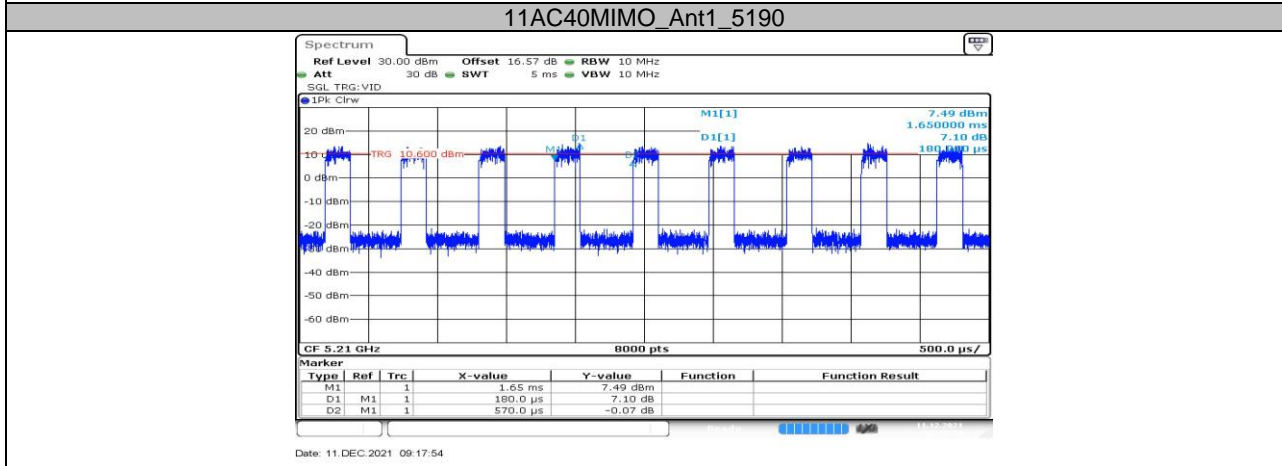
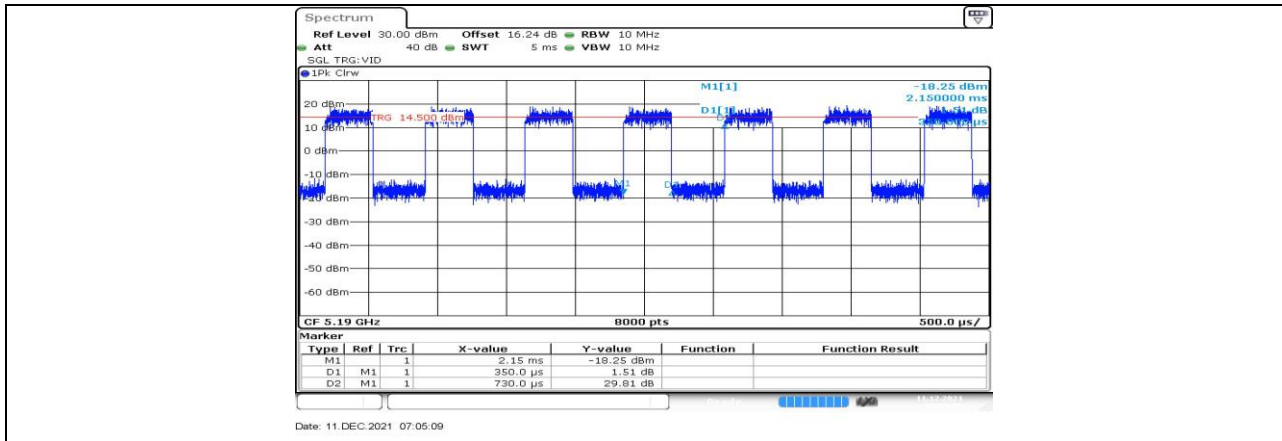
Where: x is Duty Cycle (Linear)

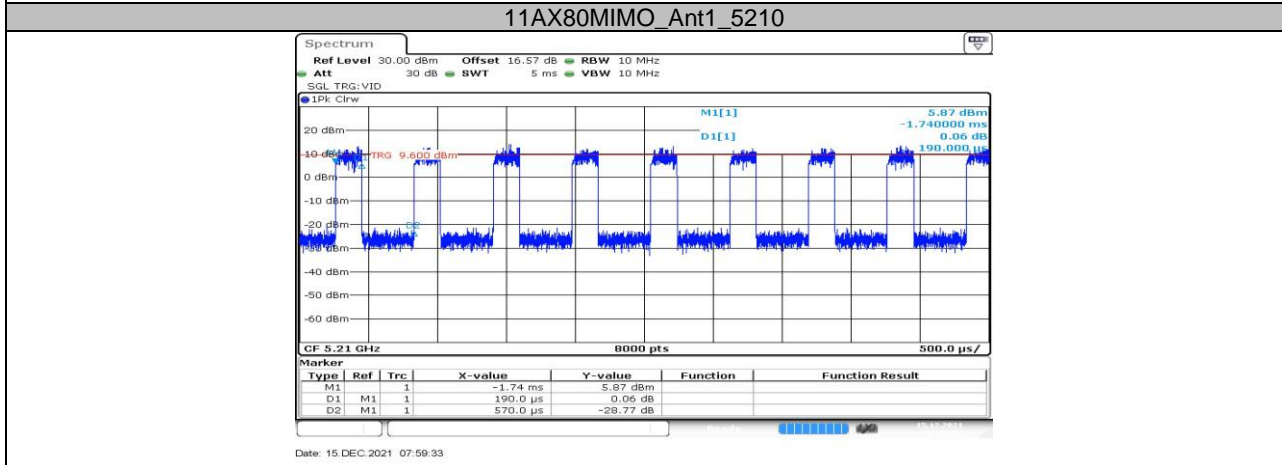
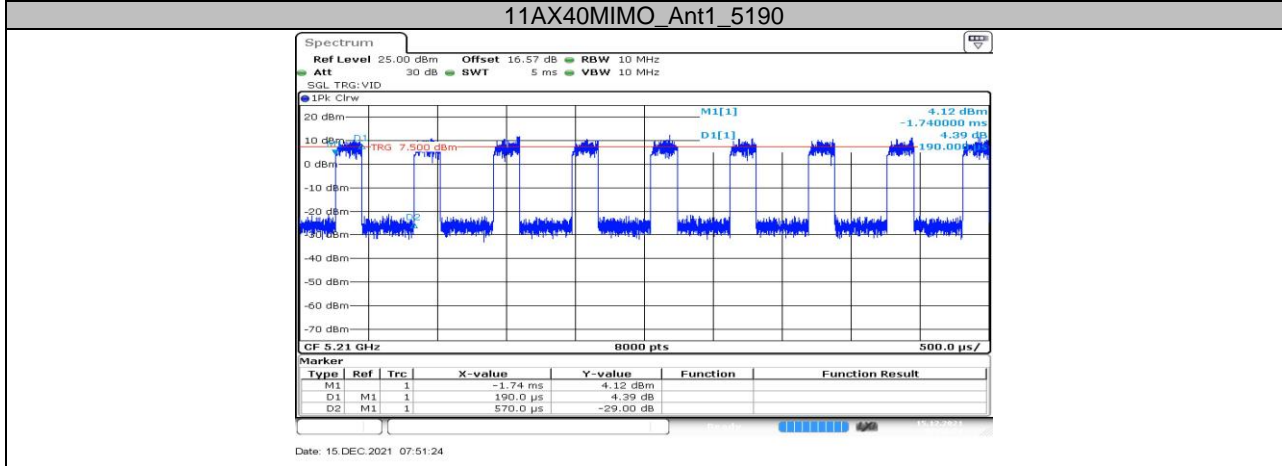
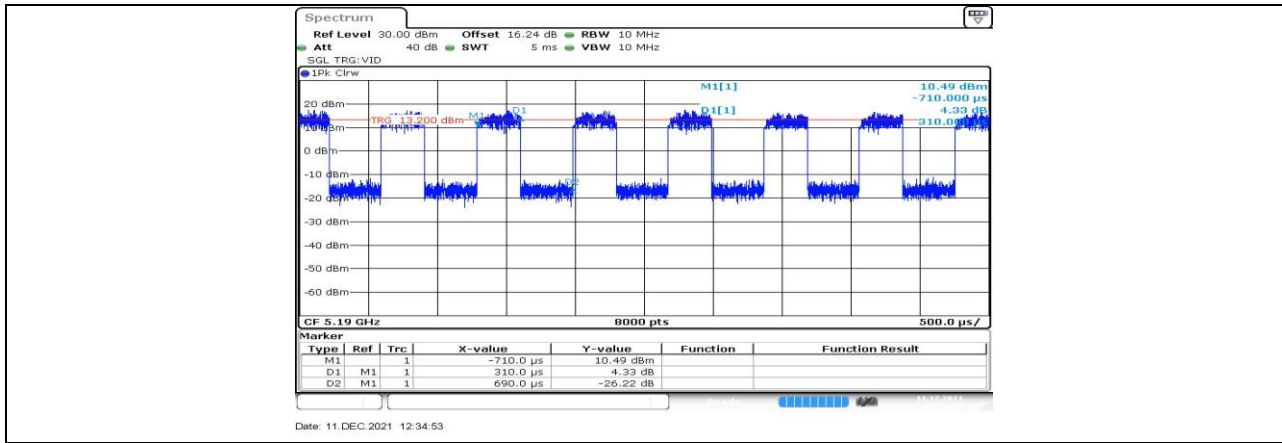
Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

12.6.2. Test Graphs







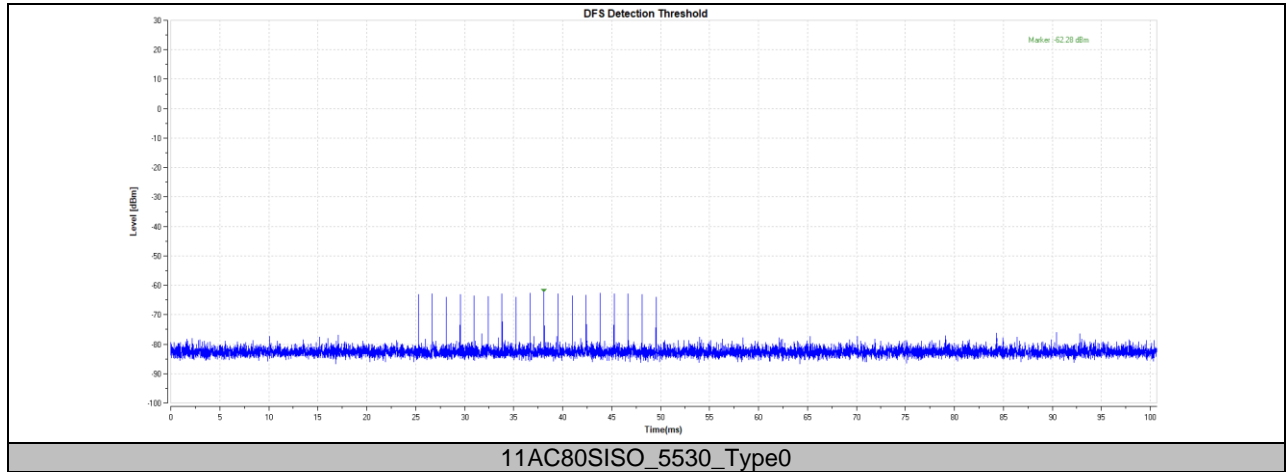


12.7. Appendix E: DFS Detection Thresholds

12.7.1. Test Result

Test Mode	Channel	Radar Type	Result	Limit[dbm]	Verdict
11AC80SISO	5530	Type0	-62.28	-54.99	PASS

12.7.2. Test Graphs





12.8. Appendix F: Channel Move Time and Channel Closing Transmission Time

12.8.1. Test Result

Test Mode	Channel	CCT[ms]	Limit[ms]	CMT[ms]	Limit[ms]	Verdict
11AC80SISO	5530	200+5.2	200+60	478.1	10000	PASS