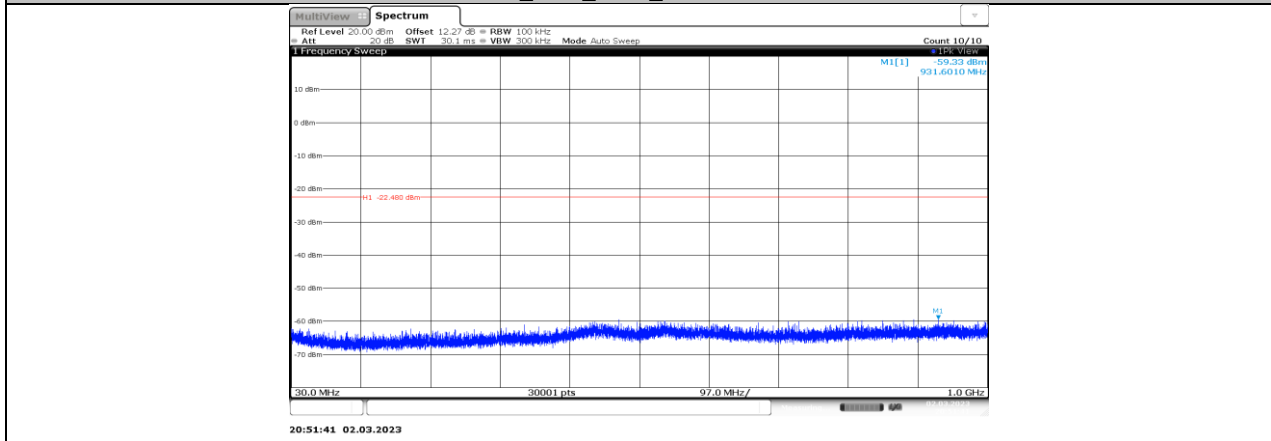
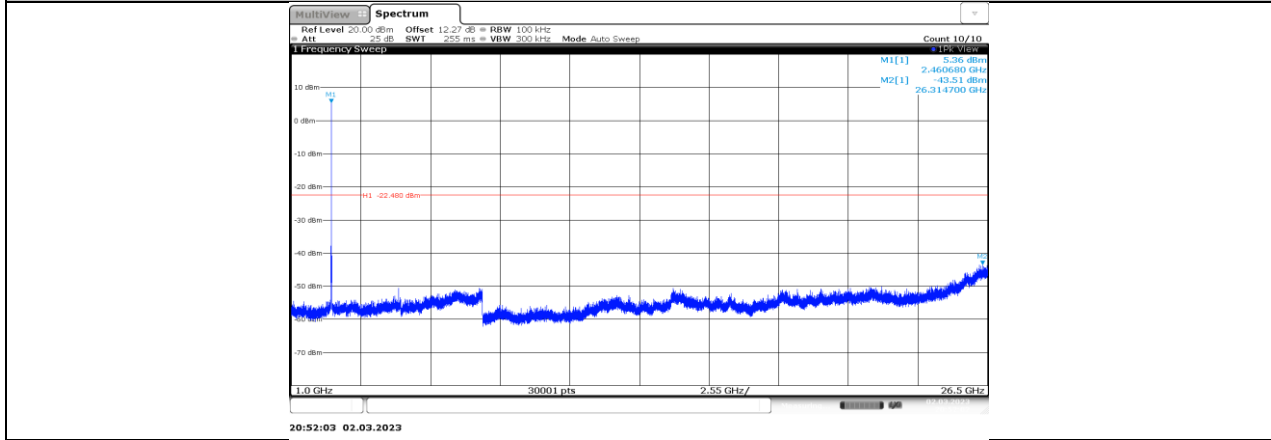


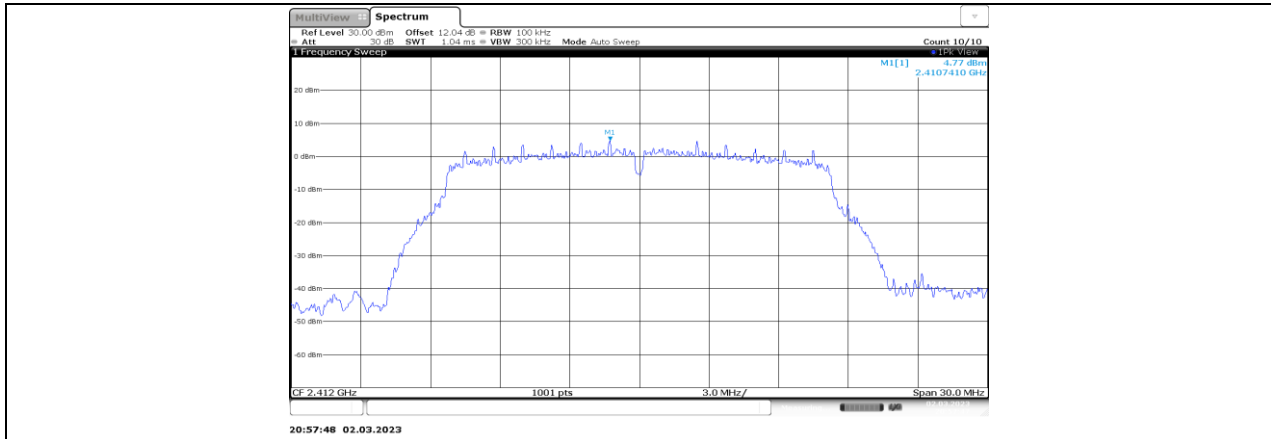
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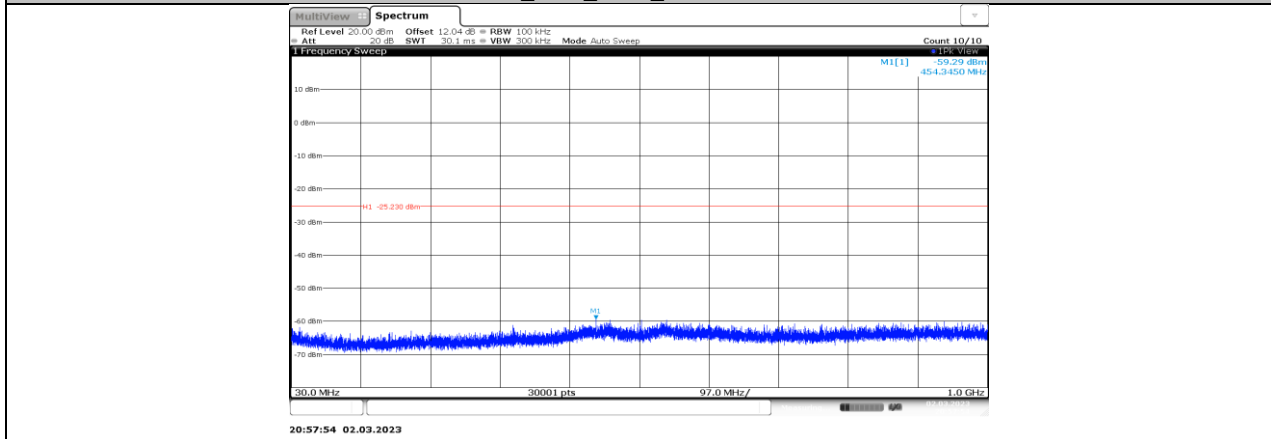
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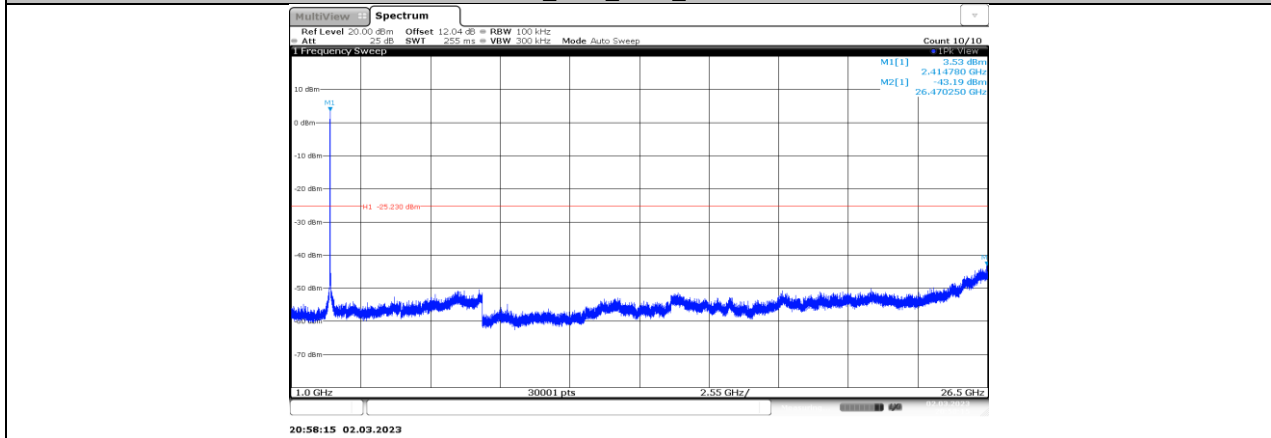
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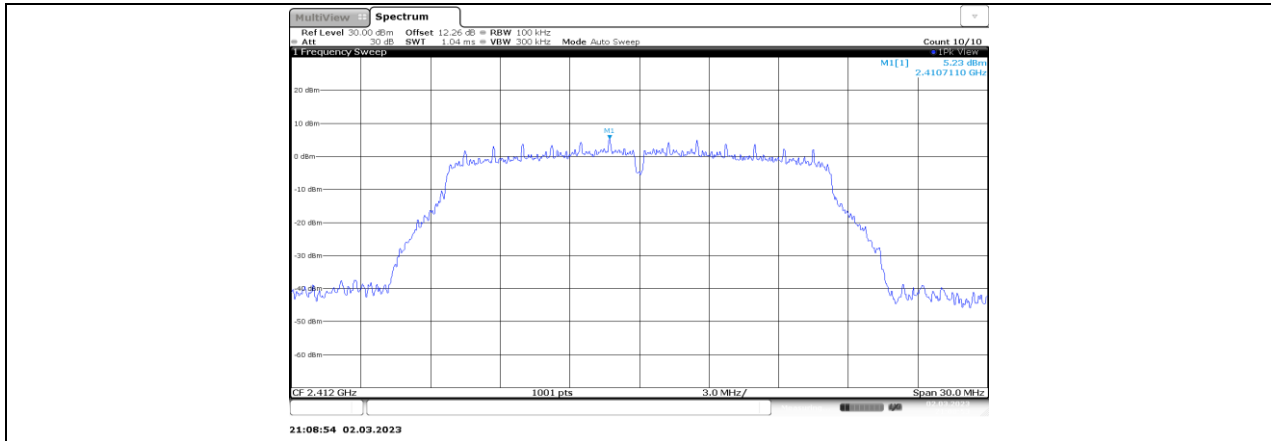
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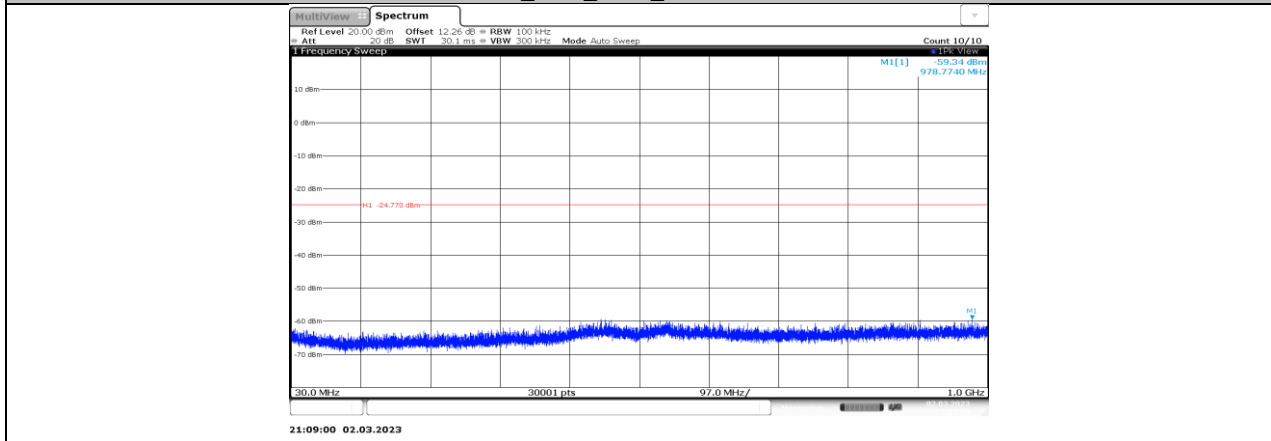
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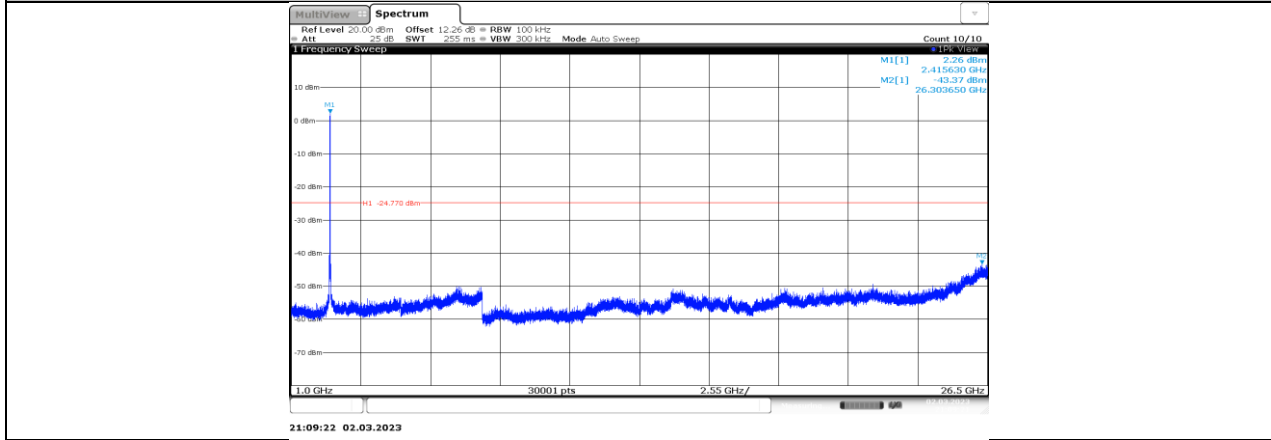
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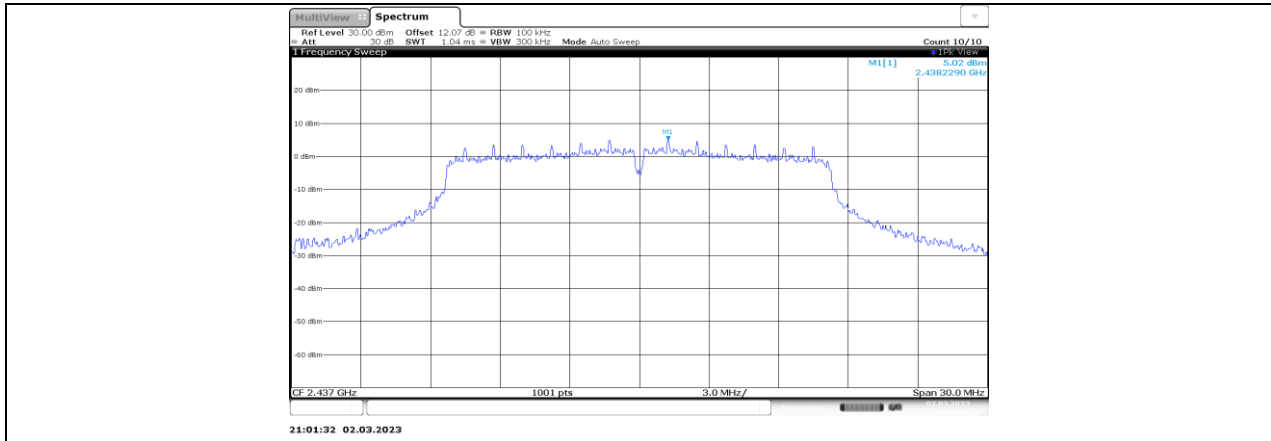
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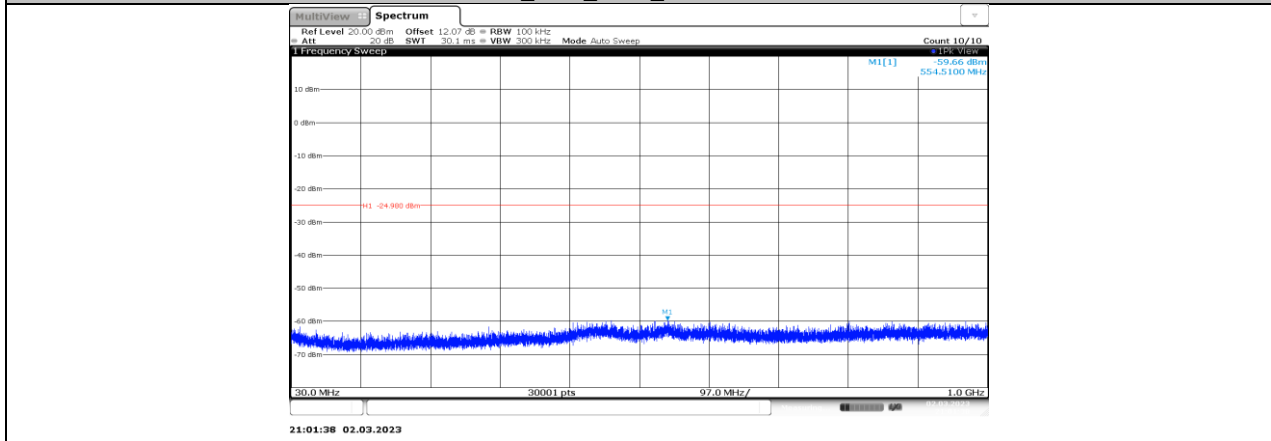
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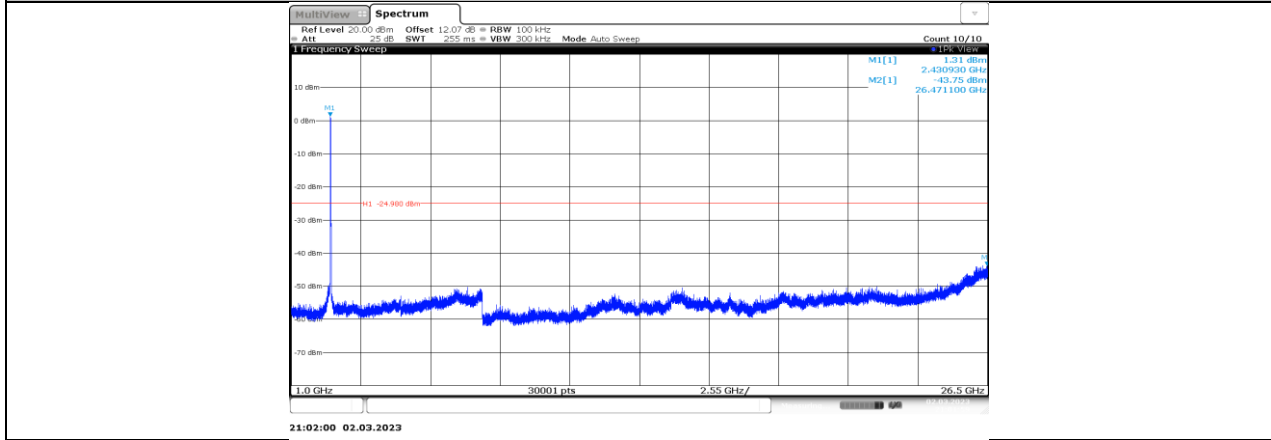
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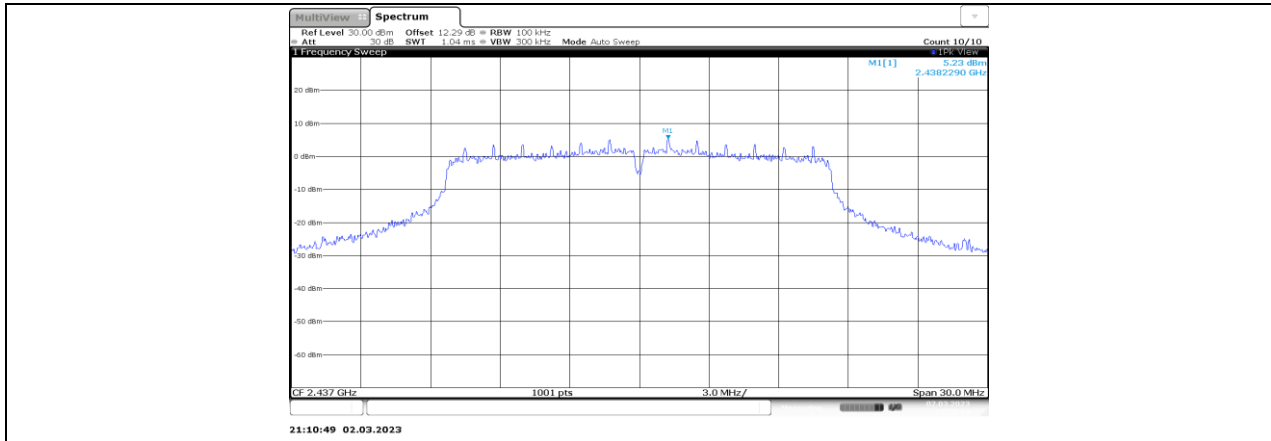
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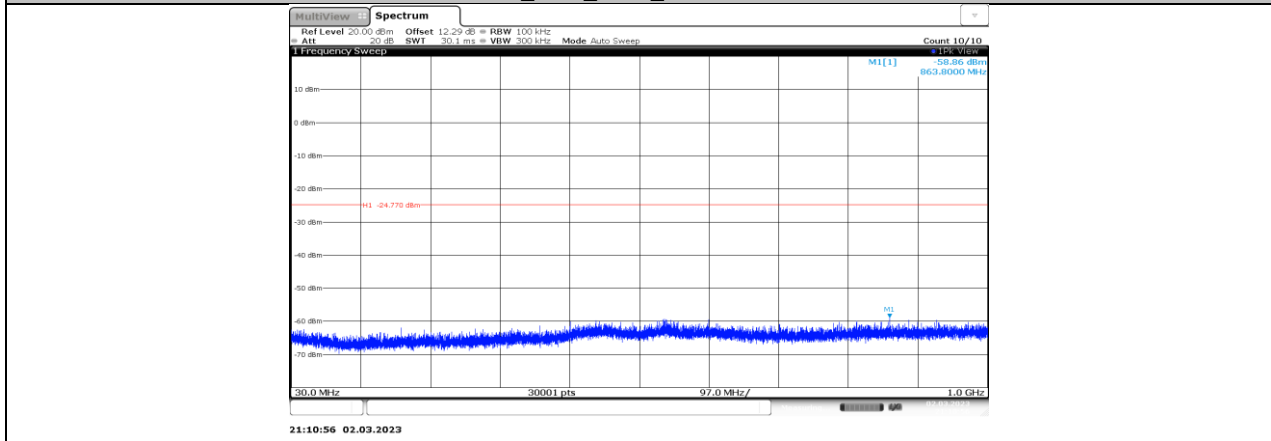
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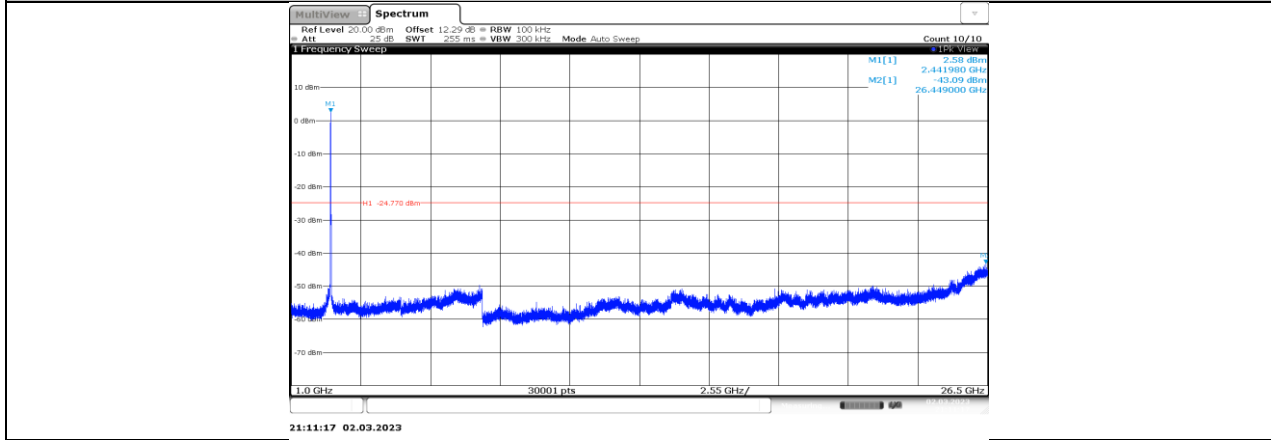
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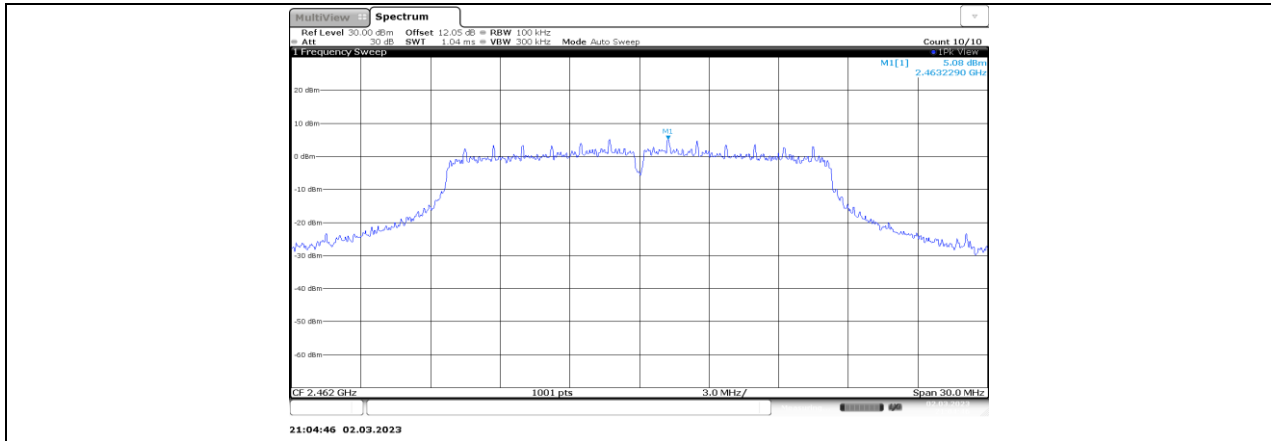
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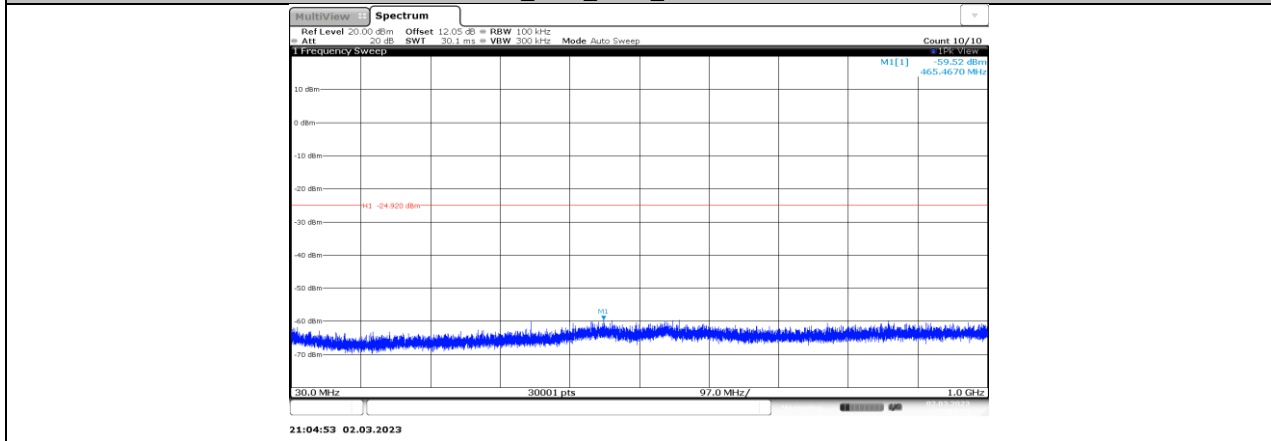
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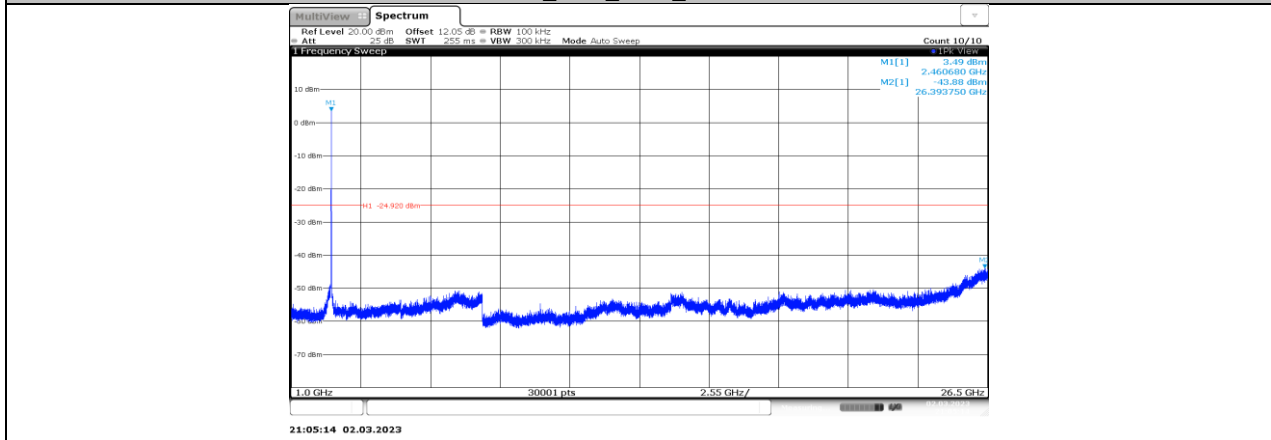
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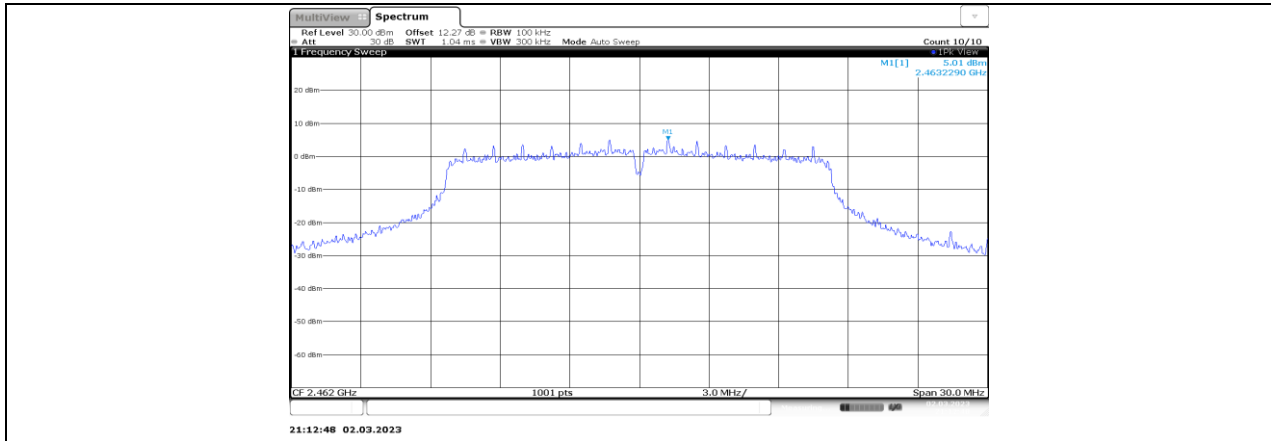
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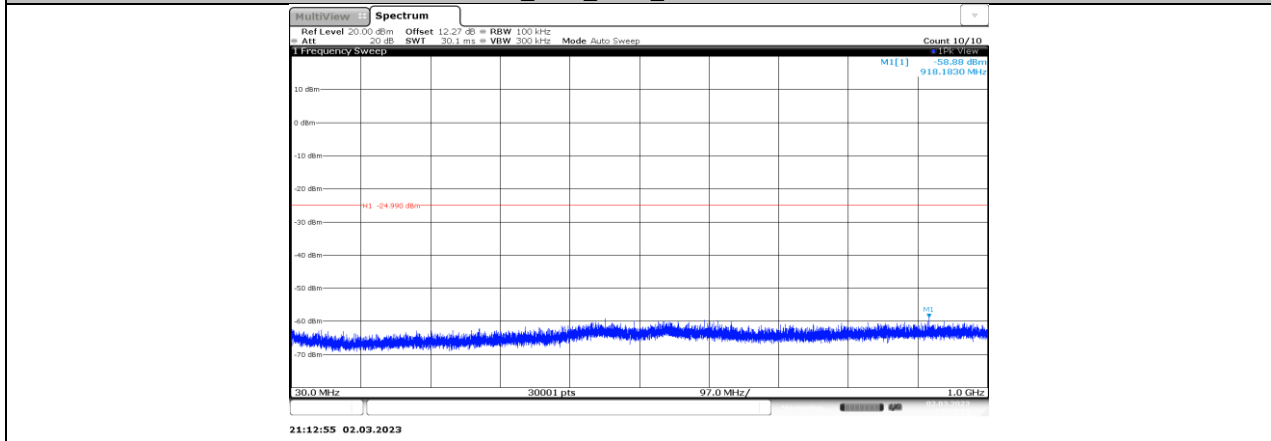
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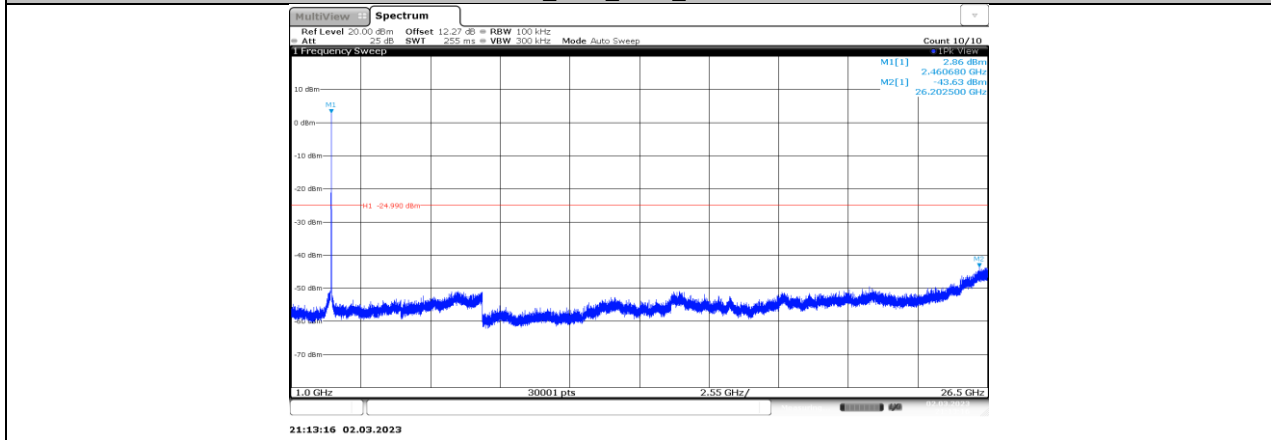
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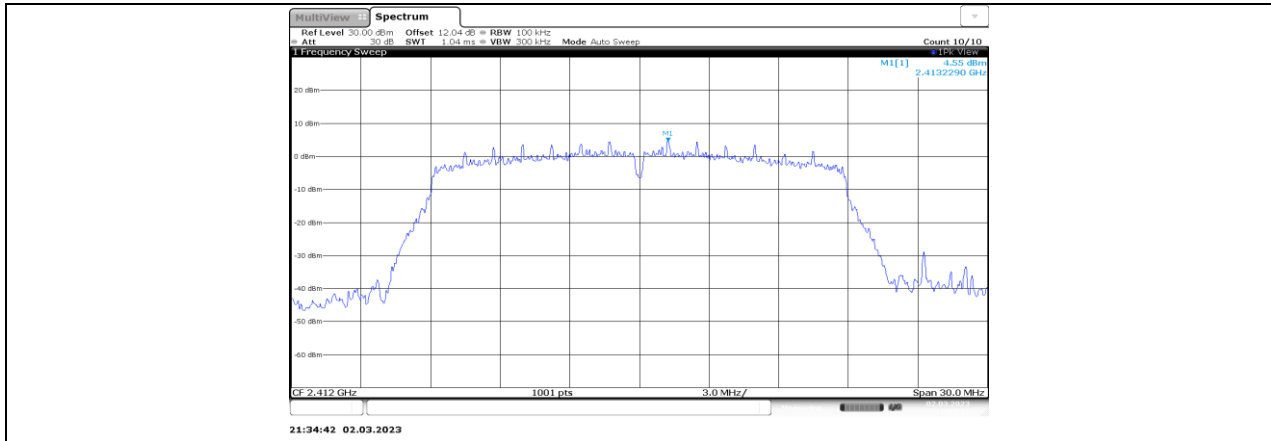
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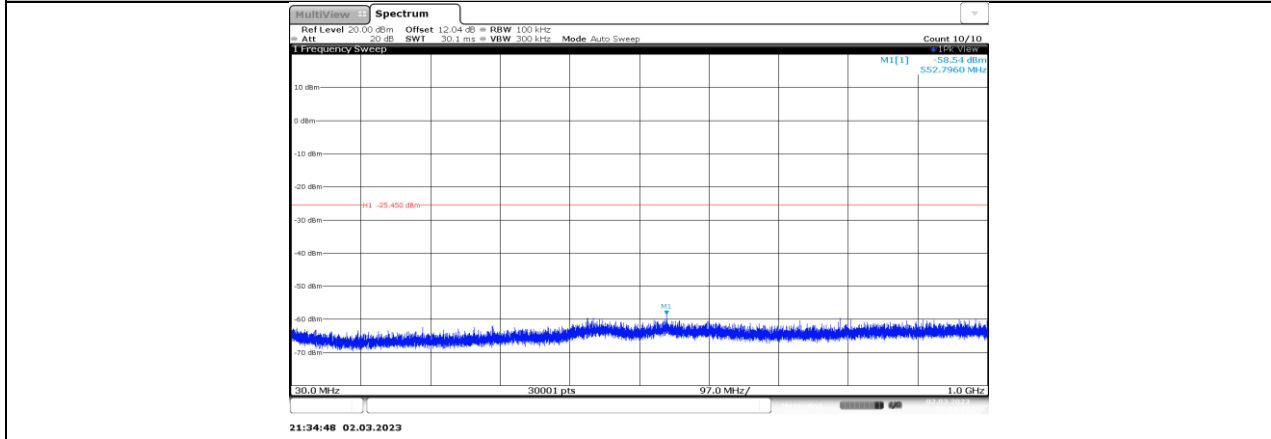
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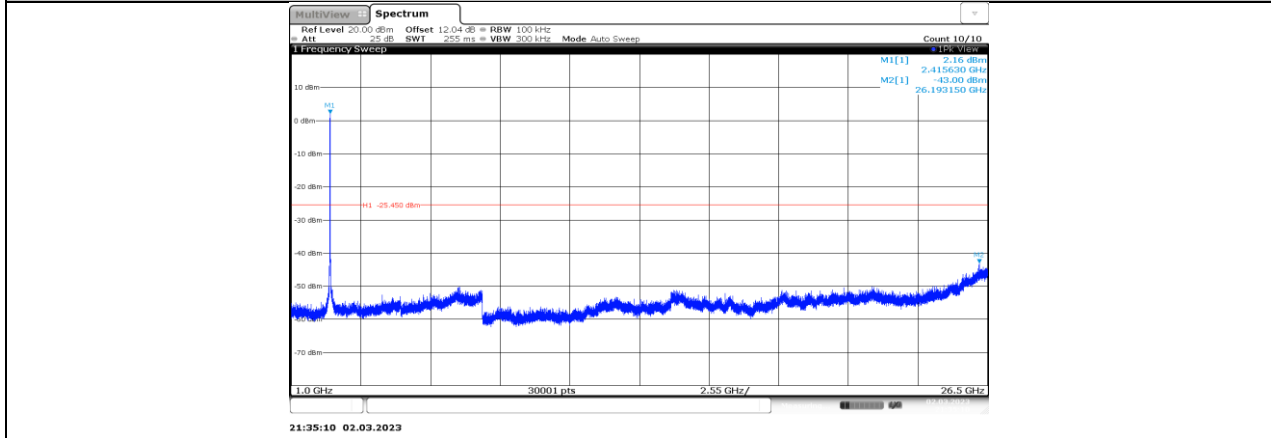
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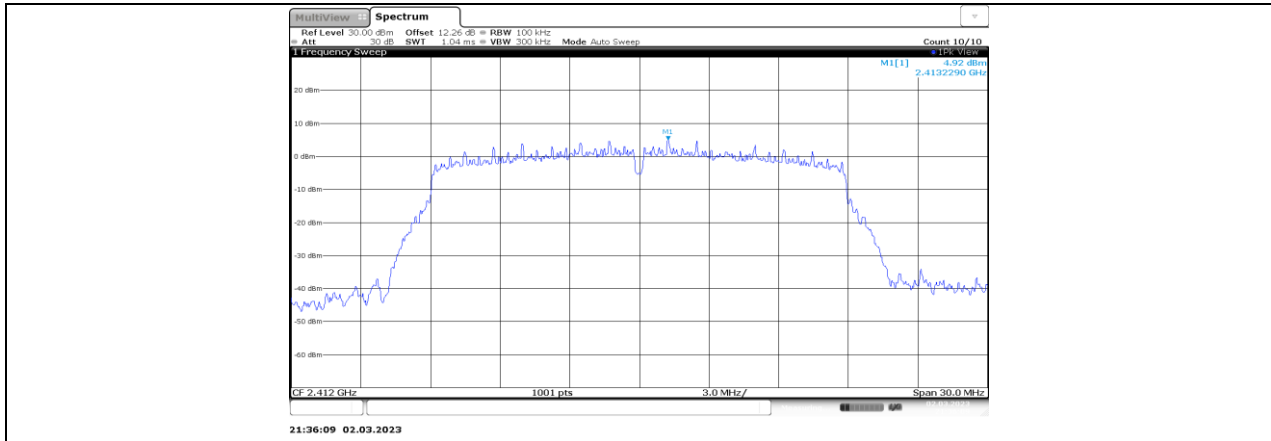
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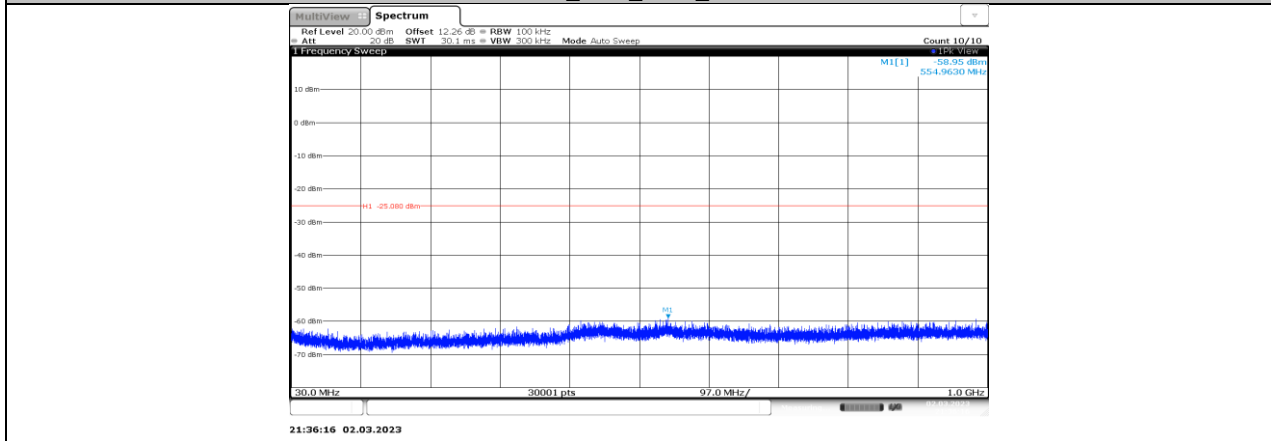
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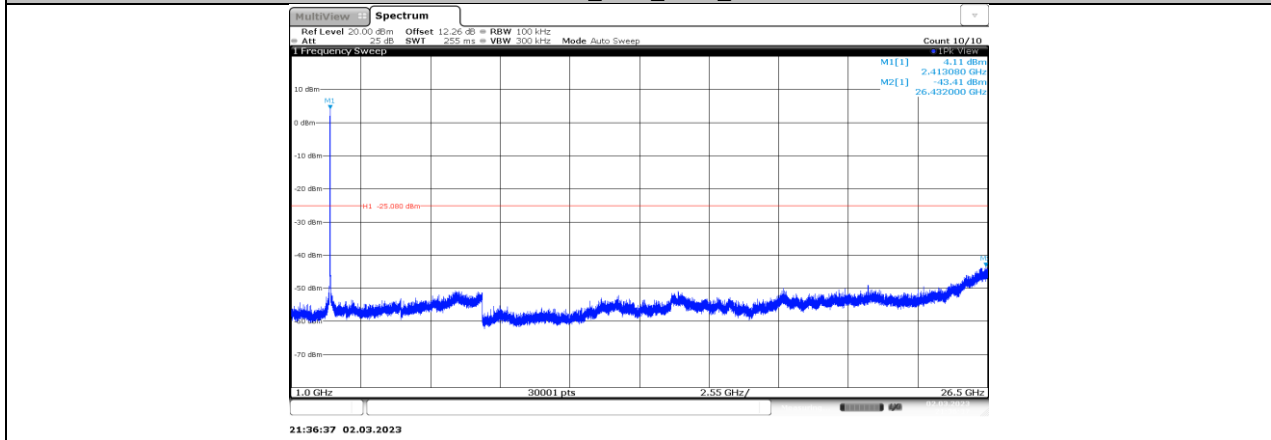
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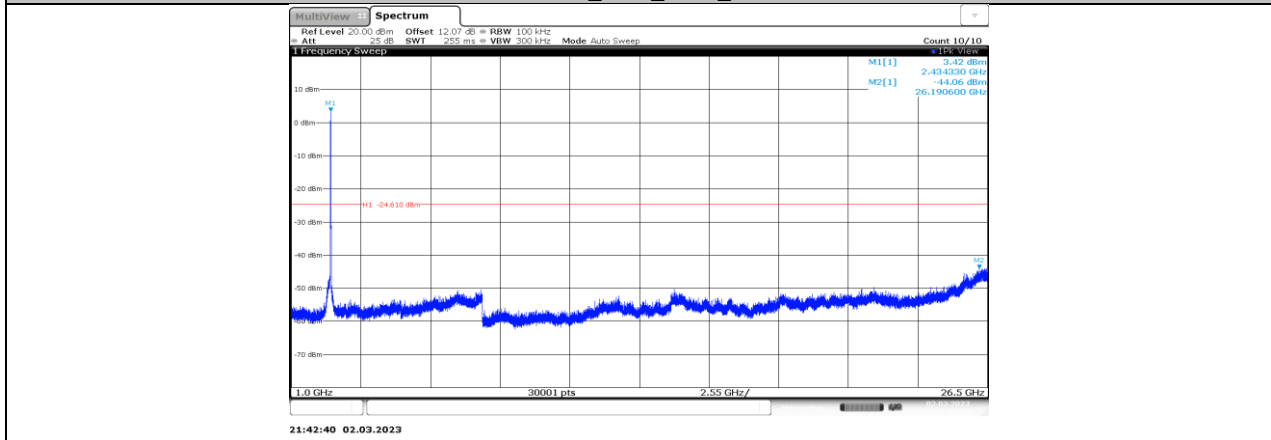
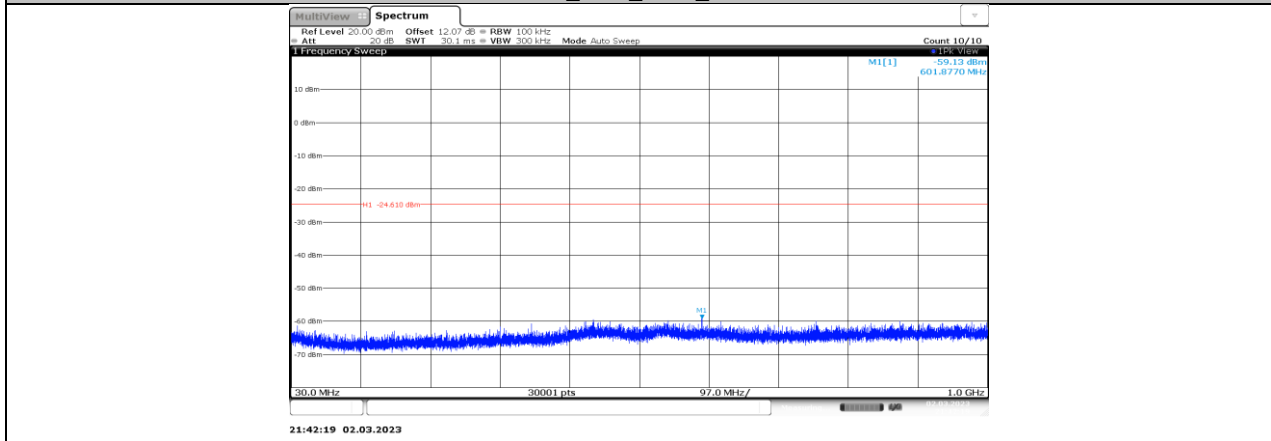
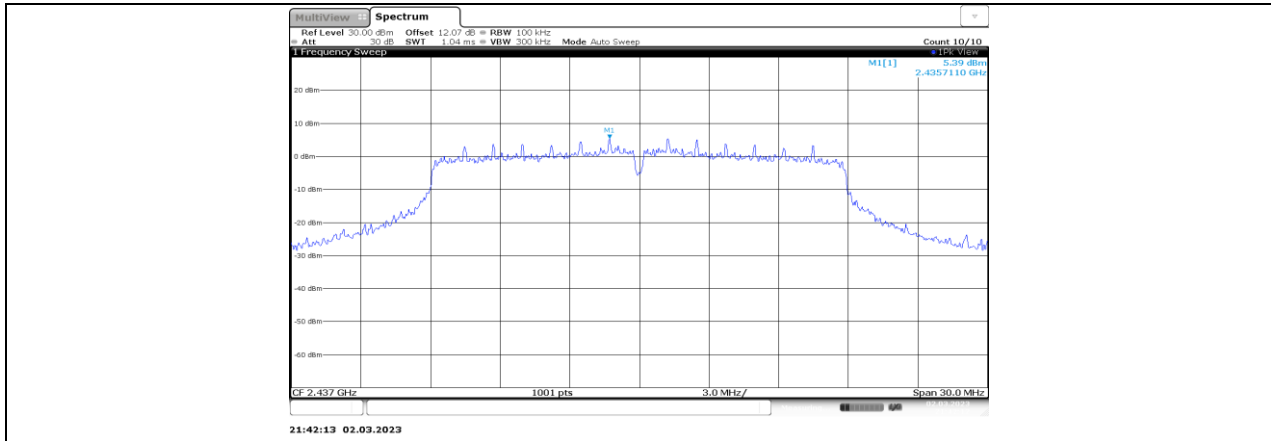
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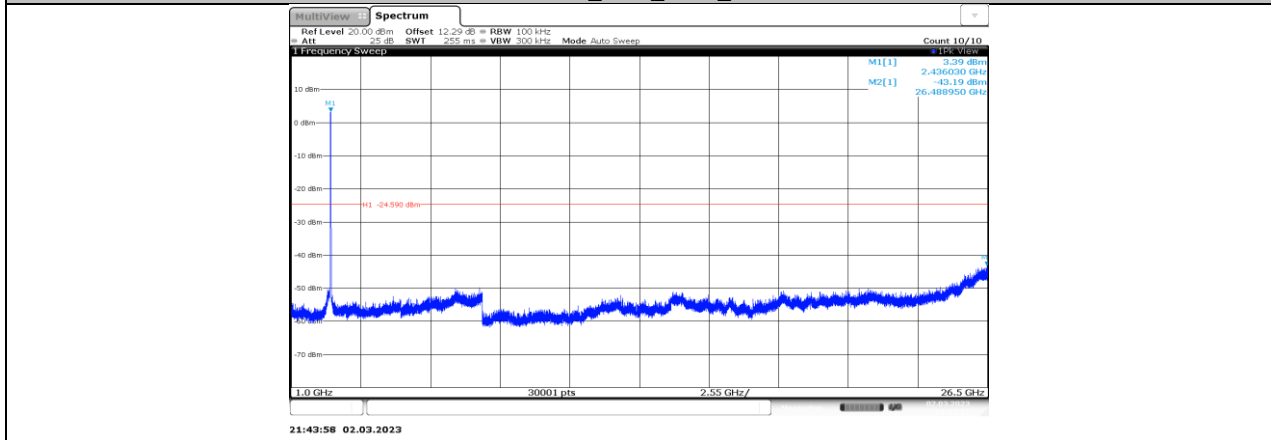
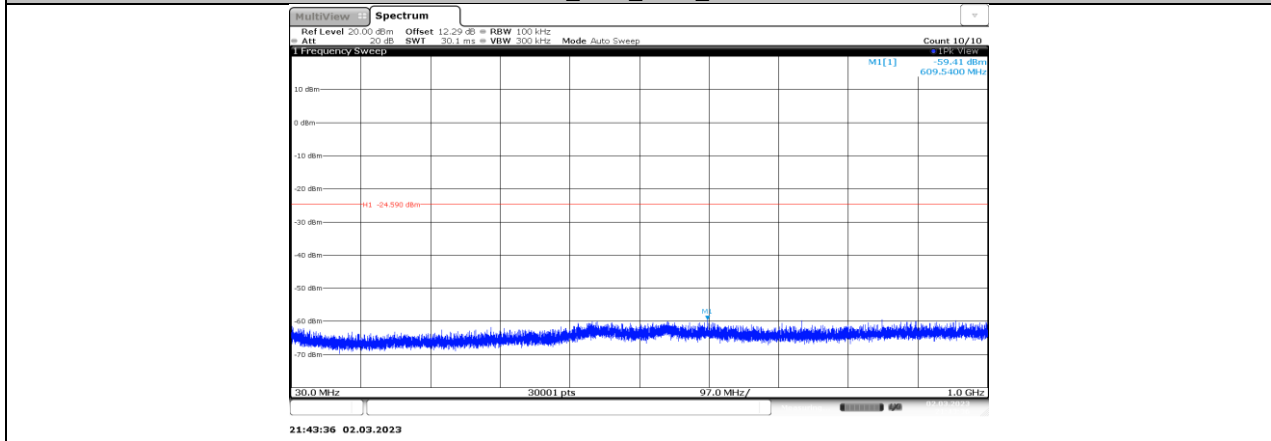
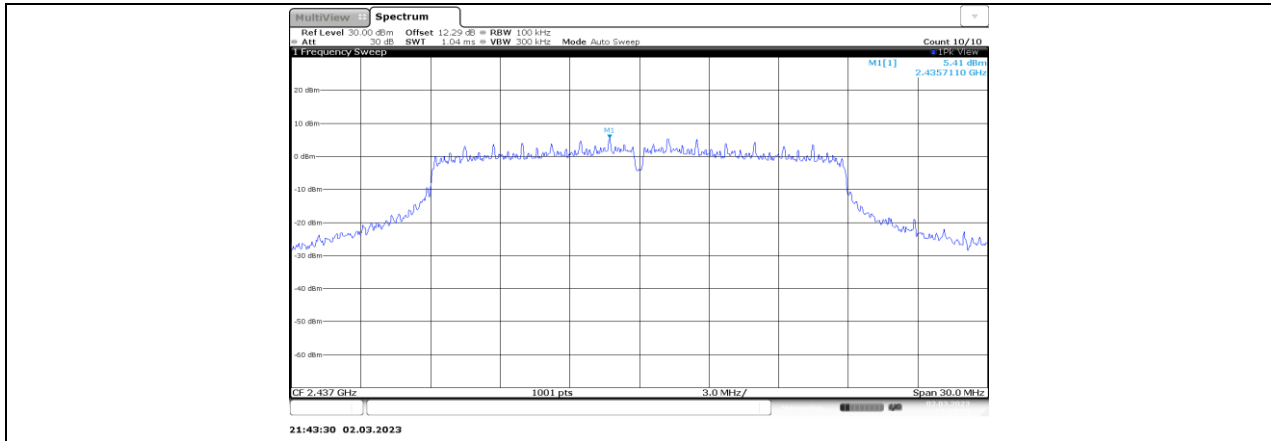


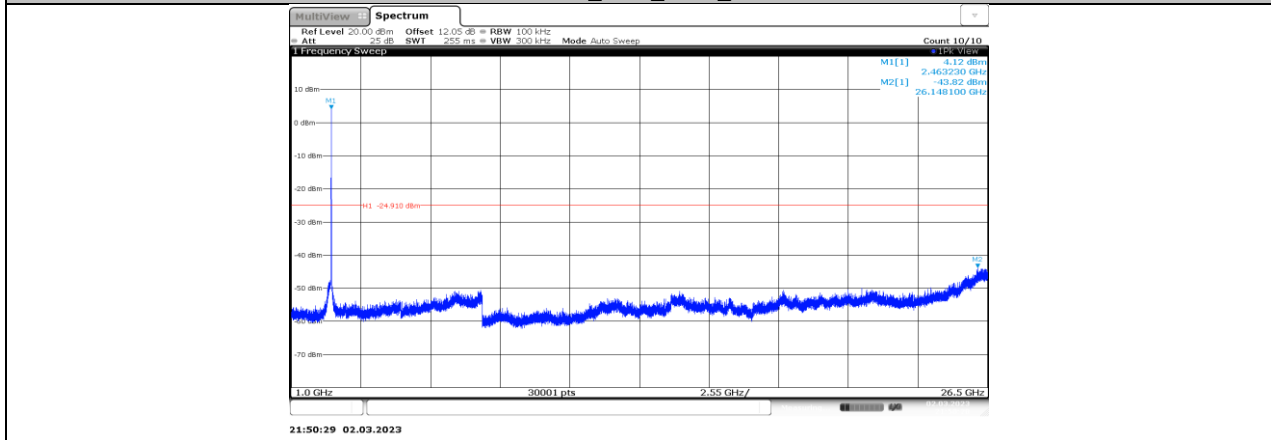
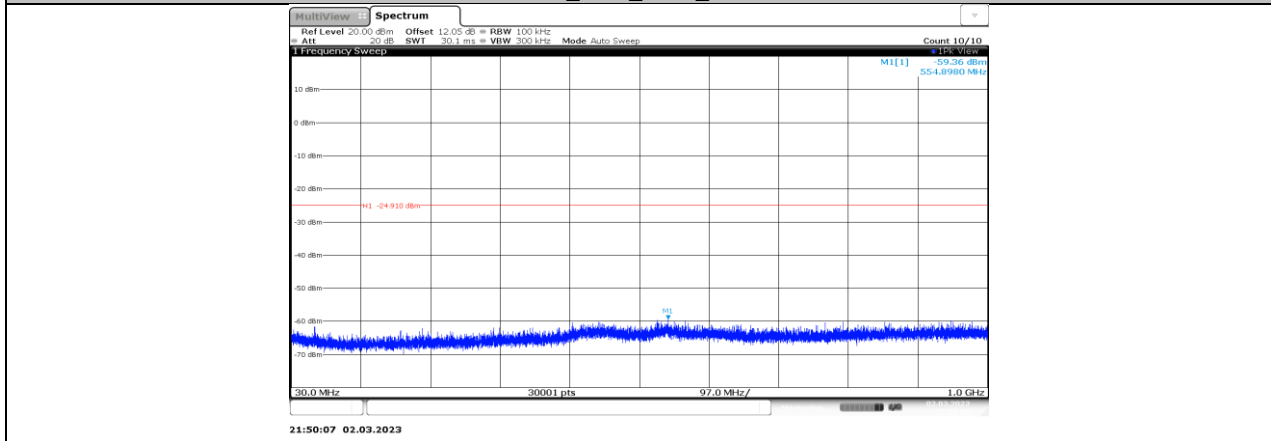
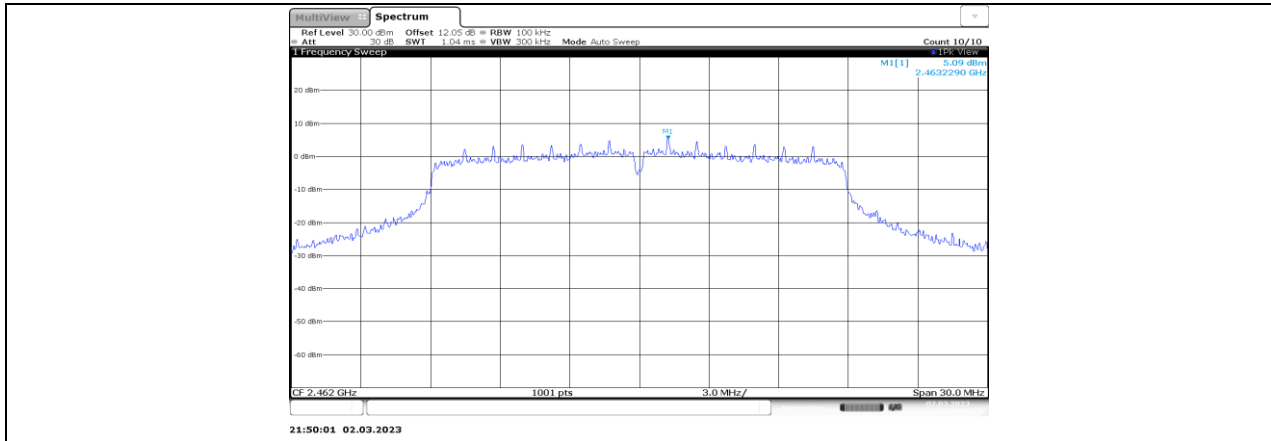
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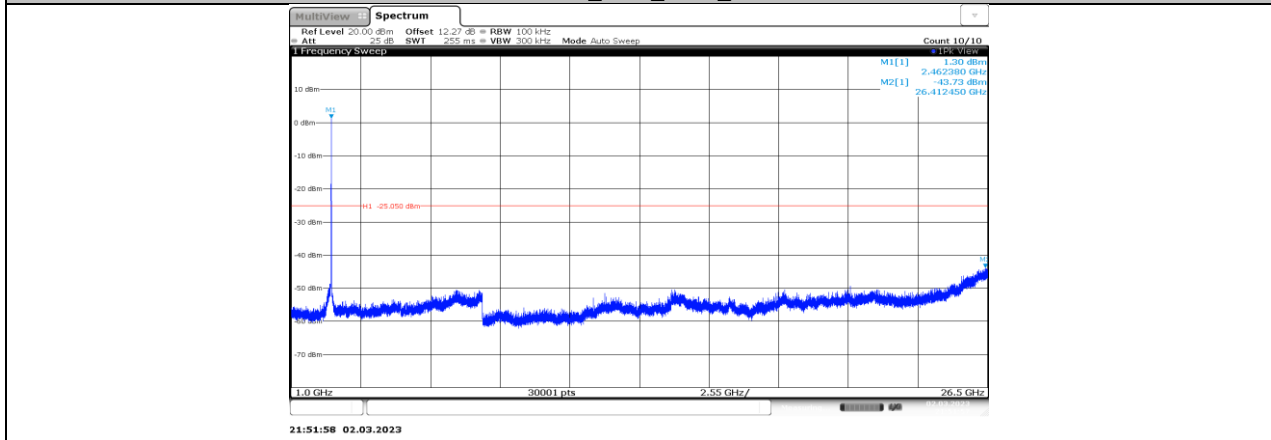
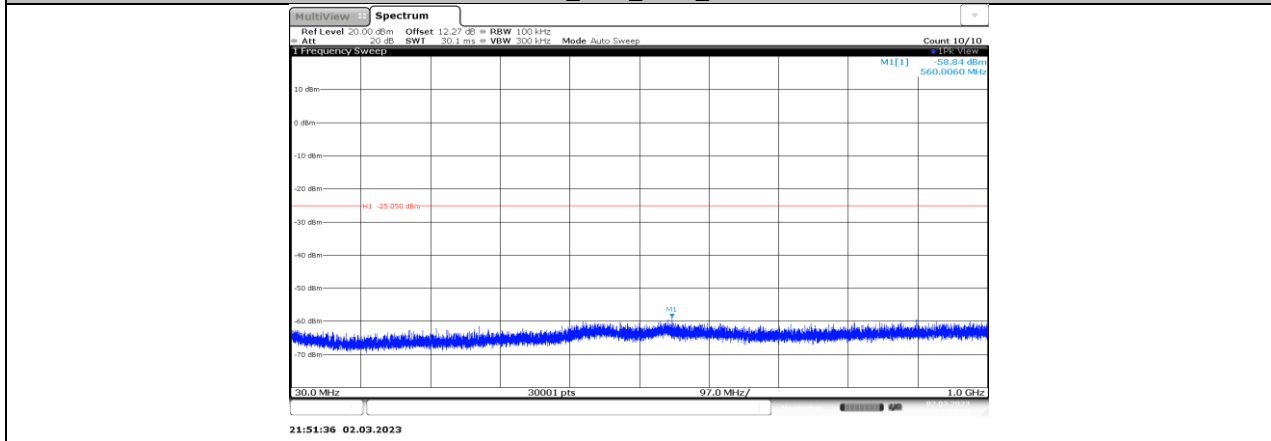
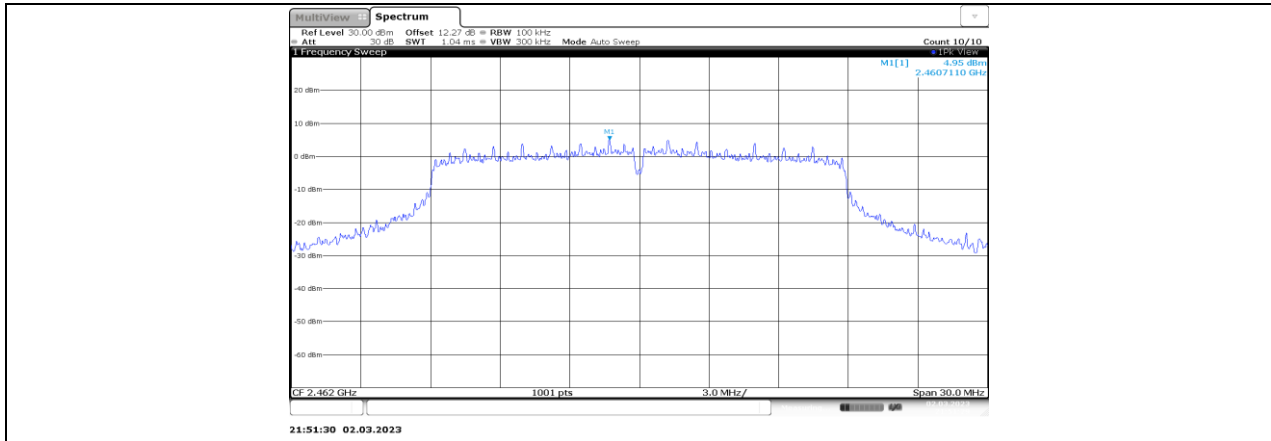


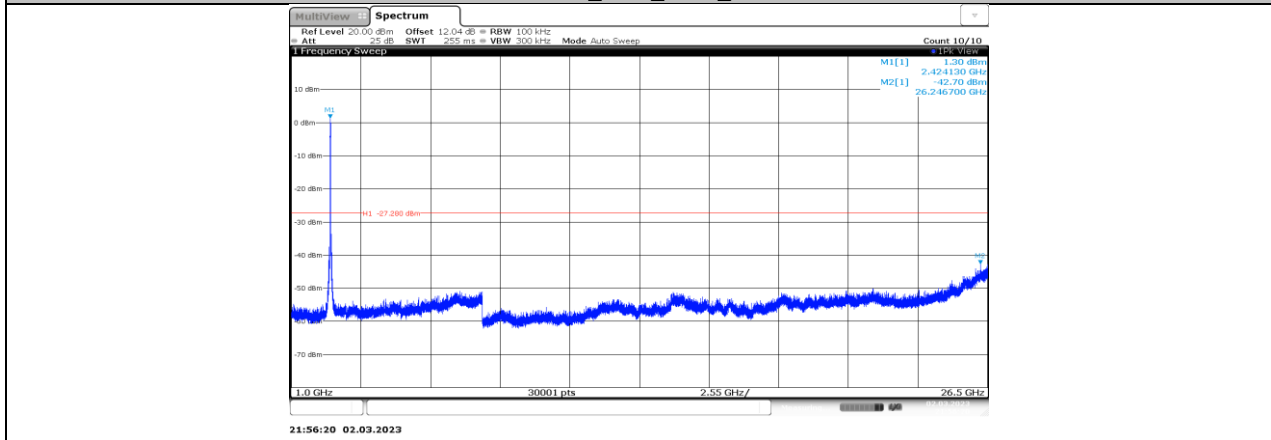
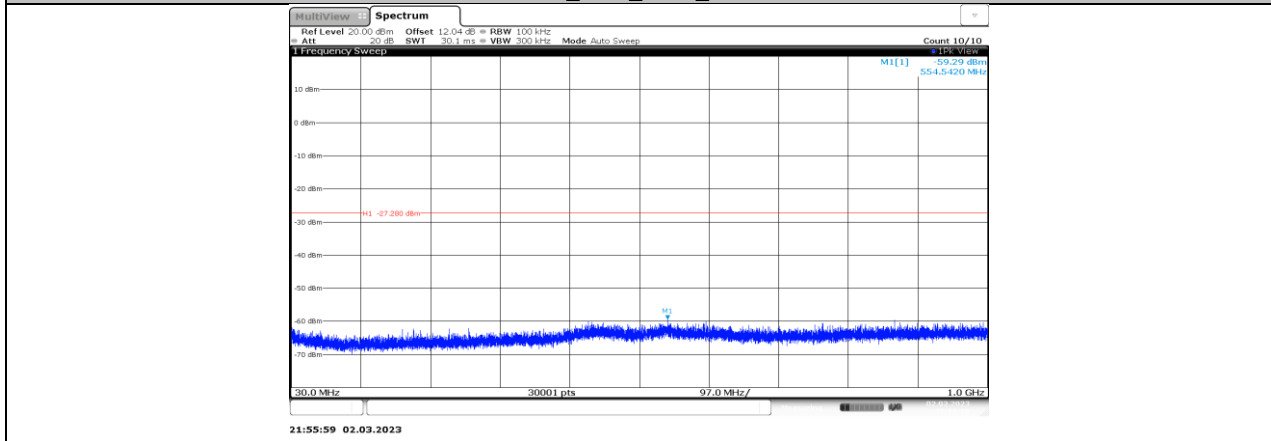
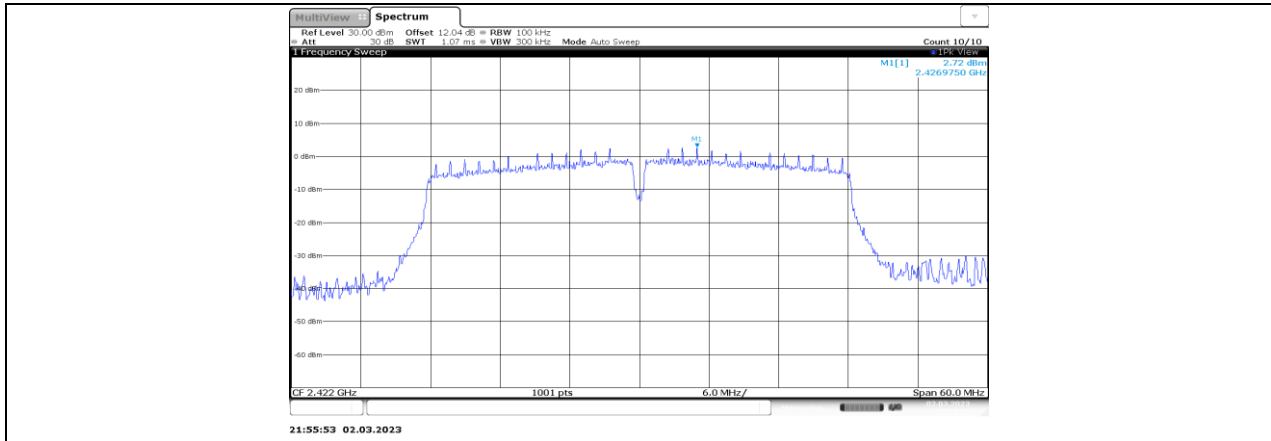
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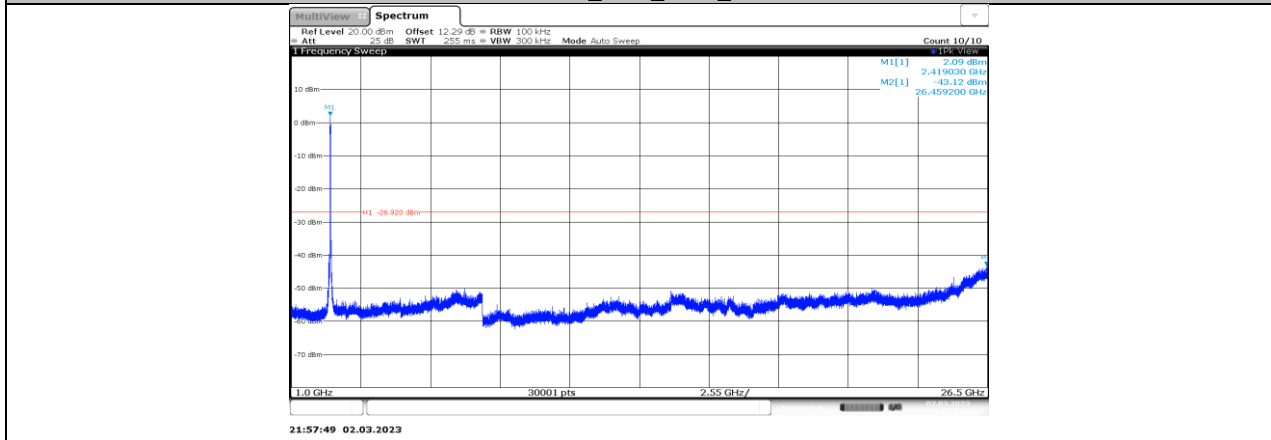
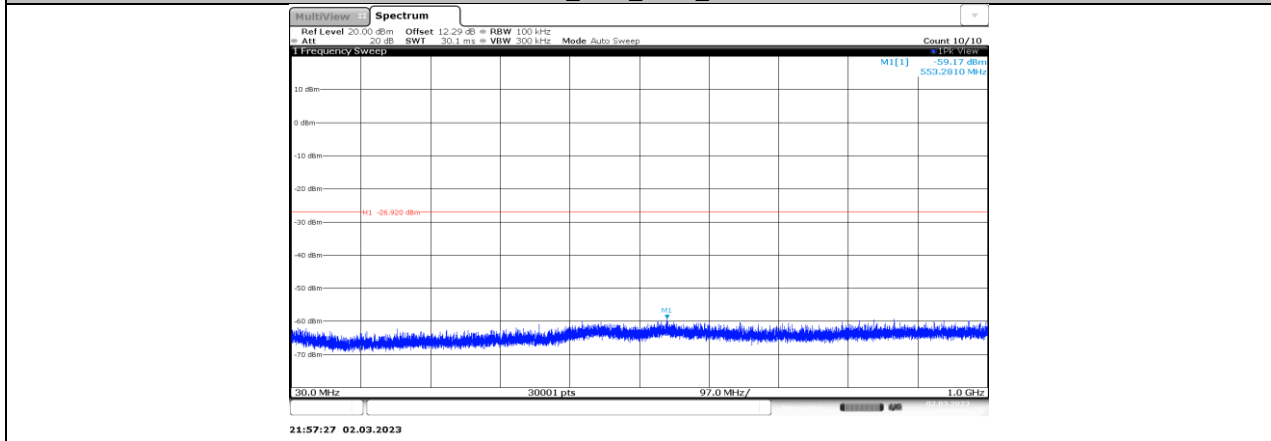
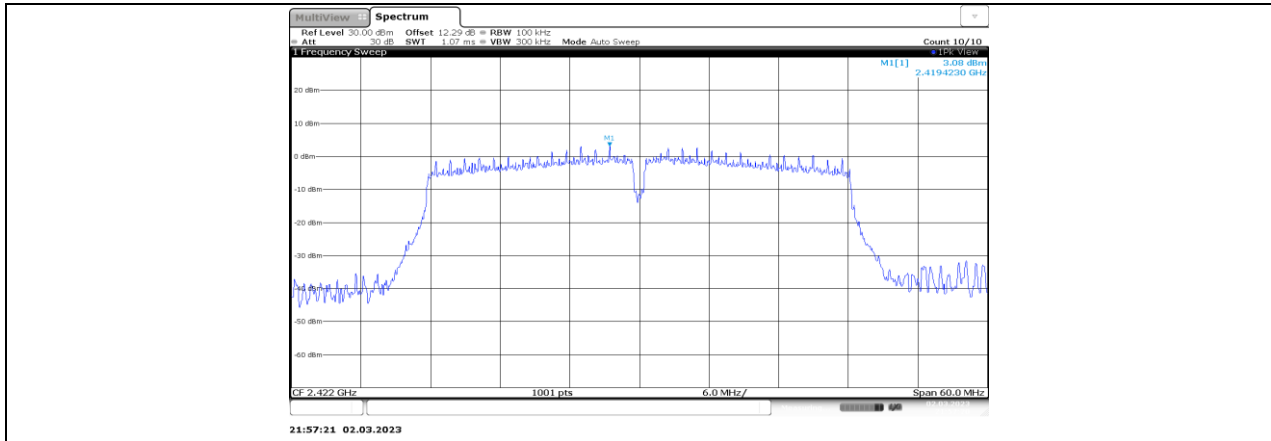


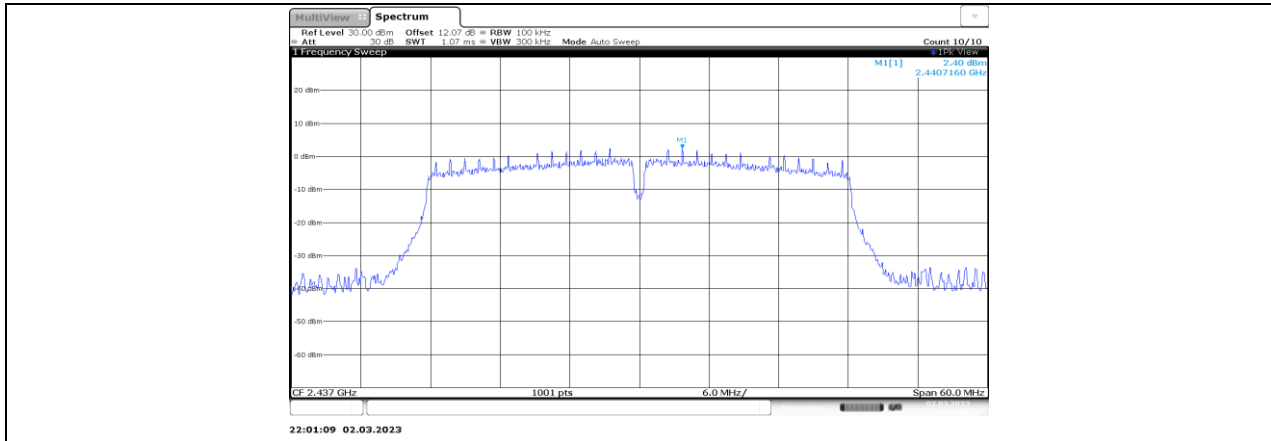




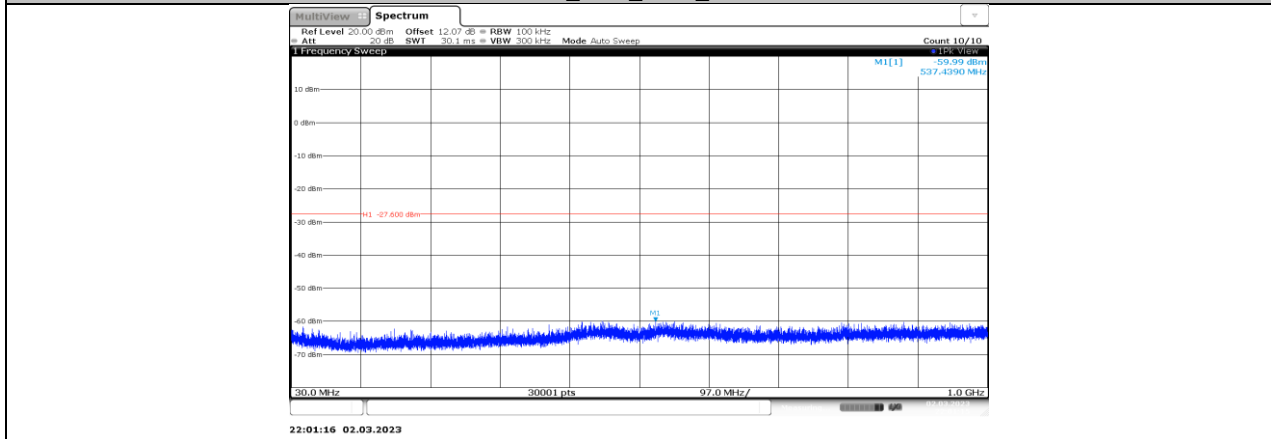




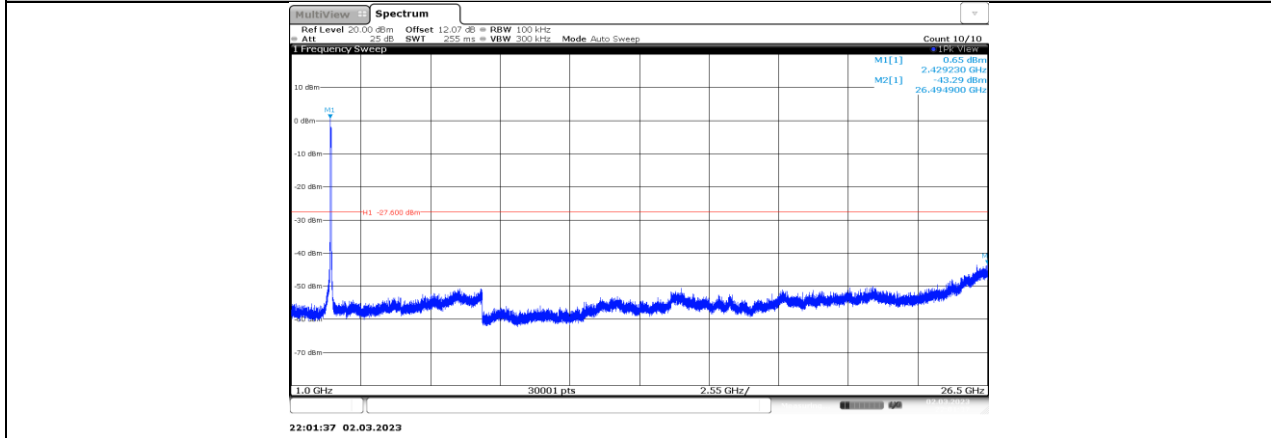




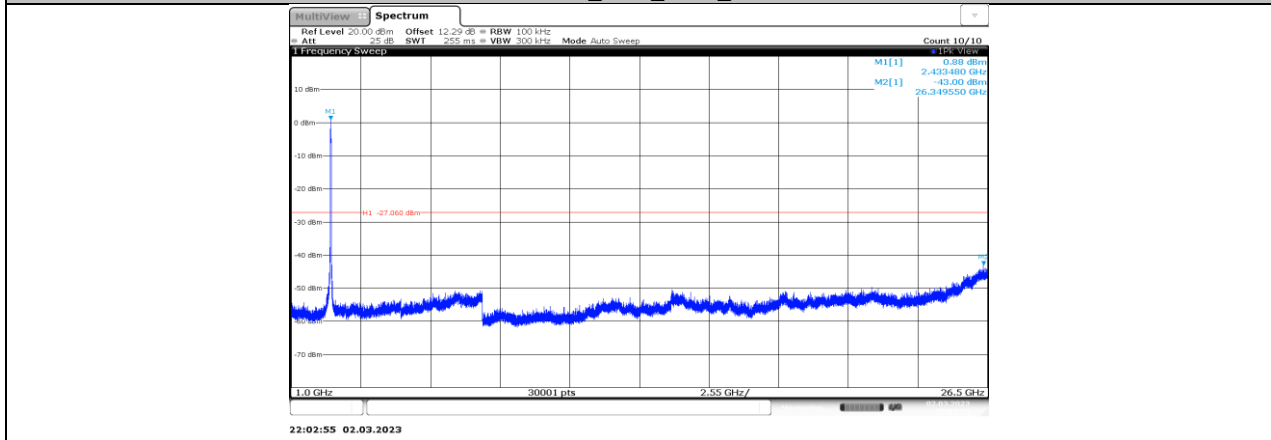
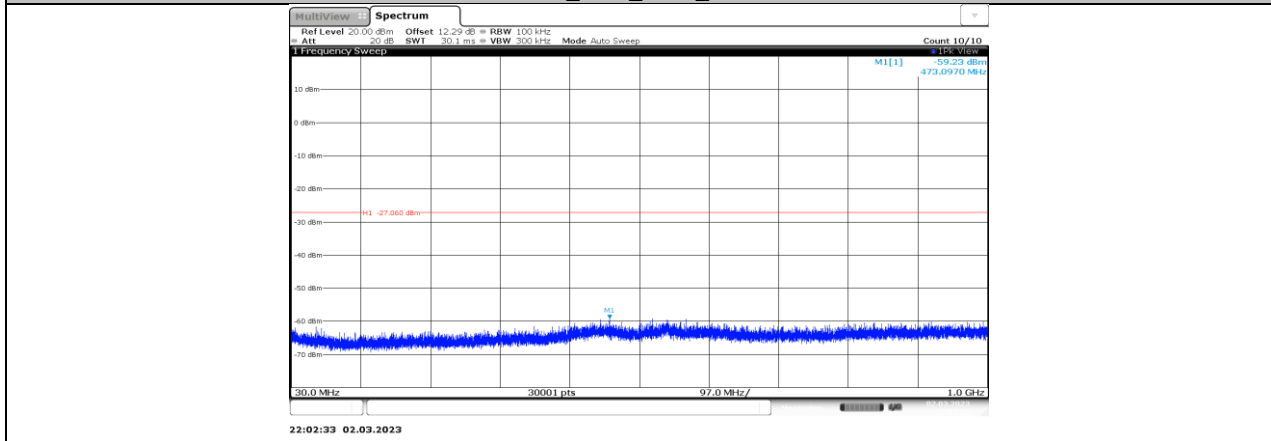
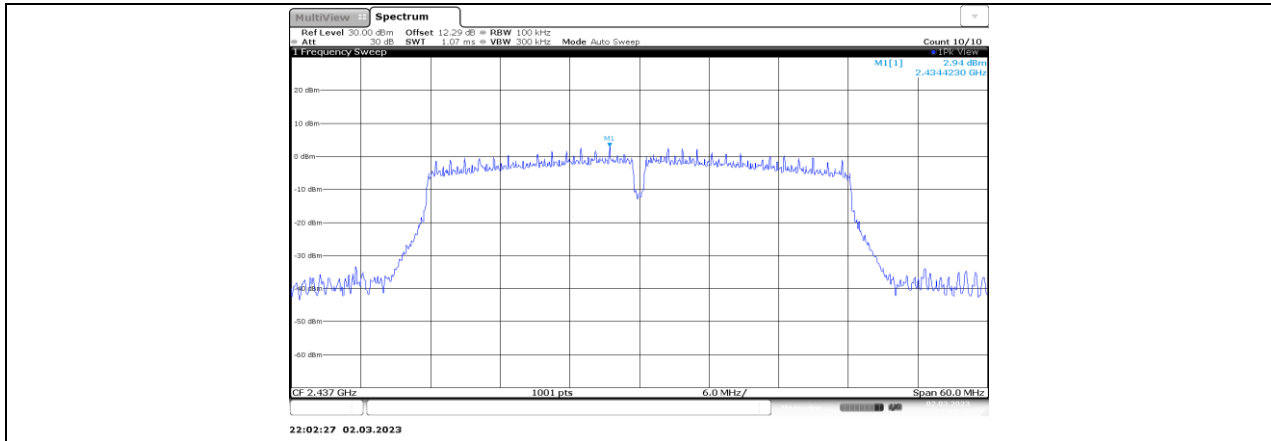
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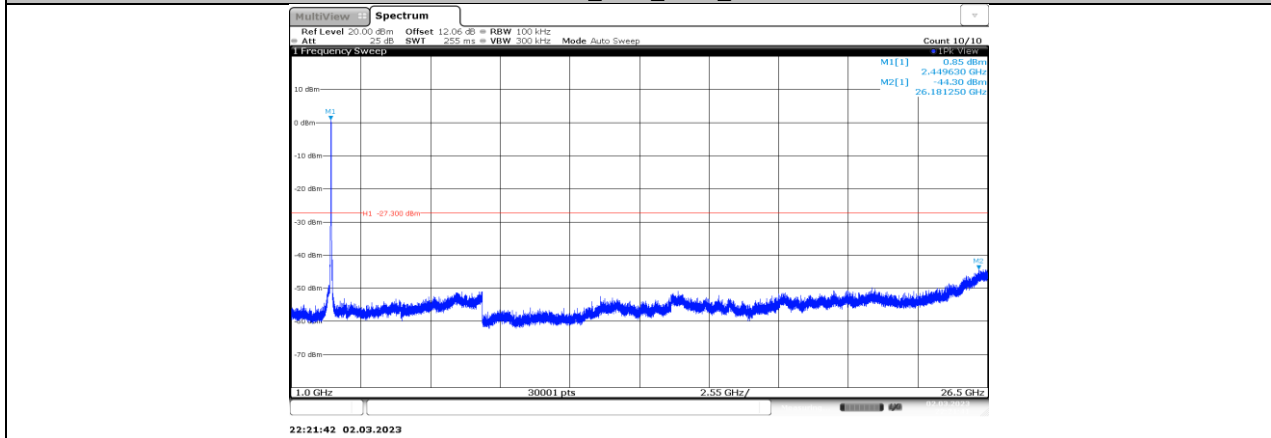
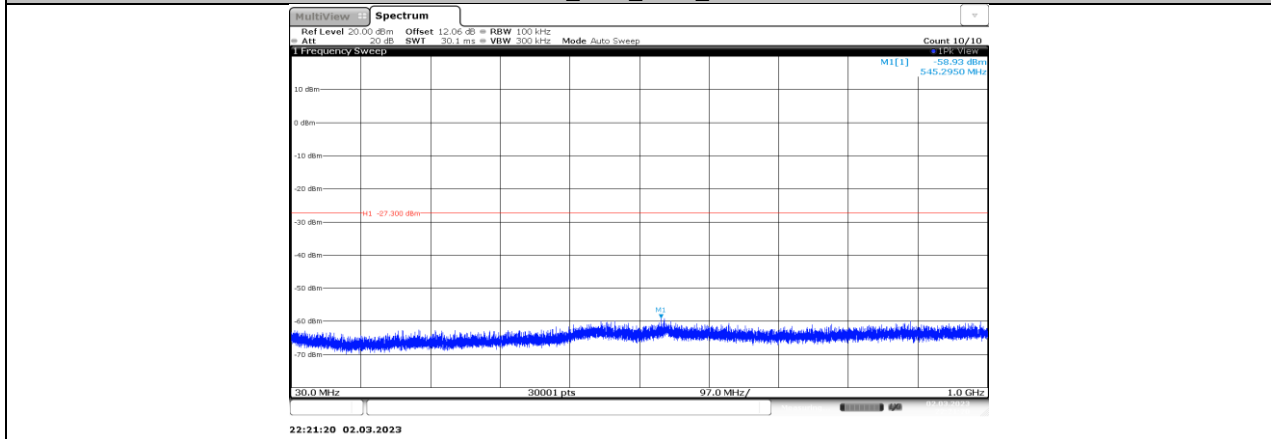
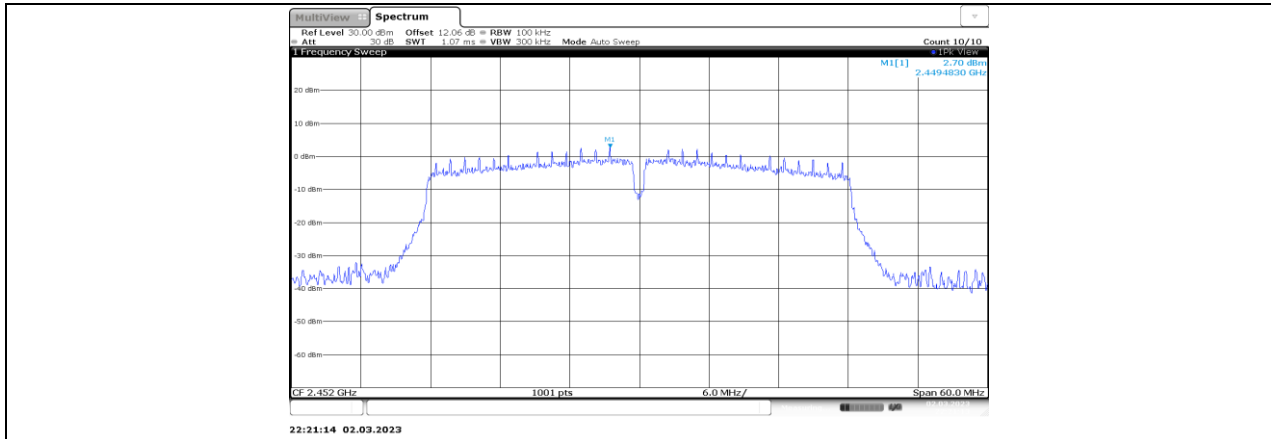


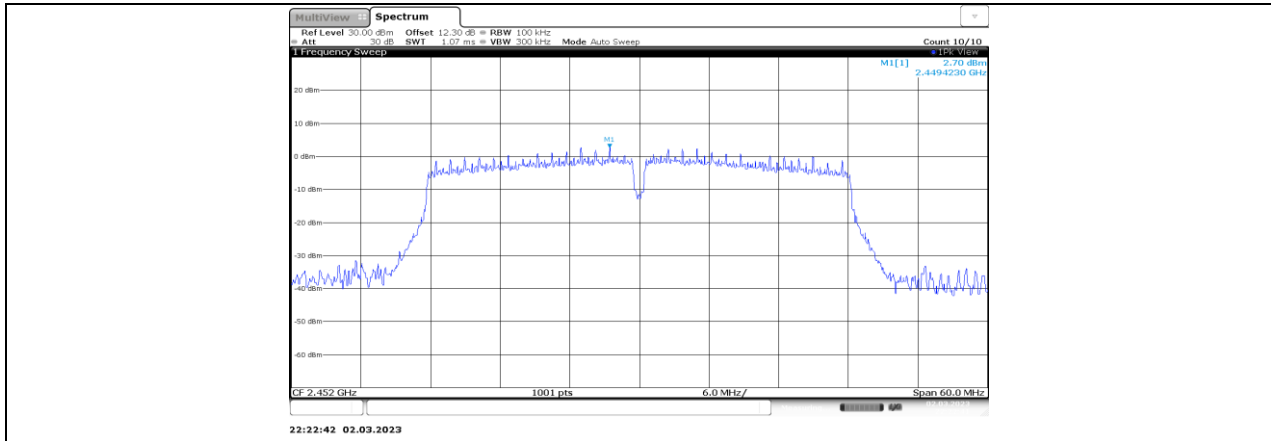
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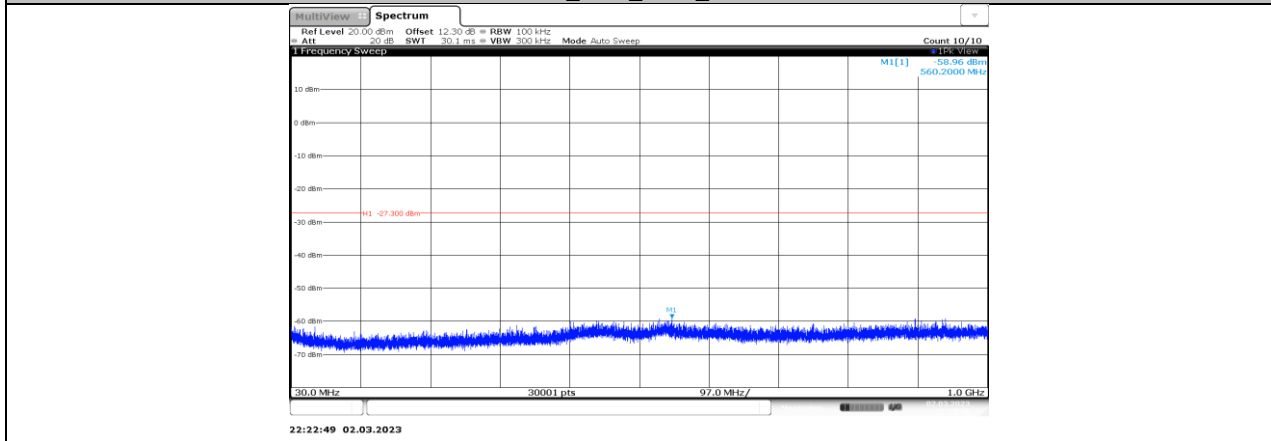
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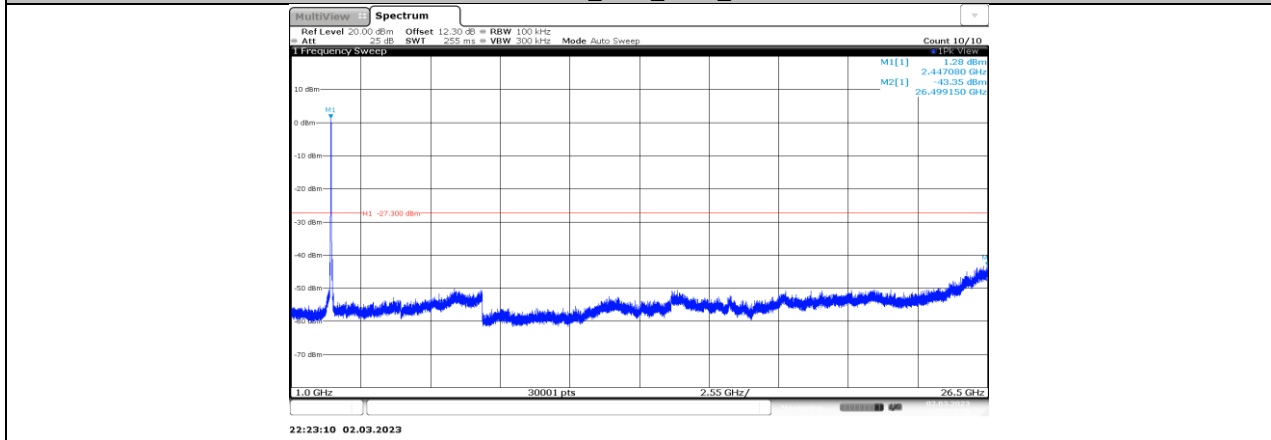




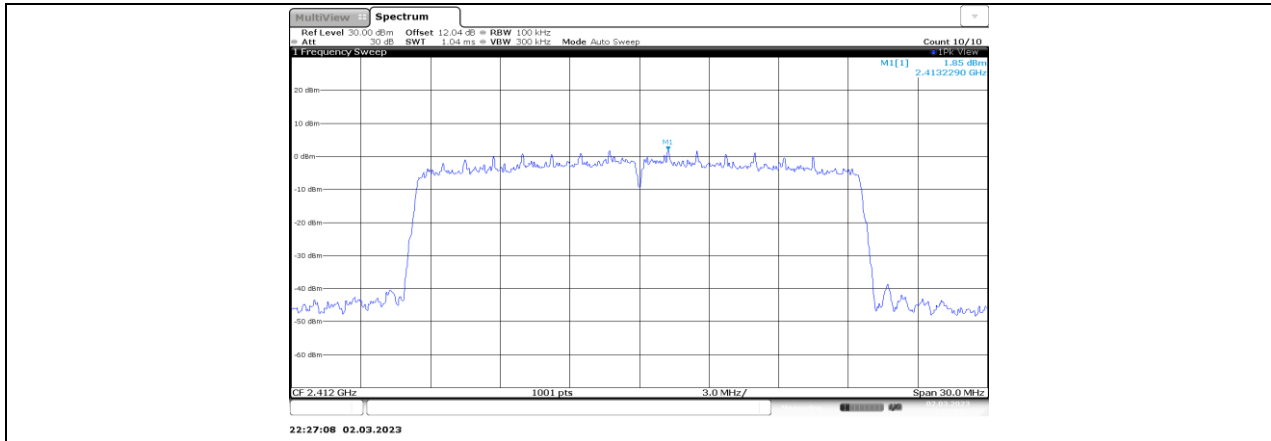
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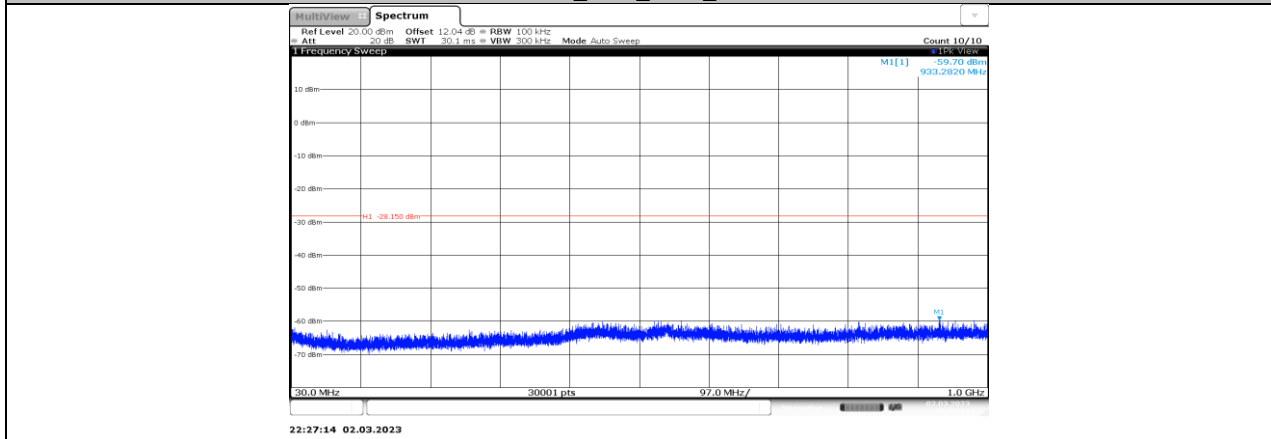
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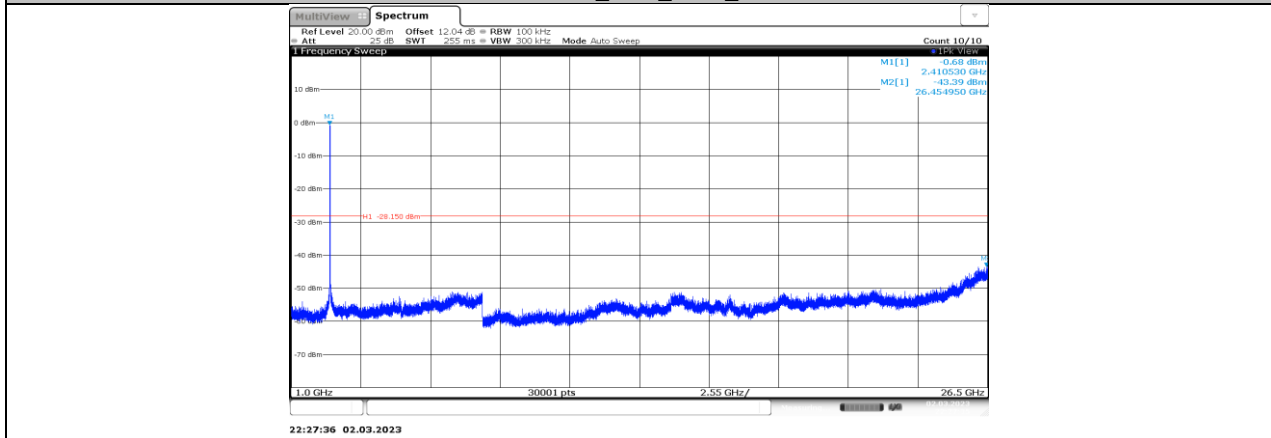
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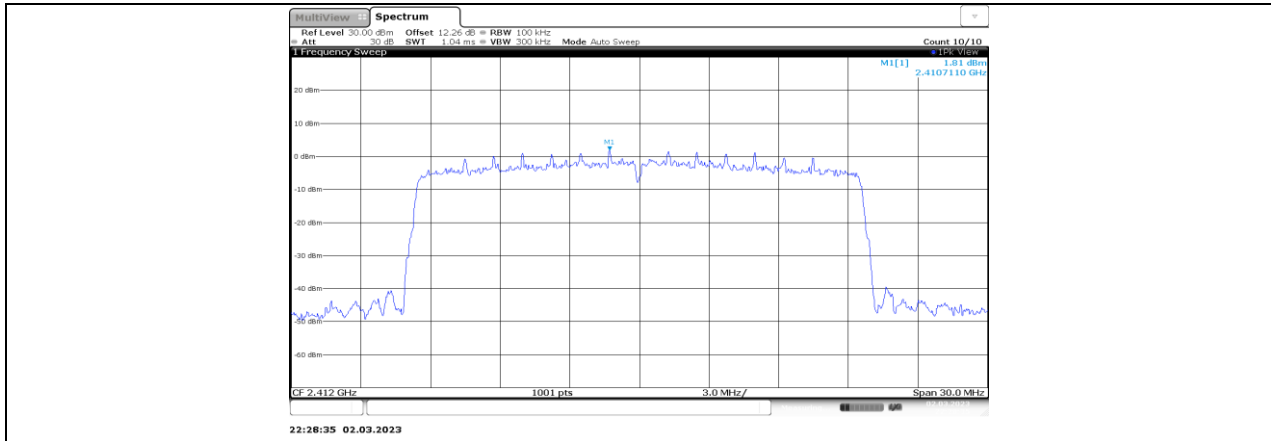
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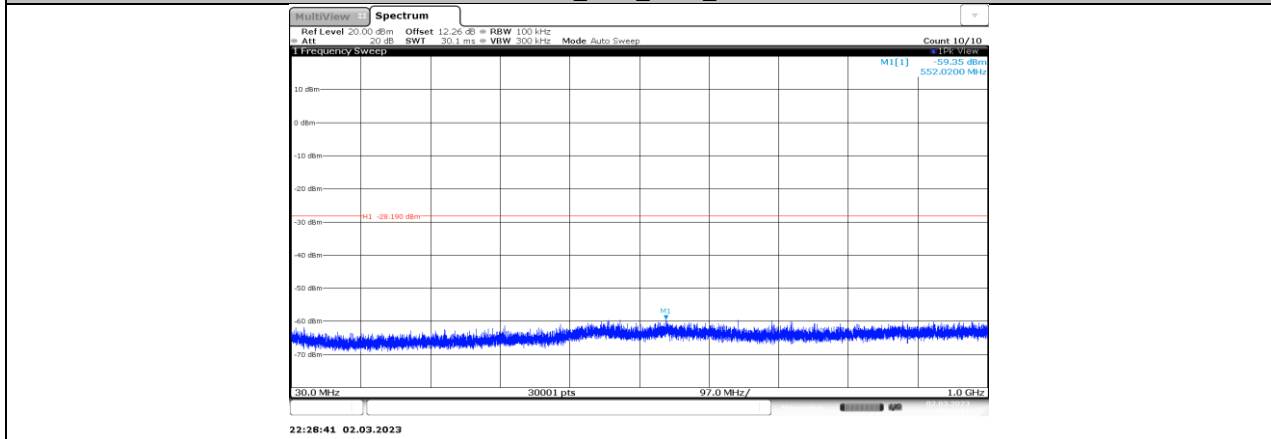
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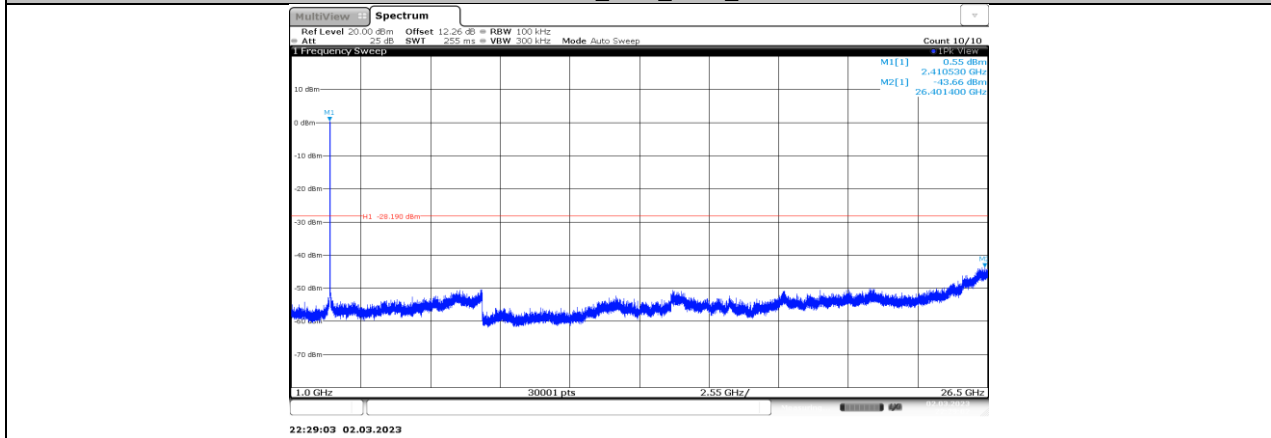
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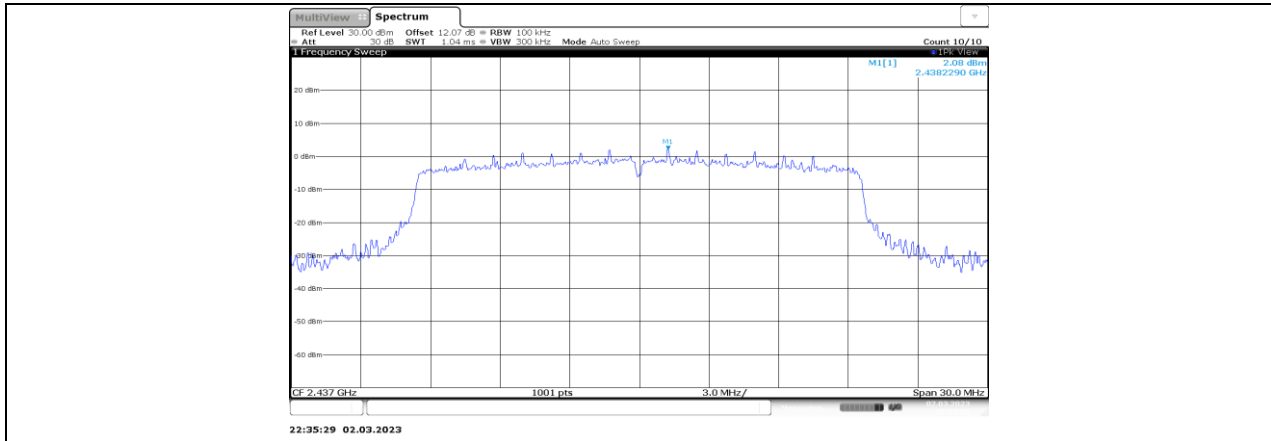
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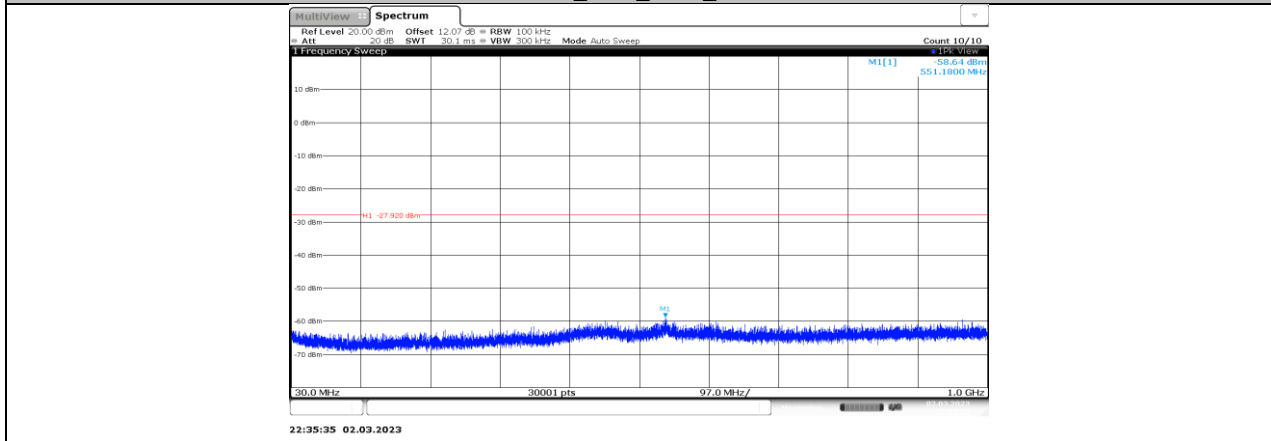
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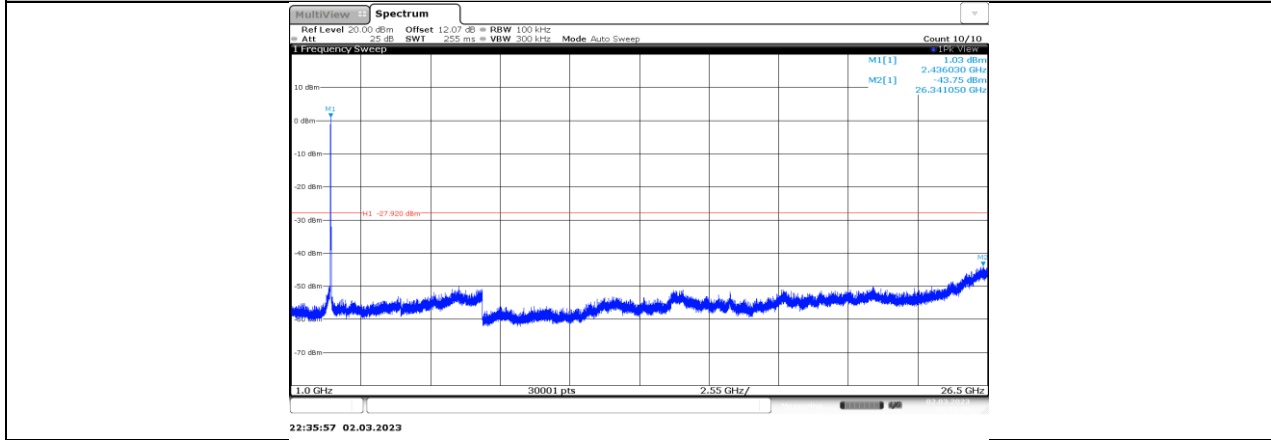
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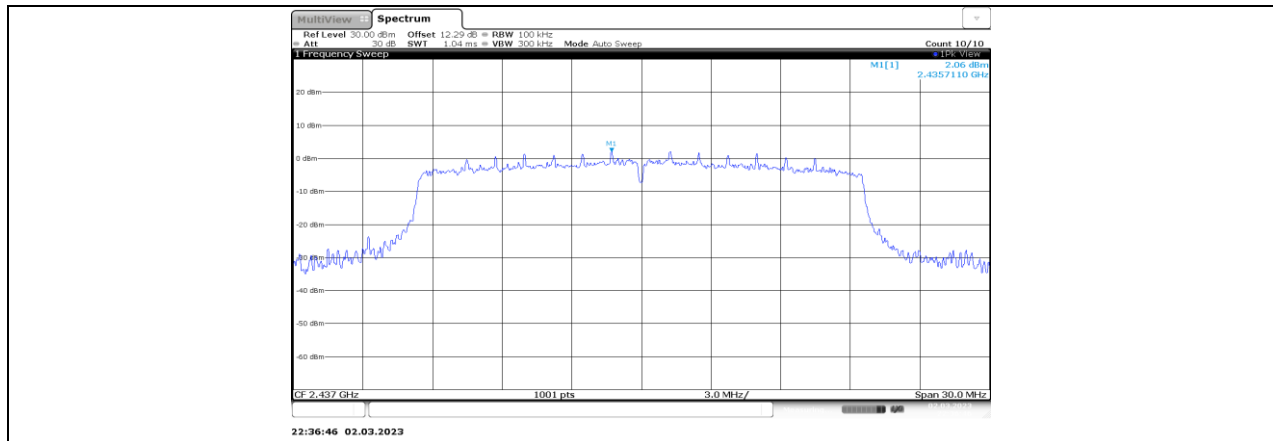
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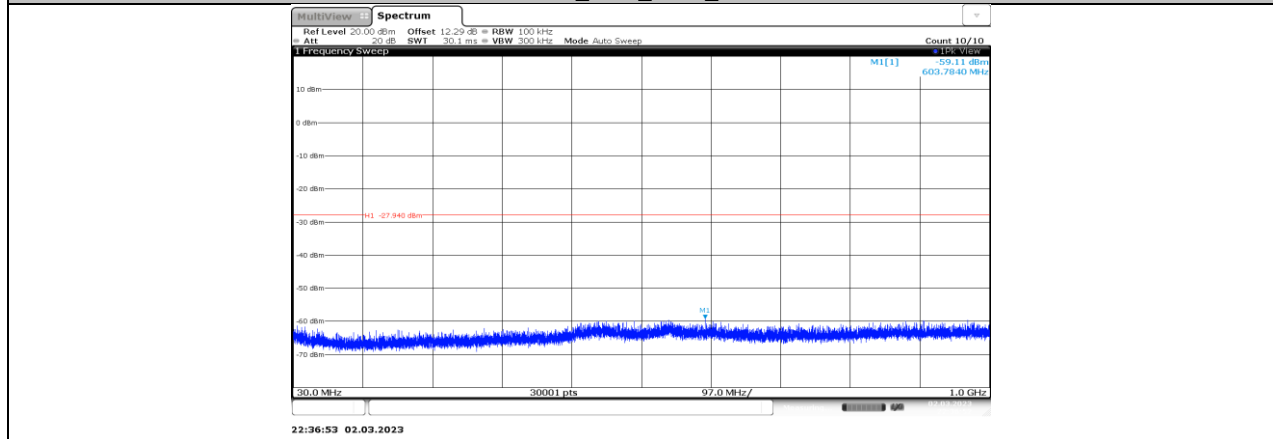
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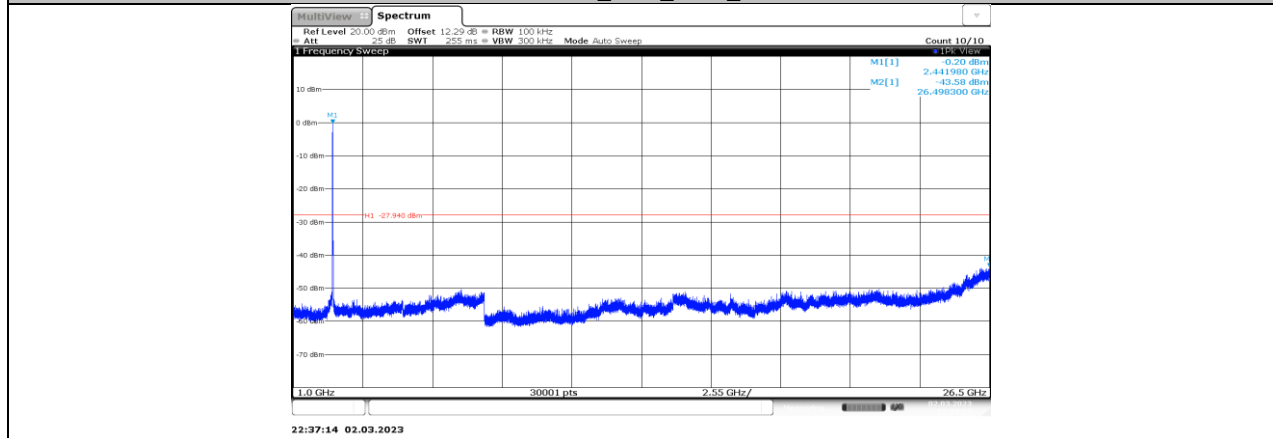
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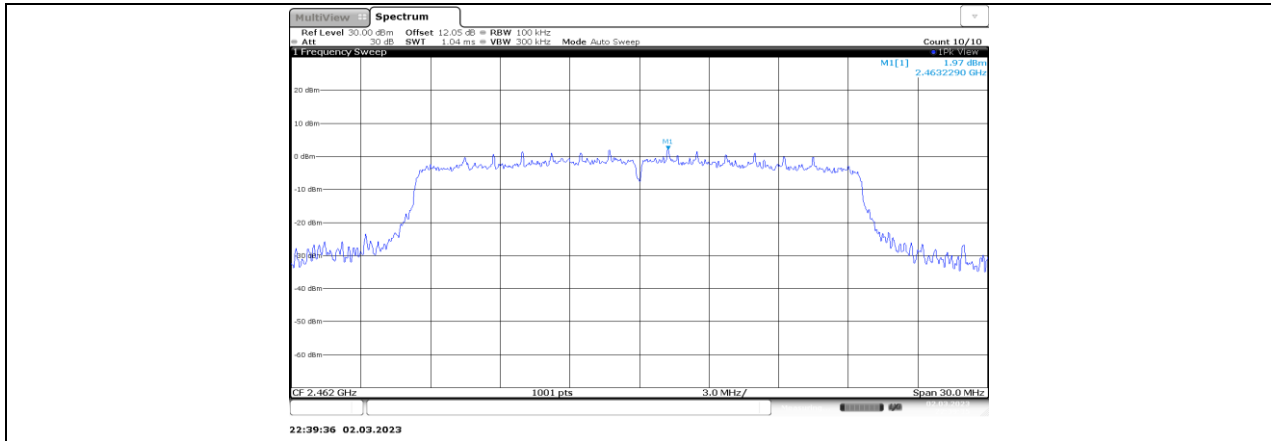
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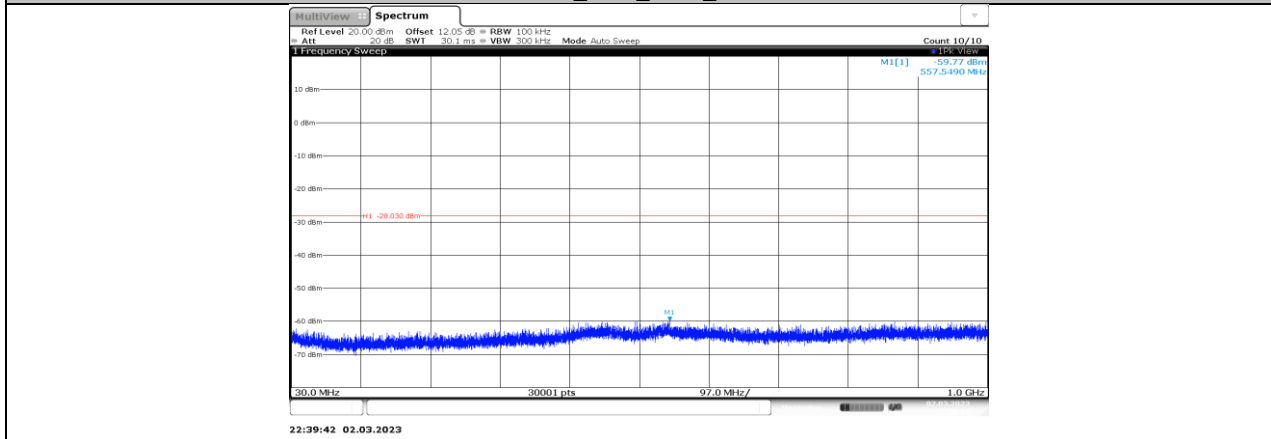
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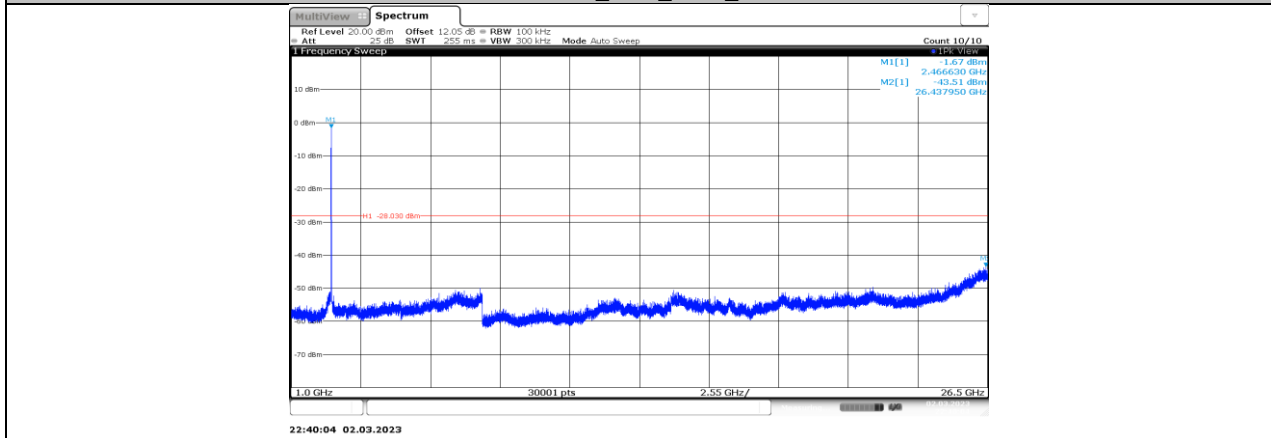
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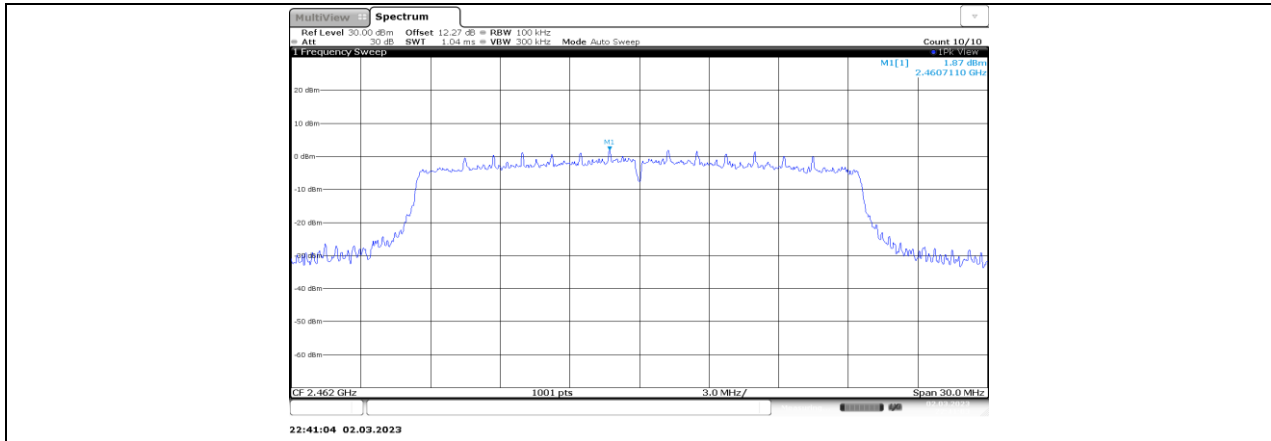
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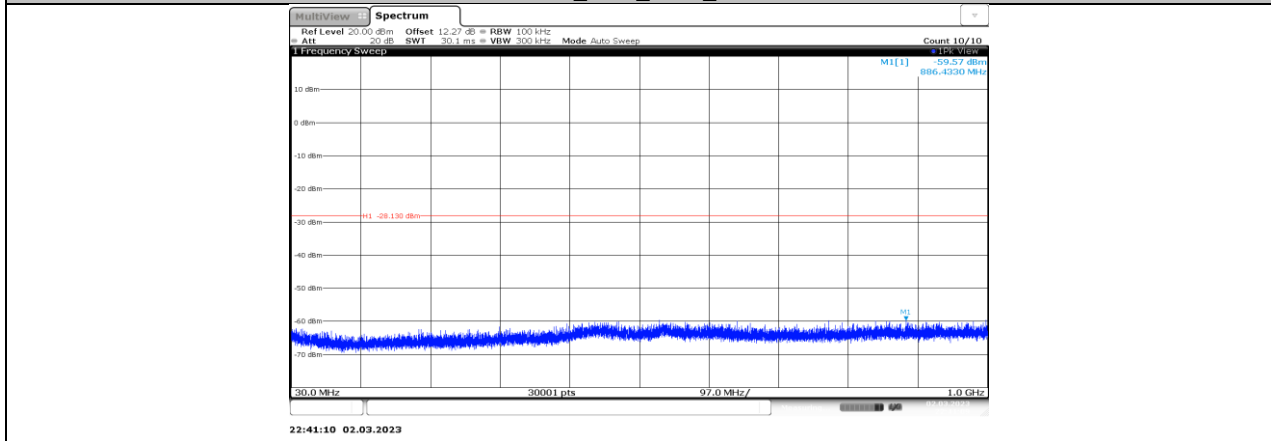
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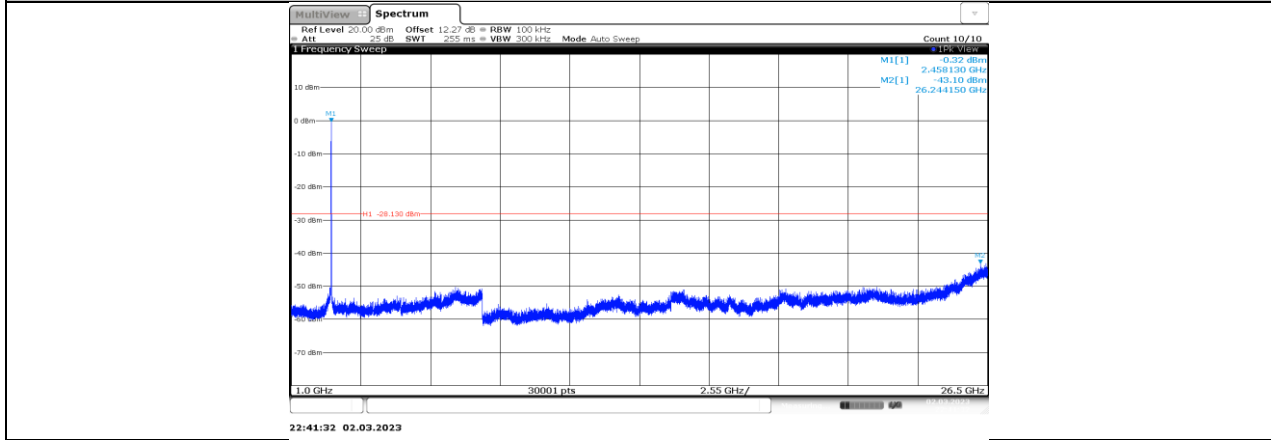
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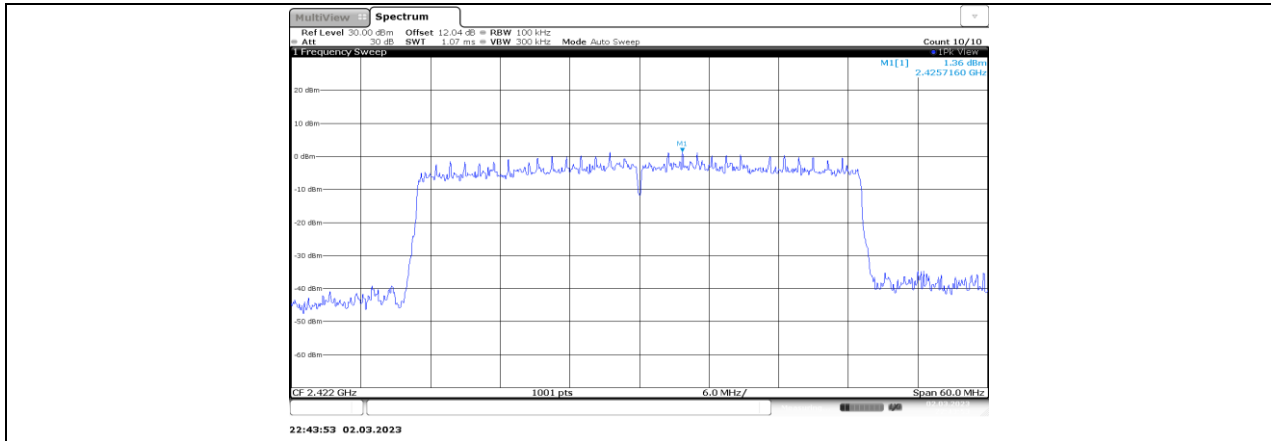
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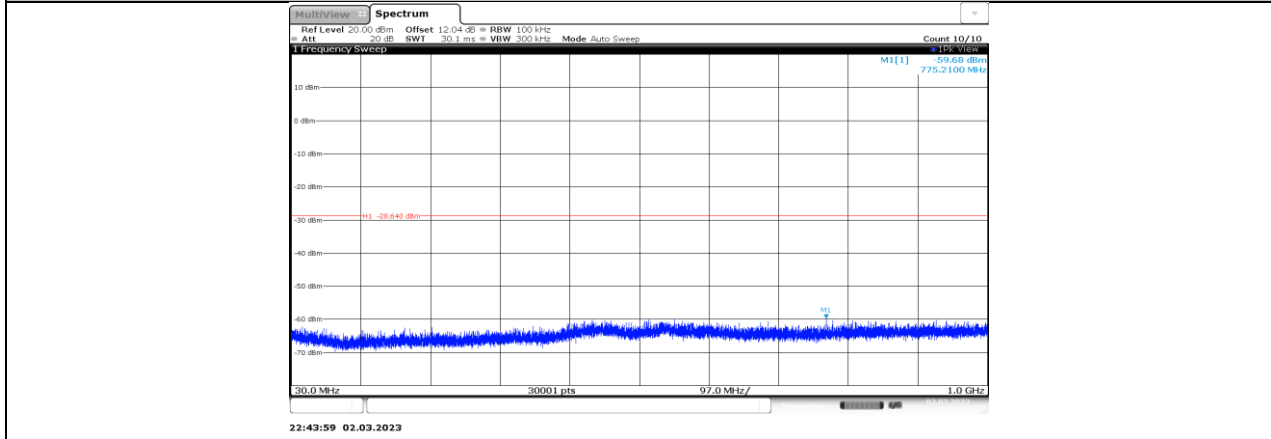
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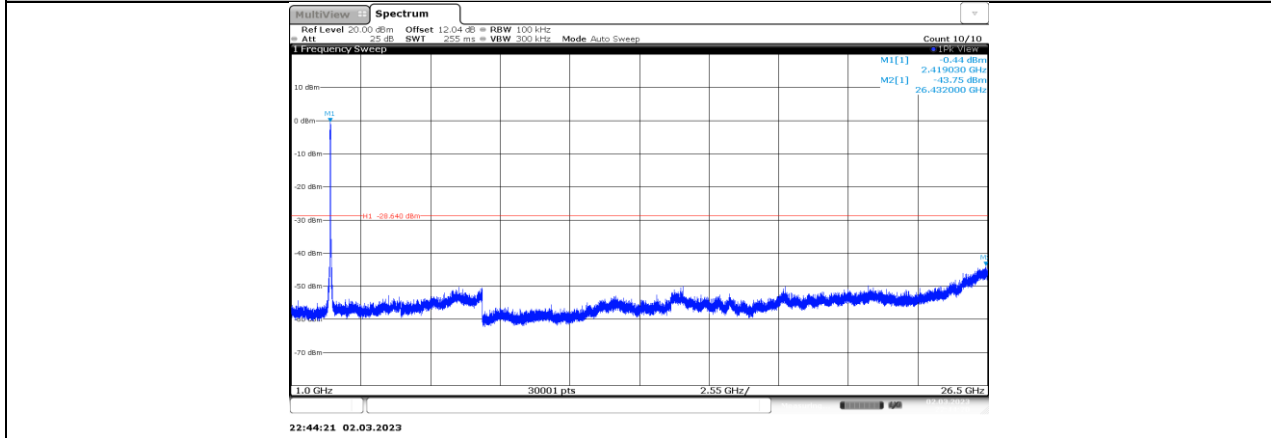
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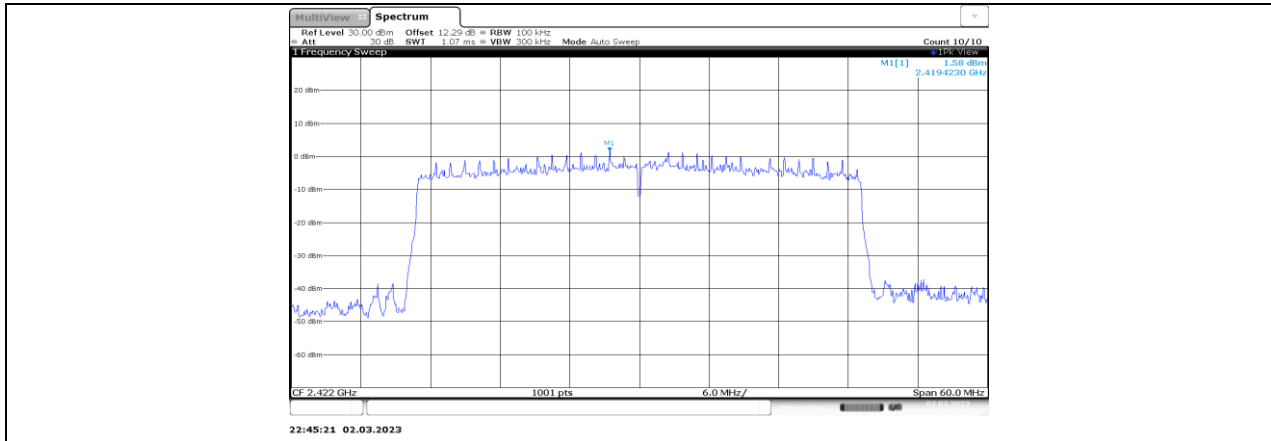
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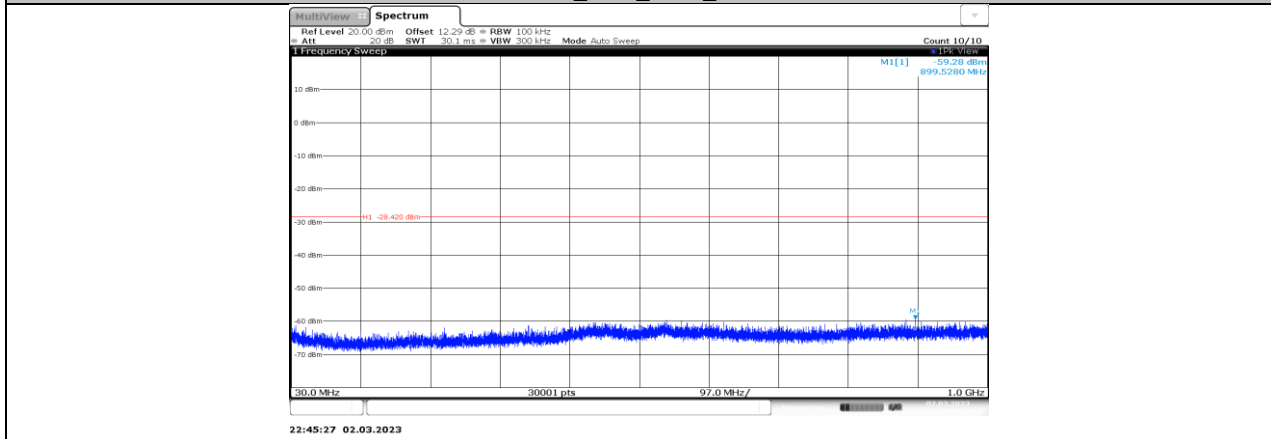
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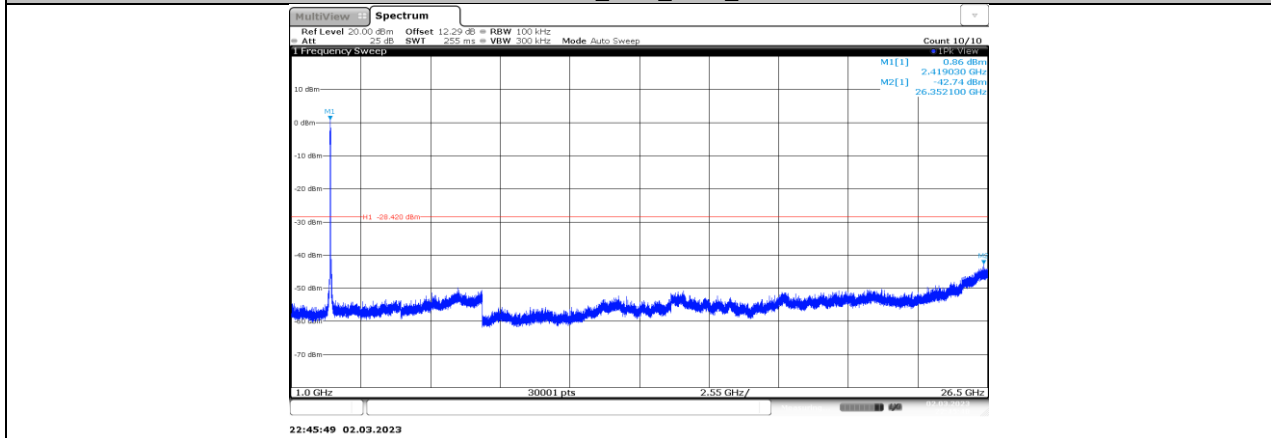
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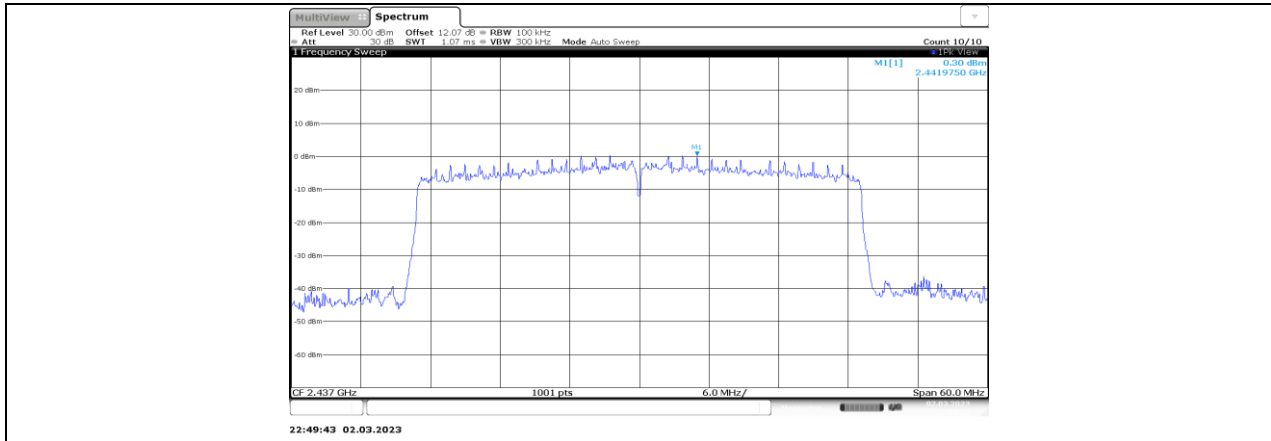
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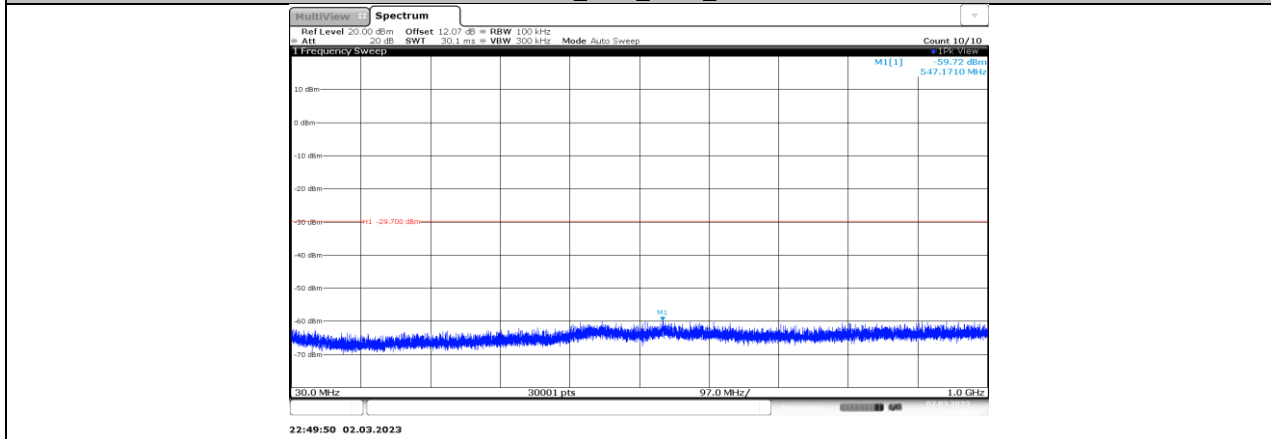
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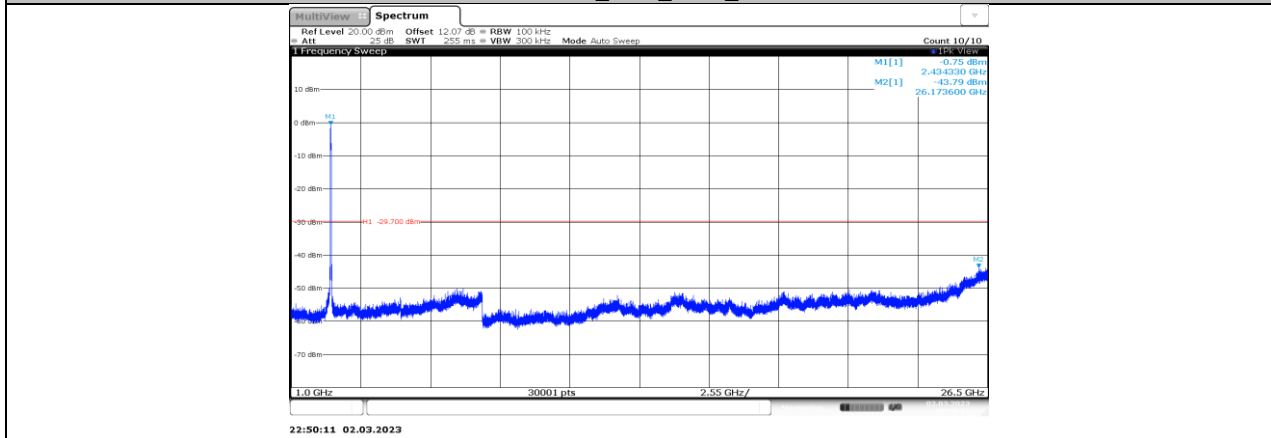
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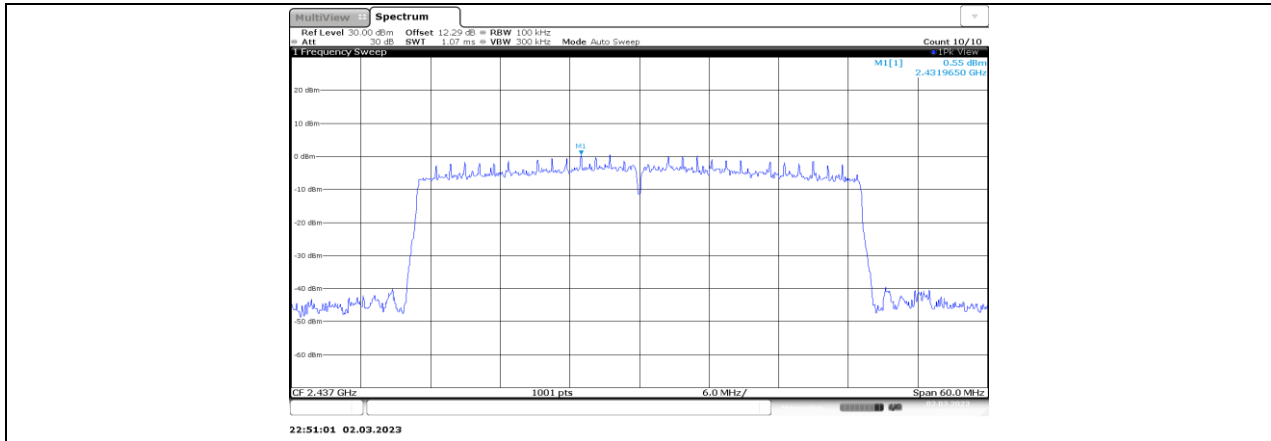
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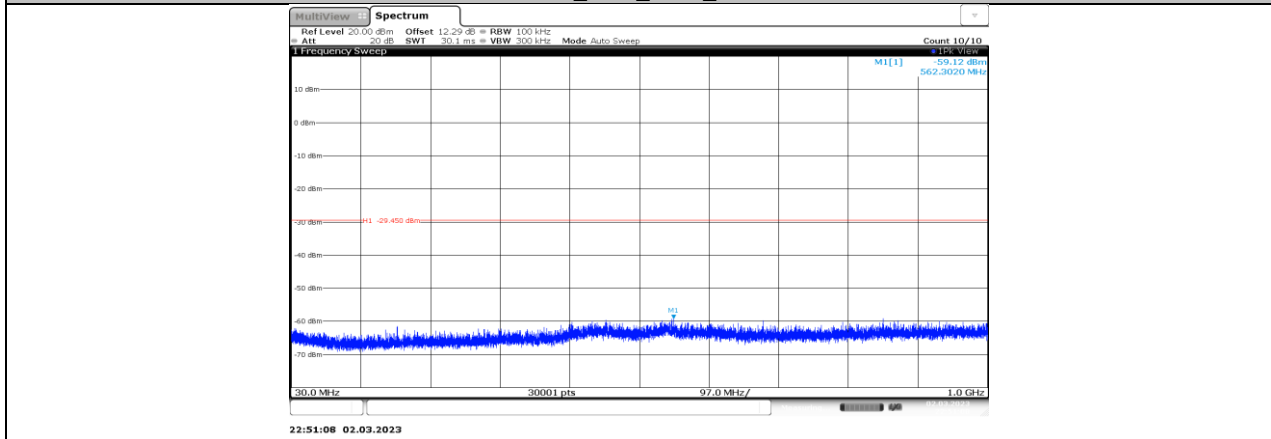
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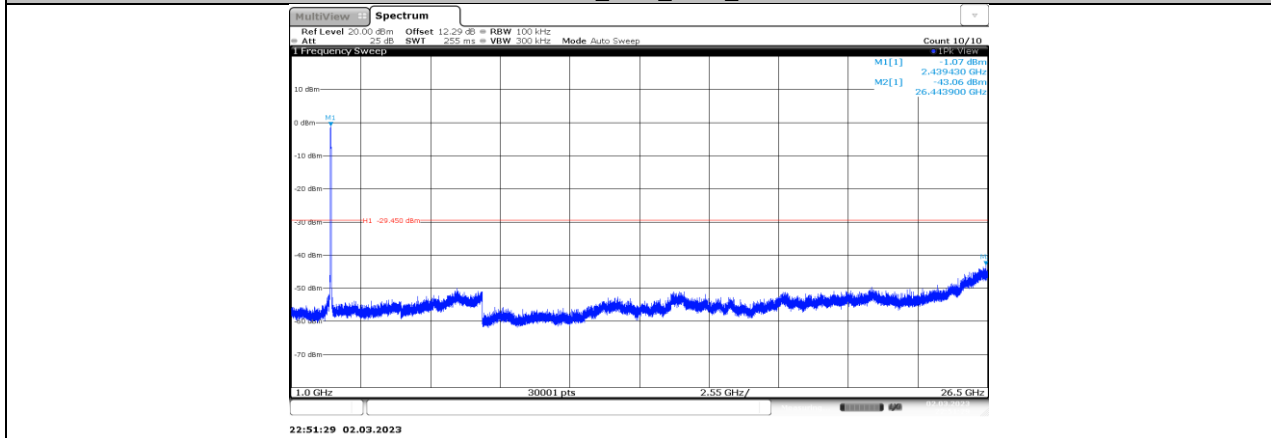
11AX40MIMO_Ant1_2437_1000~26500



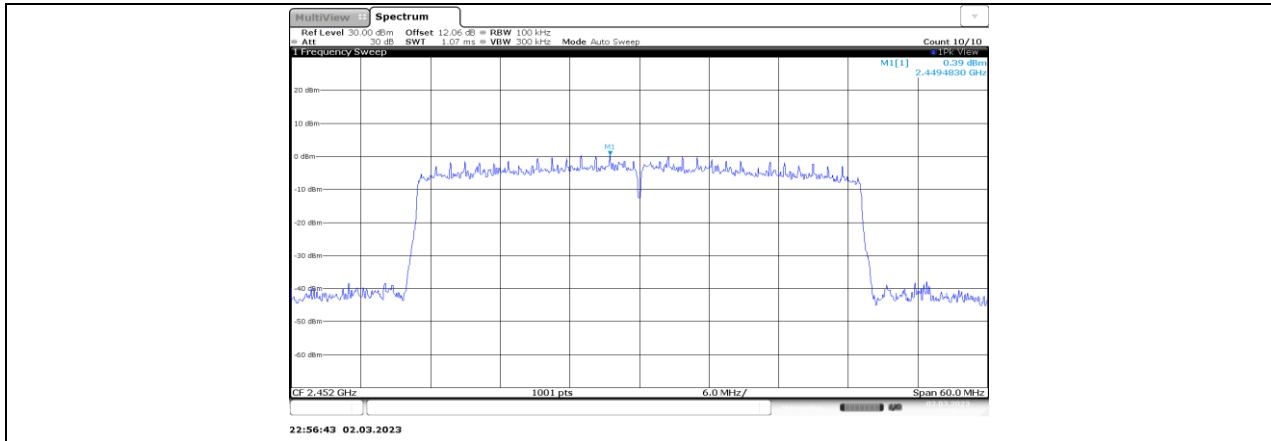
11AX40MIMO_Ant2_2437_0-Reference



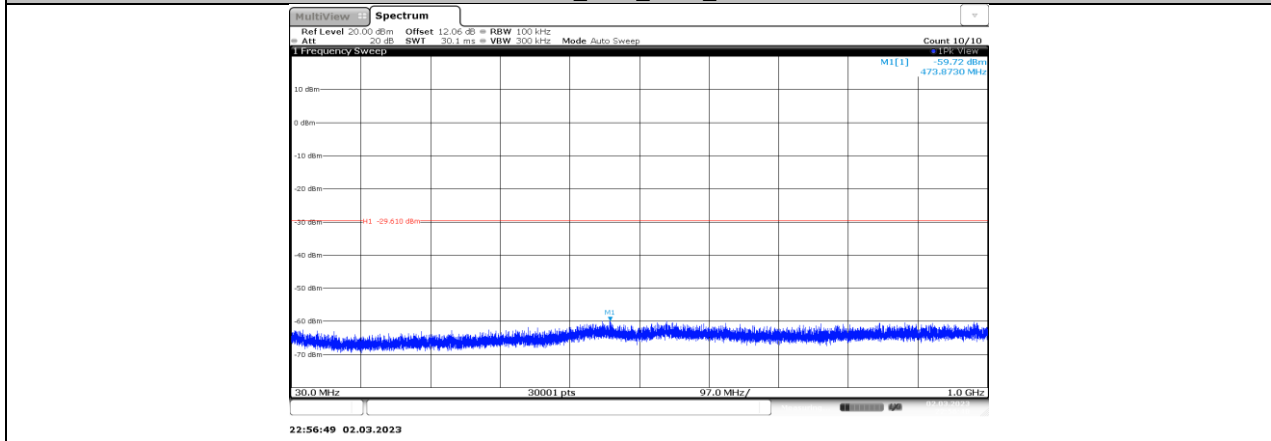
11AX40MIMO_Ant2_2437_30~1000



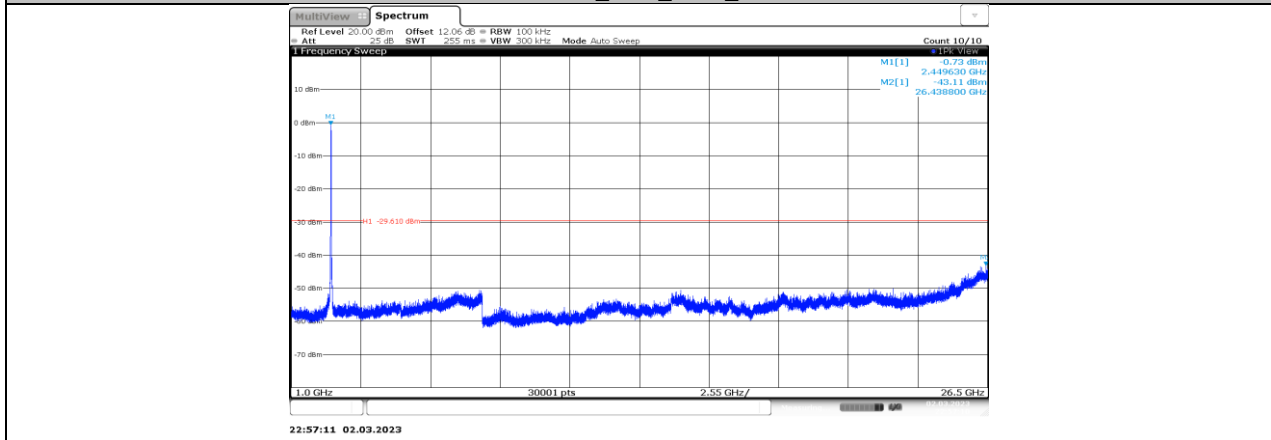
11AX40MIMO_Ant2_2437_1000~26500



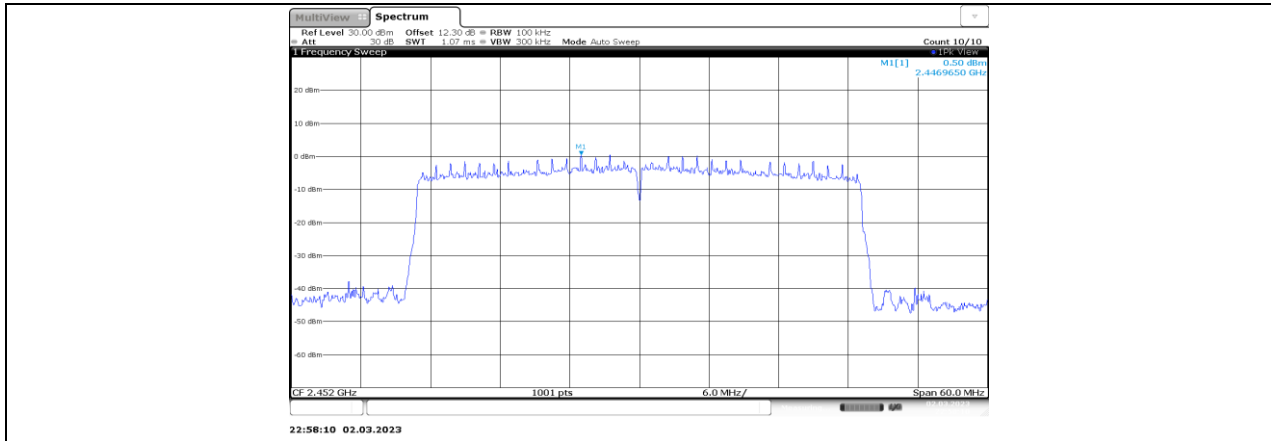
11AX40MIMO_Ant1_2452_0-Reference



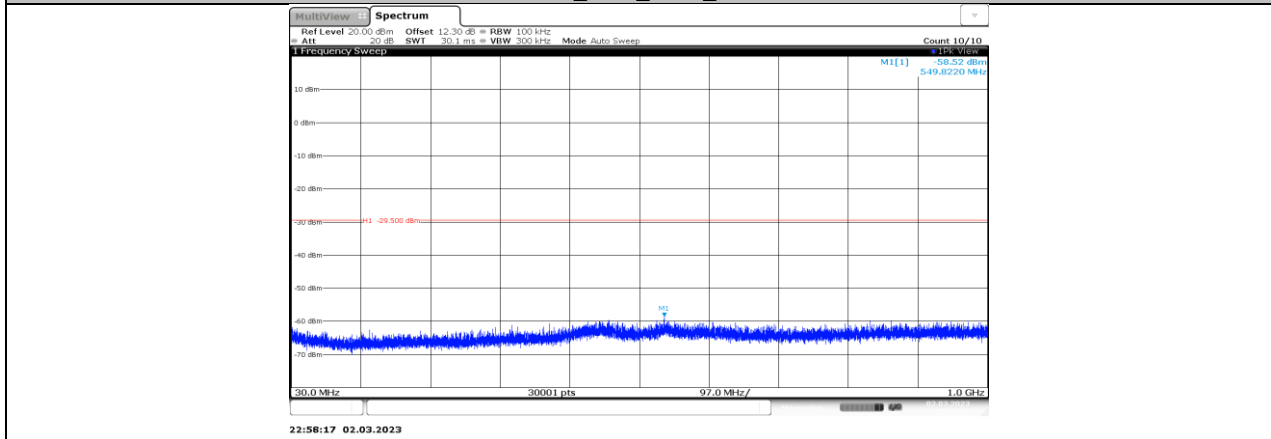
11AX40MIMO_Ant1_2452_30~1000



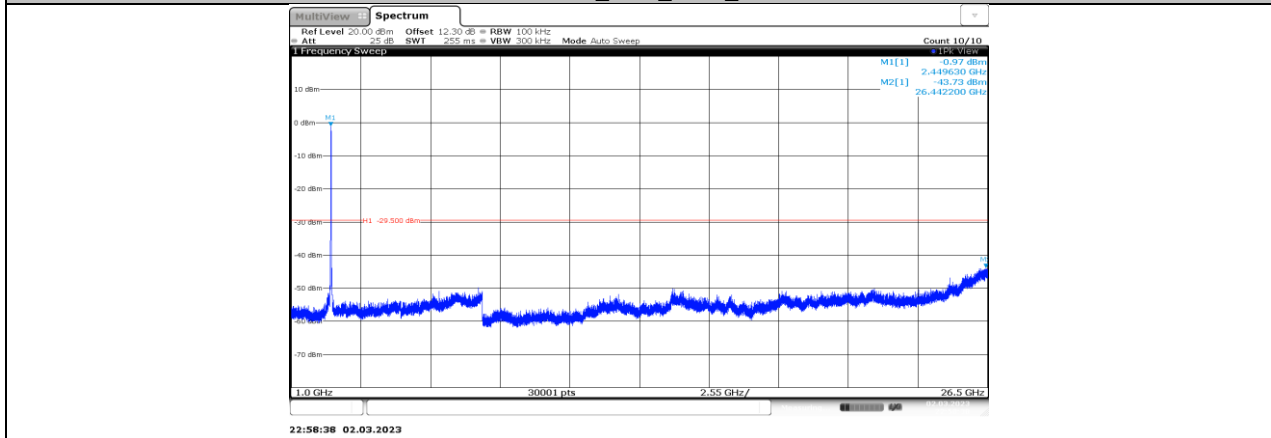
11AX40MIMO_Ant1_2452_1000~26500



11AX40MIMO_Ant2_2452_0-Reference



11AX40MIMO_Ant2_2452_30~1000



11AX40MIMO_Ant2_2452_1000~26500

11.7. APPENDIX G: DUTY CYCLE

11.7.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle ^x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11B	8.37	8.74	0.9577	95.77	0.19	0.12	0.5
11G	1.39	1.77	0.7853	78.53	1.05	0.72	1
11N20MIMO	1.30	1.68	0.7738	77.38	1.11	0.77	1
11N40MIMO	0.64	1.05	0.6095	60.95	2.15	1.56	2
11AX20MIMO	0.56	0.95	0.5895	58.95	2.30	1.79	2
11AX40MIMO	0.20	0.58	0.3448	34.48	4.62	5.00	5

Note:

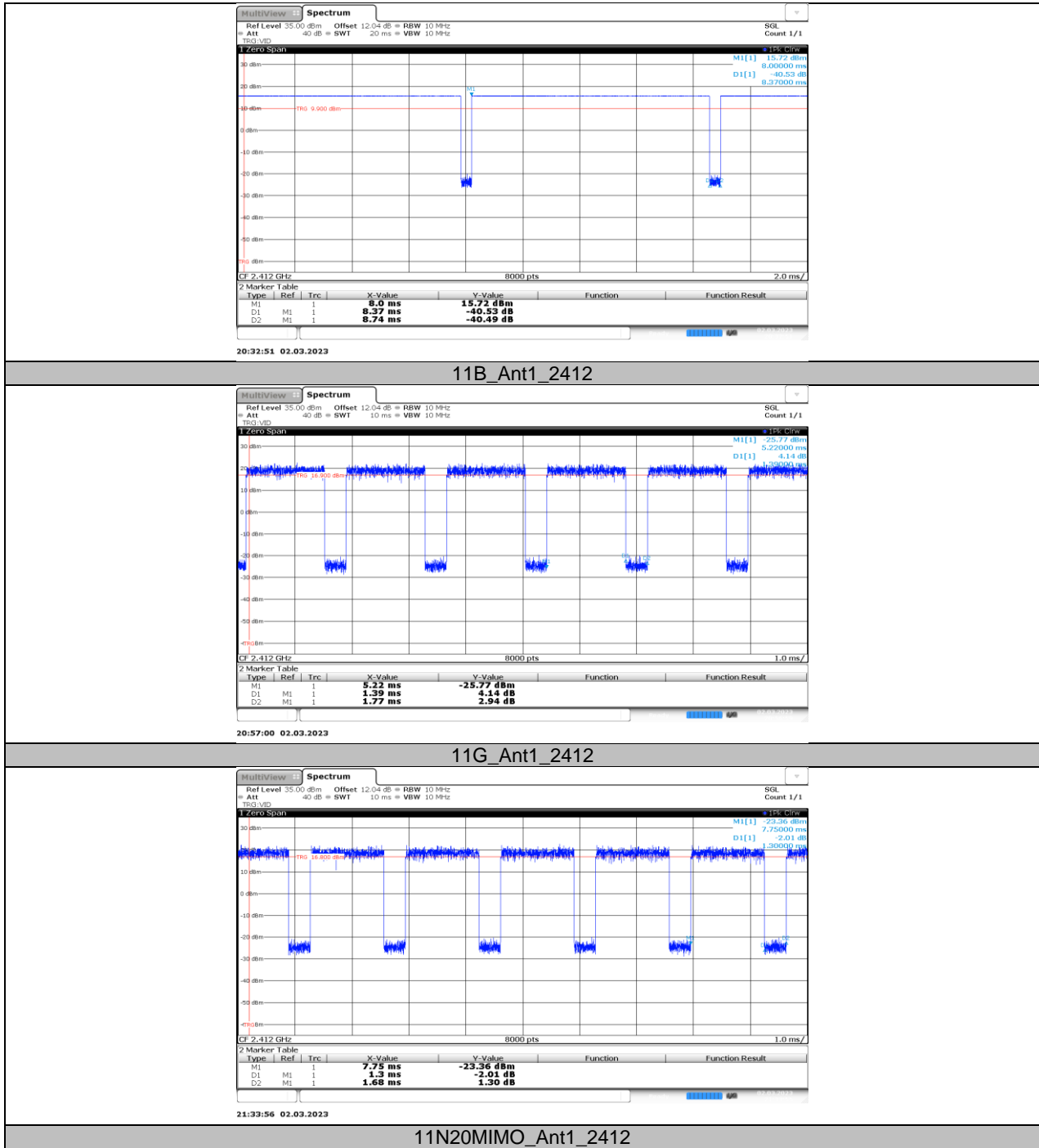
Duty Cycle Correction Factor=10log (1/x).

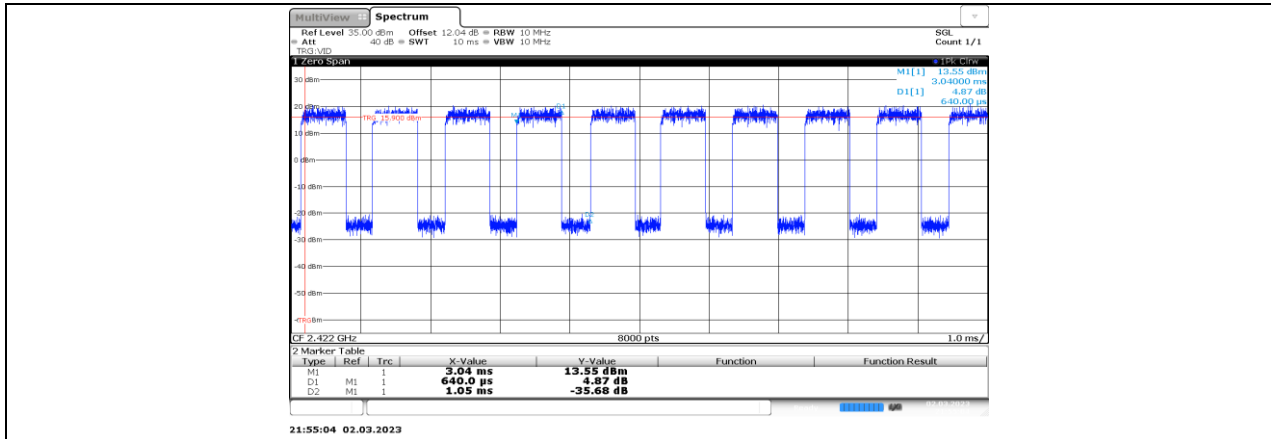
Where: x is Duty Cycle (Linear)

Where: T is On Time

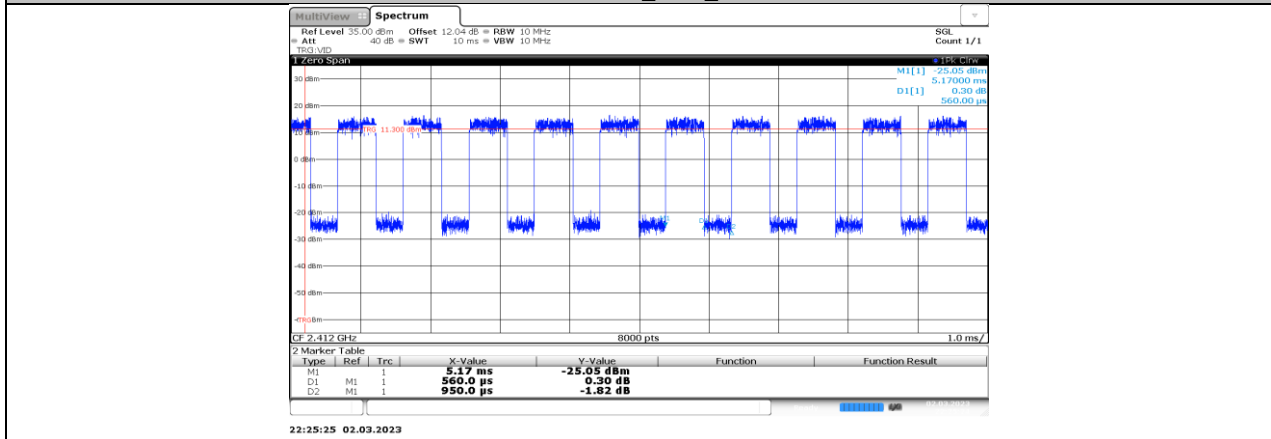
If that calculated VBW is not available on the analyzer then the next higher value should be used.

11.7.2. Test Graphs

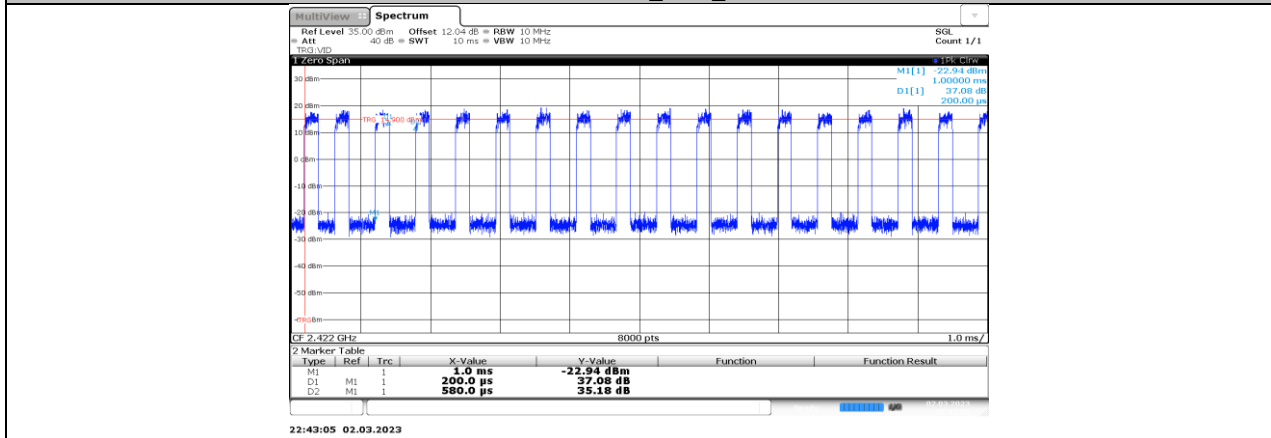




11N40MIMO_Ant1_2422



11AX20MIMO_Ant1_2412



11AX40MIMO_Ant1_2422

END OF REPORT