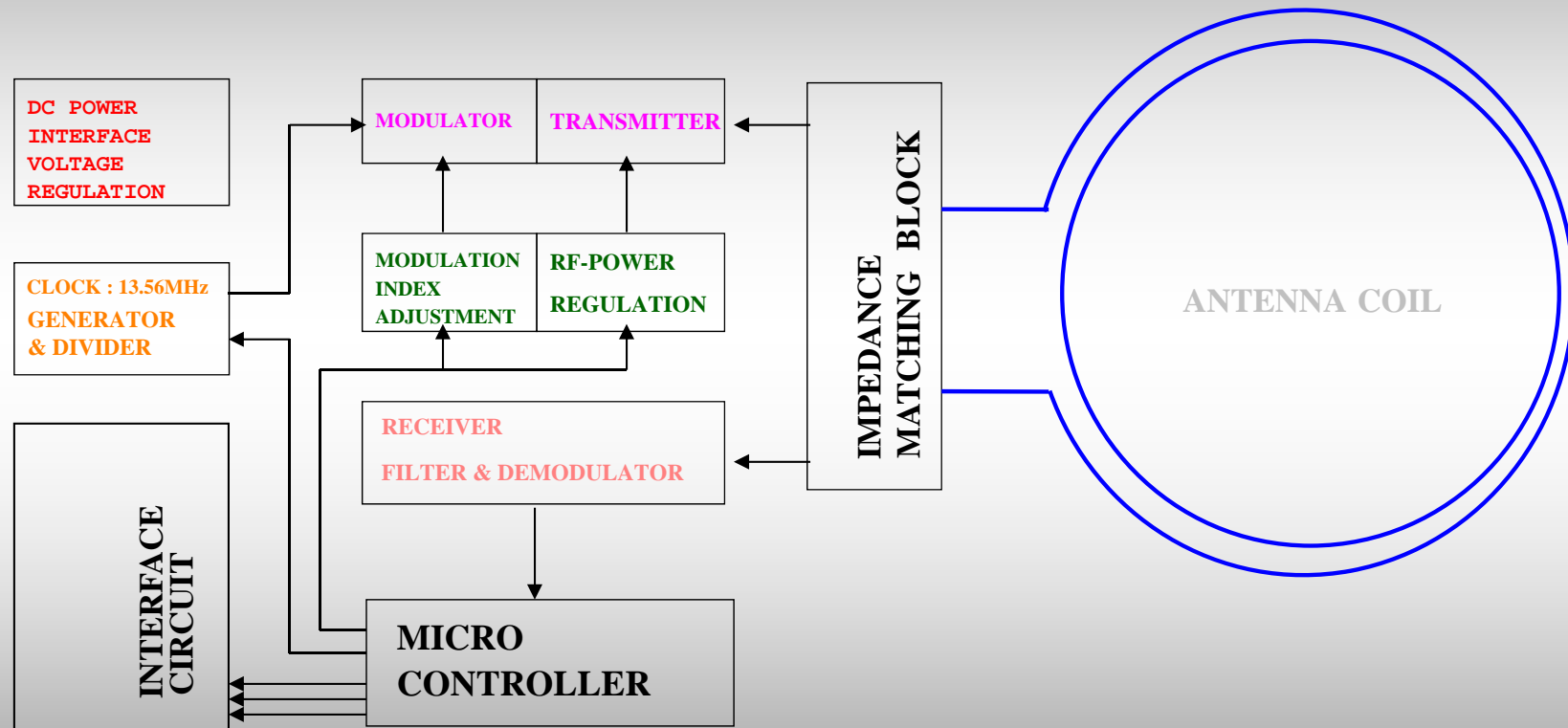


BLOCK DIAGRAM



BLOCK DIAGRAM DESCRIPTIONS

Voltage Regulation

All necessary voltage for the Reader are generated internally from the 12VDC power supply voltage

Clock Generator

This part generates the 13.56MHz clock for the transmitter amplifier. A divided clock signal is connected to the micro controller for synchronization purpose

Modulator, Transmitter Amplifier

To transmit data to the Tag the carrier has to be modulated by the Modulator with a digital signal from the micro controller according to Air interface protol. The modulated carrier is amplified by the Transmitter Amplifier.

Modulation Index, RF-Power Regulation

The Antenna Output voltage is kept to a software adjustable value by the RF-Power Regulator.

Receiver, Filter, Demodulator, Interface block

A amplitude modulated signal is received from the Tag. After filtering, demodulation and amplification The analog data signal is converted by the Interface block for further digital processing.

Microcontroller

The Microcontroller processes the protocol for the communication the between Tag and Reader.

Interface block

Data Output port