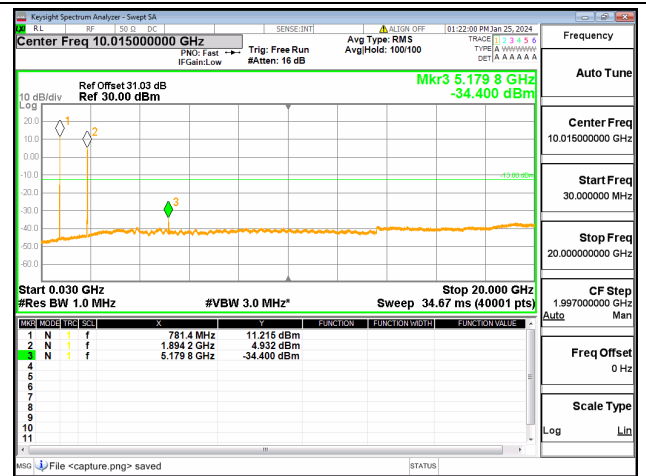
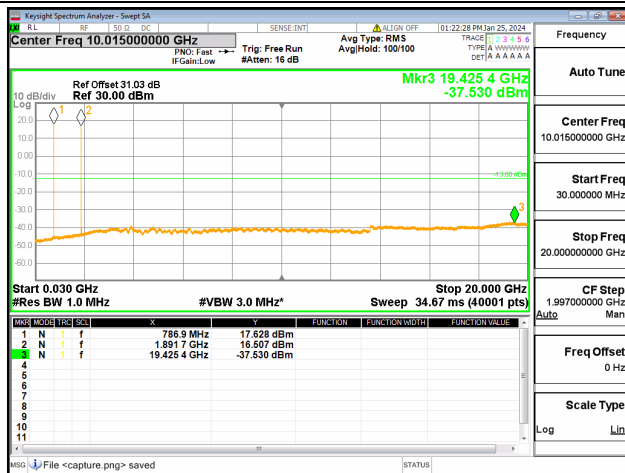


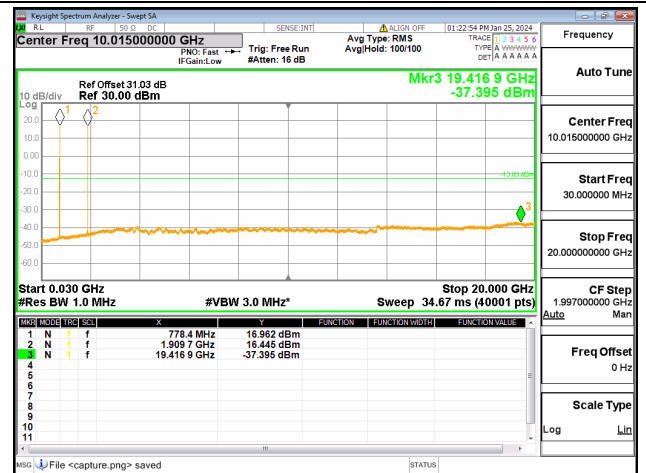
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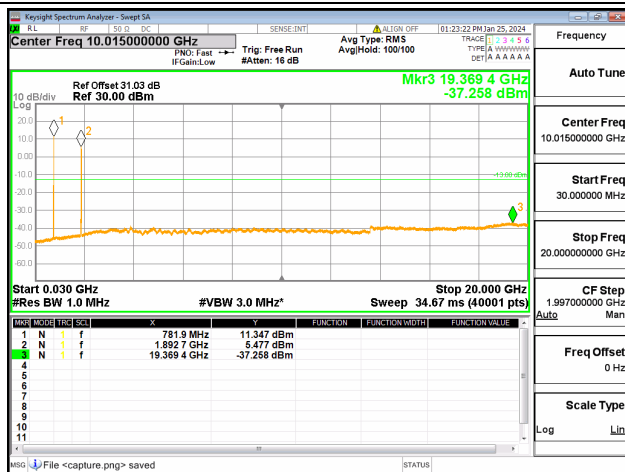
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2A-13A / 20+10MHz / QPSK / High+High CH / 1@0-1@49



2A-13A / 20+10MHz / QPSK / High+High CH / 1@99-1@0

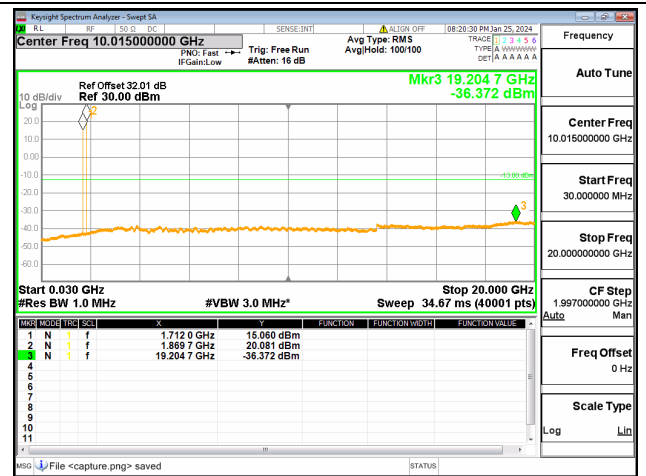


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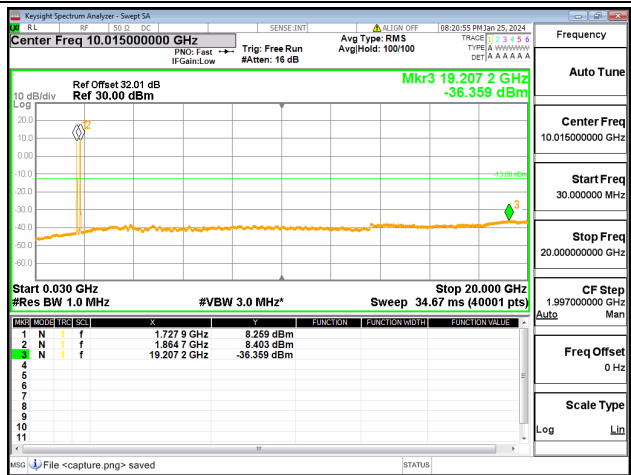
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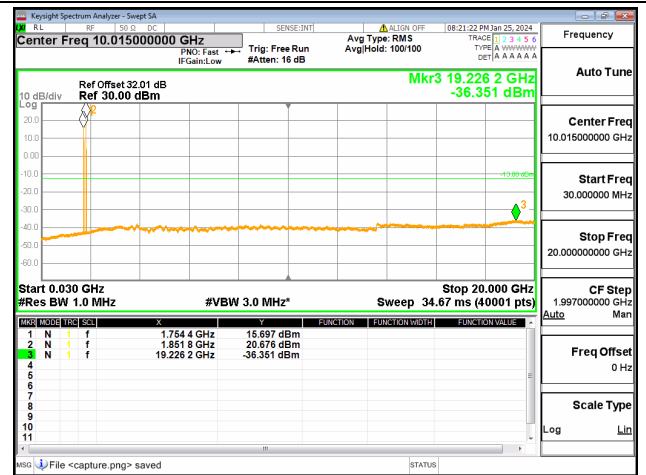
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1@0-1@99



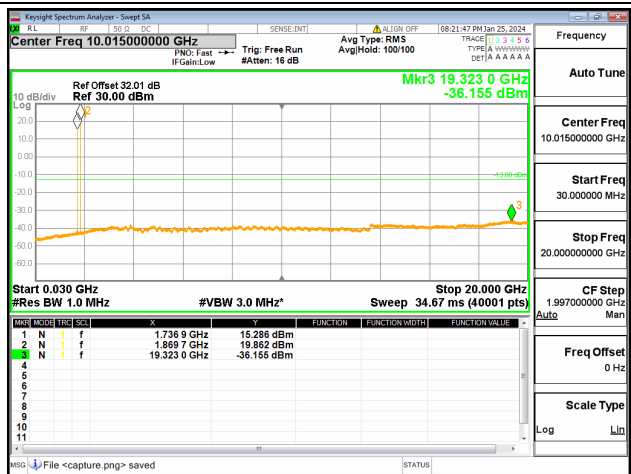
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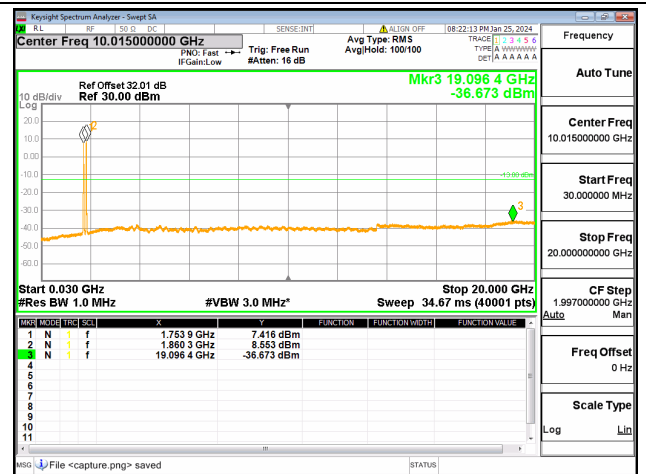
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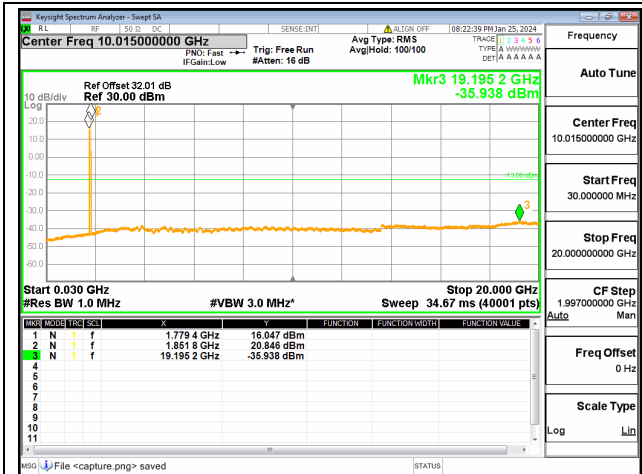
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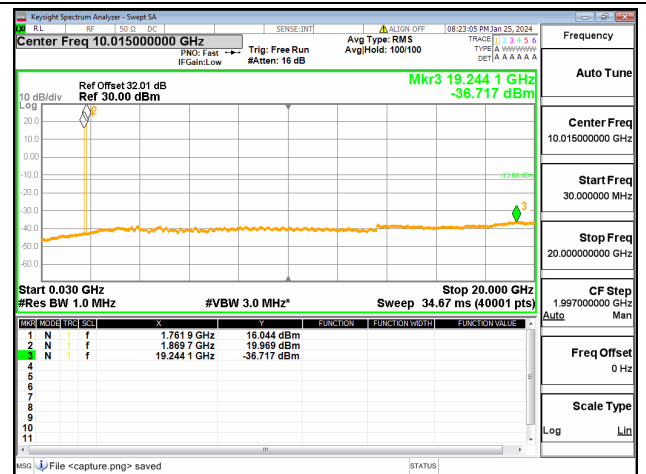
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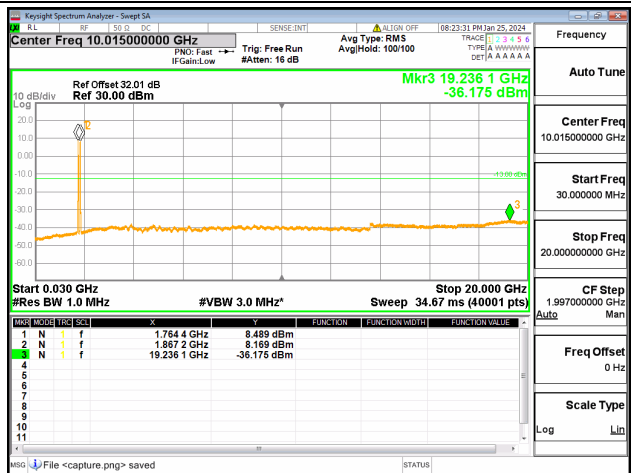
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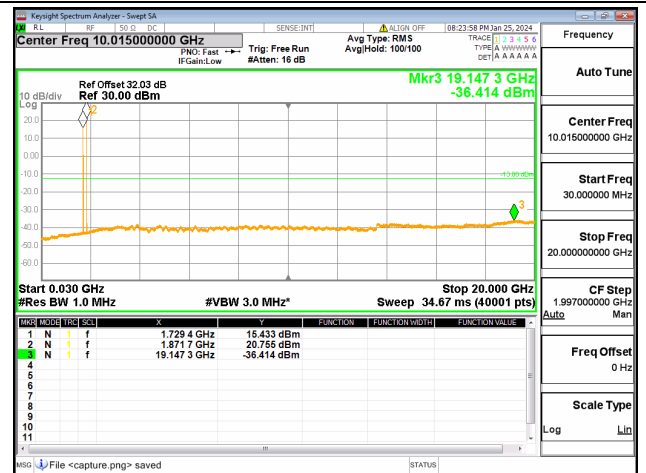
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1@0-1@99



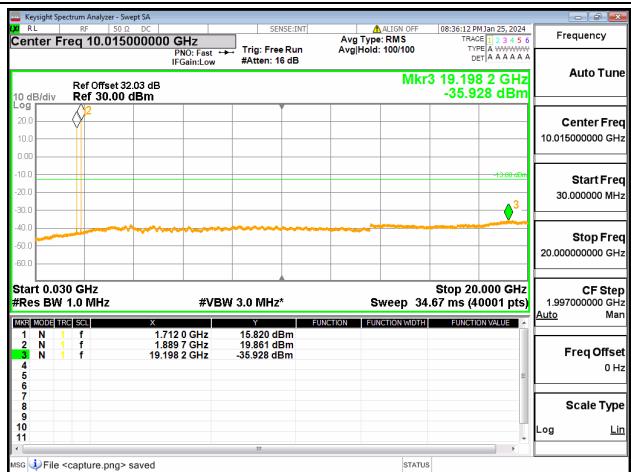
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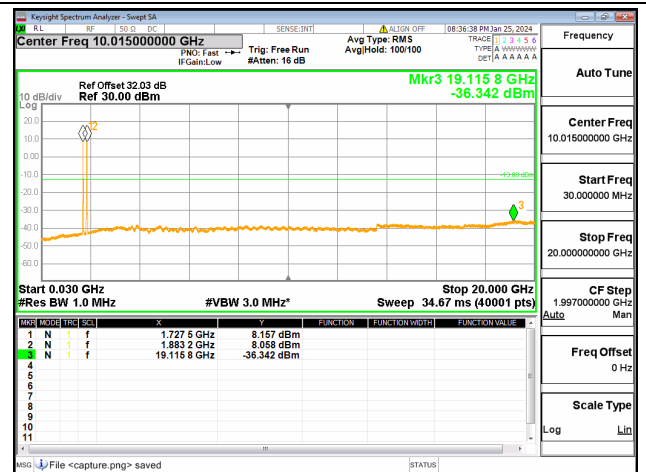
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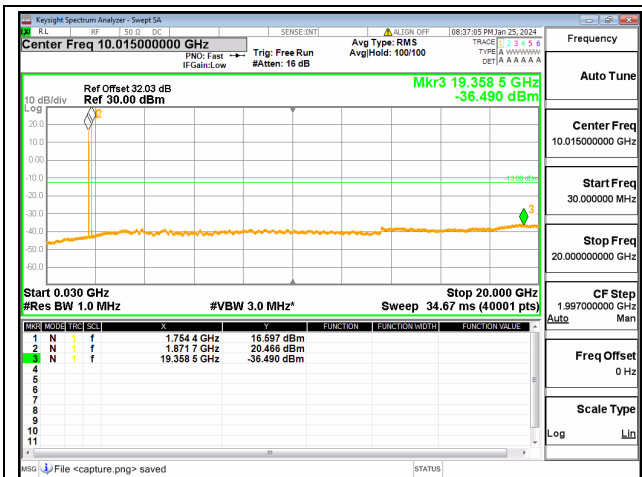
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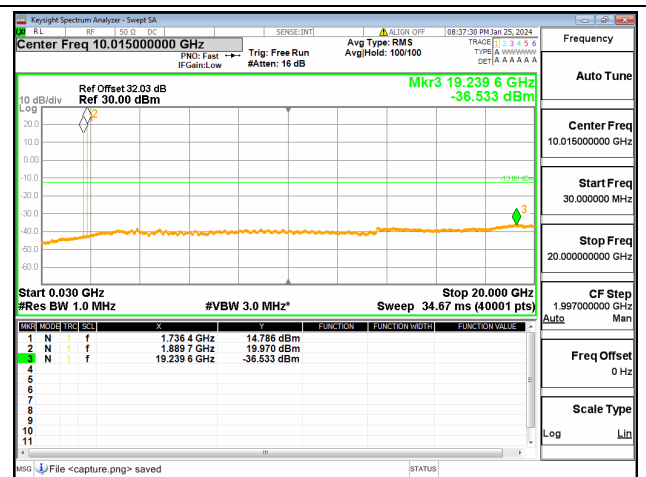
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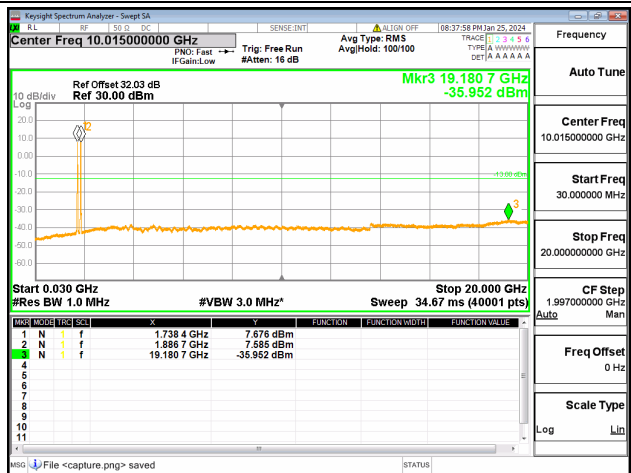
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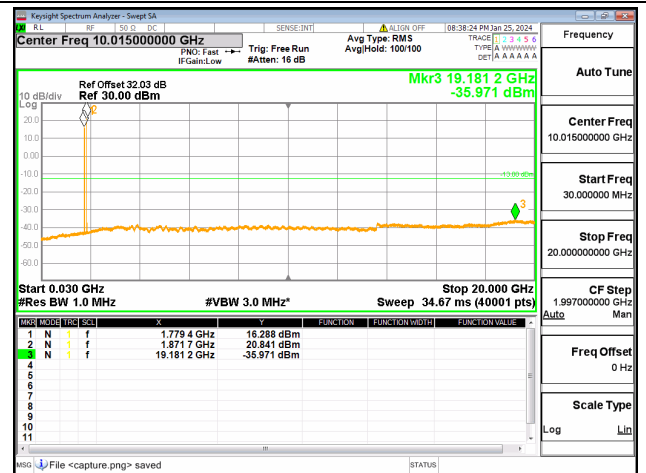
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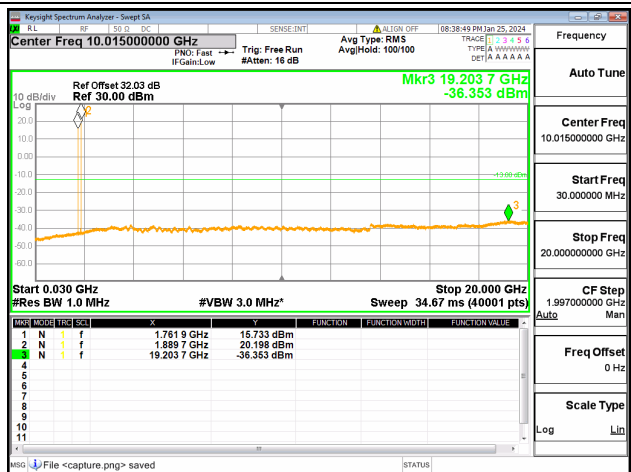
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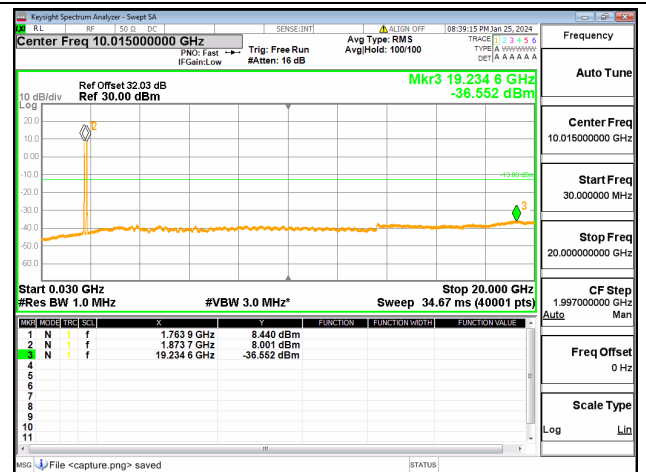
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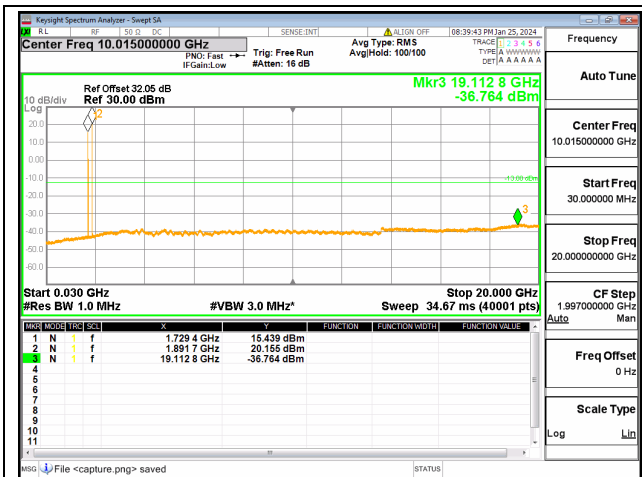
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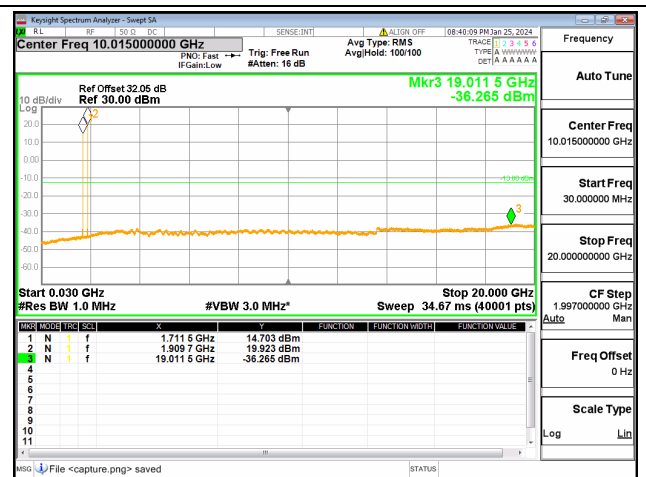
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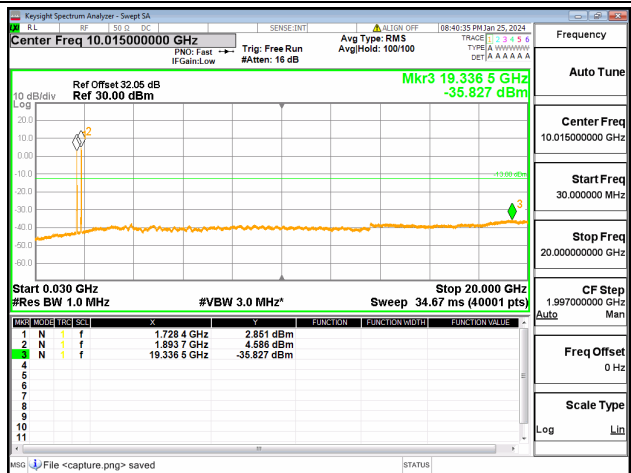
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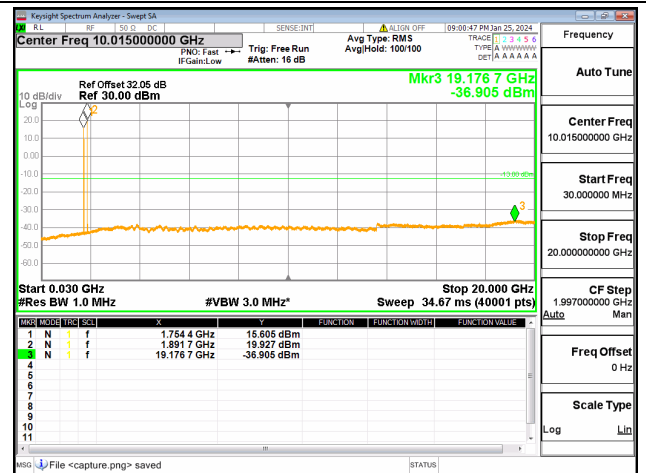
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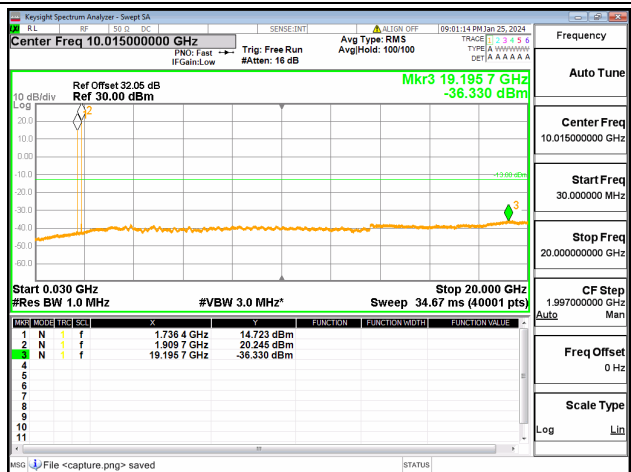
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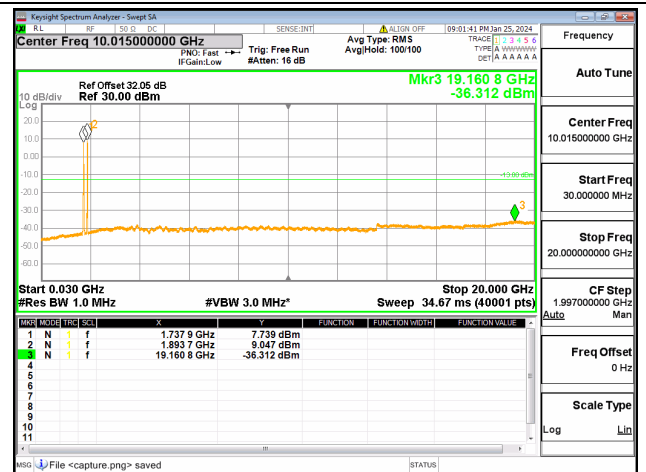
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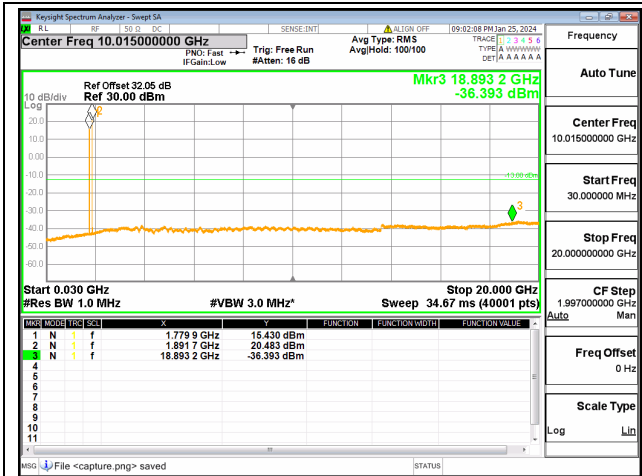
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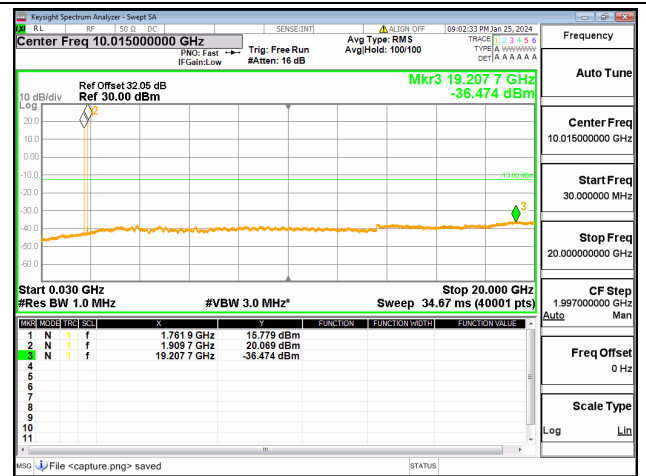
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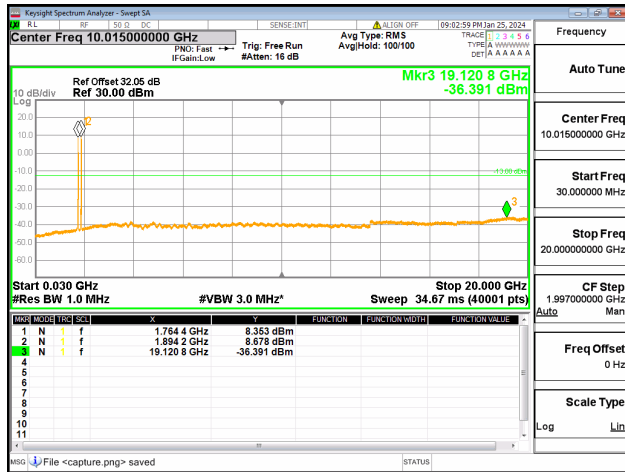
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2A-66A / 20+20MHz / QPSK / High+High CH /
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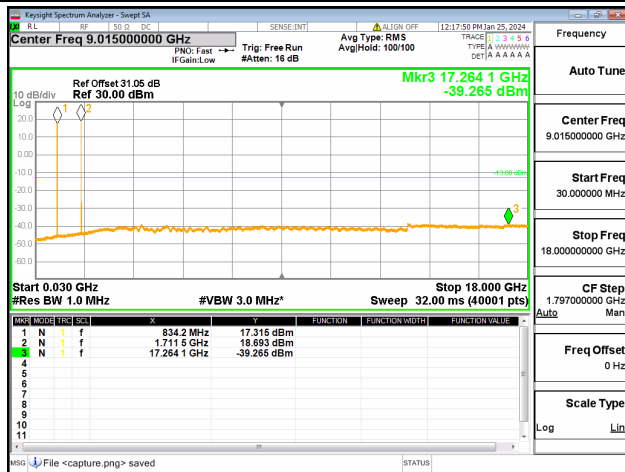


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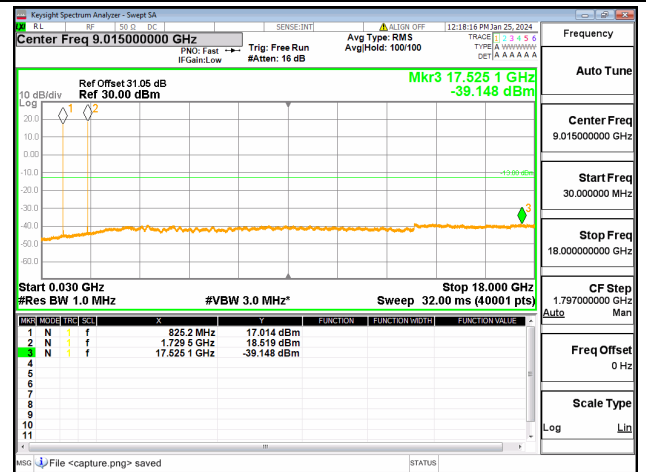


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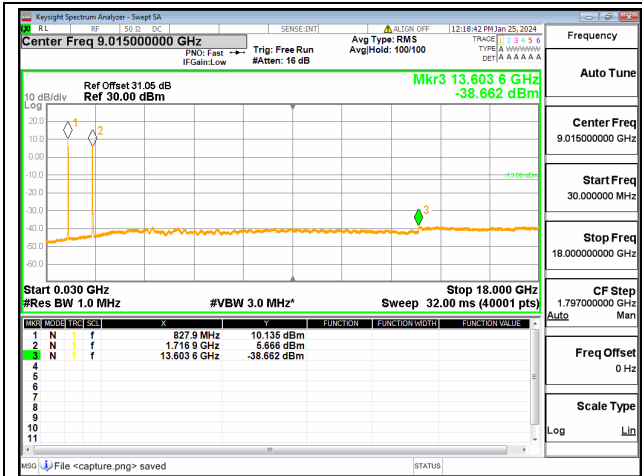
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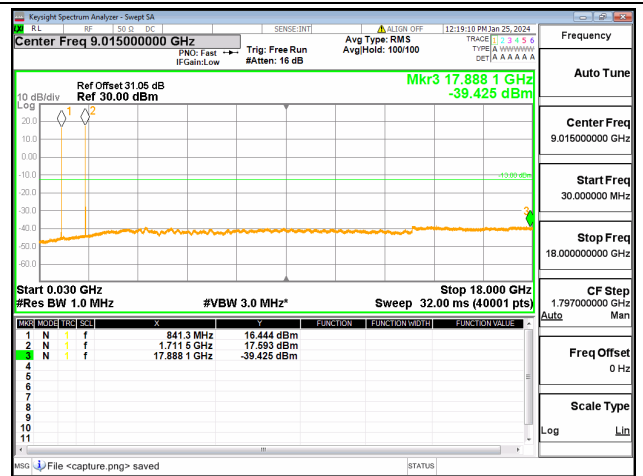
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1@0-1@49



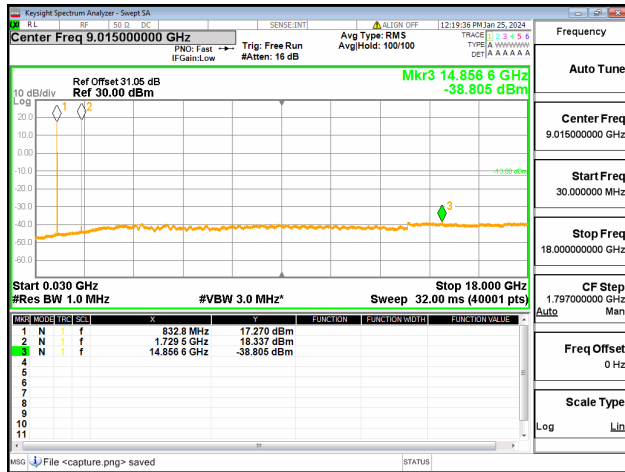
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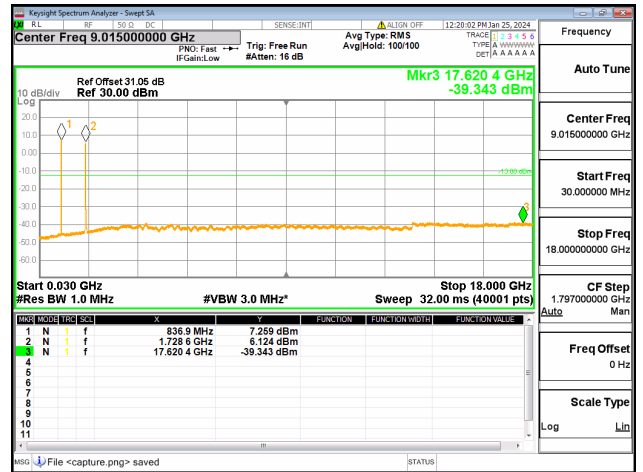
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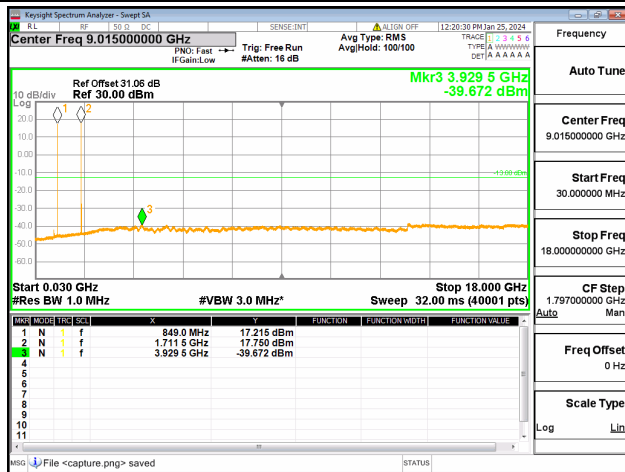
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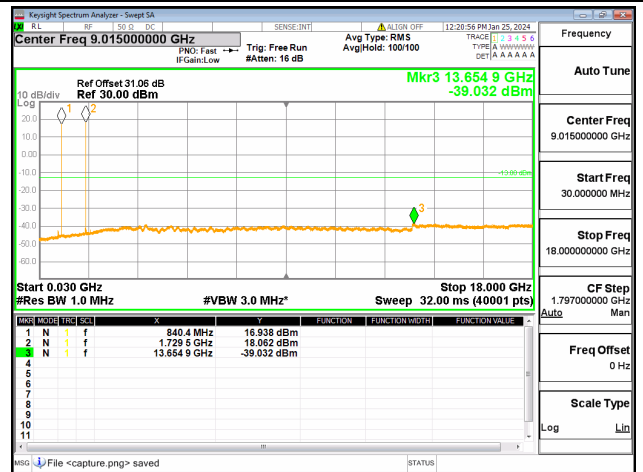
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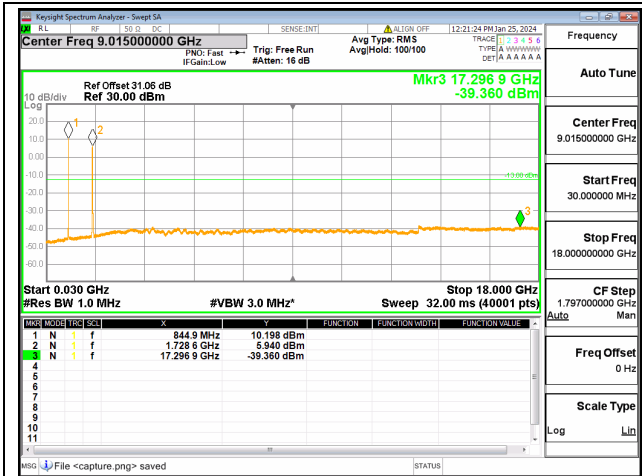
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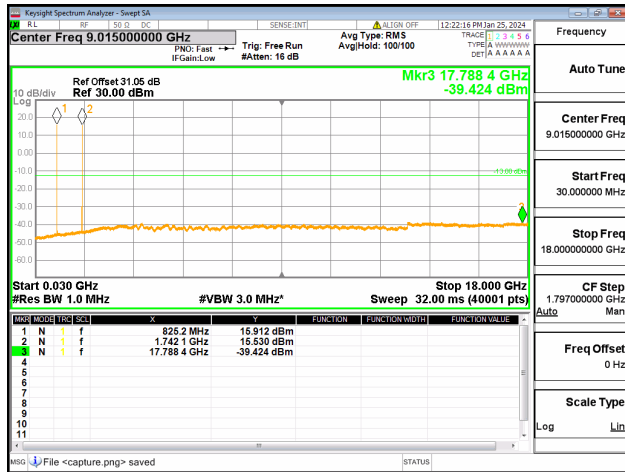
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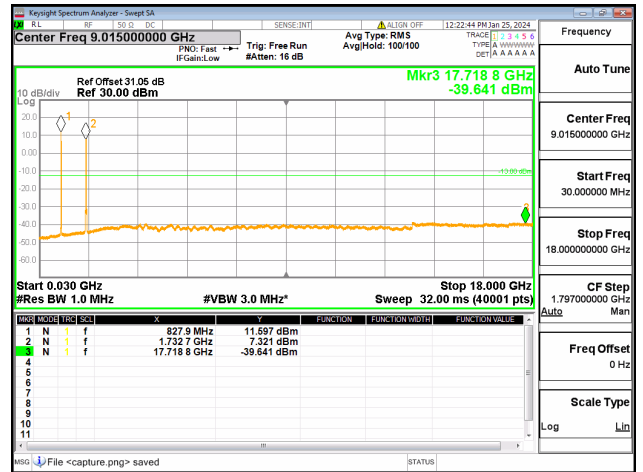
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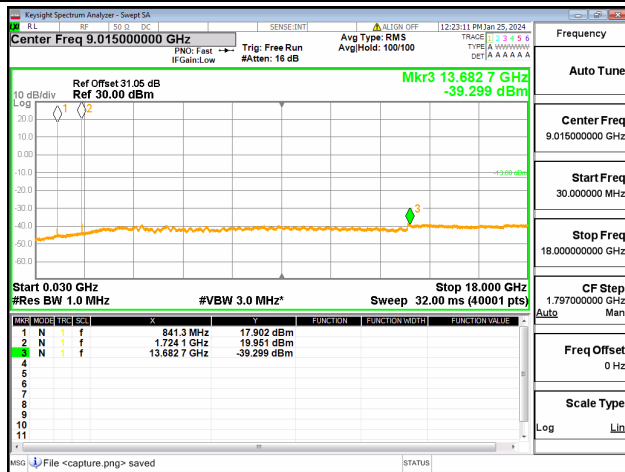
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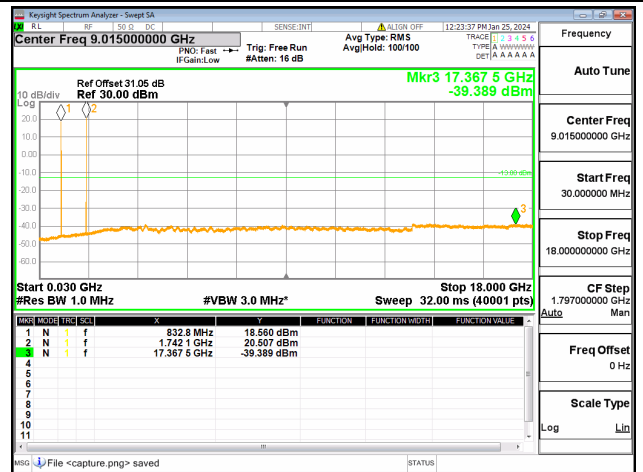
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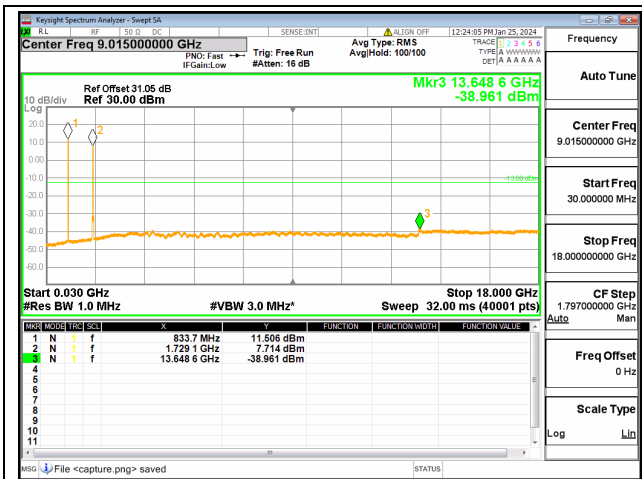
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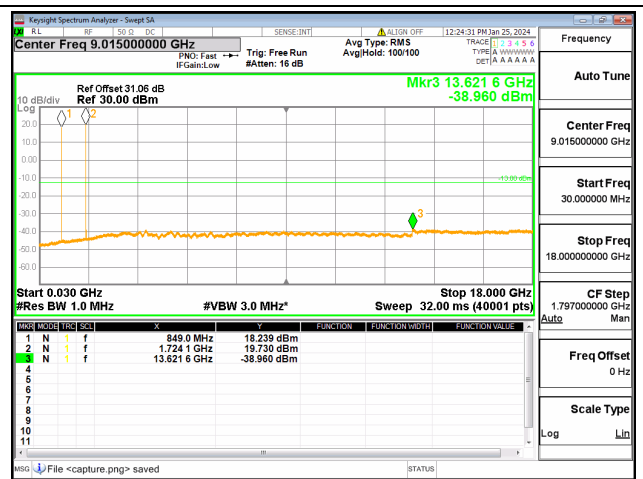
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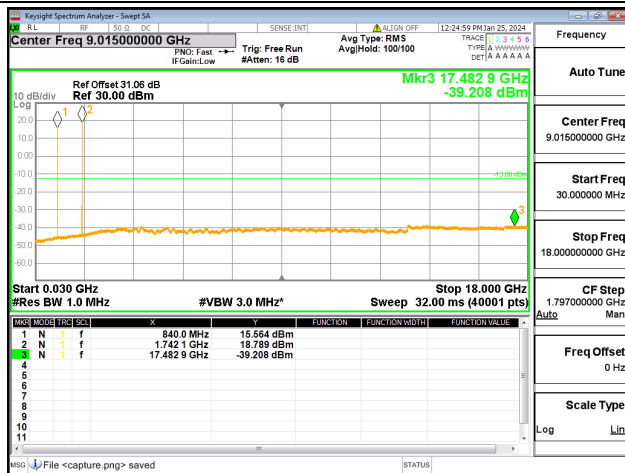
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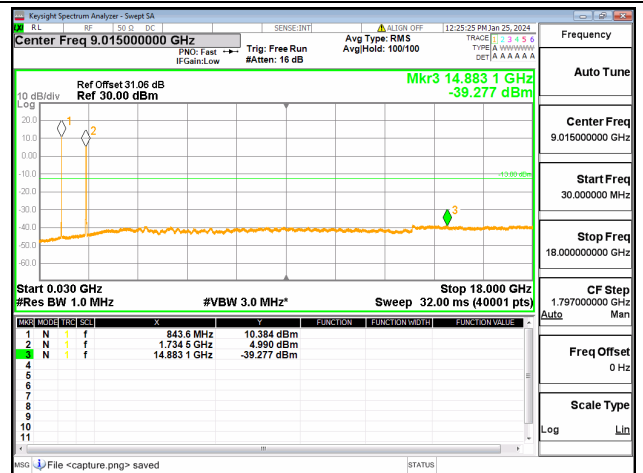
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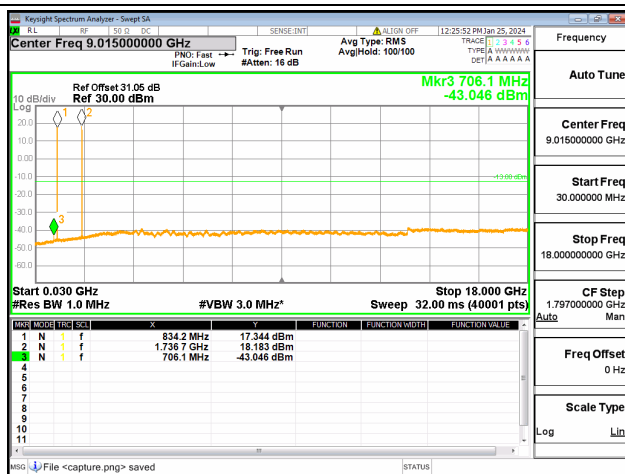
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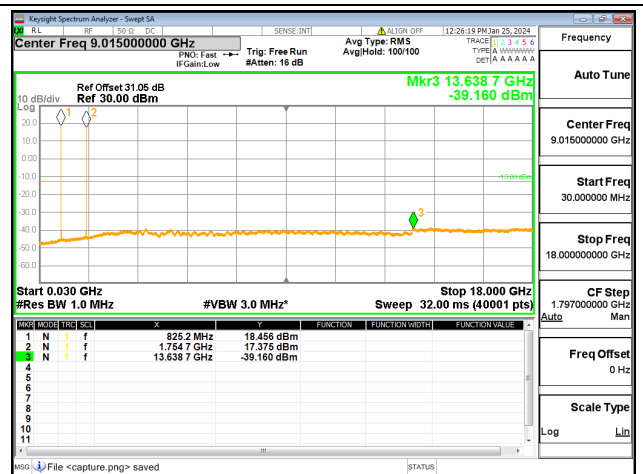
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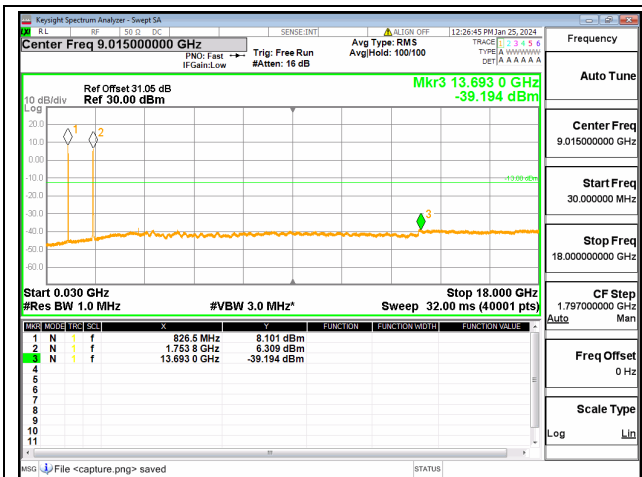
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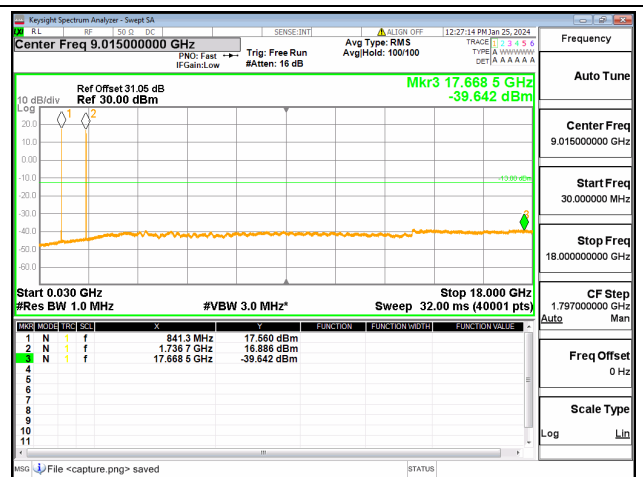
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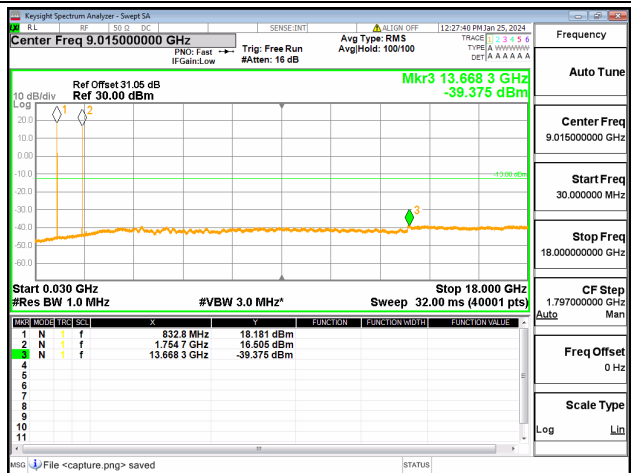
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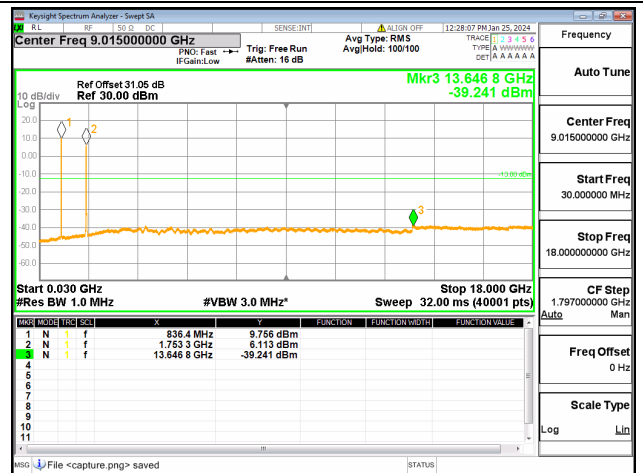
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4A-5A / 20+10MHz / QPSK / High+Mid CH / 1@0-1@49



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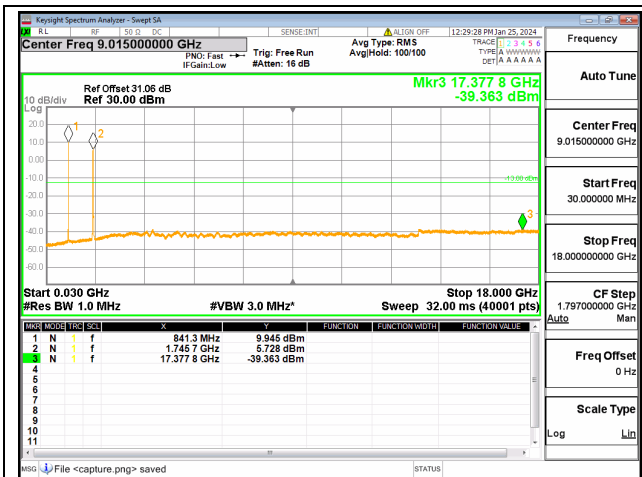
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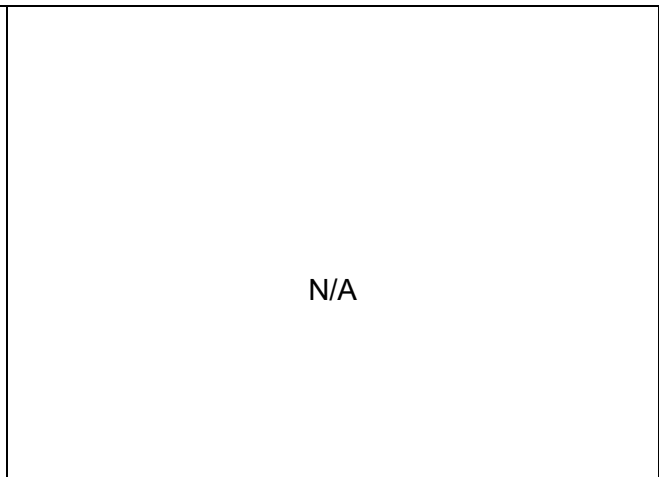
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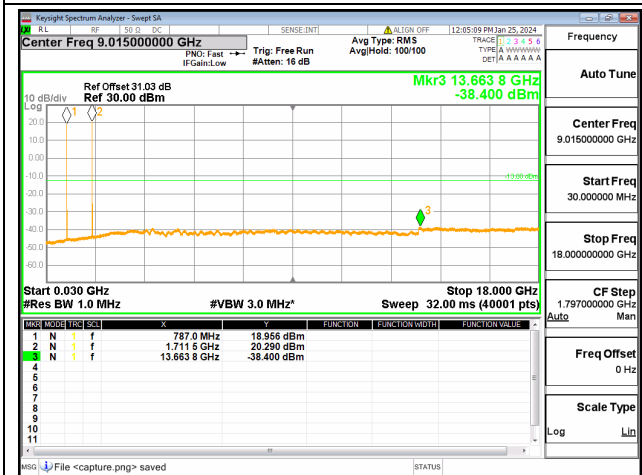
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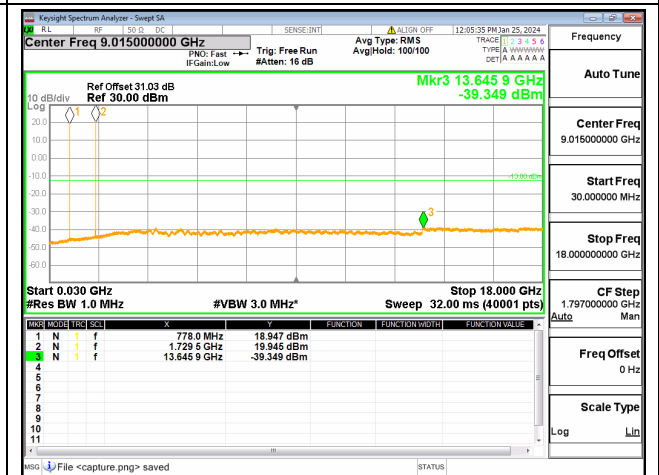
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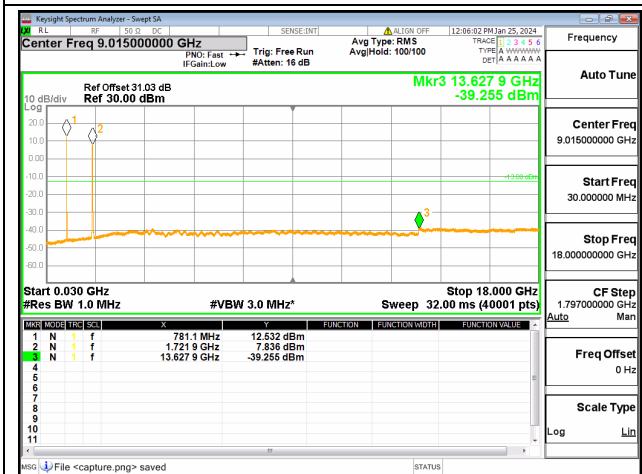
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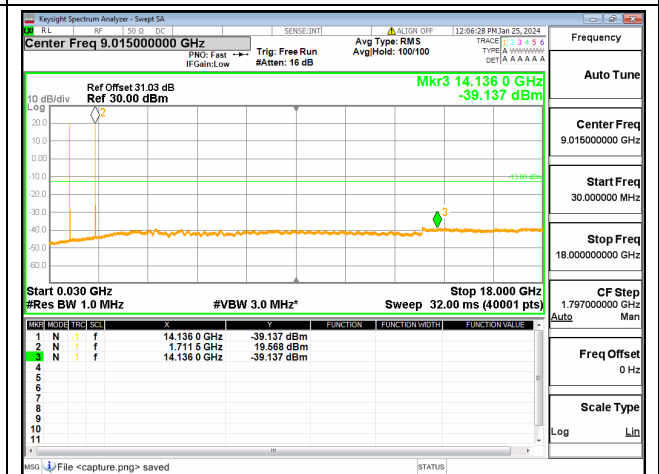
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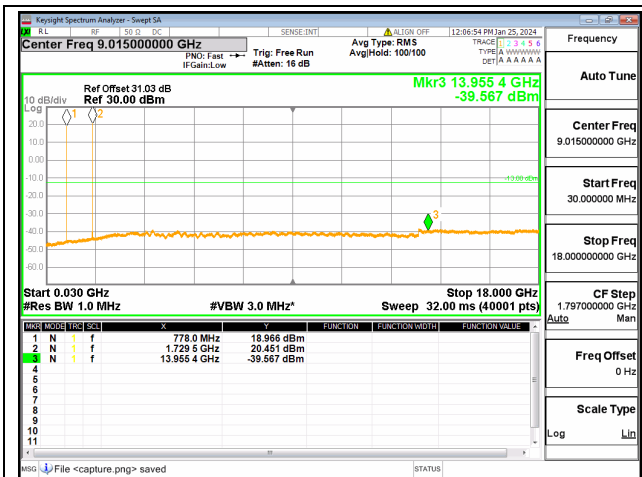
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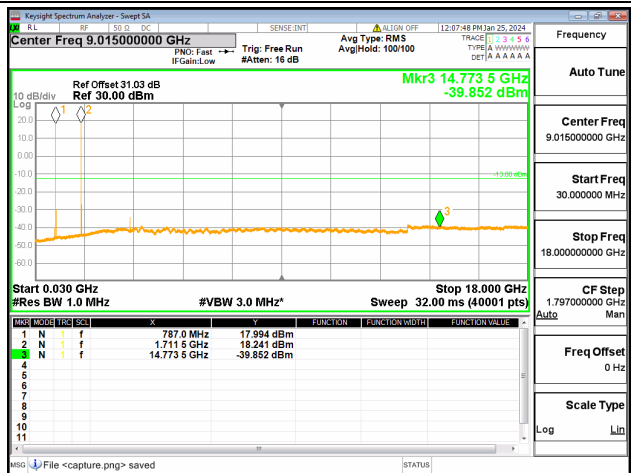
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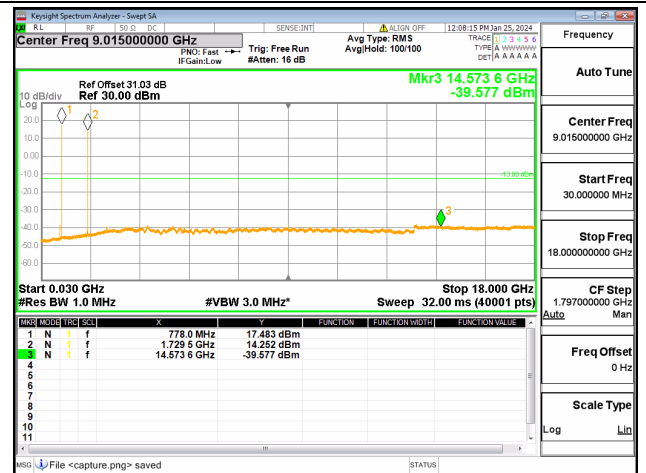
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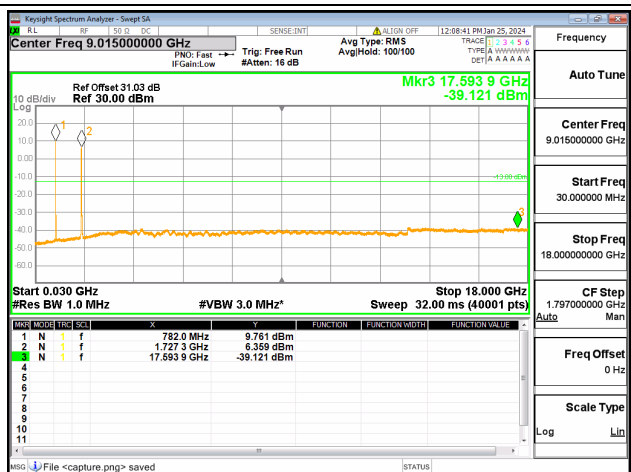
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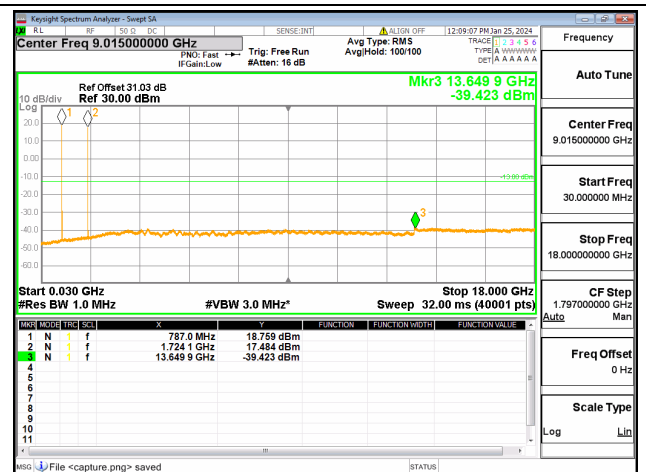
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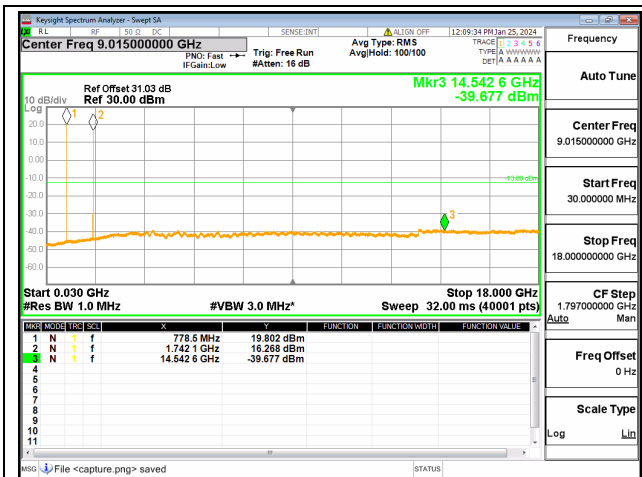
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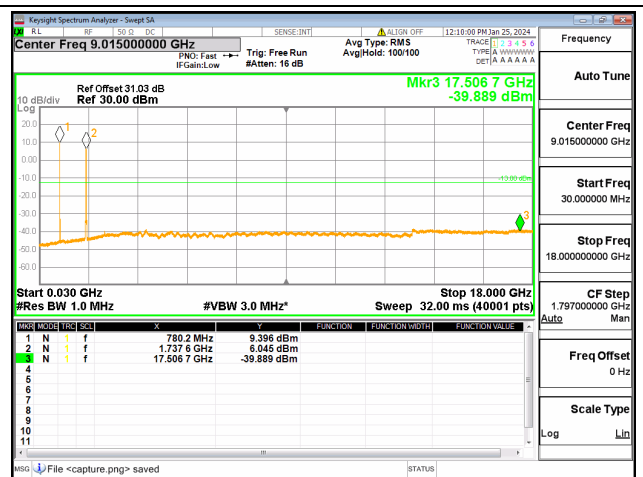
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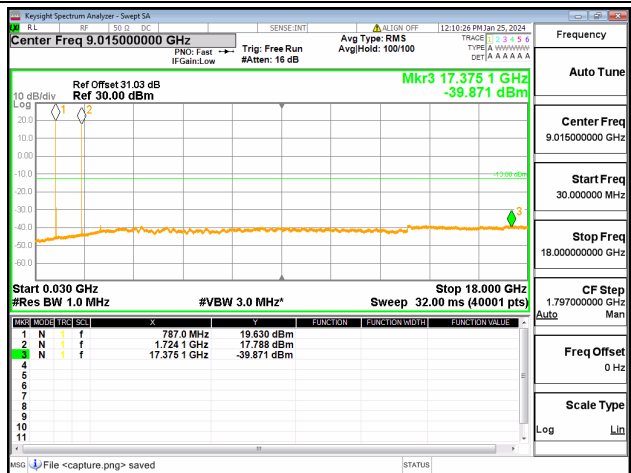
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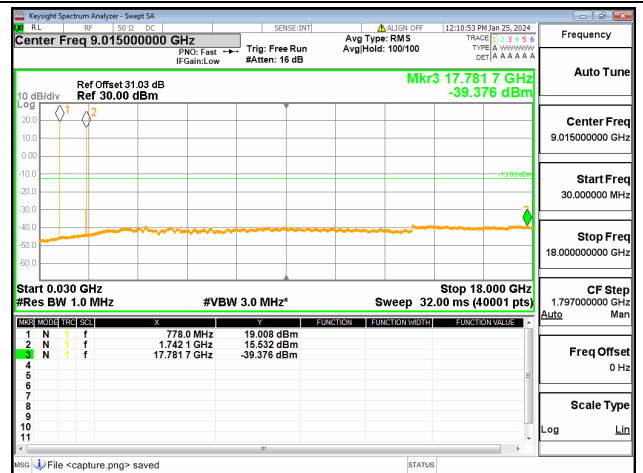
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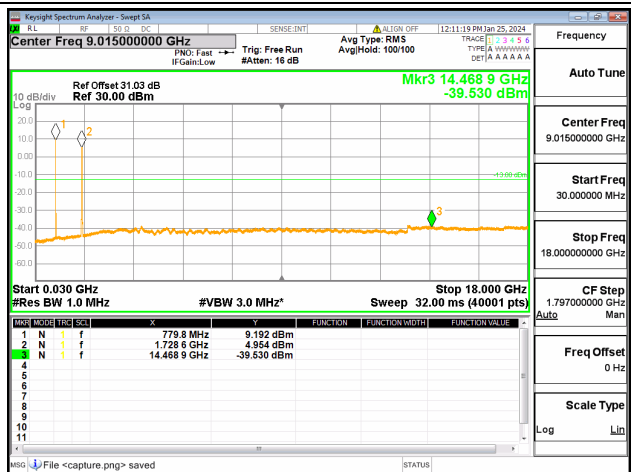
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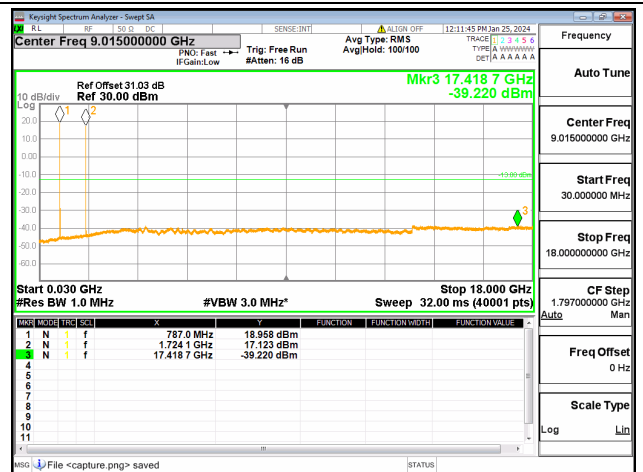
4A-13A / 20+10MHz / QPSK / Mid+Mid CH /
1@0-1@49



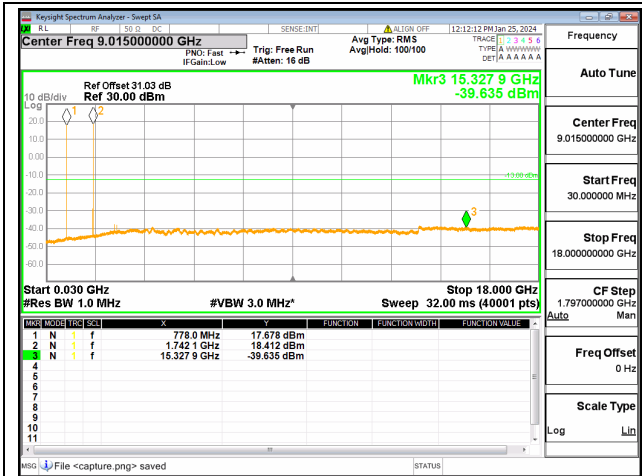
4A-13A / 20+10MHz / QPSK / Mid+Mid CH /
1@99-1@0



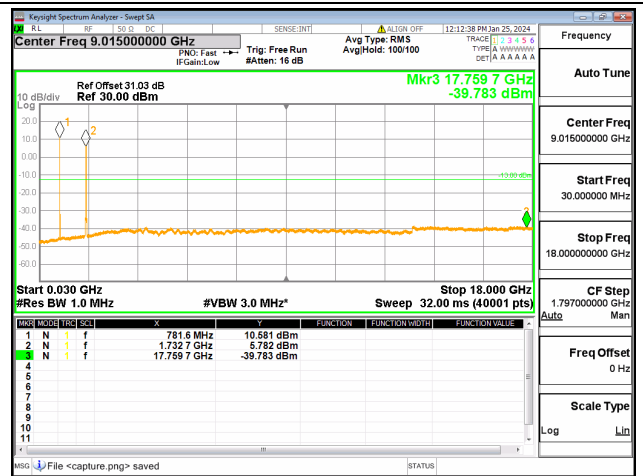
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100@0-50@0



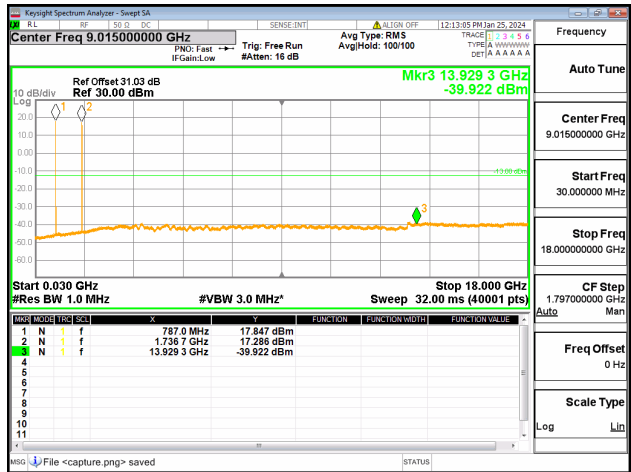
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1@0-1@49



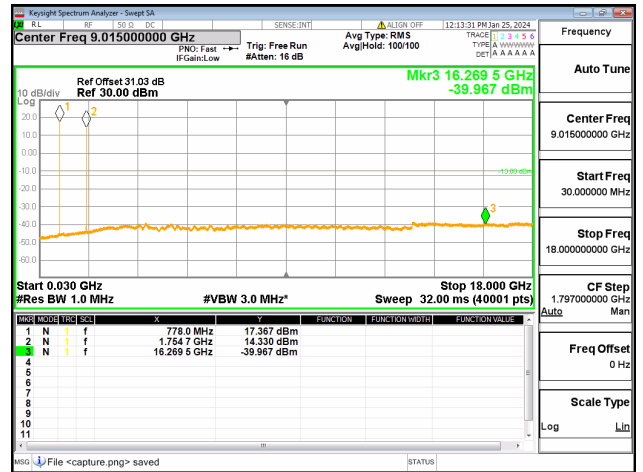
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1@99-1@0



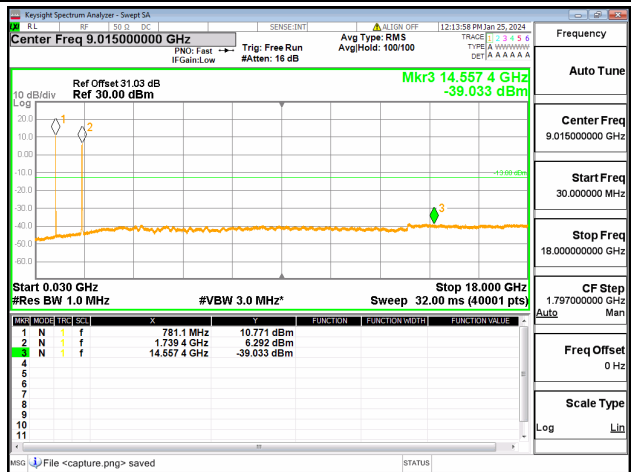
4A-13A / 20+10MHz / QPSK / Mid+High CH /
100@0-50@0



4A-13A / 20+10MHz / QPSK / High+Low CH /
1@0-1@49



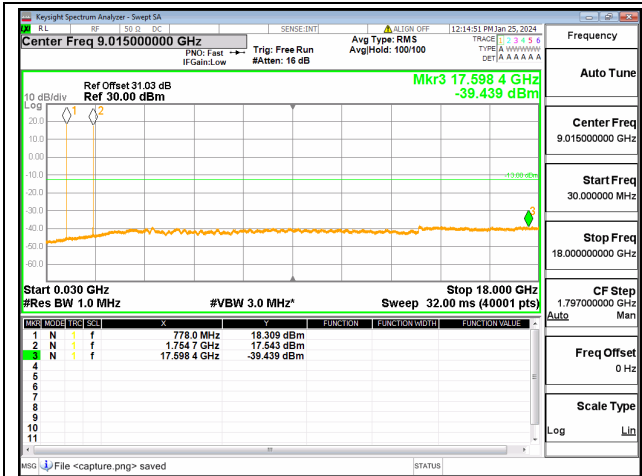
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1@99-1@0



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100@0-50@0



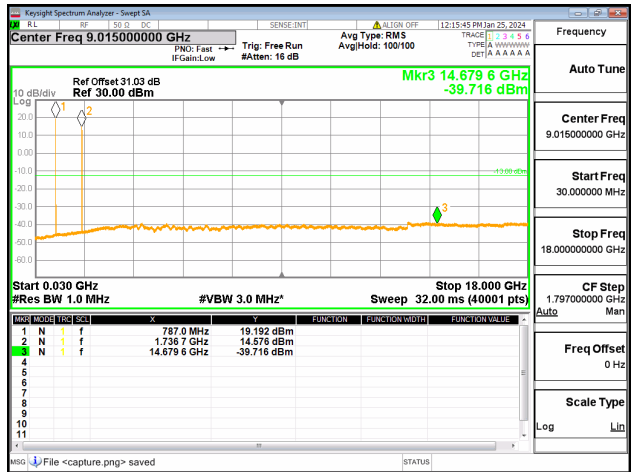
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1@0-1@49



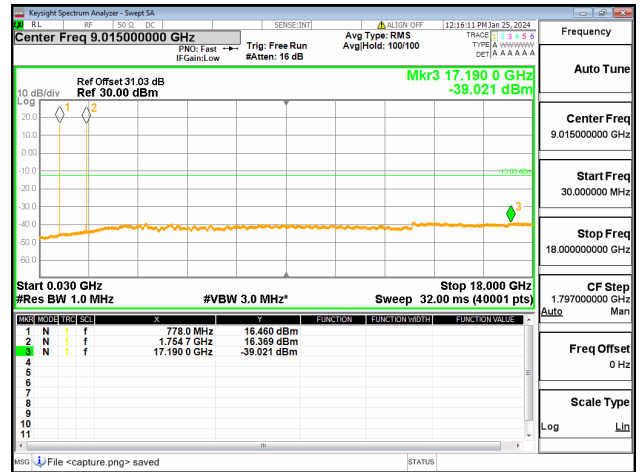
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1@99-1@0



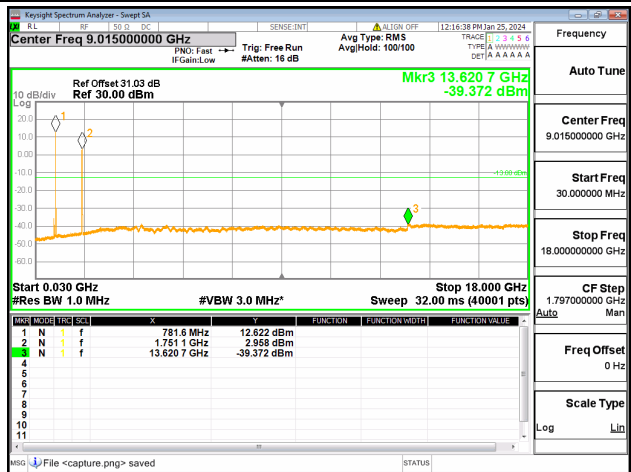
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100@0-50@0



4A-13A / 20+10MHz / QPSK / High+High CH /
1@0-1@49

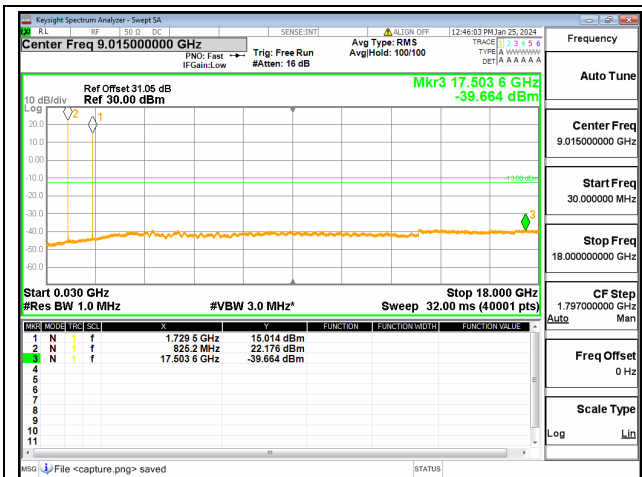


4A-13A / 20+10MHz / QPSK / High+High CH /
1@99-1@0

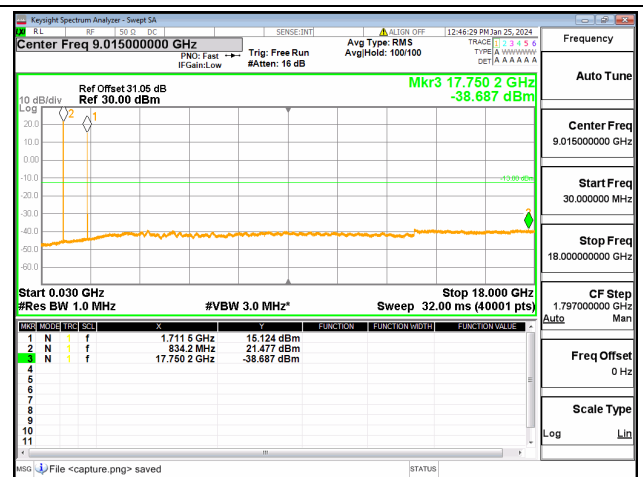


4A-13A / 20+10MHz / QPSK / High+High CH /
100@0-50@0

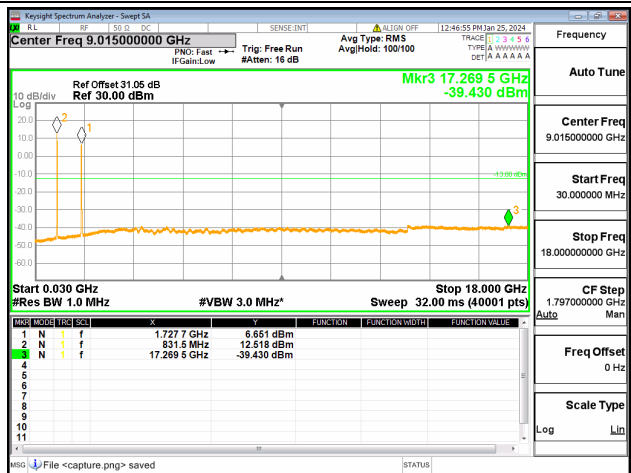
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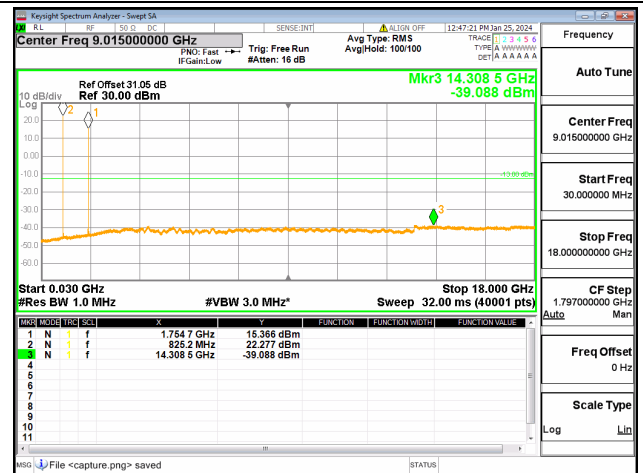
5A-66A / 10+20MHz / QPSK / Low+Low CH / 1@0-1@99



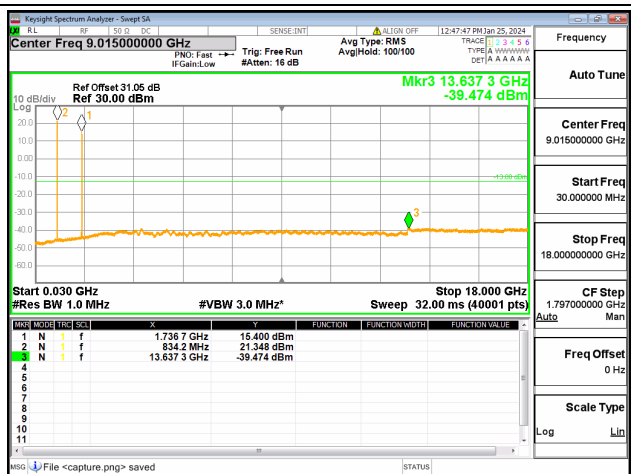
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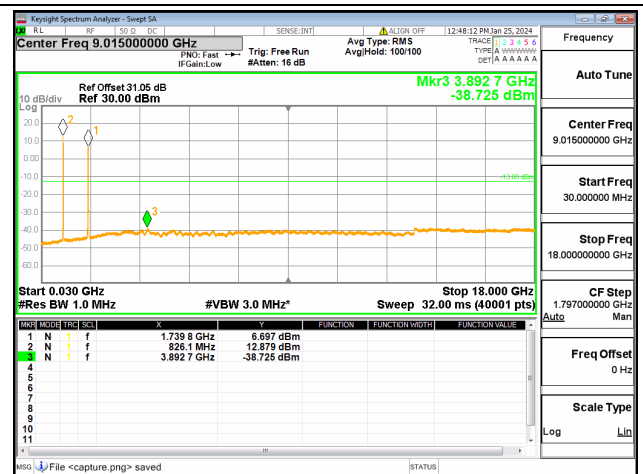
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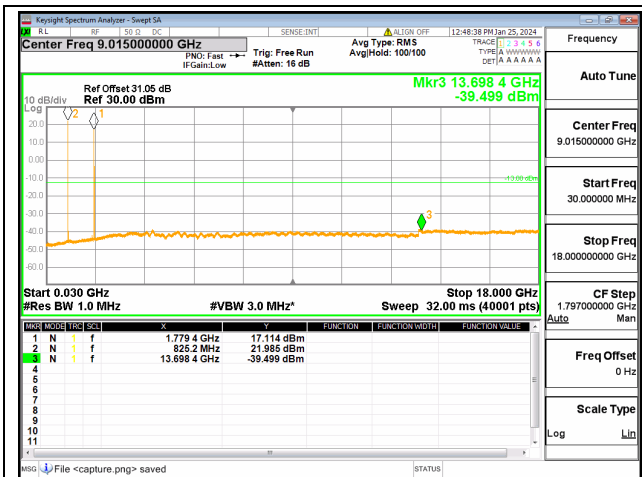
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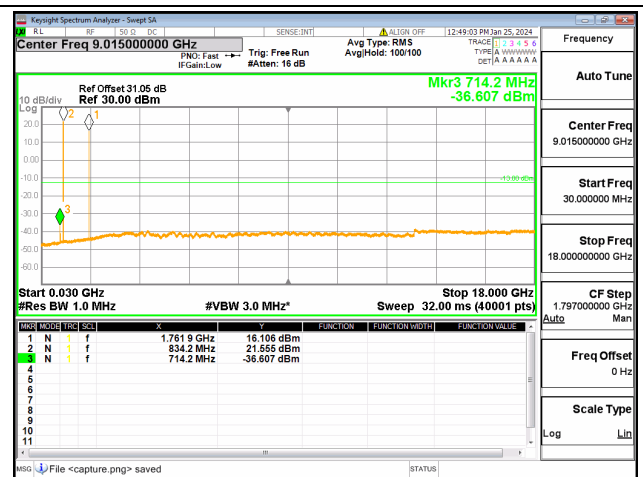
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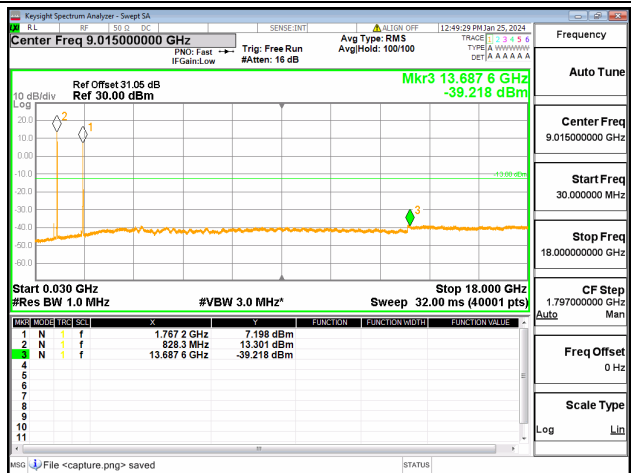
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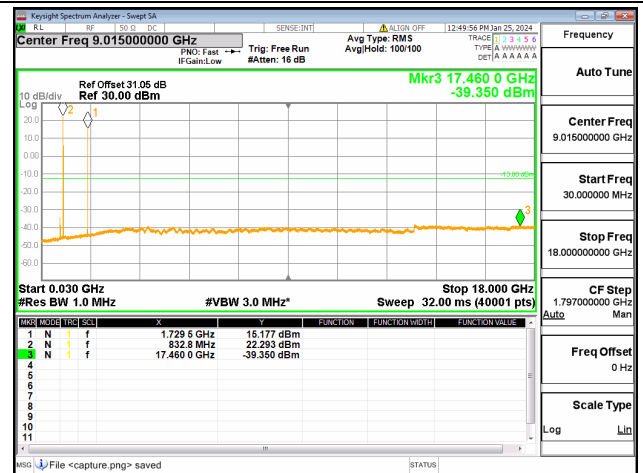
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1@0-1@99



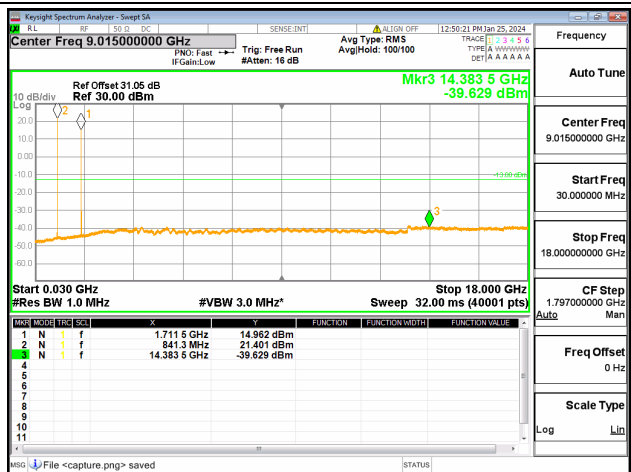
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1@49-1@0



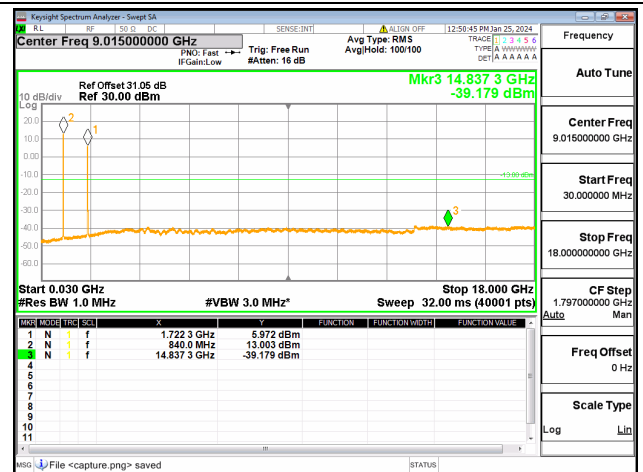
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50@0-100@0



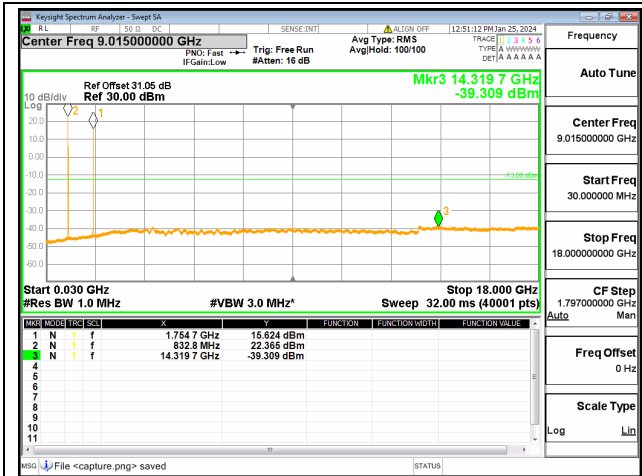
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1@0-1@99



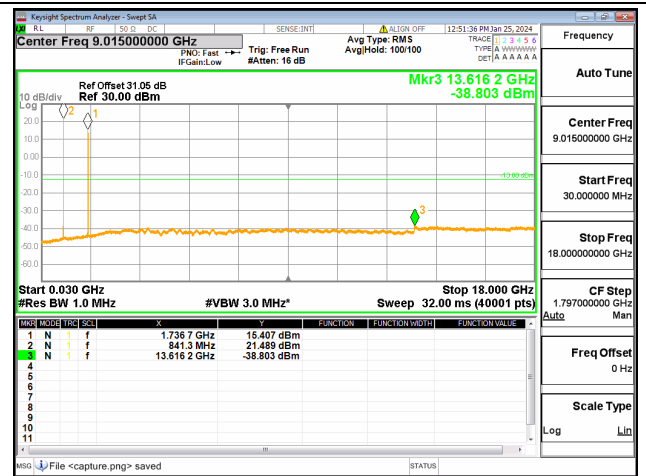
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1@49-1@0



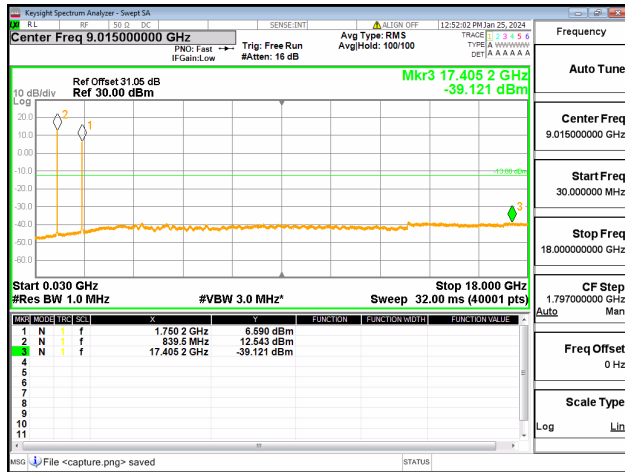
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50@0-100@0



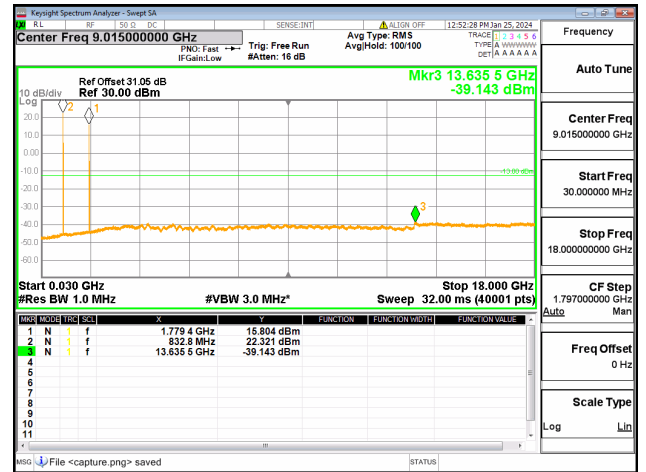
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1@0-1@99



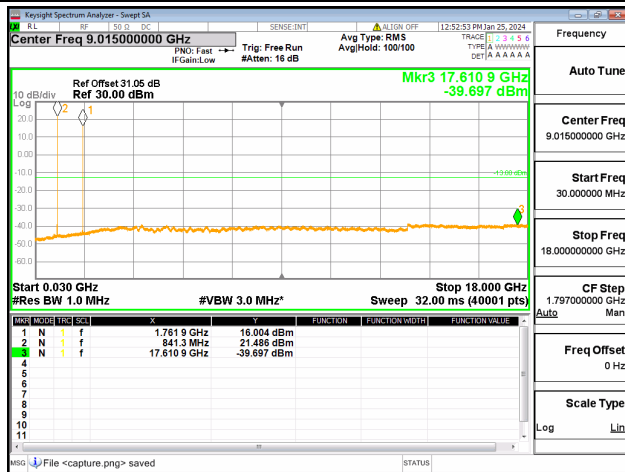
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1@49-1@0



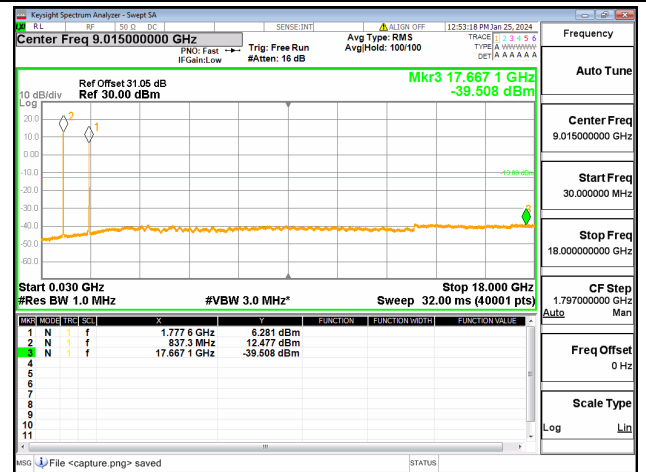
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50@0-100@0



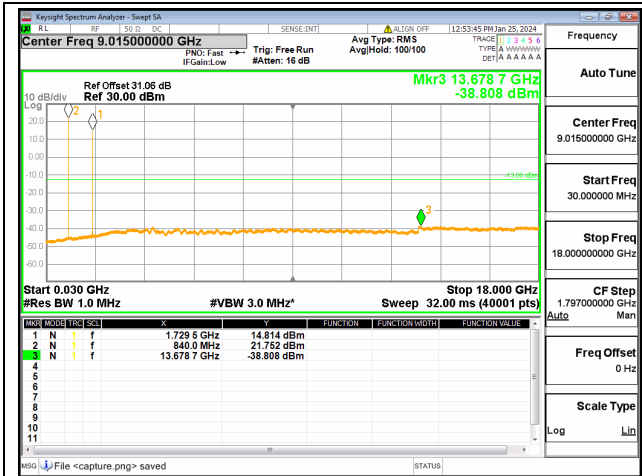
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1@0-1@99



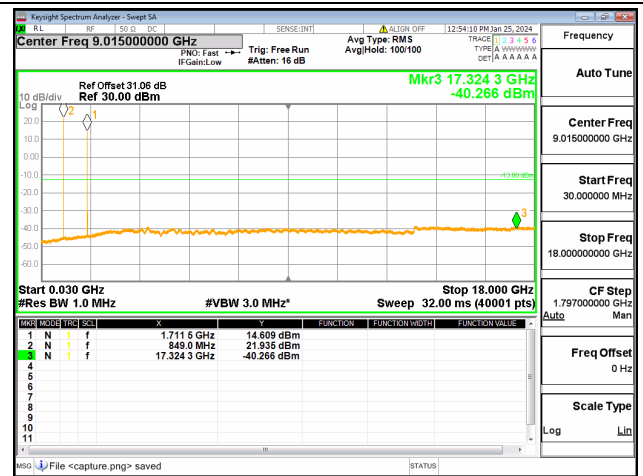
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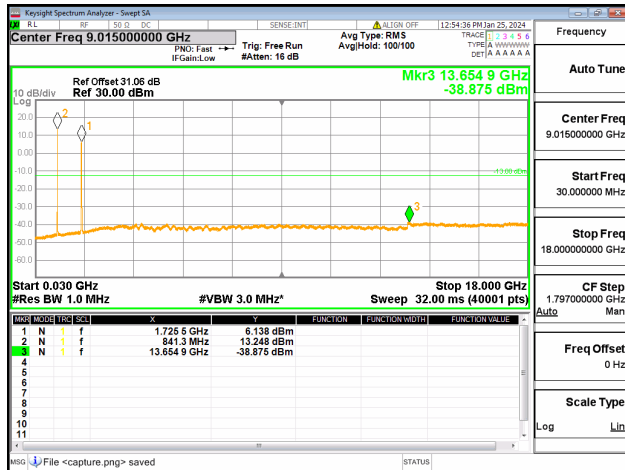
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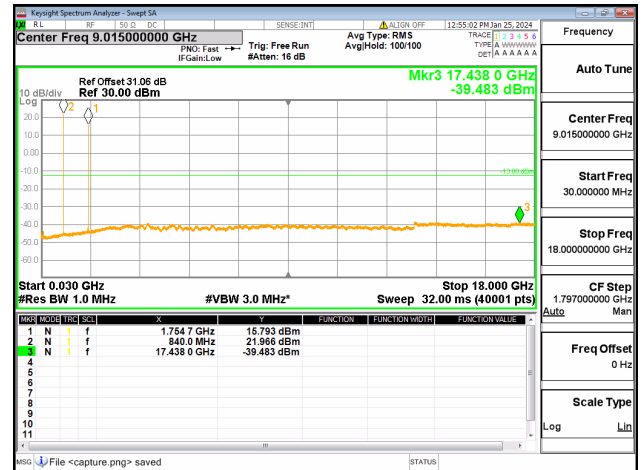
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1@0-1@99



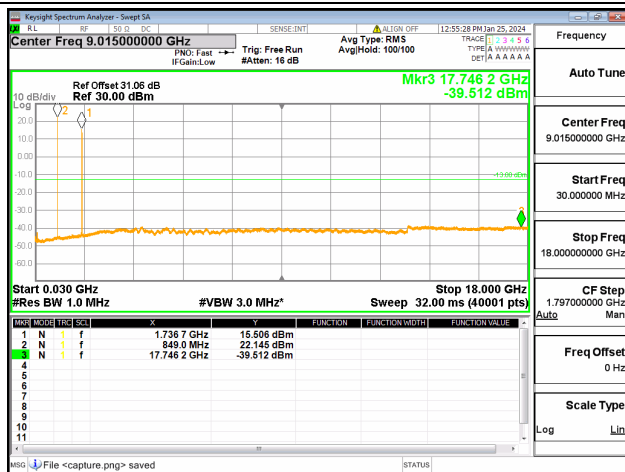
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1@49-1@0



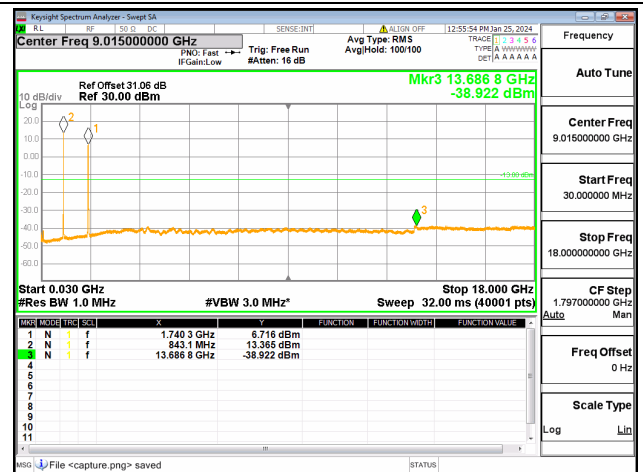
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50@0-100@0



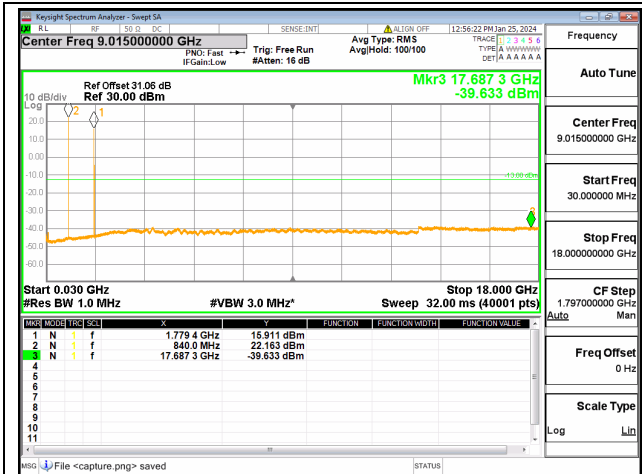
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1@0-1@99



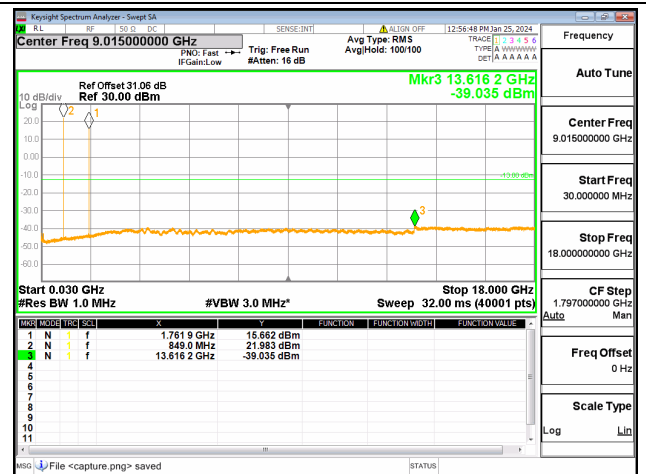
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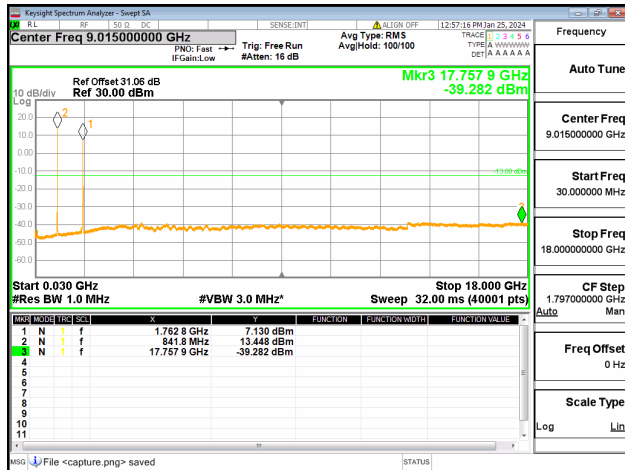
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5A-66A / 10+20MHz / QPSK / High+High CH /
1@0-1@99

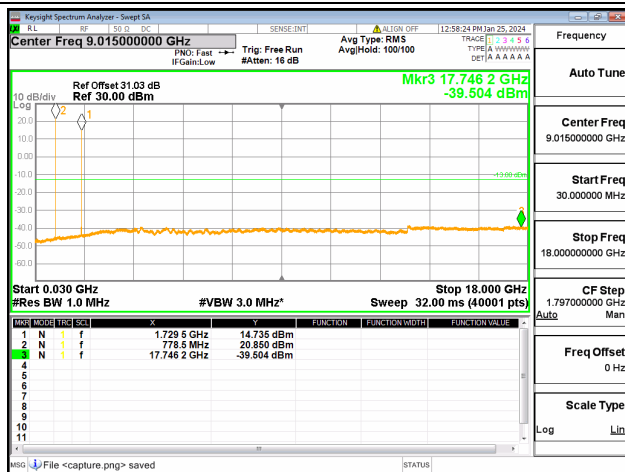


5A-66A / 10+20MHz / QPSK / High+High CH /
1@49-1@0

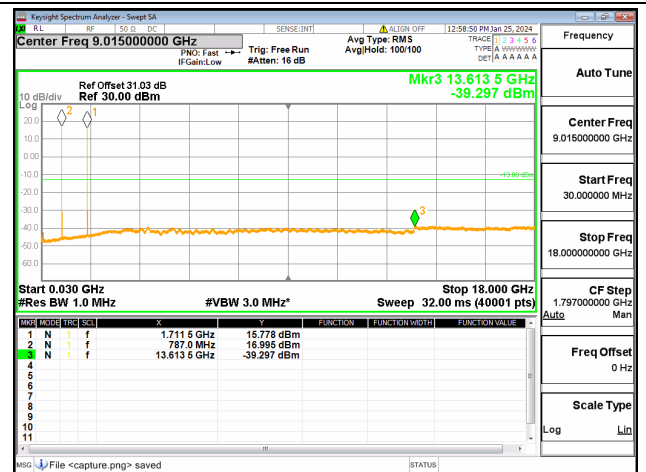


5A-66A / 10+20MHz / QPSK / High+High CH /
50@0-100@0

N/A



13A-66A / 10+20MHz / QPSK / Low+Low CH /
1@0-1@99



13A-66A / 10+20MHz / QPSK / Low+Low CH /
1@49-1@0