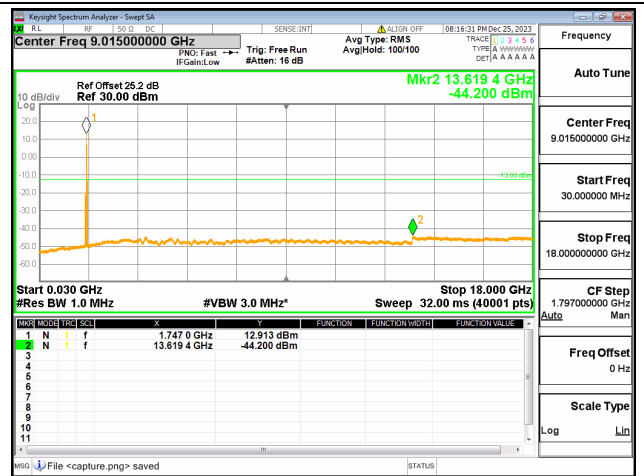
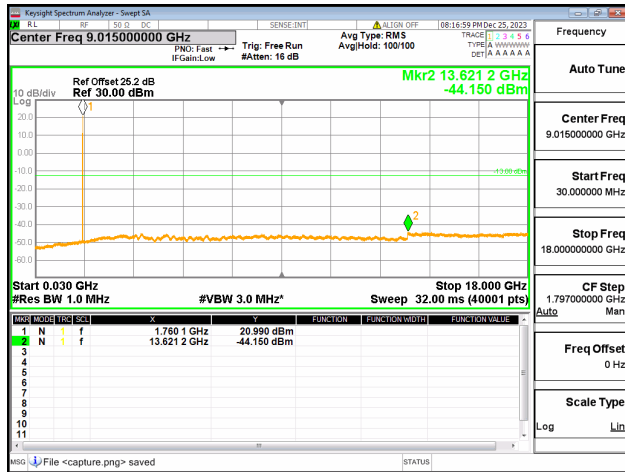


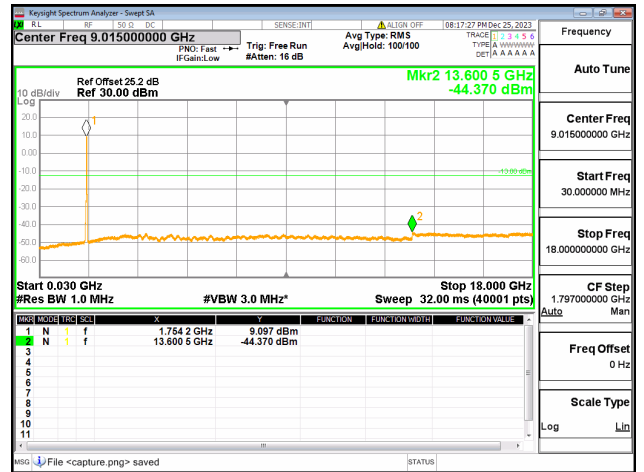
66C / 15+15MHz / QPSK / High CH /  
75@0-75@0



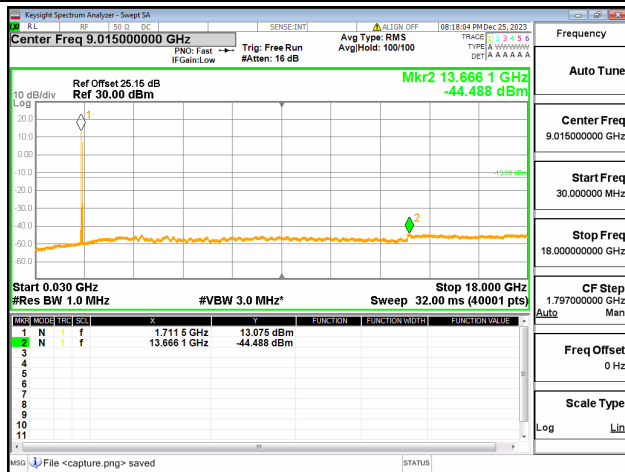
66C / 15+20MHz / QPSK / High CH /  
1@0-1@99



66C / 15+20MHz / QPSK / High CH /  
1@74-1@0



66C / 15+20MHz / QPSK / High CH /  
75@0-100@0



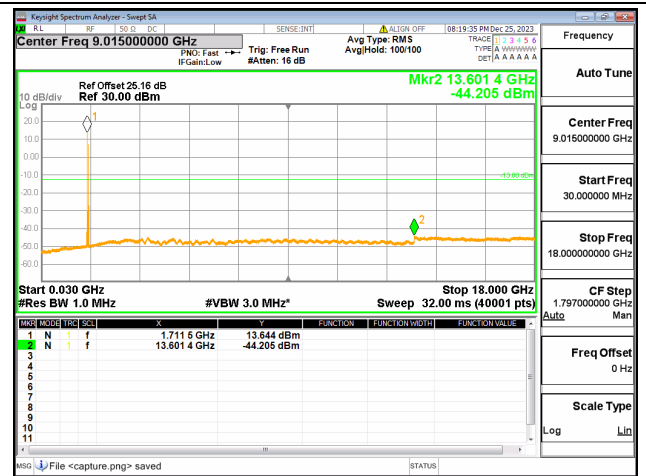
66C / 20+10MHz / QPSK / Low CH /  
1@0-1@49



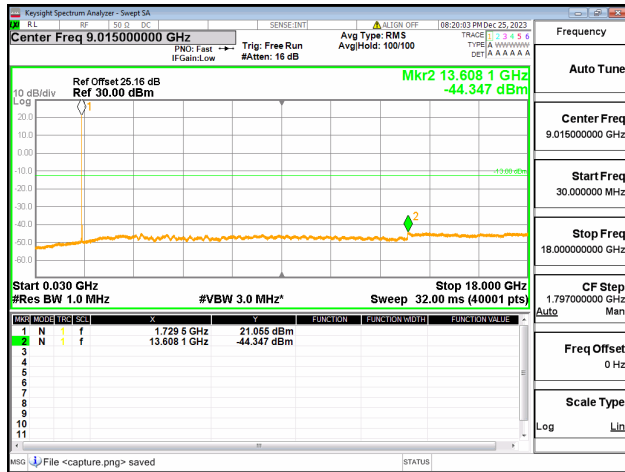
66C / 20+10MHz / QPSK / Low CH /  
1@99-1@0



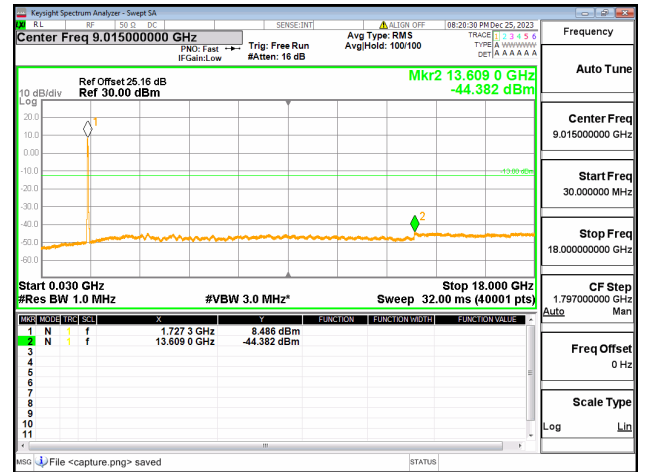
66C / 20+10MHz / QPSK / Low CH /  
100@0-50@0



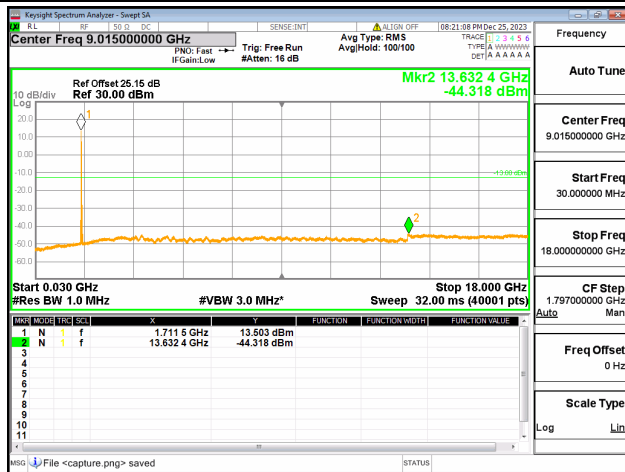
66C / 20+15MHz / QPSK / Low CH /  
1@0-1@74



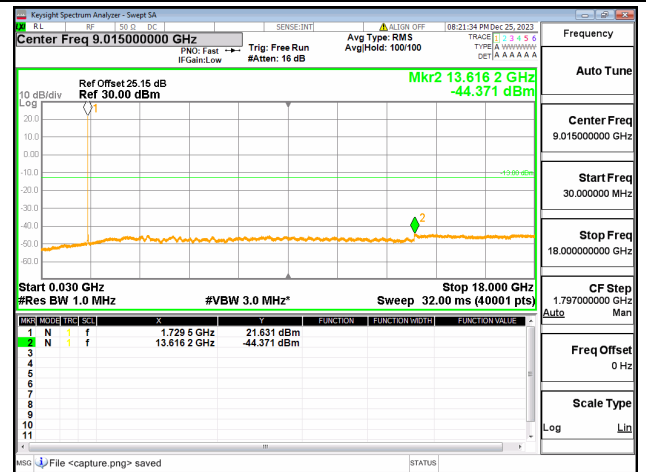
66C / 20+15MHz / QPSK / Low CH /  
1@99-1@0



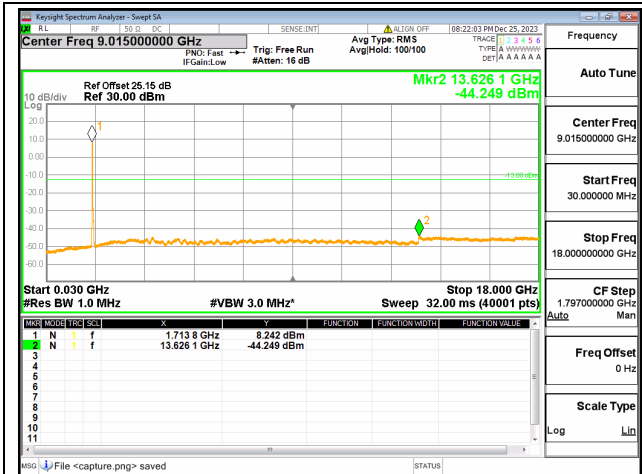
66C / 20+15MHz / QPSK / Low CH /  
100@0-75@0



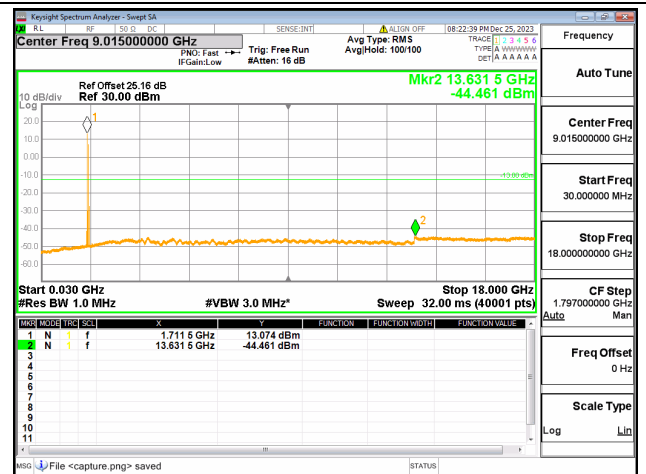
66C / 20+5MHz / QPSK / Low CH / 1@0-1@24



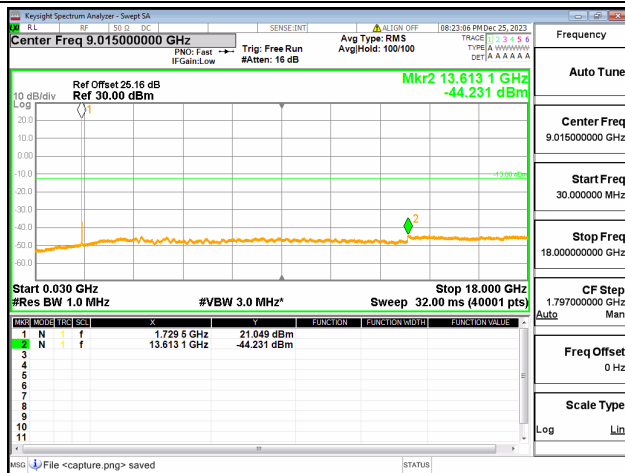
66C / 20+5MHz / QPSK / Low CH / 1@99-1@0



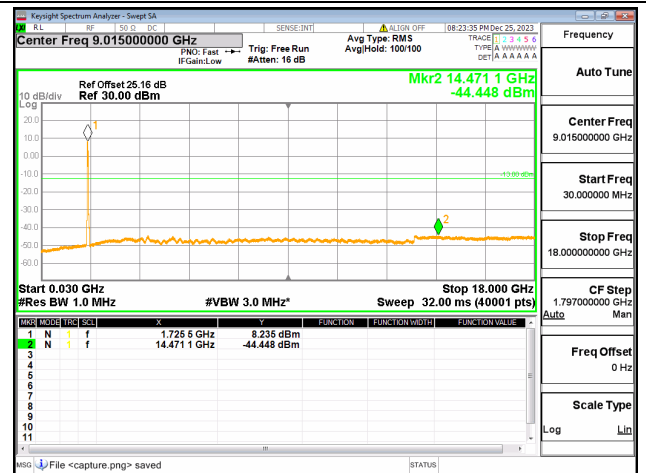
66C / 20+5MHz / QPSK / Low CH /  
100@0-25@0



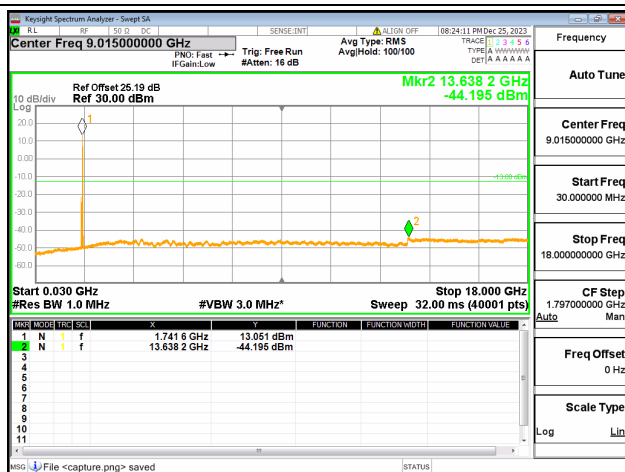
66C / 20+20MHz / QPSK / Low CH /  
1@0-1@99



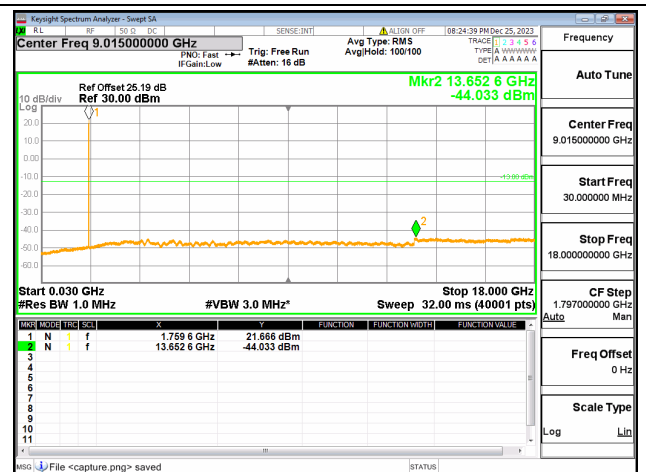
66C / 20+20MHz / QPSK / Low CH /  
1@99-1@0



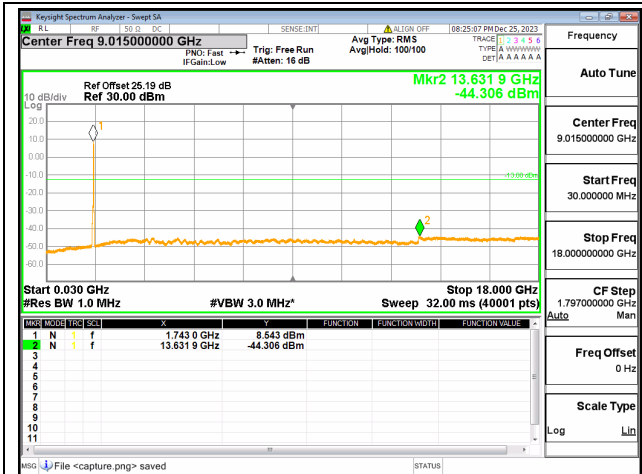
66C / 20+20MHz / QPSK / Low CH /  
100@0-100@0



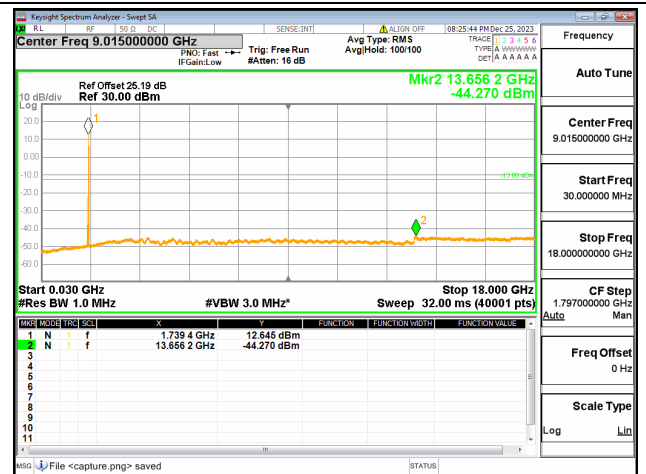
66C / 20+10MHz / QPSK / Mid CH / 1@0-1@49



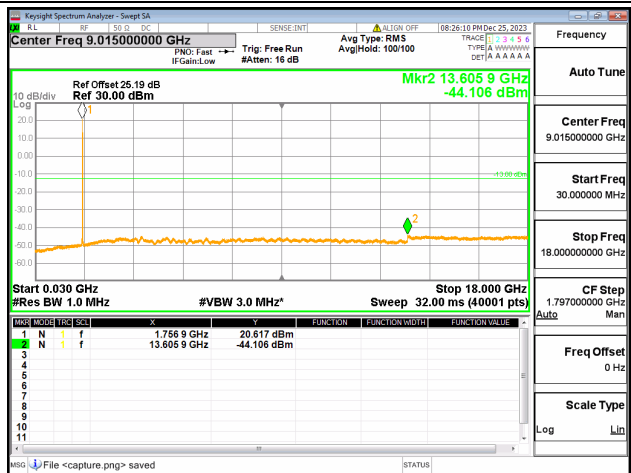
66C / 20+10MHz / QPSK / Mid CH / 1@99-1@0



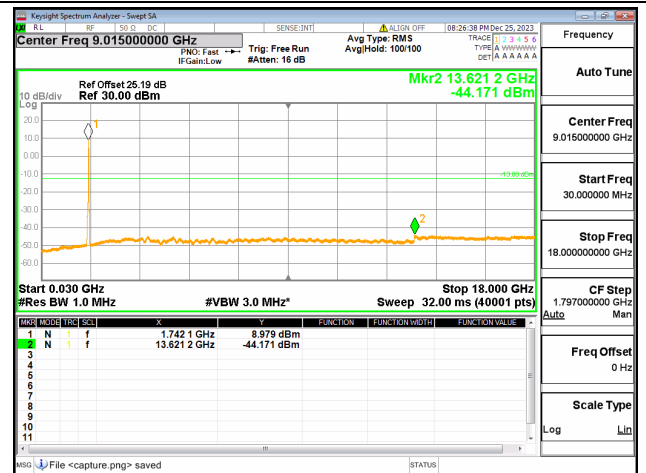
66C / 20+10MHz / QPSK / Mid CH /  
100@0-50@0



66C / 20+15MHz / QPSK / Mid CH / 1@0-1@74



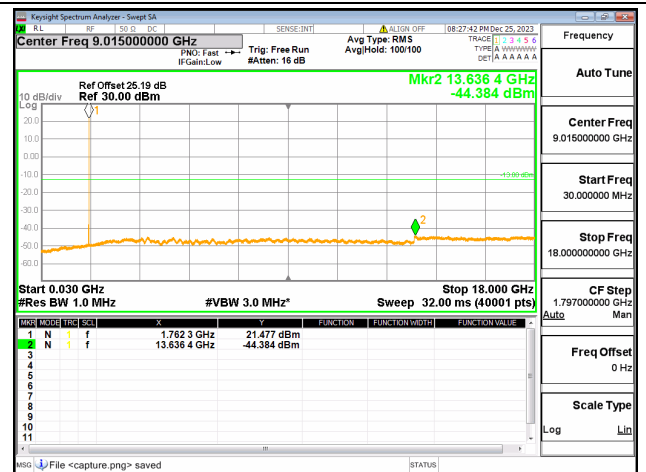
66C / 20+15MHz / QPSK / Mid CH / 1@99-1@0



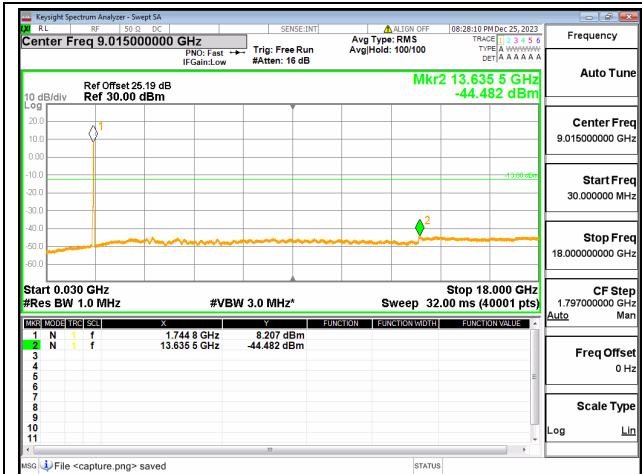
66C / 20+15MHz / QPSK / Mid CH /  
100@0-75@0



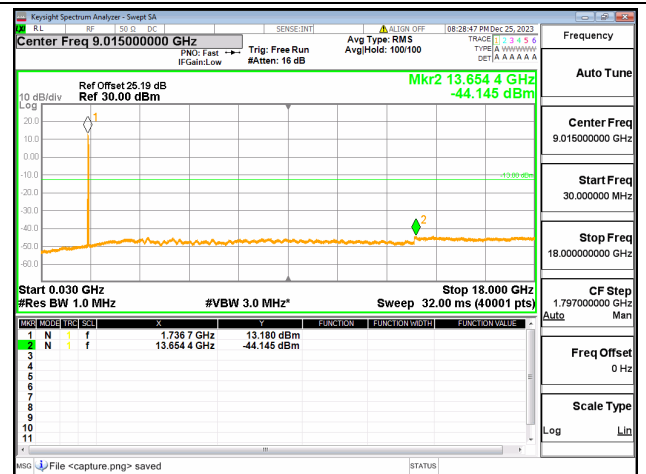
66C / 20+5MHz / QPSK / Mid CH / 1@0-1@24



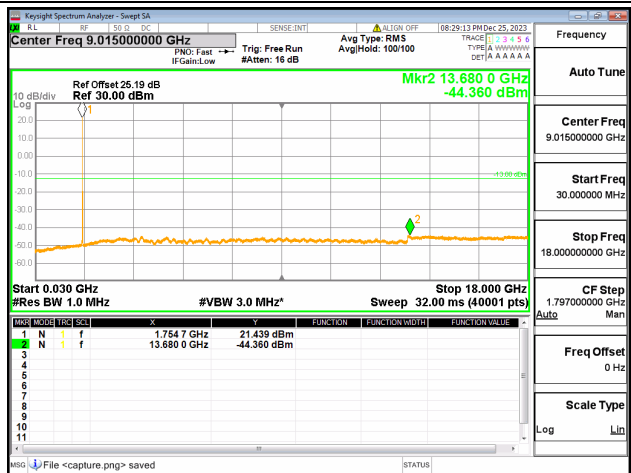
66C / 20+5MHz / QPSK / Mid CH / 1@99-1@0



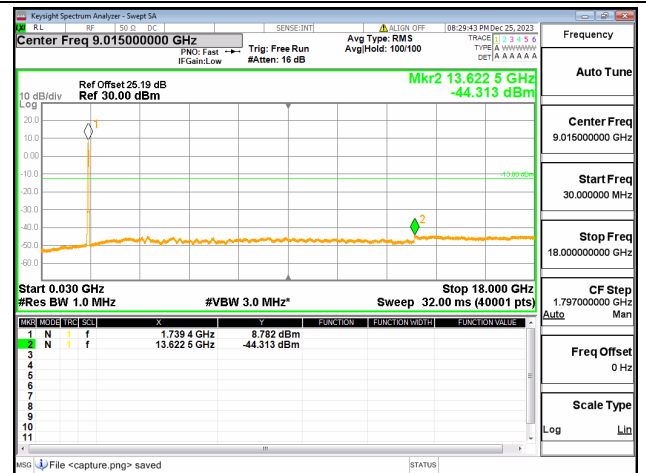
66C / 20+5MHz / QPSK / Mid CH /  
100@0-25@0



66C / 20+20MHz / QPSK / Mid CH / 1@0-1@99



66C / 20+20MHz / QPSK / Mid CH / 1@99-1@0



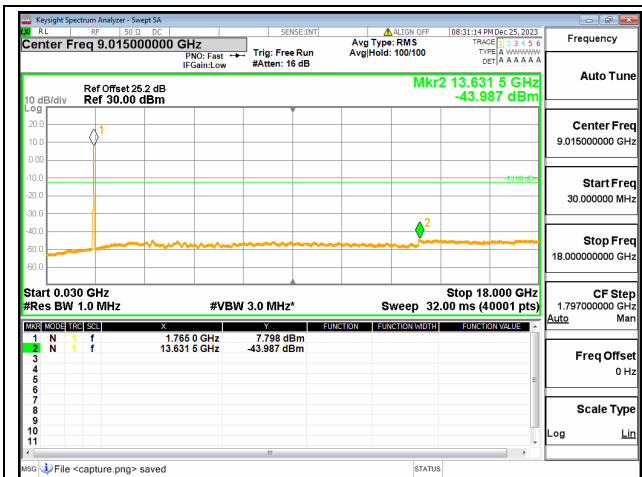
66C / 20+20MHz / QPSK / Mid CH /  
100@0-100@0



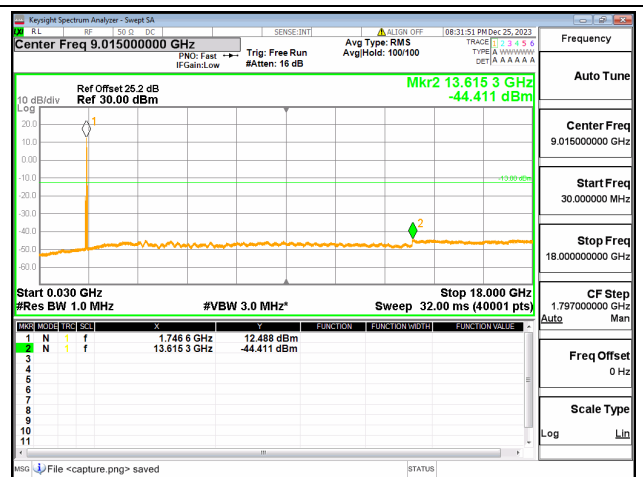
66C / 20+10MHz / QPSK / High CH /  
1@0-1@49



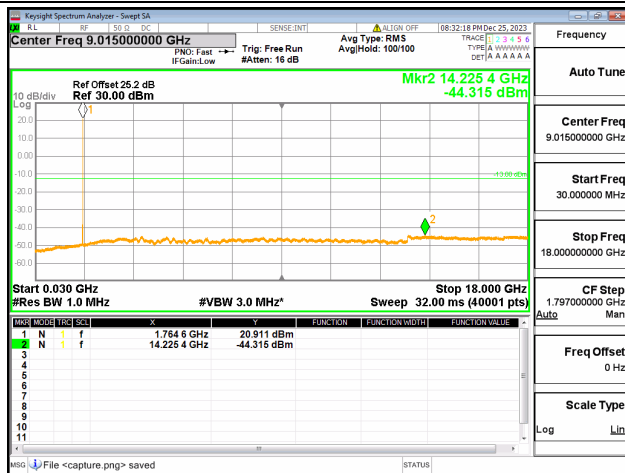
66C / 20+10MHz / QPSK / High CH /  
1@99-1@0



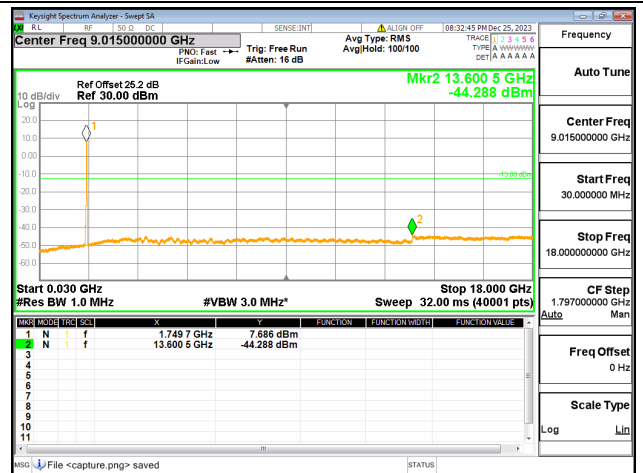
66C / 20+10MHz / QPSK / High CH / 100@0-50@0



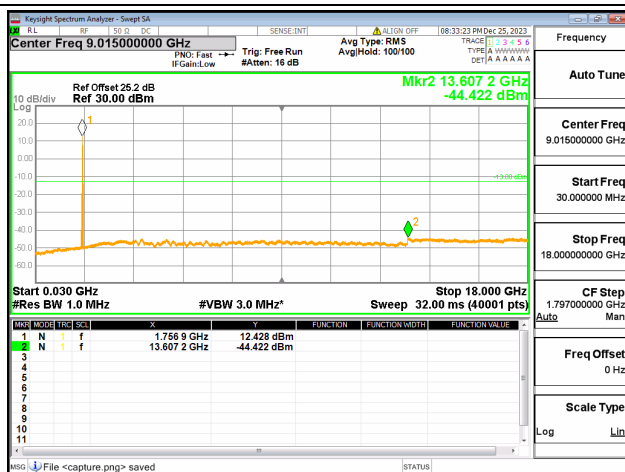
66C / 20+15MHz / QPSK / High CH / 1@0-1@74



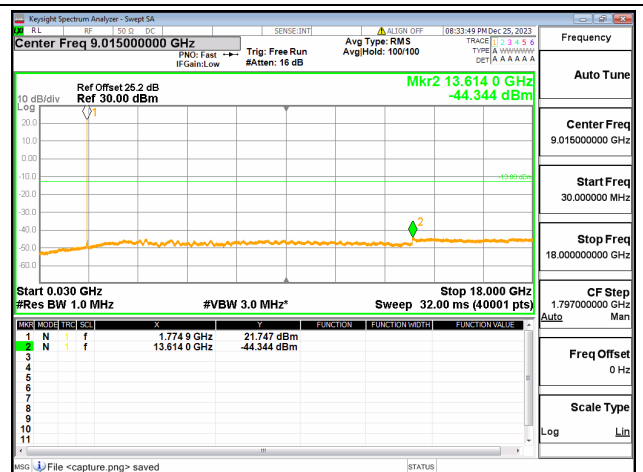
66C / 20+15MHz / QPSK / High CH / 1@99-1@0



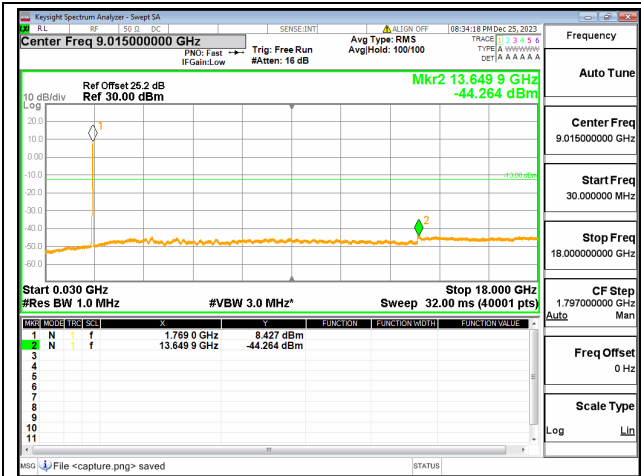
66C / 20+15MHz / QPSK / High CH / 100@0-75@0



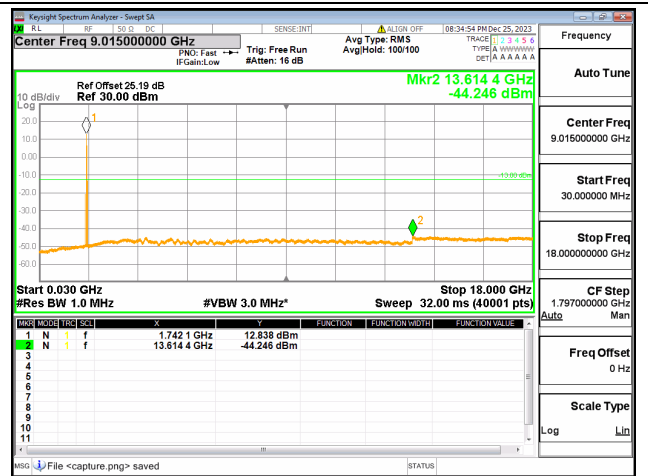
66C / 20+5MHz / QPSK / High CH / 1@0-1@24



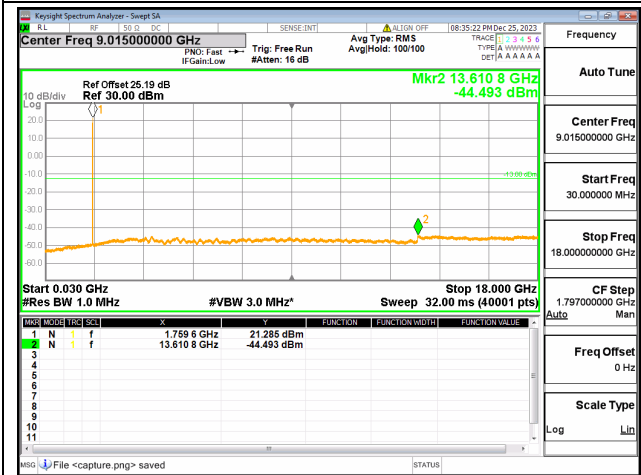
66C / 20+5MHz / QPSK / High CH / 1@99-1@0



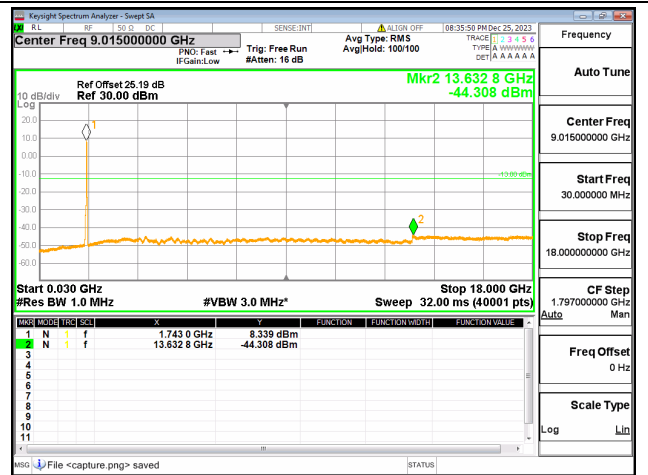
66C / 20+5MHz / QPSK / High CH /  
100@0-25@0



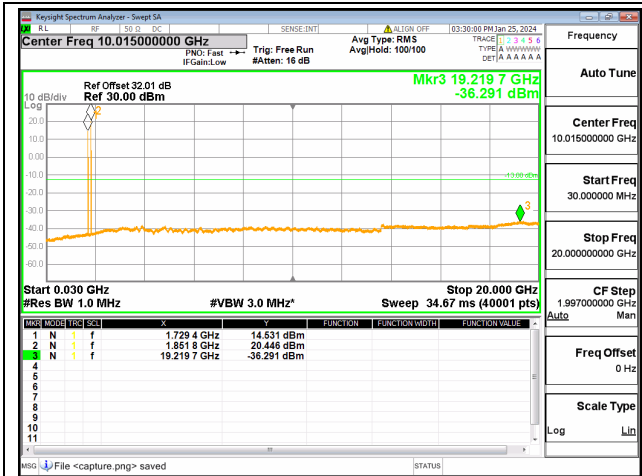
66C / 20+20MHz / QPSK / High CH /  
1@0-1@99



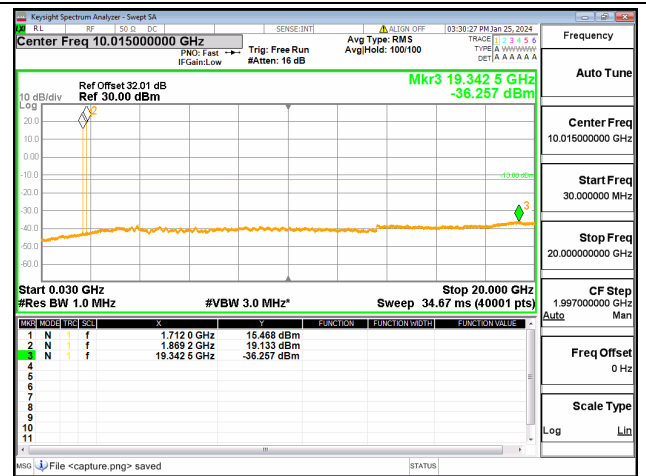
66C / 20+20MHz / QPSK / High CH /  
1@99-1@0



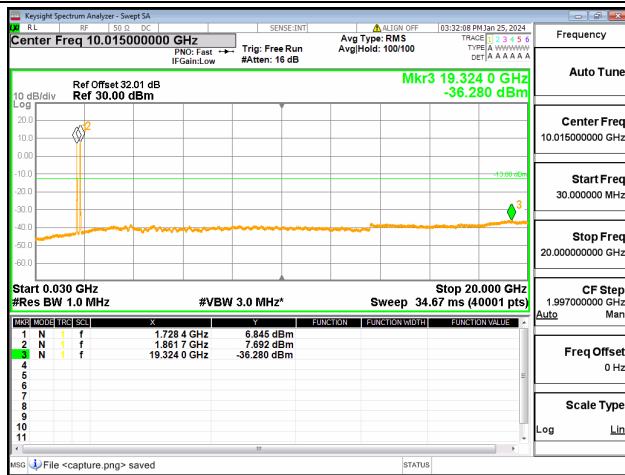
66C / 20+20MHz / QPSK / High CH /  
100@0-100@0



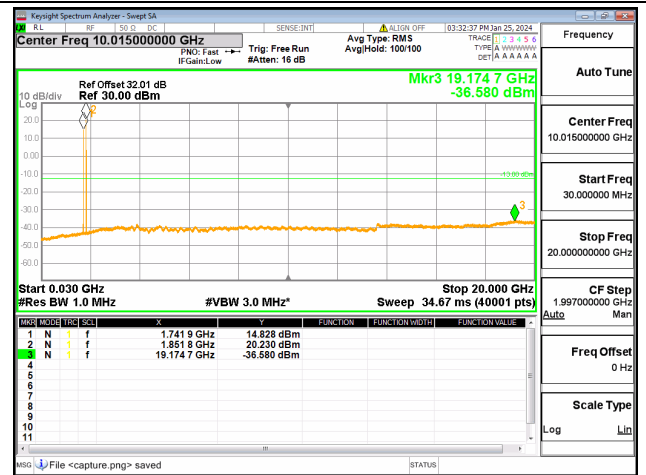
2A-4A / 20+20MHz / QPSK / Low+Low CH /  
1@0-1@99



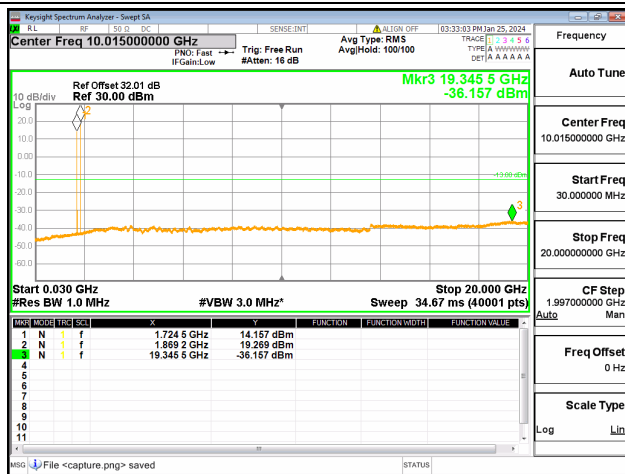
2A-4A / 20+20MHz / QPSK / Low+Low CH /  
1@99-1@0



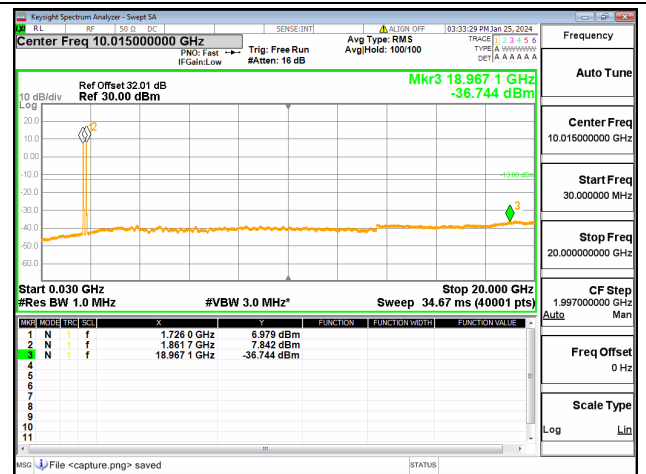
2A-4A / 20+20MHz / QPSK / Low+Low CH /  
100@0-100@0



2A-4A / 20+20MHz / QPSK / Low+Mid CH /  
1@0-1@99

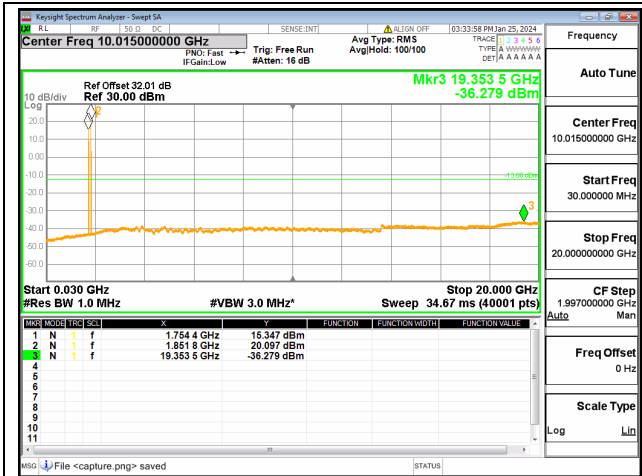


2A-4A / 20+20MHz / QPSK / Low+Mid CH /  
1@99-1@0

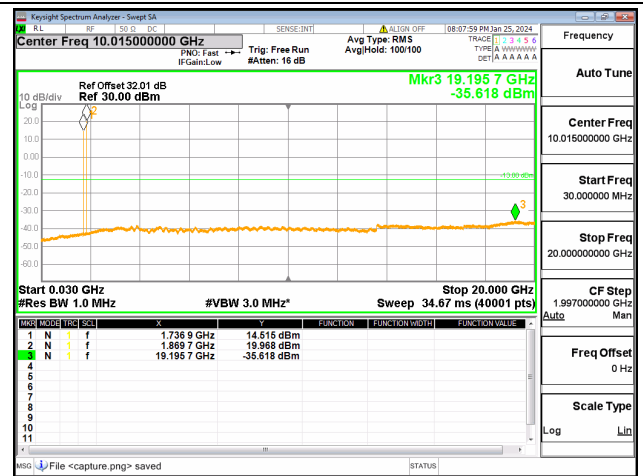


2A-4A / 20+20MHz / QPSK / Low+Mid CH /  
100@0-100@0

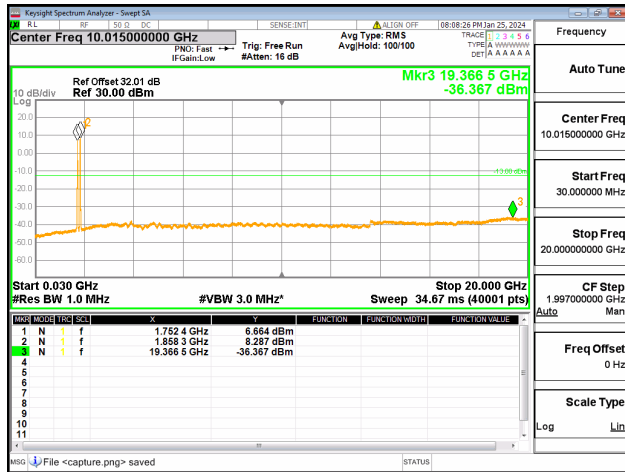




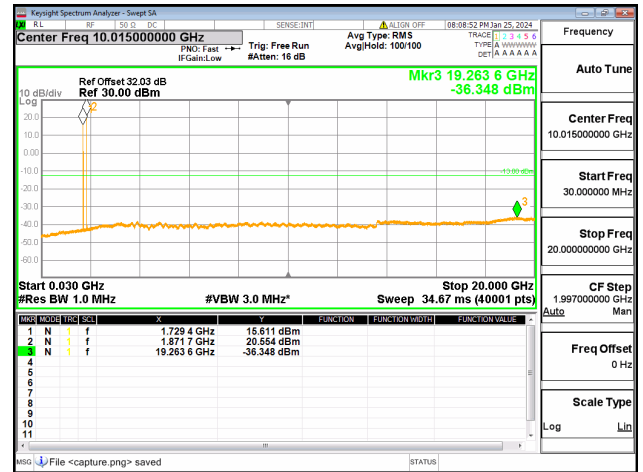
2A-4A / 20+20MHz / QPSK / Low+High CH / 1@0-1@99



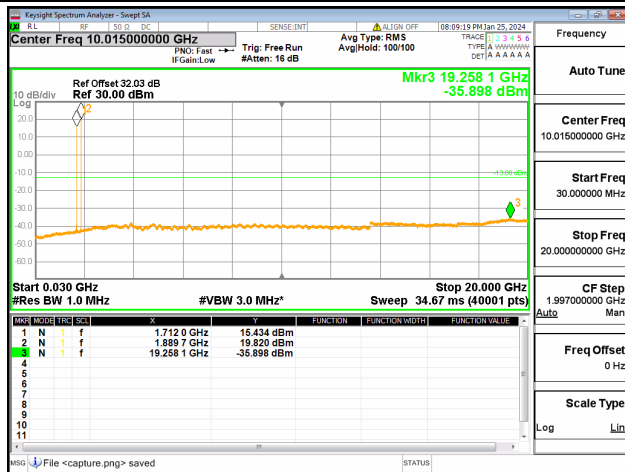
2A-4A / 20+20MHz / QPSK / Low+High CH / 1@99-1@0



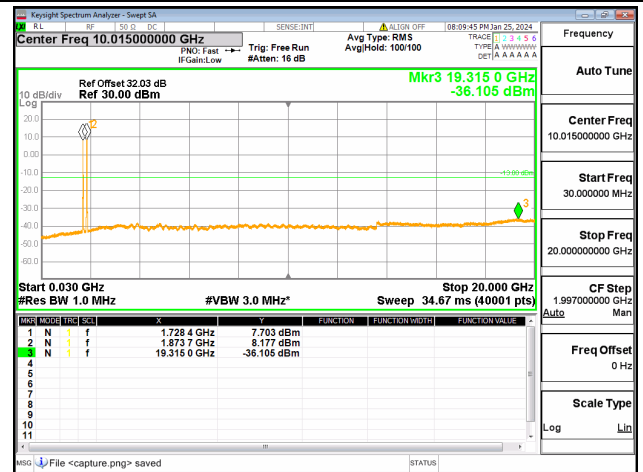
2A-4A / 20+20MHz / QPSK / Low+High CH / 100@0-100@0



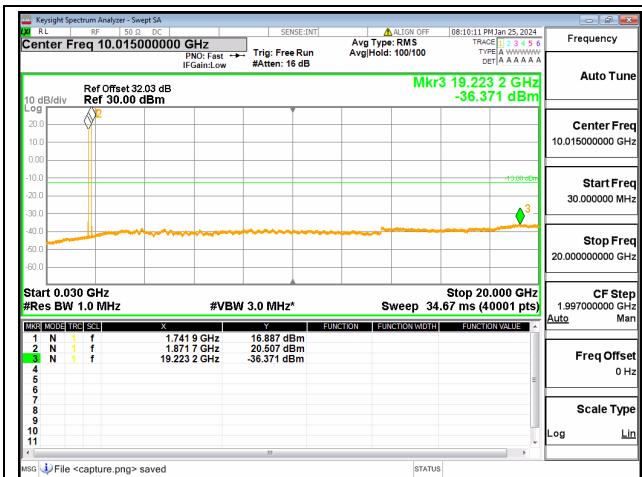
2A-4A / 20+20MHz / QPSK / Mid+Low CH / 1@0-1@99



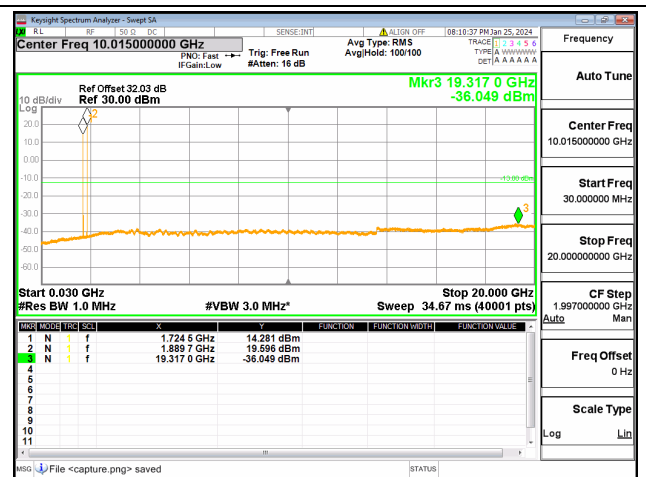
2A-4A / 20+20MHz / QPSK / Mid+Low CH / 1@99-1@0



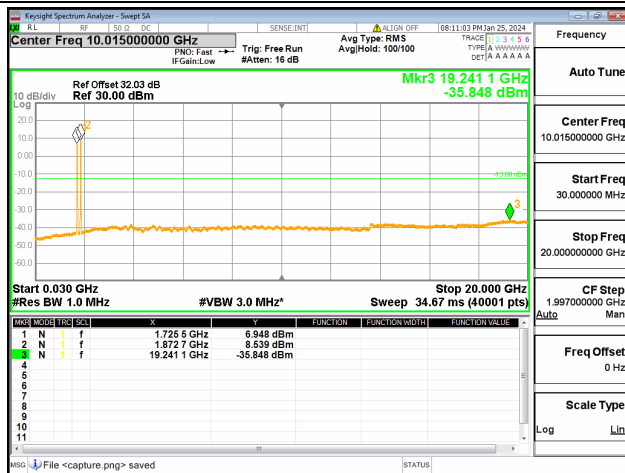
2A-4A / 20+20MHz / QPSK / Mid+Low CH / 100@0-100@0



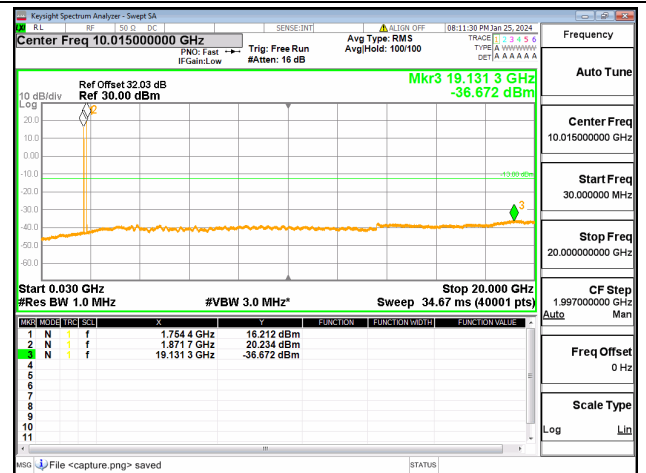
2A-4A / 20+20MHz / QPSK / Mid+Mid CH /  
1@0-1@99



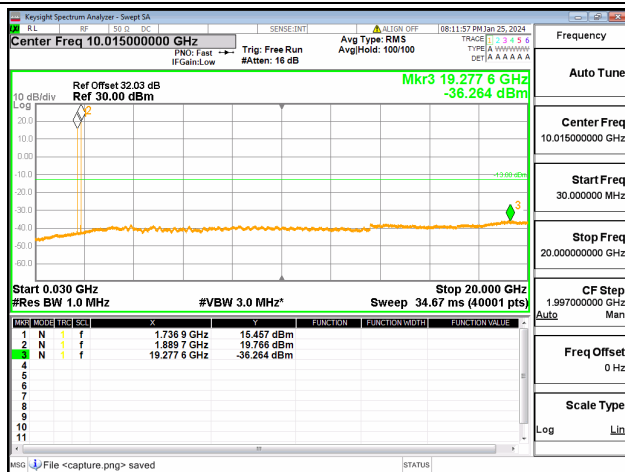
2A-4A / 20+20MHz / QPSK / Mid+Mid CH /  
1@99-1@0



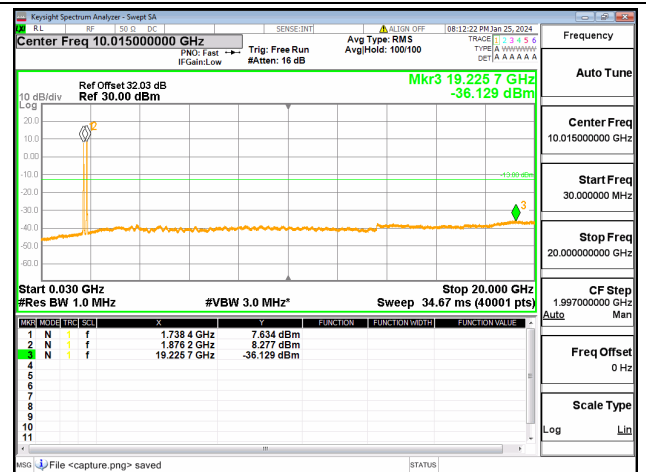
2A-4A / 20+20MHz / QPSK / Mid+High CH /  
100@0-100@0



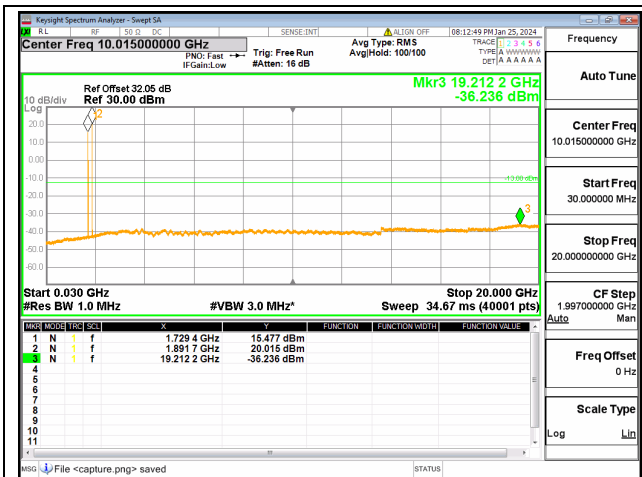
2A-4A / 20+20MHz / QPSK / Mid+High CH /  
1@0-1@99



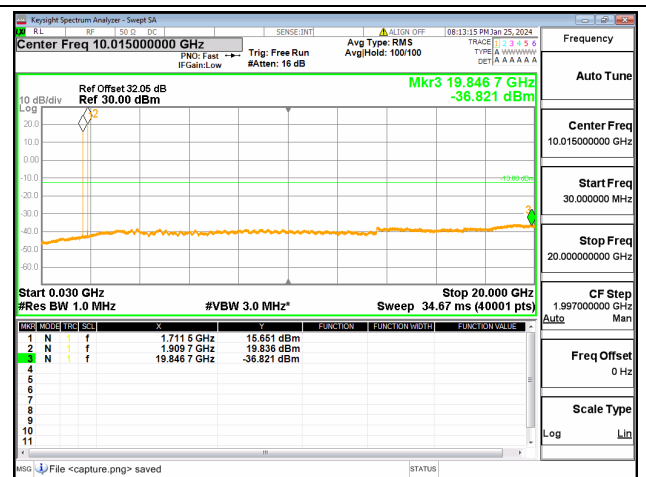
2A-4A / 20+20MHz / QPSK / Mid+High CH /  
1@99-1@0



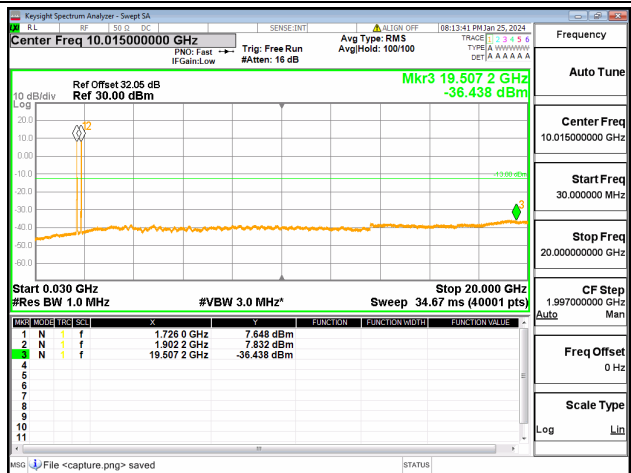
2A-4A / 20+20MHz / QPSK / Mid+High CH /  
100@0-100@0



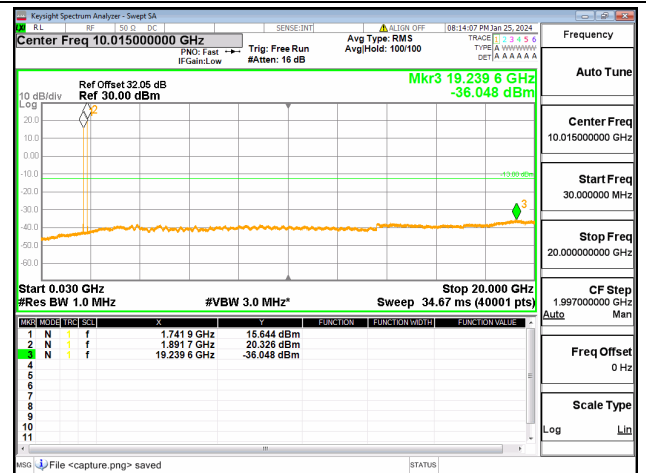
2A-4A / 20+20MHz / QPSK / High+Low CH / 1@0-1@99



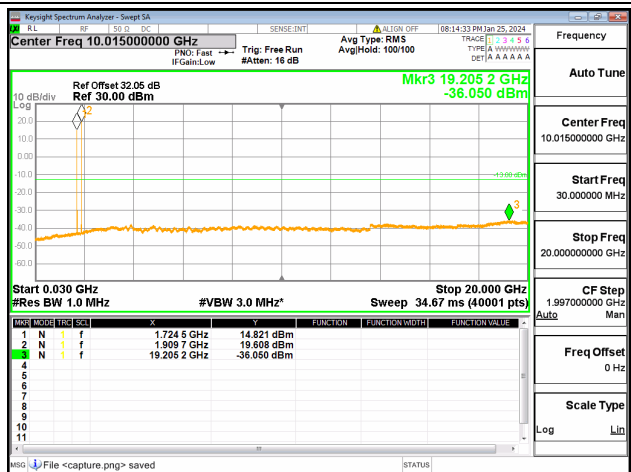
2A-4A / 20+20MHz / QPSK / High+Low CH / 1@99-1@0



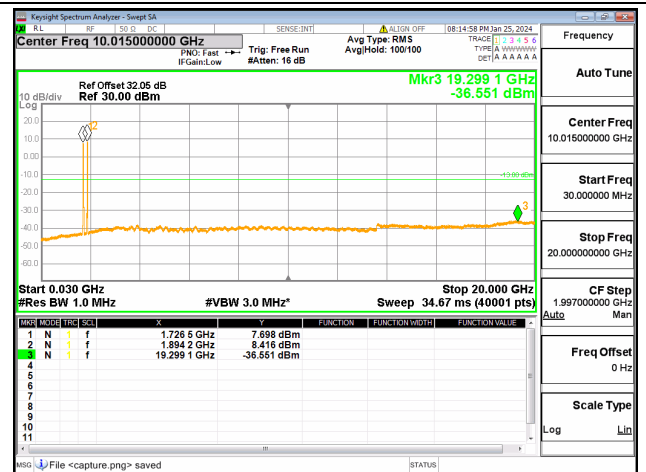
2A-4A / 20+20MHz / QPSK / High+Low CH / 100@0-100@0



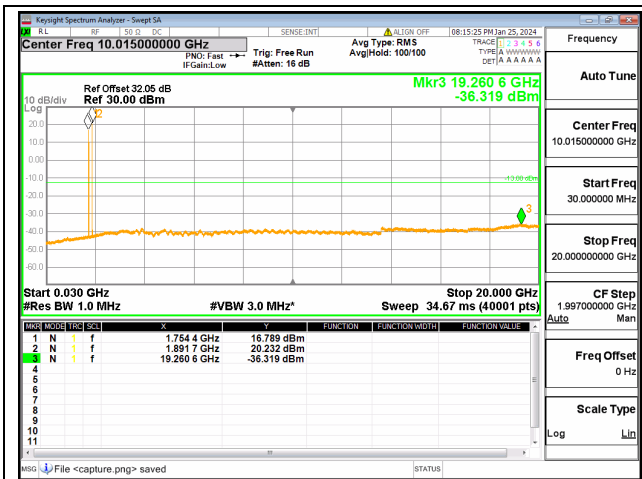
2A-4A / 20+20MHz / QPSK / High+Mid CH / 1@0-1@99



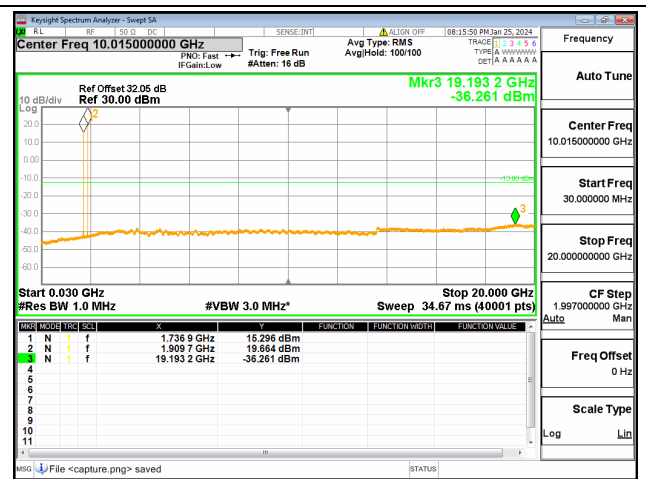
2A-4A / 20+20MHz / QPSK / High+Mid CH / 1@99-1@0



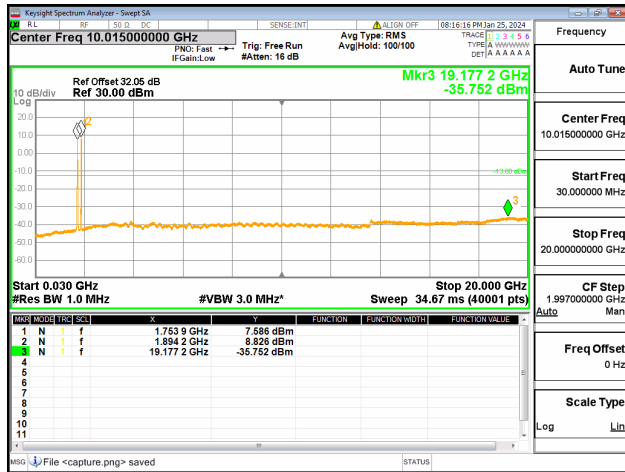
2A-4A / 20+20MHz / QPSK / High+Mid CH / 100@0-100@0



2A-4A / 20+20MHz / QPSK / High+High CH /  
1@0-1@99

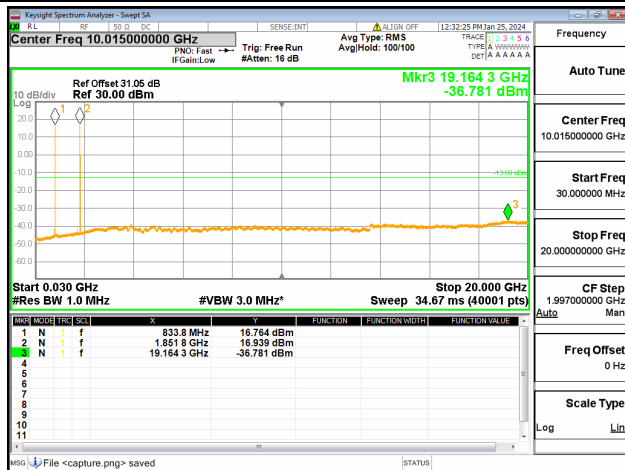


2A-4A / 20+20MHz / QPSK / High+High CH /  
1@99-1@0

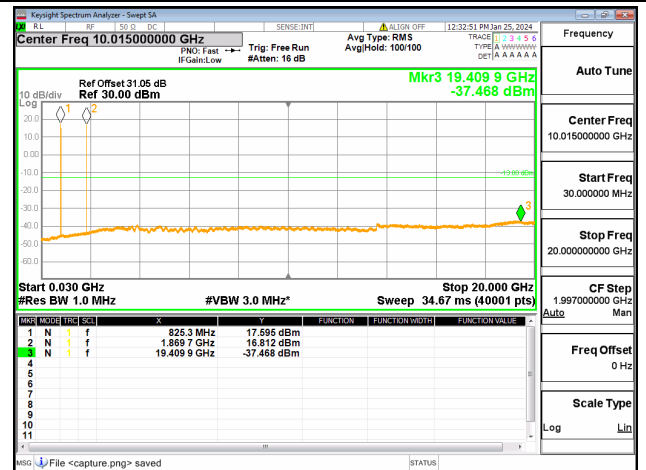


2A-4A / 20+20MHz / QPSK / High+High CH /  
100@0-100@0

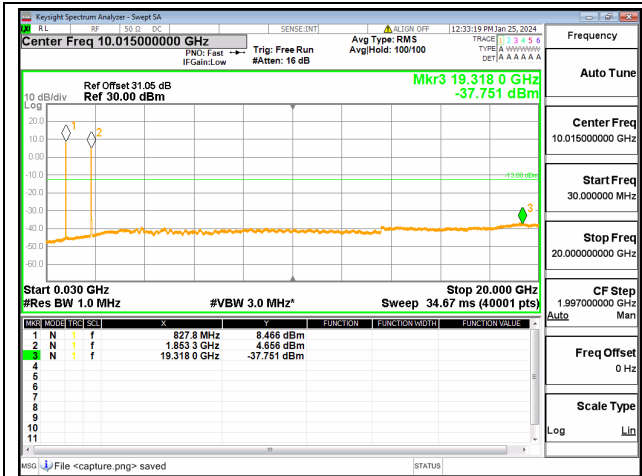
N/A



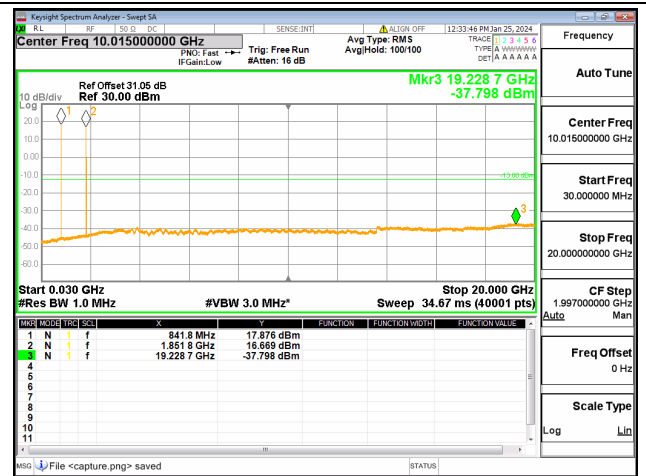
2A-5A / 20+10MHz / QPSK / Low+Low CH /  
1@0-1@49



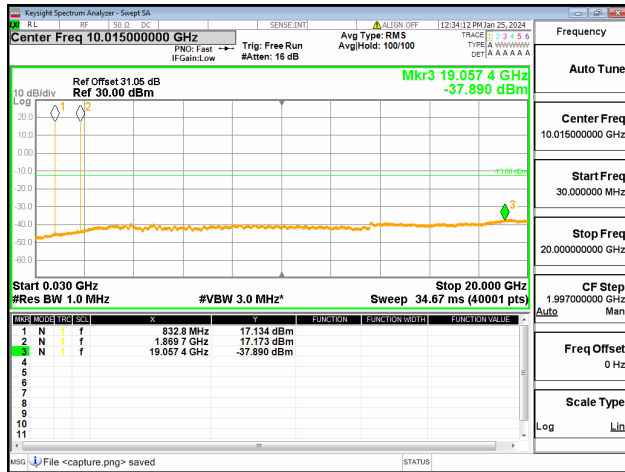
2A-5A / 20+10MHz / QPSK / Low+Low CH /  
1@99-1@0



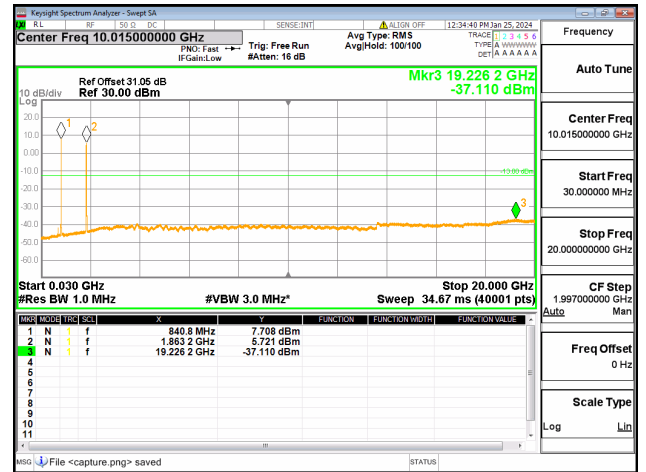
2A-5A / 20+10MHz / QPSK / Low+Low CH /  
100@0-50@0



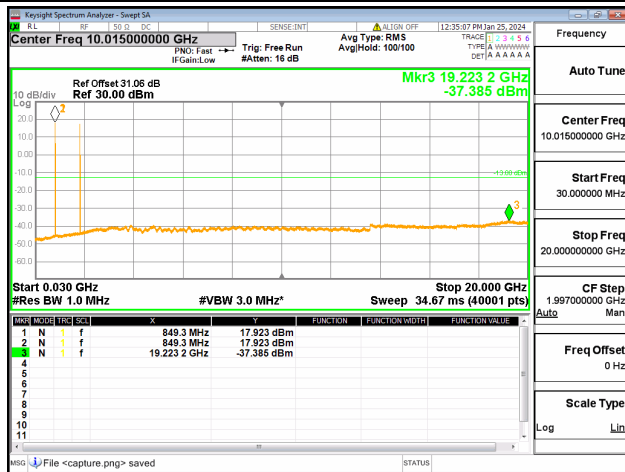
2A-5A / 20+10MHz / QPSK / Low+Mid CH /  
1@0-1@49



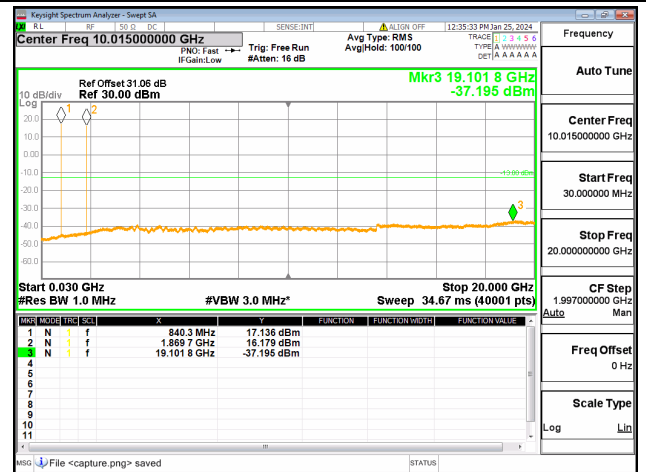
2A-5A / 20+10MHz / QPSK / Low+Mid CH /  
1@99-1@0



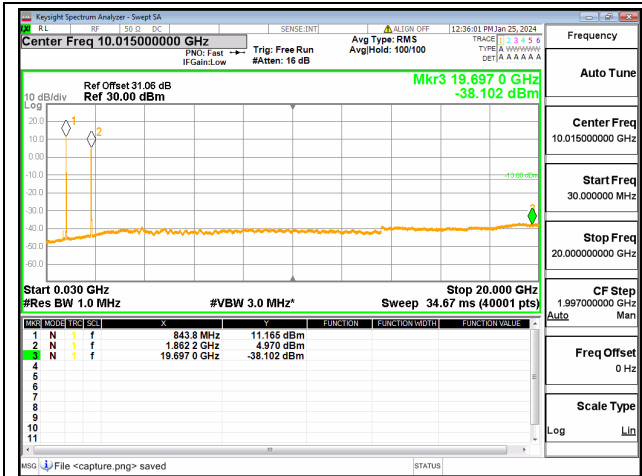
2A-5A / 20+10MHz / QPSK / Low+Mid CH /  
100@0-50@0



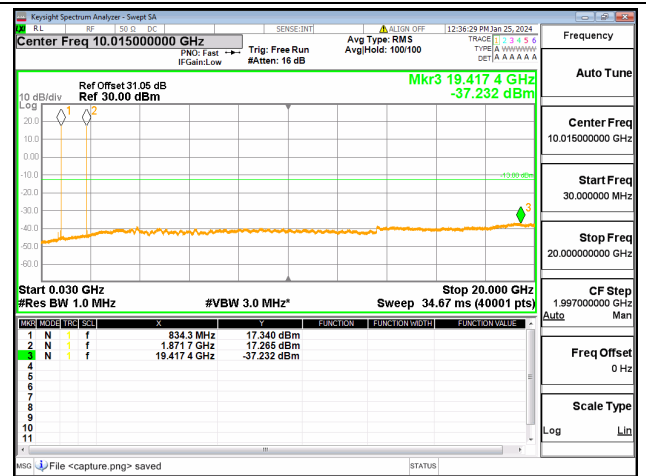
2A-5A / 20+10MHz / QPSK / Low+High CH /  
1@0-1@49



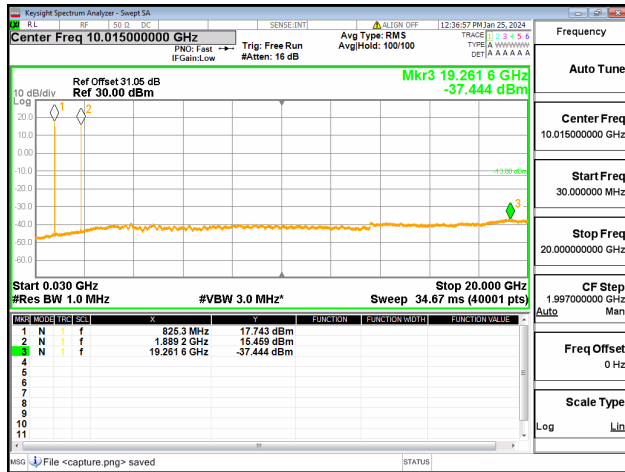
2A-5A / 20+10MHz / QPSK / Low+High CH /  
1@99-1@0



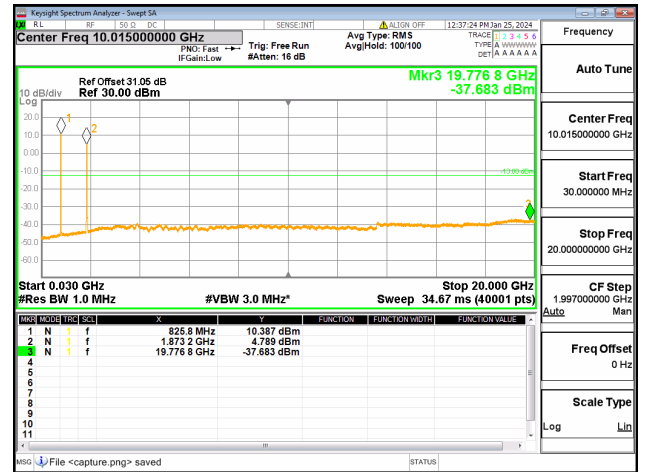
2A-5A / 20+10MHz / QPSK / Low+High CH / 100@0-50@0



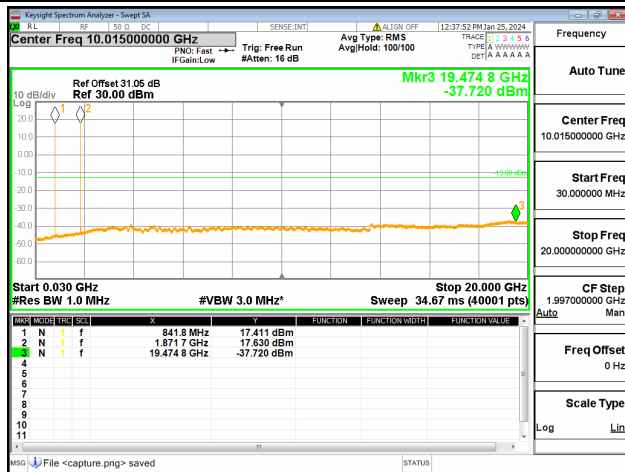
2A-5A / 20+10MHz / QPSK / Mid+Low CH / 1@0-1@49



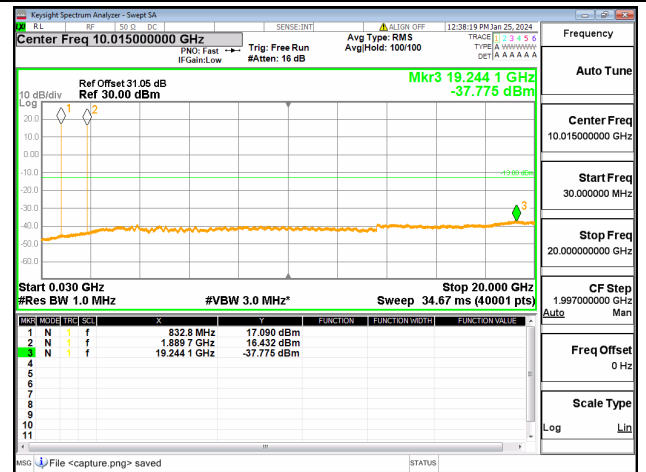
2A-5A / 20+10MHz / QPSK / Mid+Low CH / 1@99-1@0



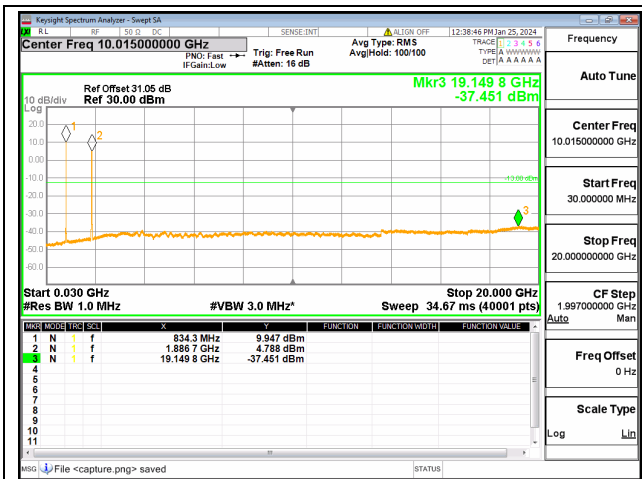
2A-5A / 20+10MHz / QPSK / Mid+Low CH / 100@0-50@0



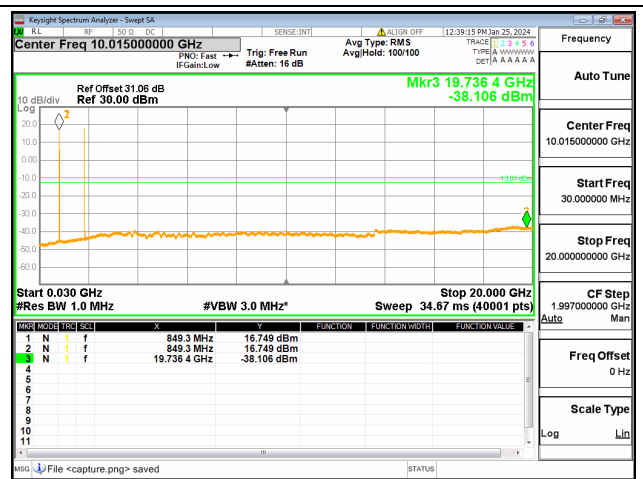
2A-5A / 20+10MHz / QPSK / Mid+Mid CH / 1@0-1@49



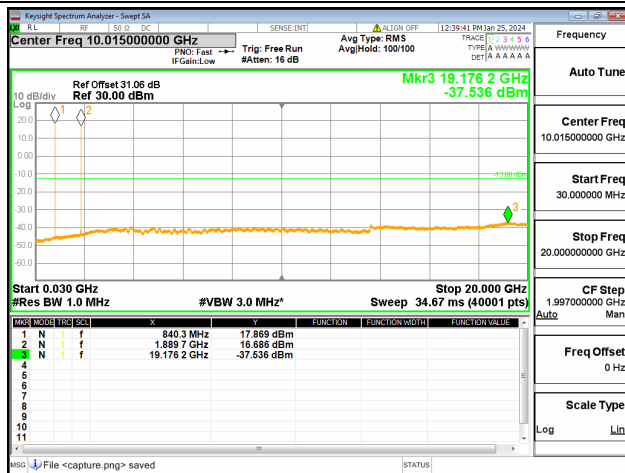
2A-5A / 20+10MHz / QPSK / Mid+Mid CH / 1@99-1@0



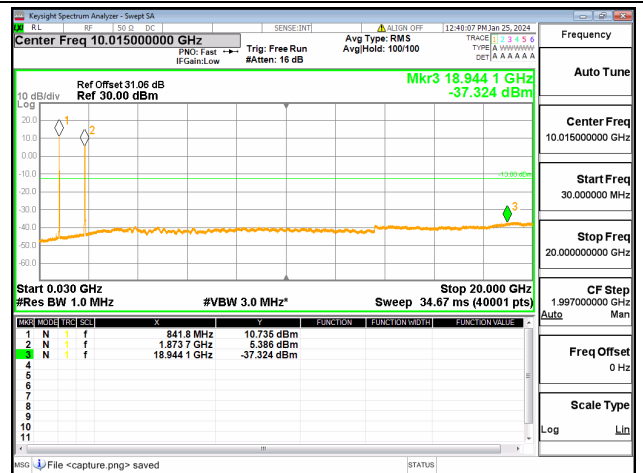
2A-5A / 20+10MHz / QPSK / Mid+Mid CH /  
100@0-50@0



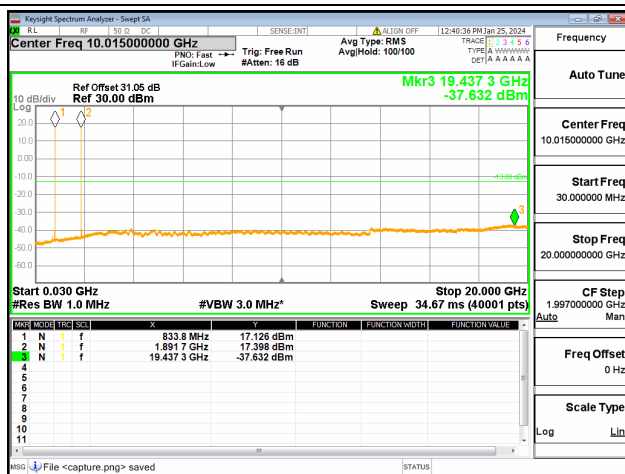
2A-5A / 20+10MHz / QPSK / Mid+High CH /  
1@0-1@49



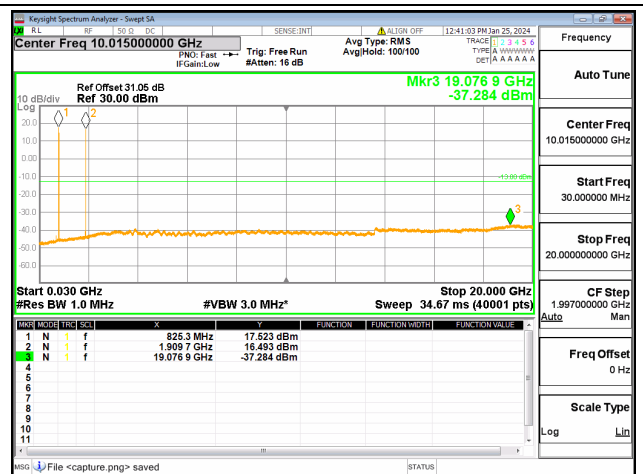
2A-5A / 20+10MHz / QPSK / Mid+High CH /  
1@99-1@0



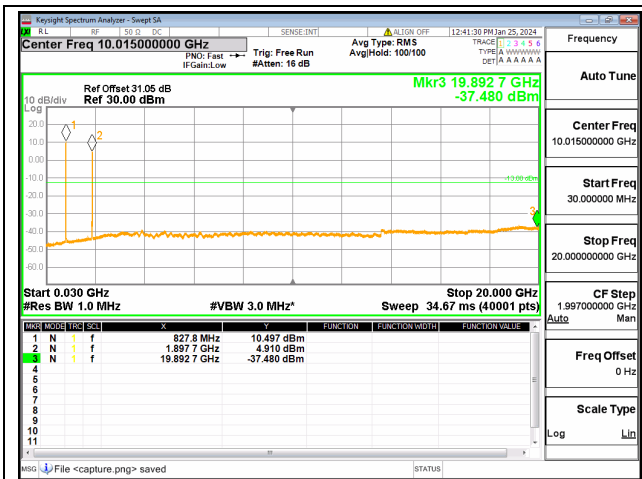
2A-5A / 20+10MHz / QPSK / Mid+High CH /  
100@0-50@0



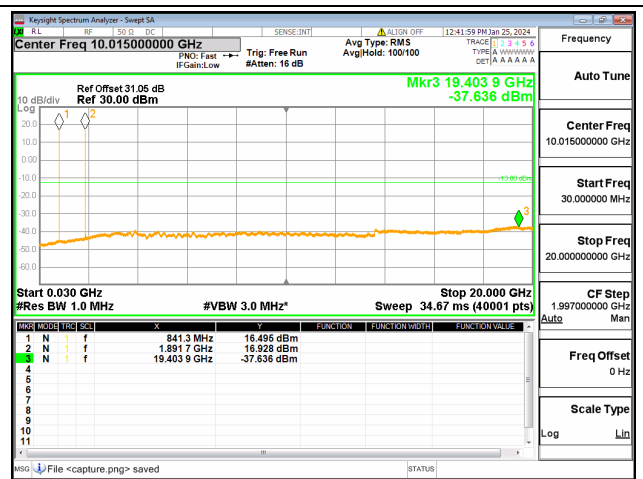
2A-5A / 20+10MHz / QPSK / High+Low CH /  
1@0-1@49



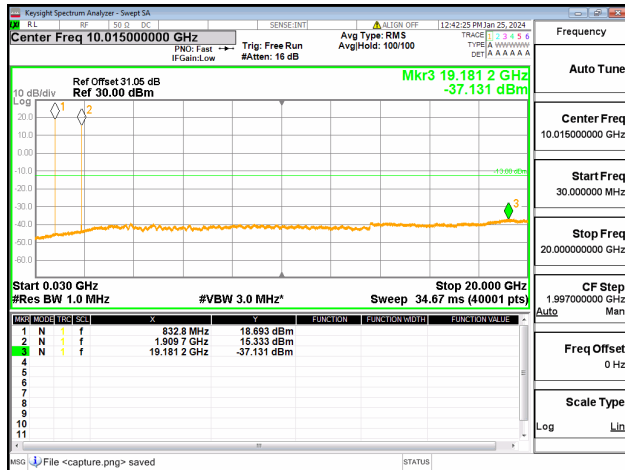
2A-5A / 20+10MHz / QPSK / High+Low CH /  
1@99-1@0



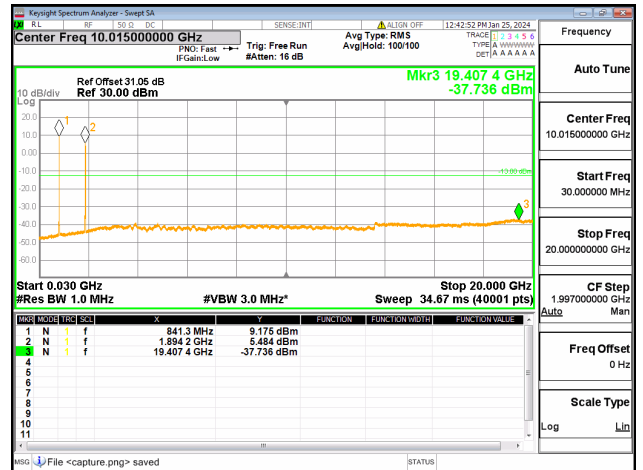
2A-5A / 20+10MHz / QPSK / High+Low CH / 100@0-50@0



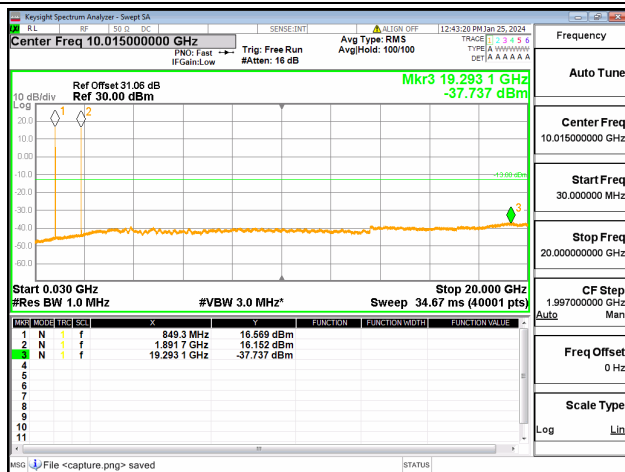
2A-5A / 20+10MHz / QPSK / High+Mid CH / 1@0-1@49



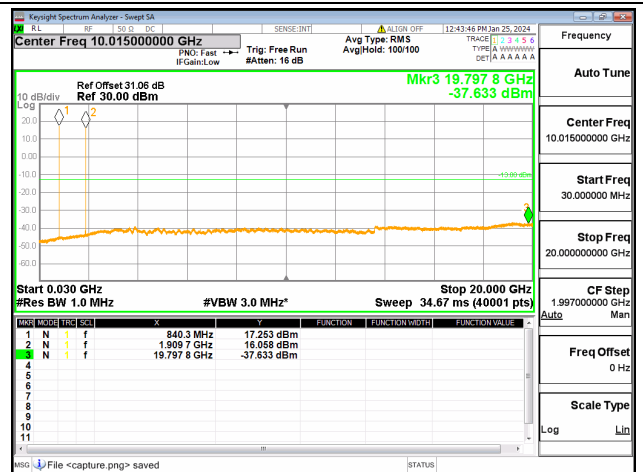
2A-5A / 20+10MHz / QPSK / High+Mid CH / 1@99-1@0



2A-5A / 20+10MHz / QPSK / High+Mid CH / 100@0-50@0

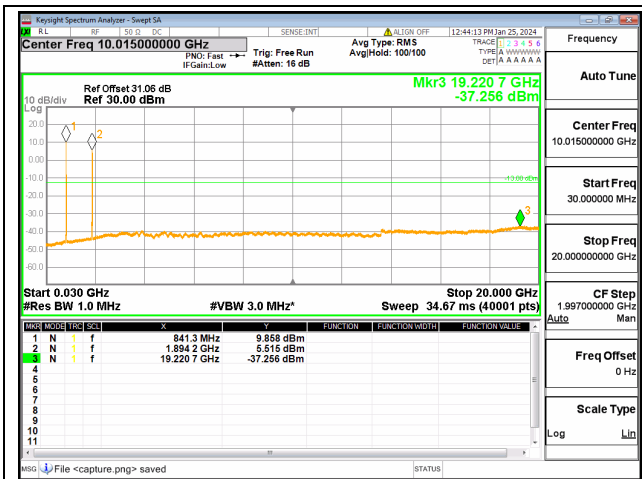


2A-5A / 20+10MHz / QPSK / High+High CH / 1@0-1@49

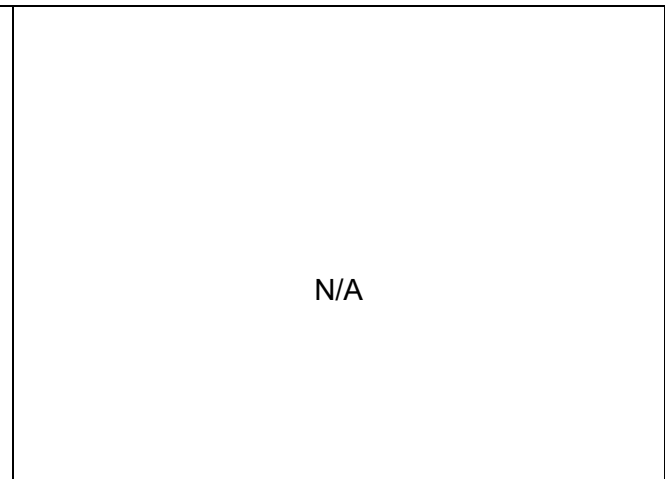


2A-5A / 20+10MHz / QPSK / High+High CH / 1@99-1@0

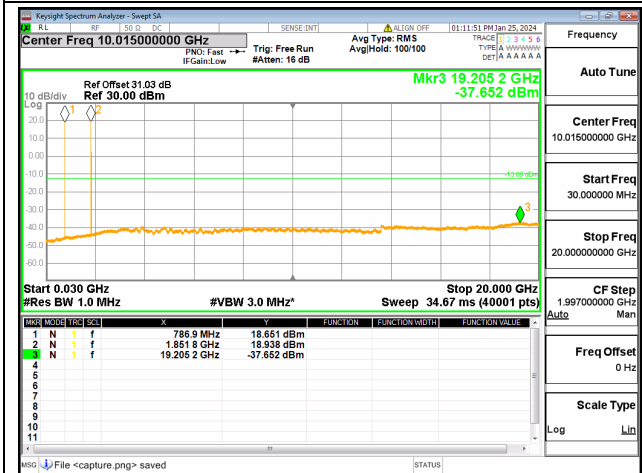




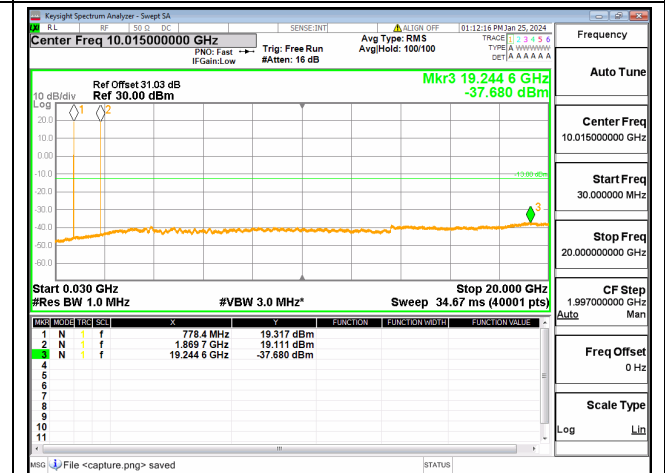
2A-5A / 20+10MHz / QPSK / High+High CH / 100@0-50@0



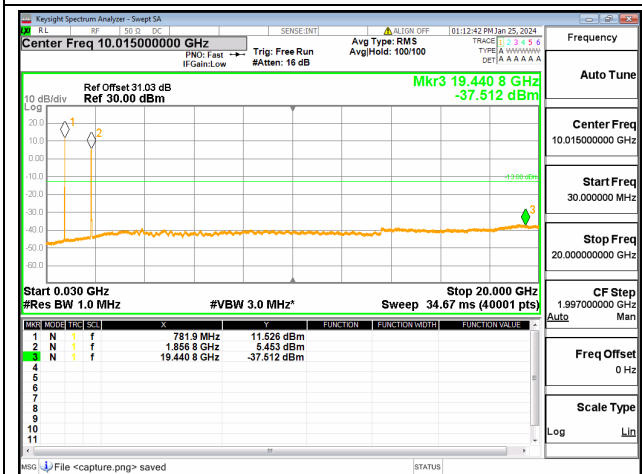
N/A



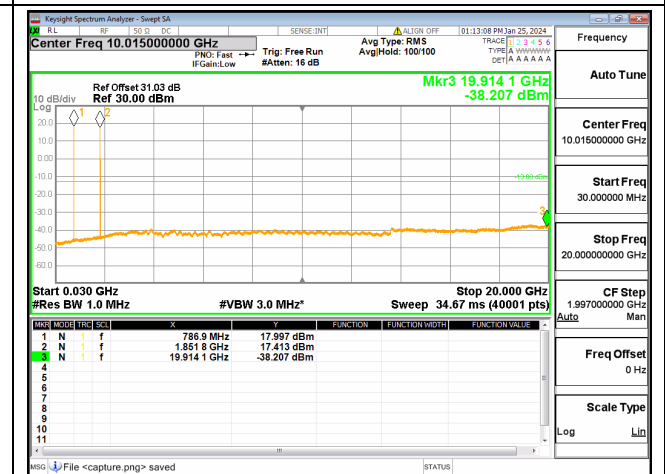
2A-13A / 20+10MHz / QPSK / Low+Low CH / 1@0-1@49



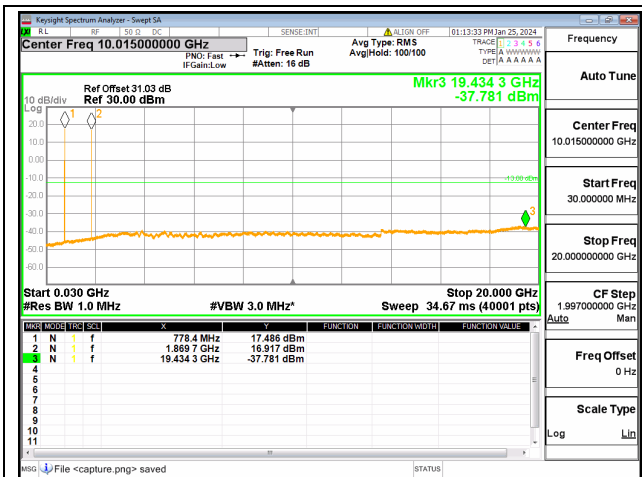
2A-13A / 20+10MHz / QPSK / Low+Low CH / 1@99-1@0



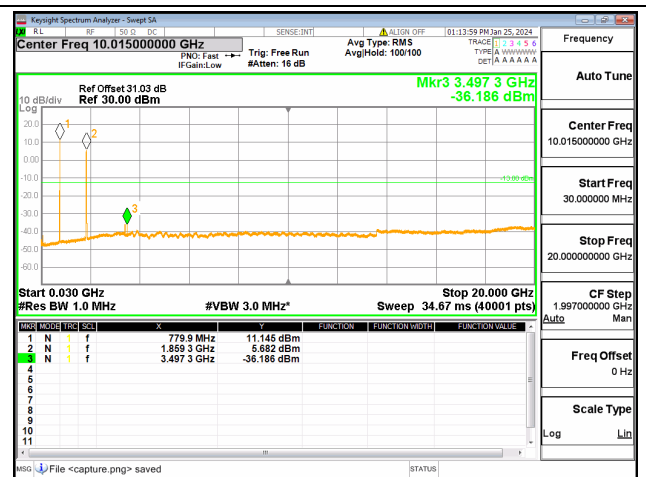
2A-13A / 20+10MHz / QPSK / Low+Low CH / 100@0-50@0



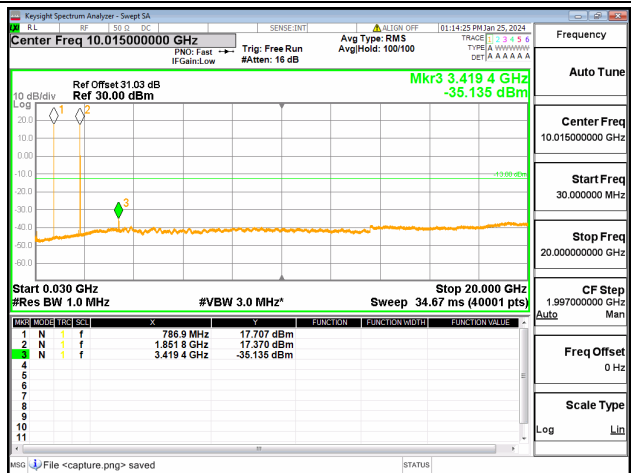
2A-13A / 20+10MHz / QPSK / Low+Mid CH / 1@0-1@49



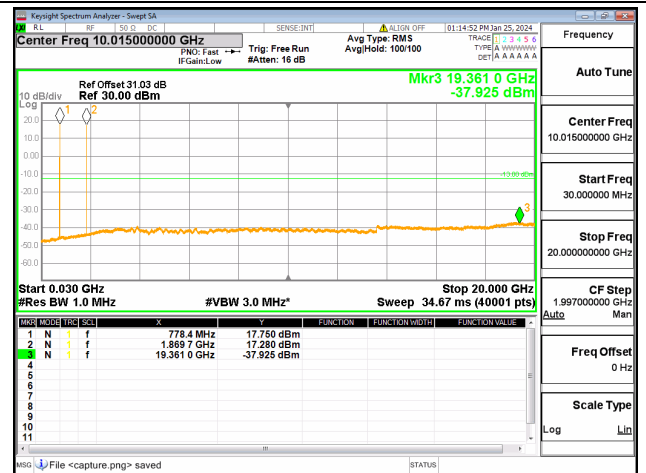
2A-13A / 20+10MHz / QPSK / Low+Mid CH / 1@99-1@0



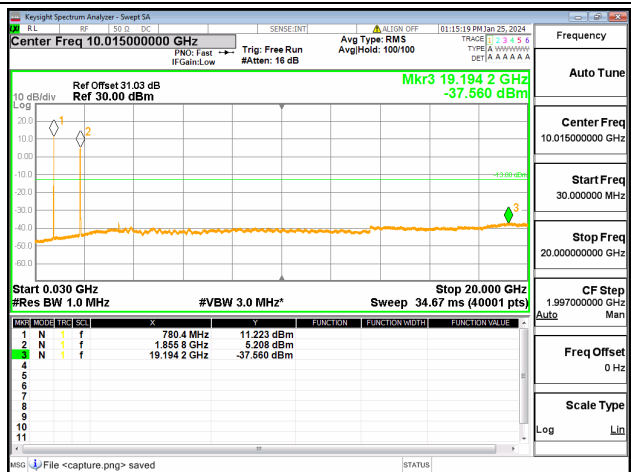
2A-13A / 20+10MHz / QPSK / Low+Mid CH / 100@0-50@0



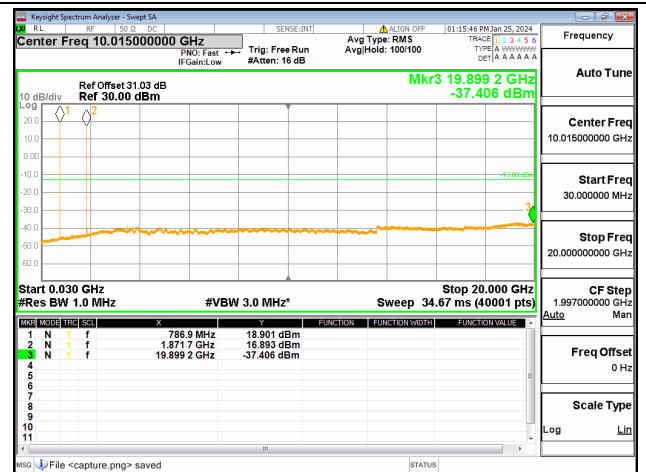
2A-13A / 20+10MHz / QPSK / Low+High CH / 1@0-1@49



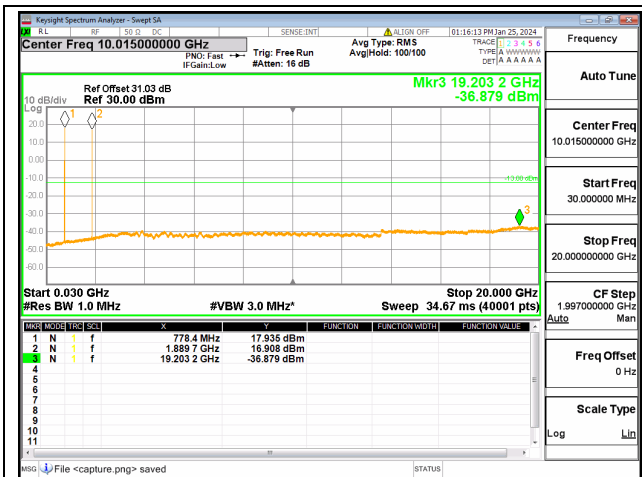
2A-13A / 20+10MHz / QPSK / Low+High CH / 1@99-1@0



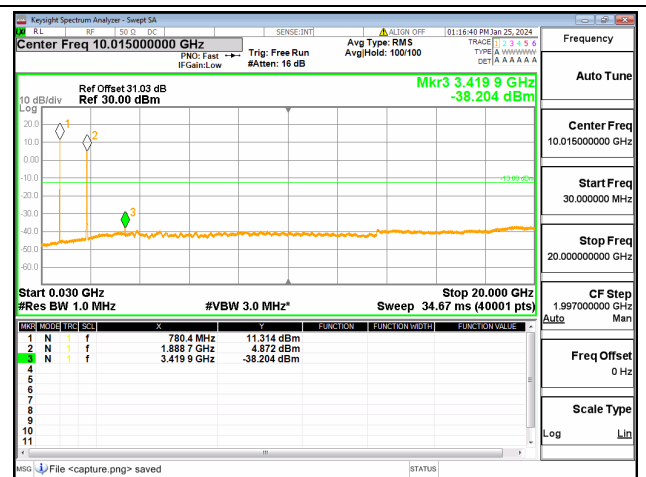
2A-13A / 20+10MHz / QPSK / Low+High CH / 100@0-50@0



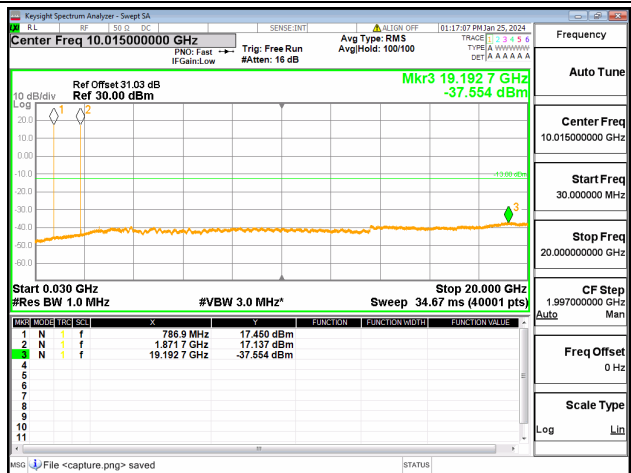
2A-13A / 20+10MHz / QPSK / Mid+Low CH / 1@0-1@49



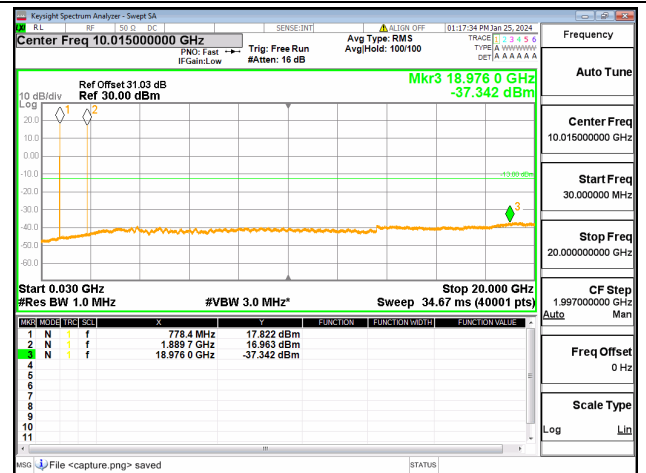
2A-13A / 20+10MHz / QPSK / Mid+Low CH / 1@99-1@0



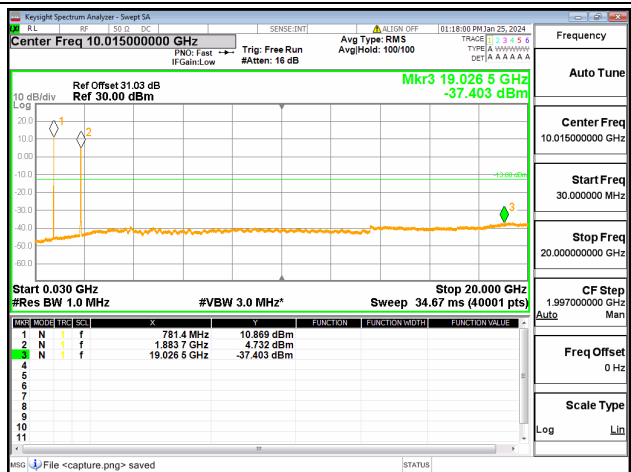
2A-13A / 20+10MHz / QPSK / Mid+Low CH / 100@0-50@0



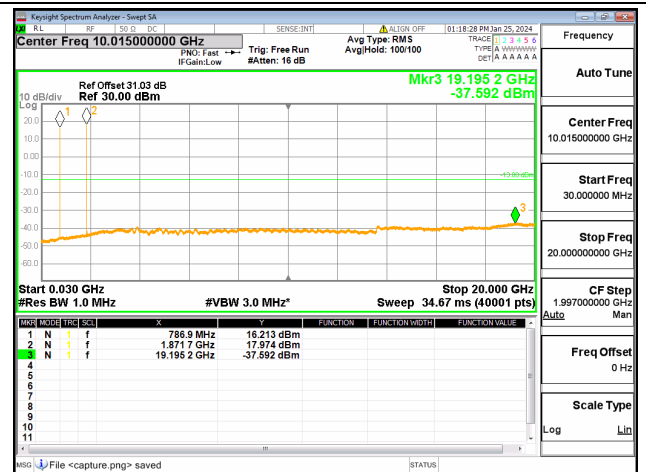
2A-13A / 20+10MHz / QPSK / Mid+Mid CH / 1@0-1@49



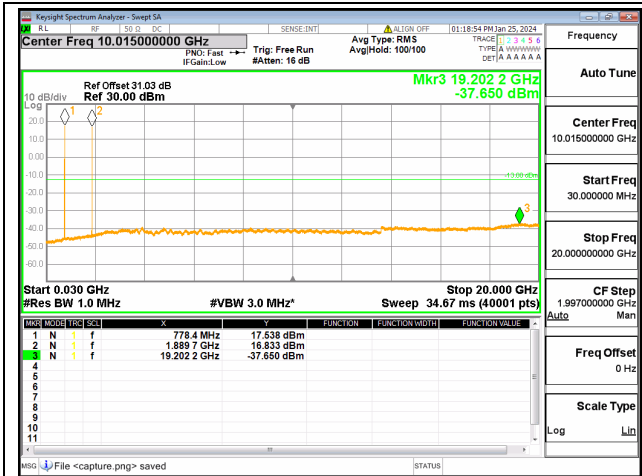
2A-13A / 20+10MHz / QPSK / Mid+Mid CH / 1@99-1@0



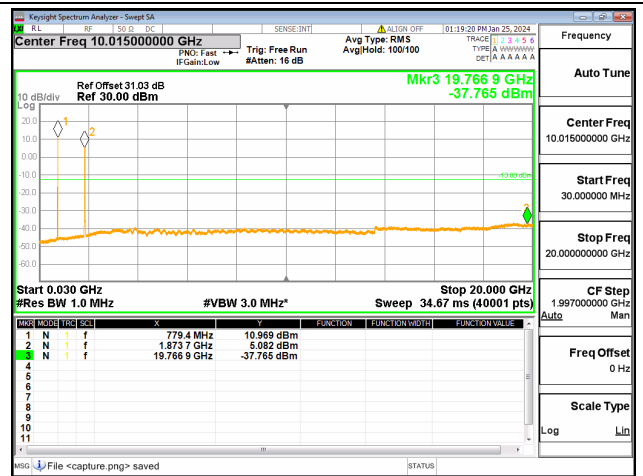
2A-13A / 20+10MHz / QPSK / Mid+Mid CH / 100@0-50@0



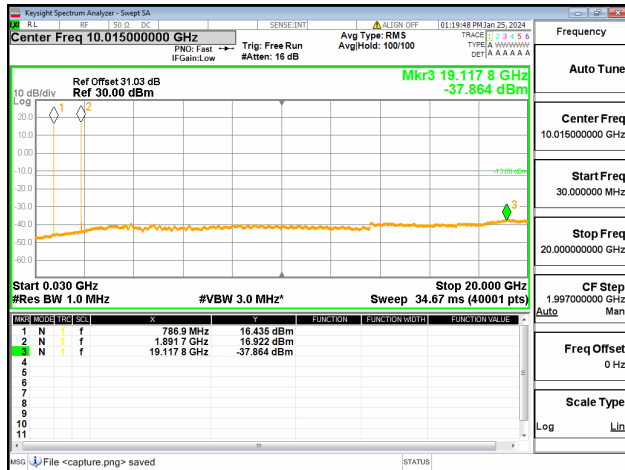
2A-13A / 20+10MHz / QPSK / Mid+High CH / 1@0-1@49



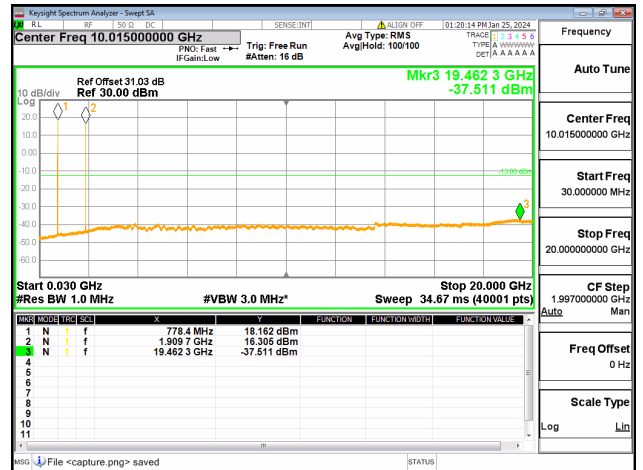
2A-13A / 20+10MHz / QPSK / Mid+High CH /  
1@99-1@0



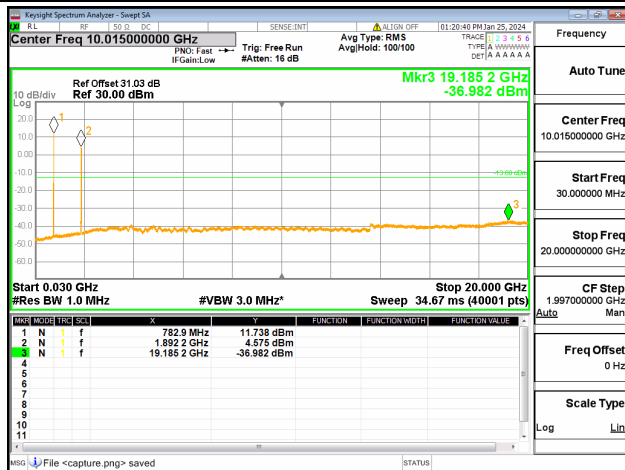
2A-13A / 20+10MHz / QPSK / Mid+High CH /  
100@0-50@0



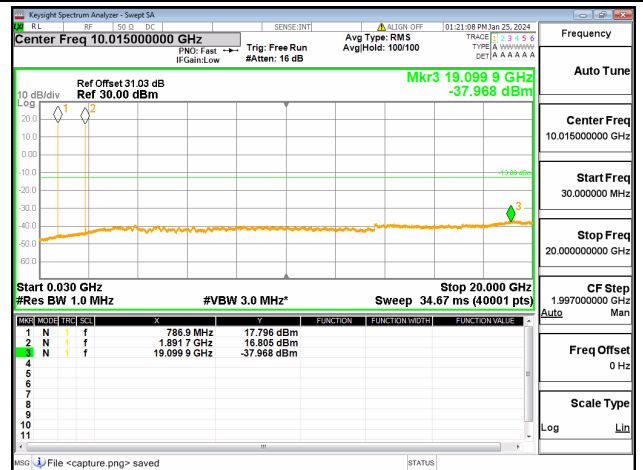
2A-13A / 20+10MHz / QPSK / High+Low CH /  
1@0-1@49



2A-13A / 20+10MHz / QPSK / High+Low CH /  
1@99-1@0



2A-13A / 20+10MHz / QPSK / High+Low CH /  
100@0-50@0



2A-13A / 20+10MHz / QPSK / High+Mid CH /  
1@0-1@49