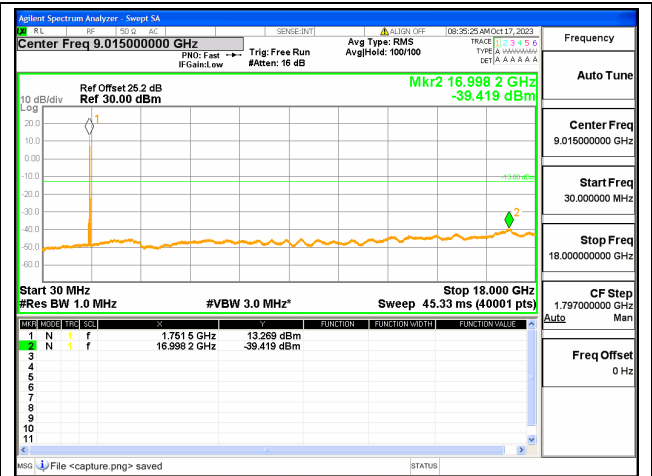
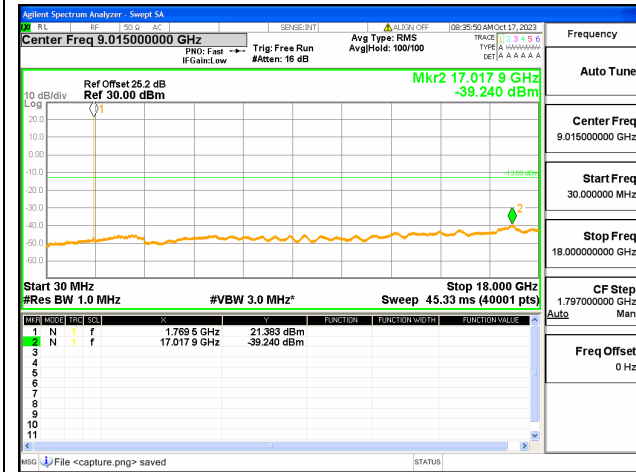


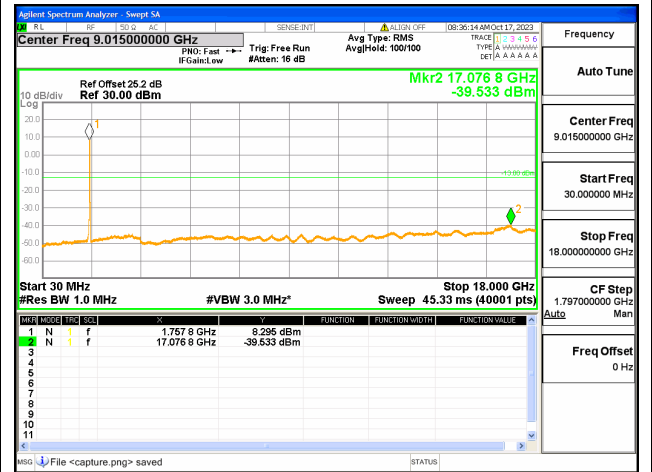
66C / 10+20MHz / QPSK / High CH /
50#0-100#0



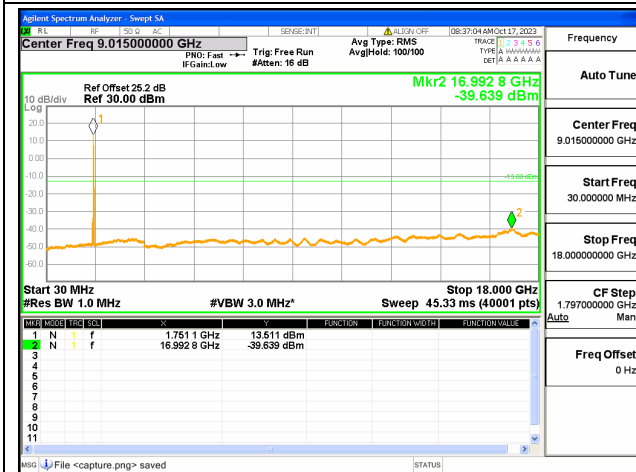
66C / 20+10MHz / QPSK / High CH / 1#0-1#49



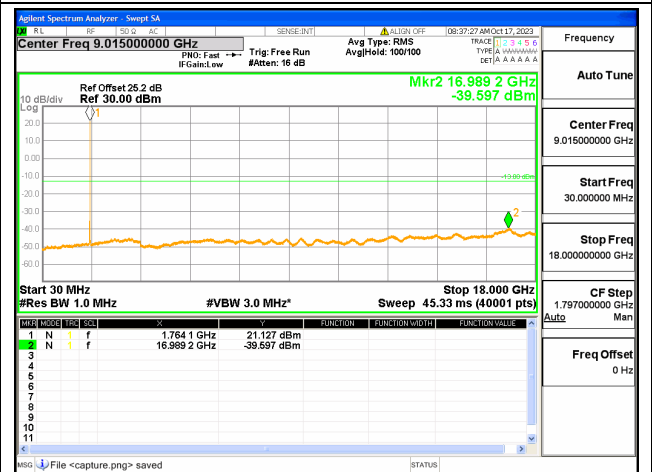
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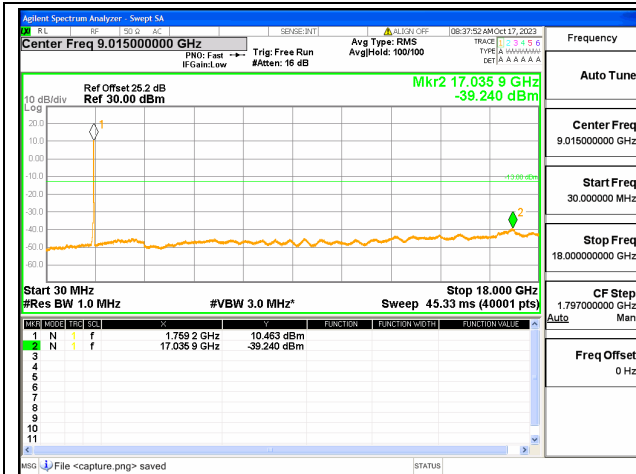
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100#0-50#0



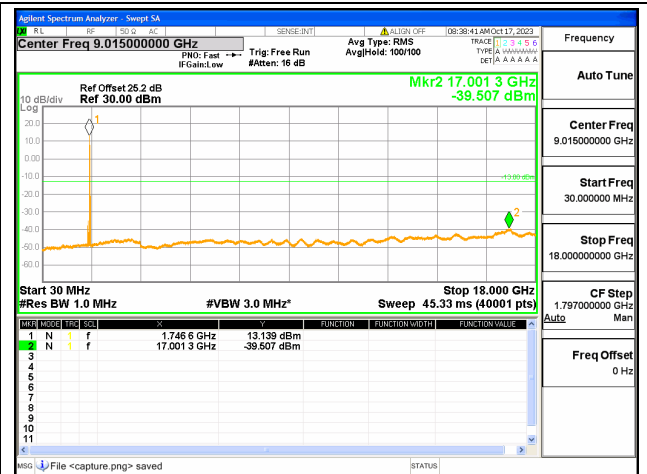
66C / 15+15MHz / QPSK / High CH / 1#0-1#74



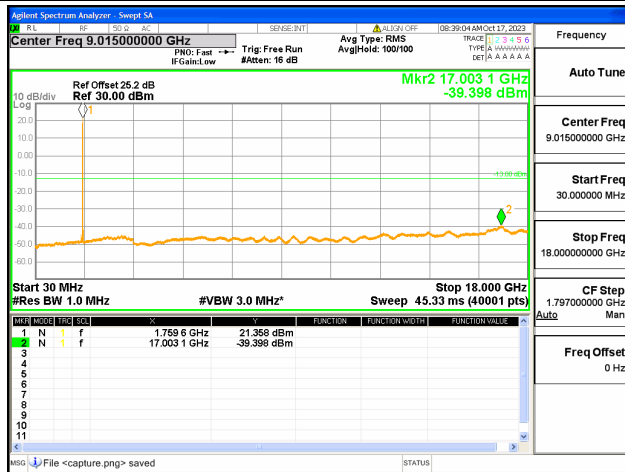
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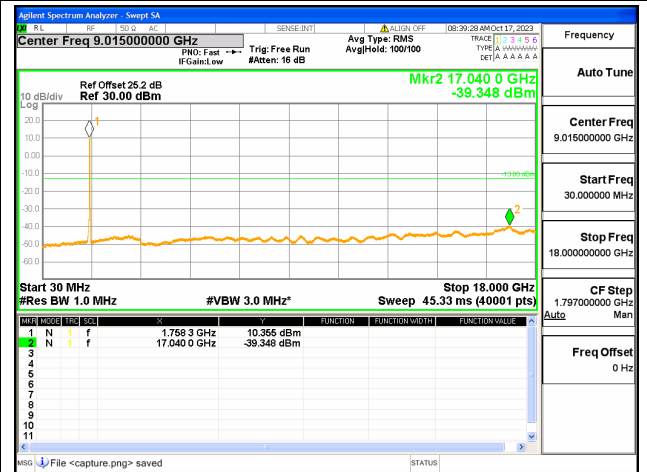
66C / 15+15MHz / QPSK / High CH / 75#0-75#0



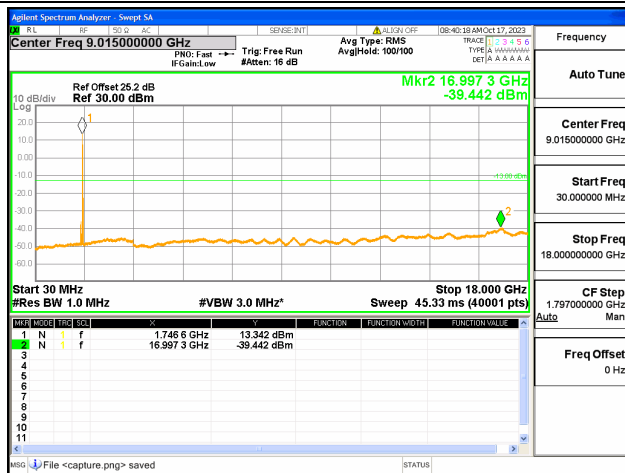
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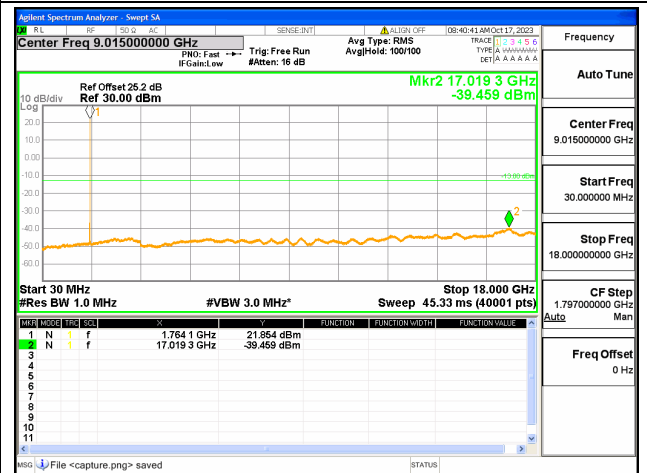
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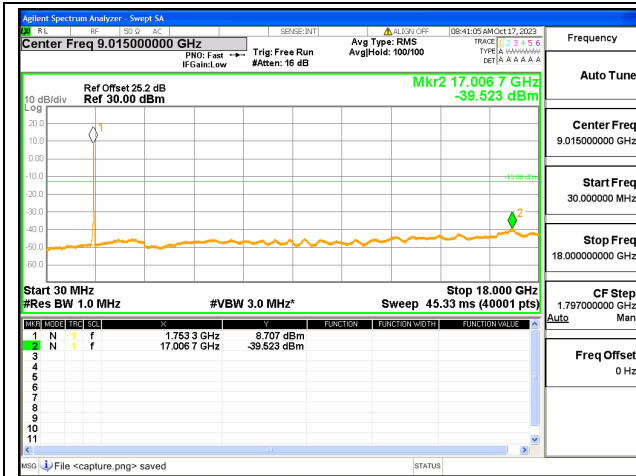
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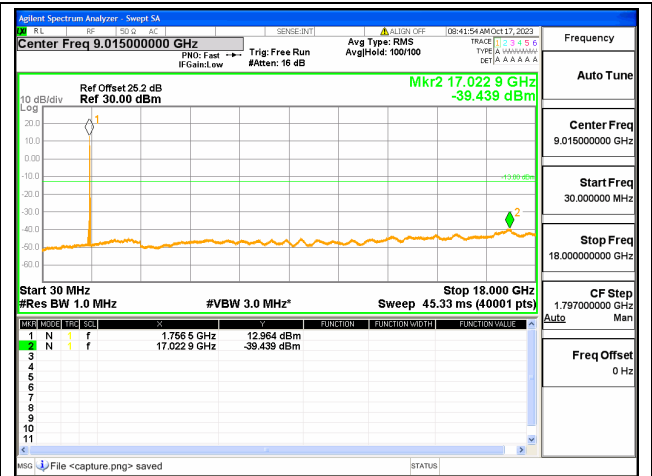
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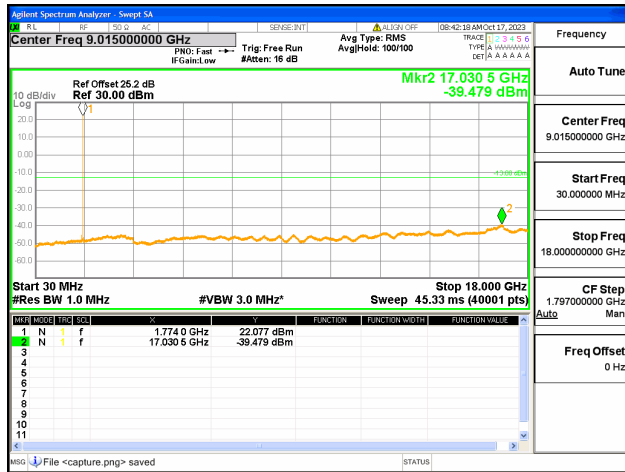
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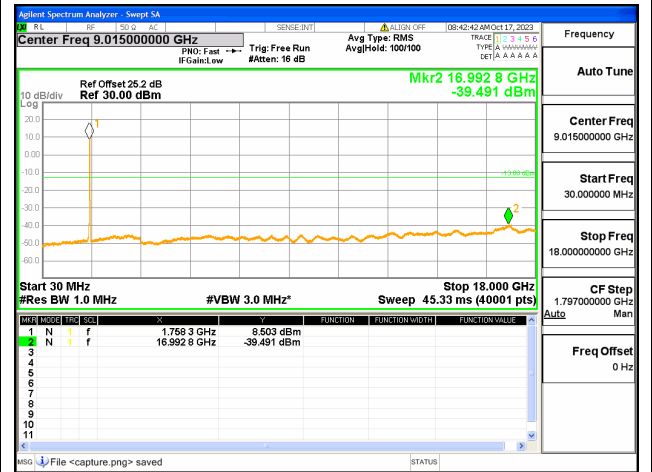
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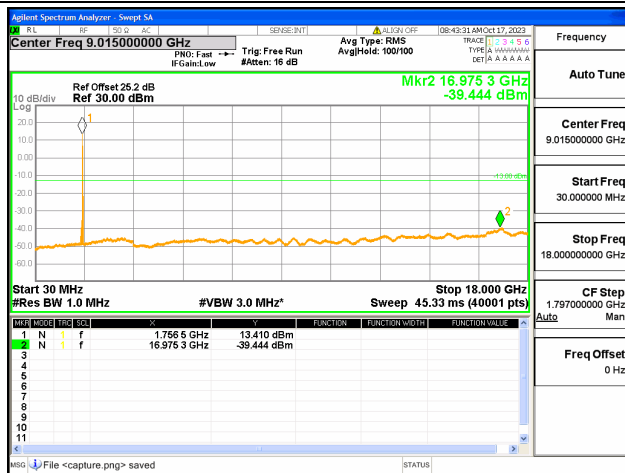
66C / 20+5MHz / QPSK / High CH / 1#0-1#24



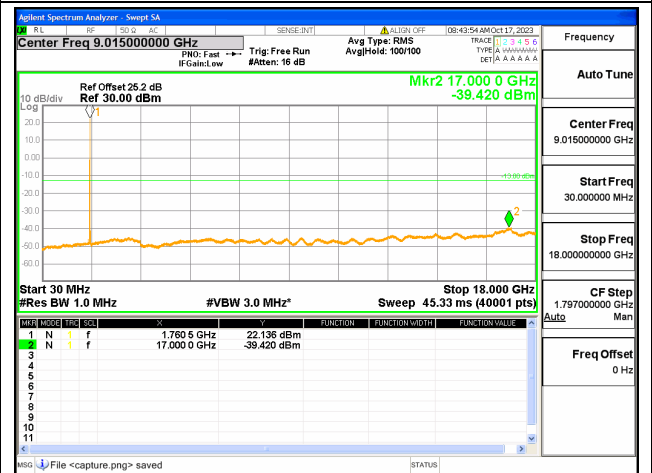
66C / 20+5MHz / QPSK / High CH / 1#99-1#0



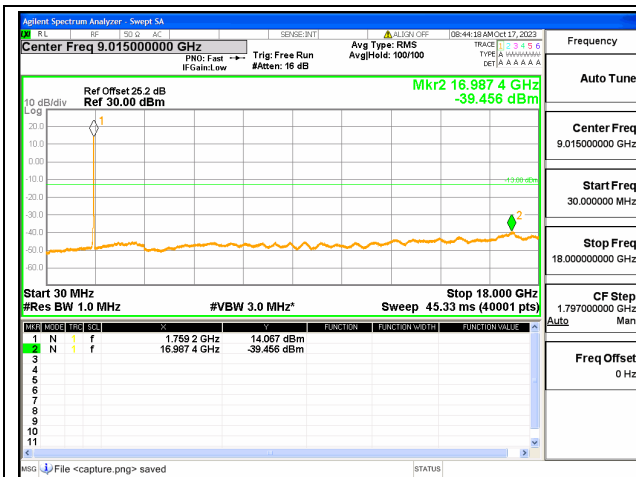
66C / 20+5MHz / QPSK / High CH / 100#0-25#0



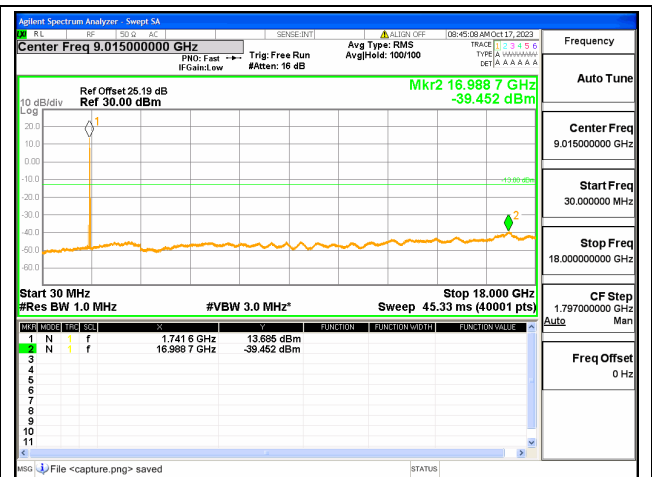
66C / 5+20MHz / QPSK / High CH / 1#0-1#99



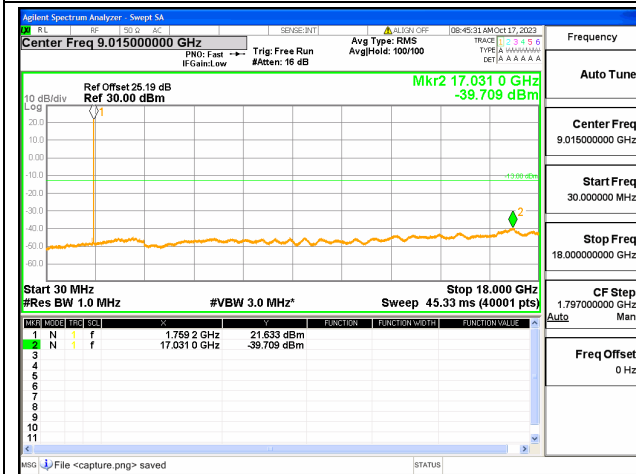
66C / 5+20MHz / QPSK / High CH / 1#24-1#0



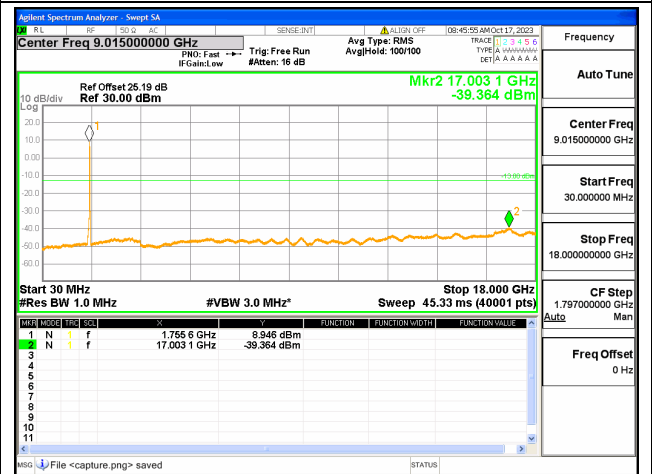
66C / 5+20MHz / QPSK / High CH / 25#0-100#0



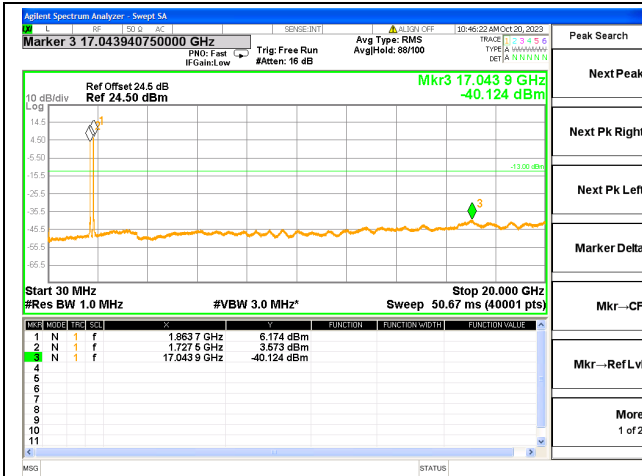
66C / 20+20MHz / QPSK / High CH / 1#0-1#99



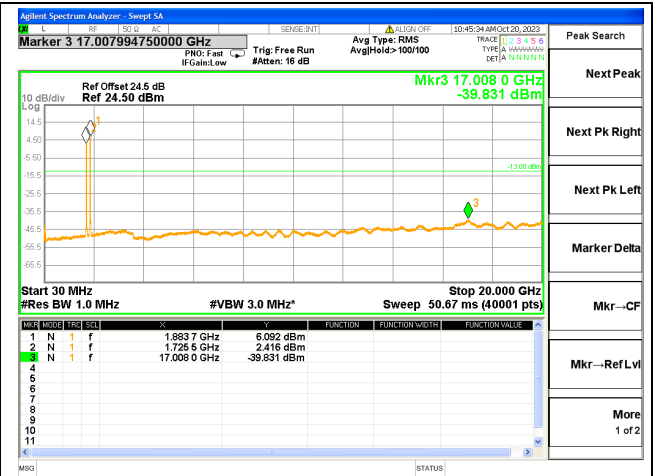
66C / 20+20MHz / QPSK / High CH / 1#99-1#0



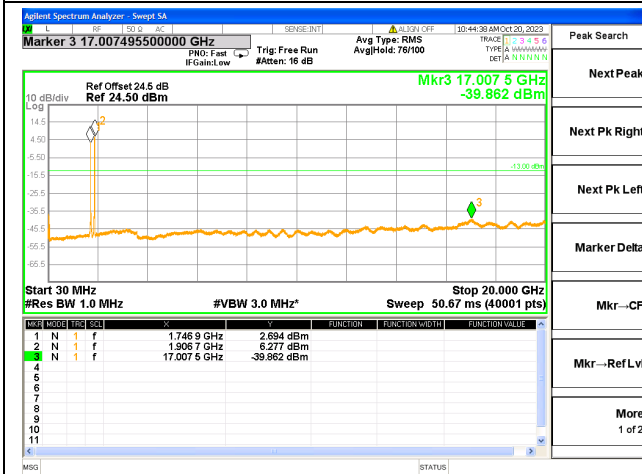
66C / 20+20MHz / QPSK / High CH / 100#0-100#0



2A-4A/20M+20M/QPSK/Low CH

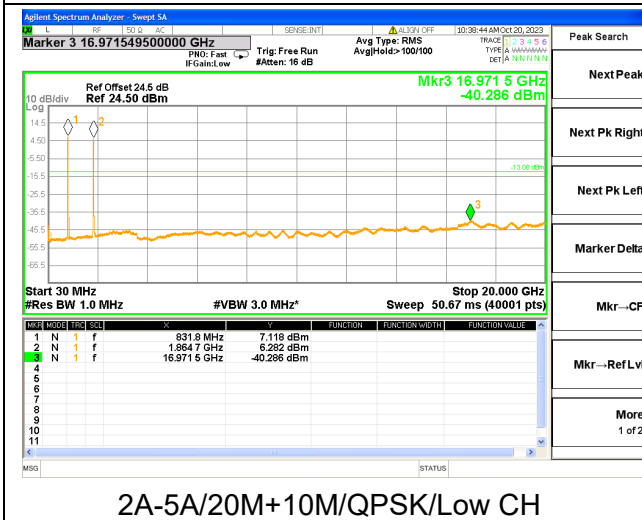


2A-4A/20M+20M/QPSK/Mid CH

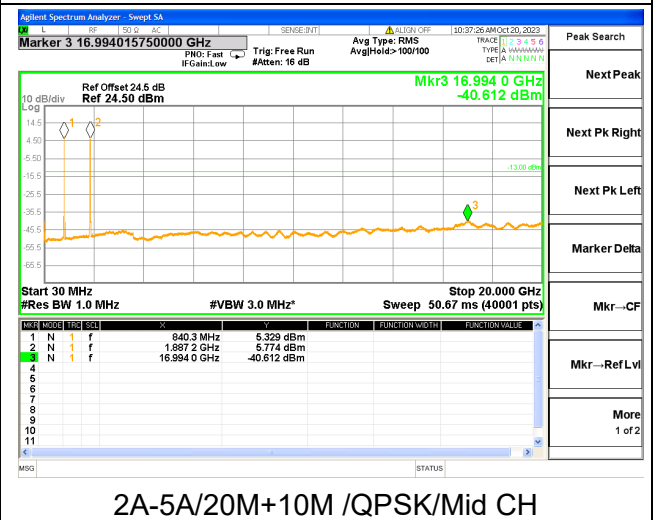


2A-4A/20M+20M/QPSK/High CH

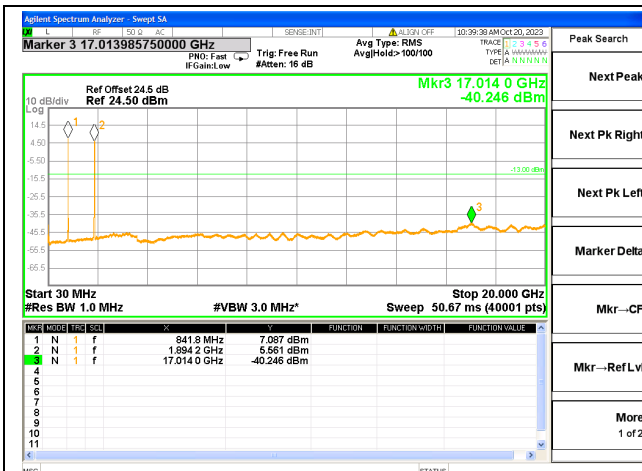
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2A-5A/20M+10M/QPSK/Low CH

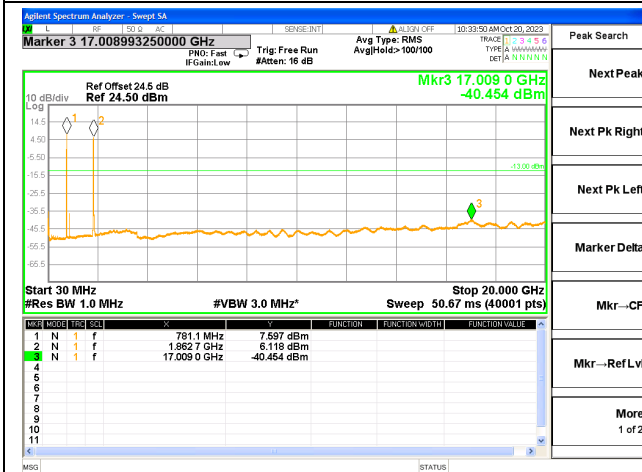


2A-5A/20M+10M/QPSK/Mid CH

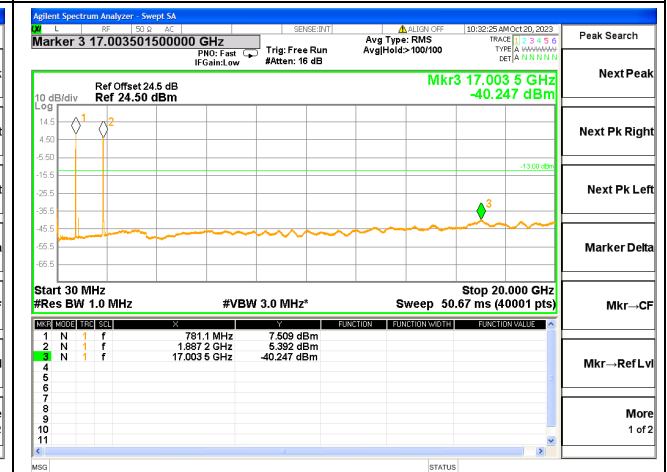


2A-5A/20M+10M QPSK/High CH

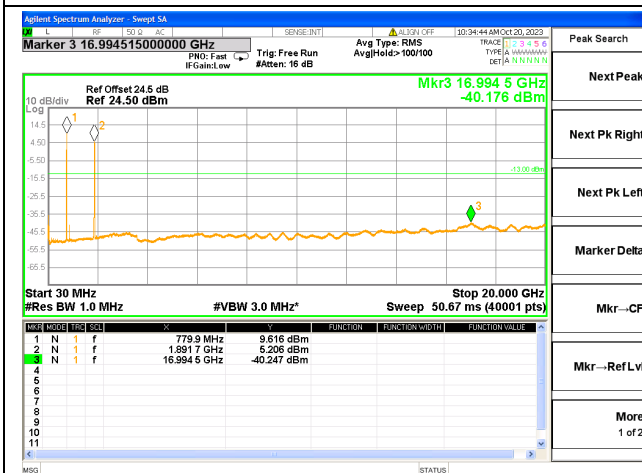
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2A-13A/20M+10M/QPSK/Low CH

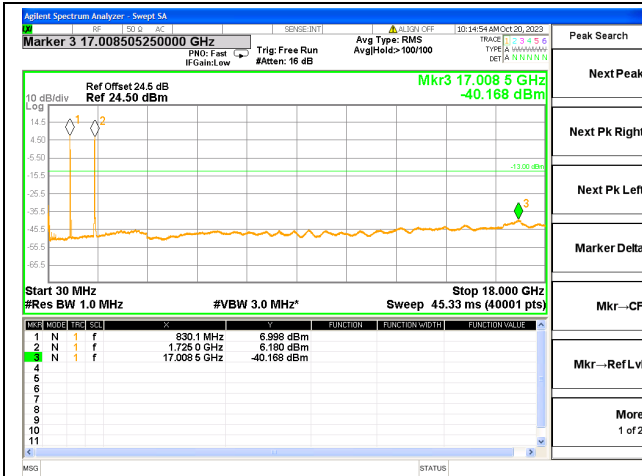


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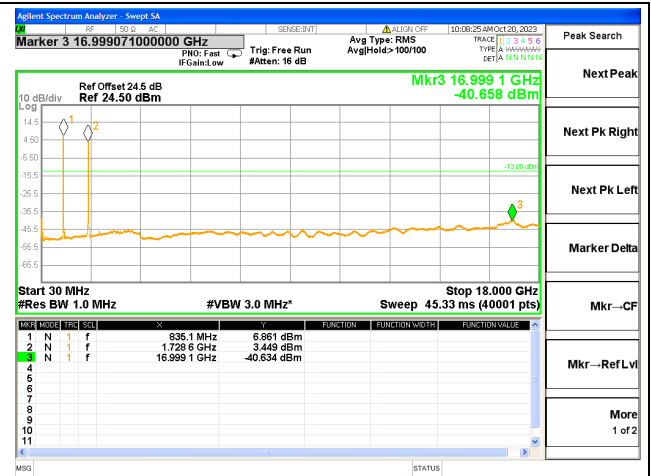


2A-13A/20M+10M/QPSK/High CH

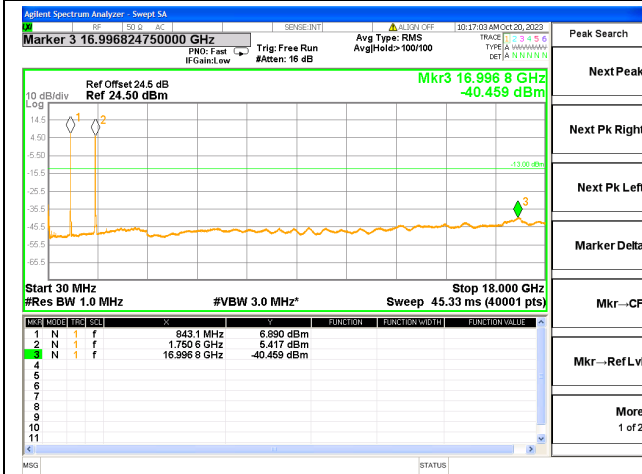
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4A-5A/20M+10M/QPSK/Low CH

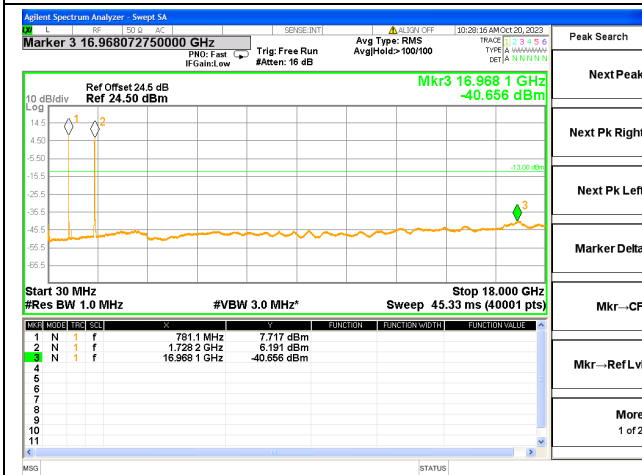


4A-5A/20M+10M /QPSK/Mid CH

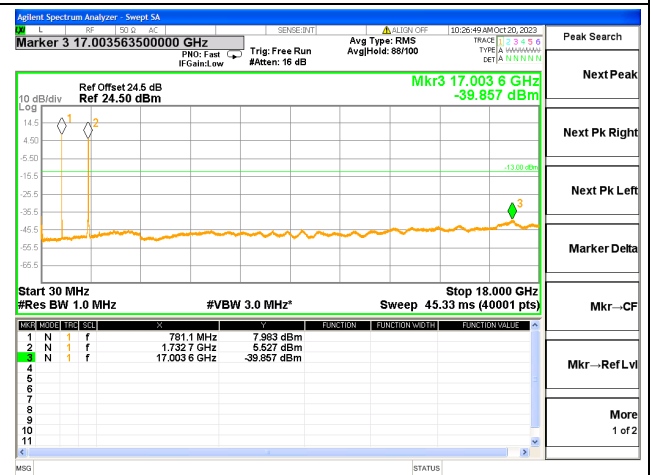


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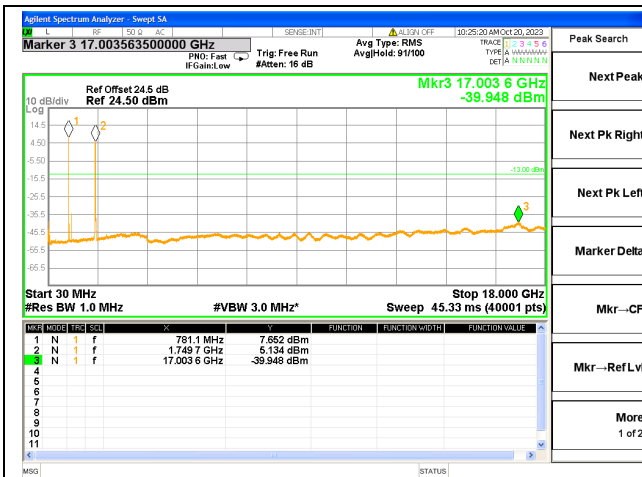
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4A-13A/20M+10M/QPSK/Low CH

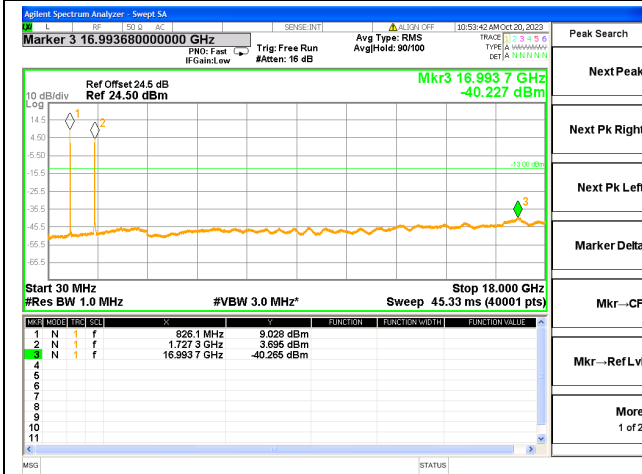


4A-13A /20M+10M /QPSK/Mid CH

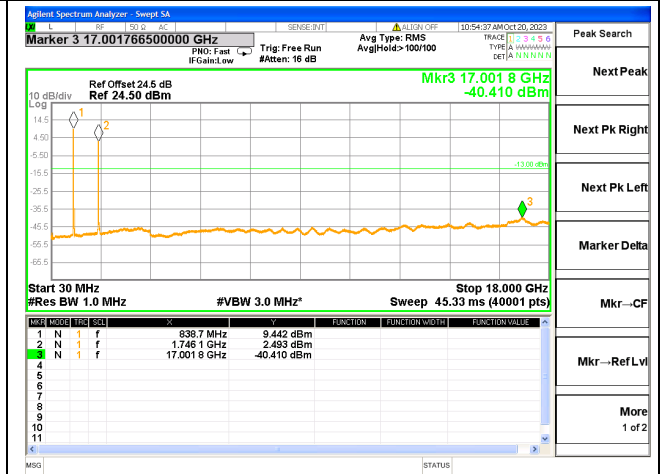


4A-13A /20M+10M/QPSK/ High CH

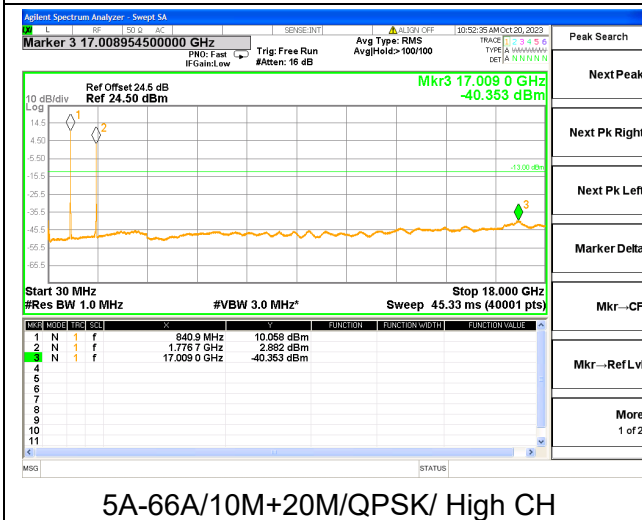
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5A-66A/10M+20M/QPSK/Low CH

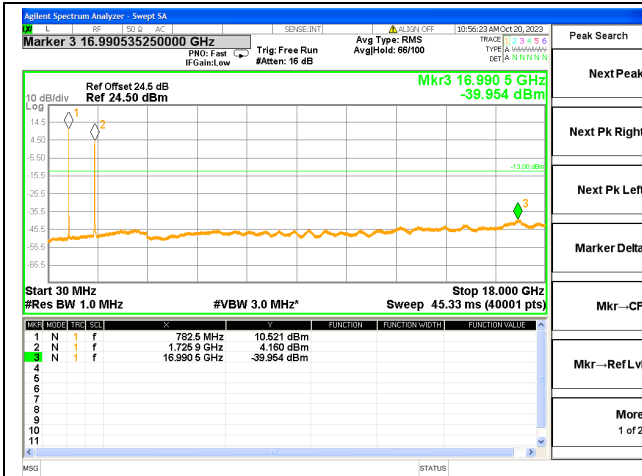


5A-66A/10M+20M /QPSK/Mid CH

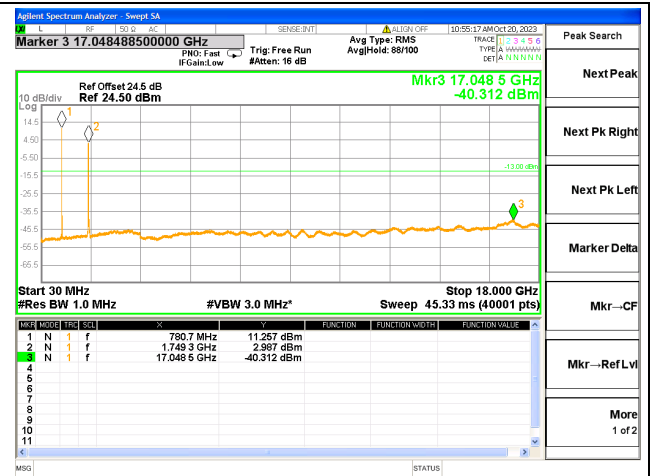


5A-66A/10M+20M/QPSK/ High CH

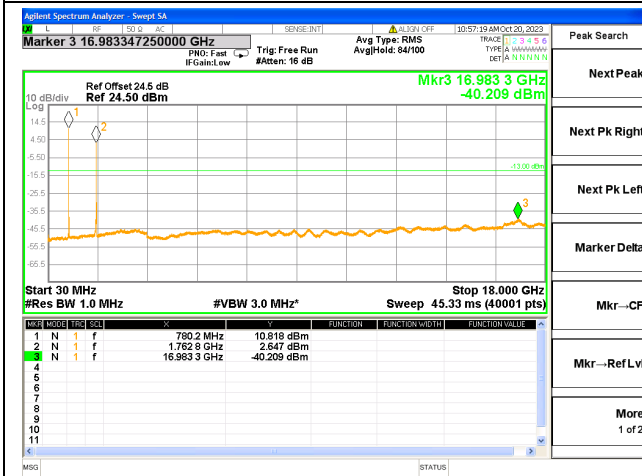
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13A-66A/10M+20M/QPSK/Low CH

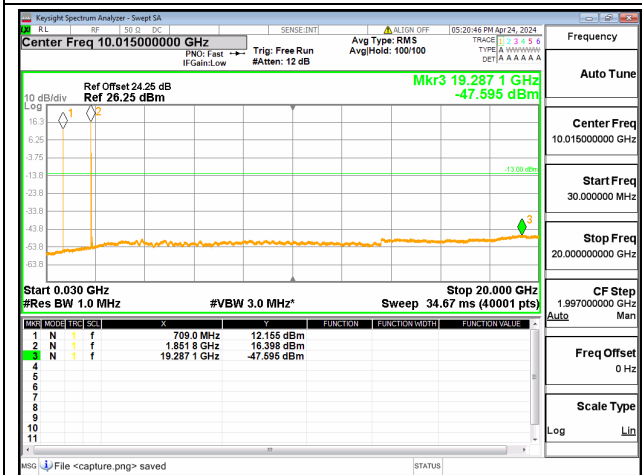


13A-66A/10M+20M /QPSK/Mid CH

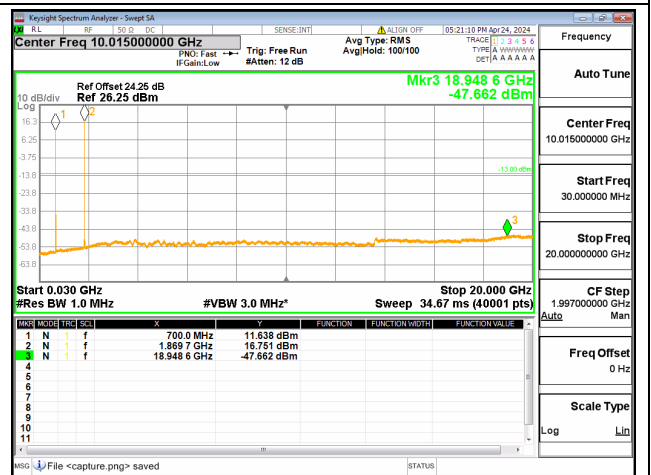


13A-66A/10M+20M /QPSK/ High CH

N/A



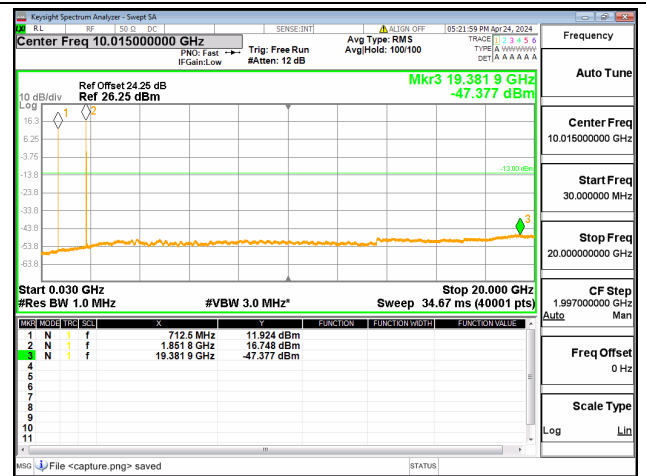
2A-12A / 20+10MHz / QPSK / Low+Low CH /
1@0-1@49



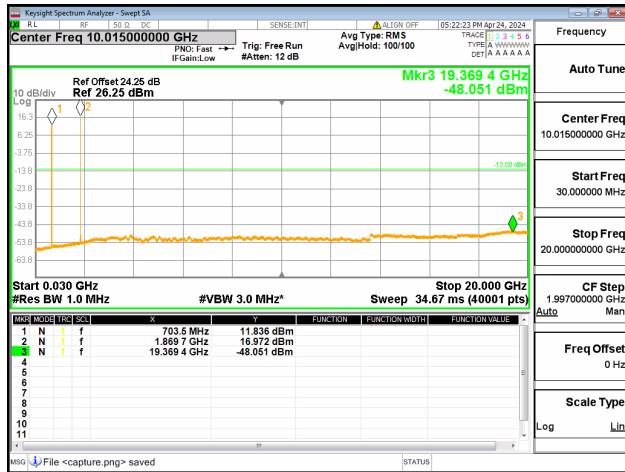
2A-12A / 20+10MHz / QPSK / Low+Low CH /
1@99-1@0



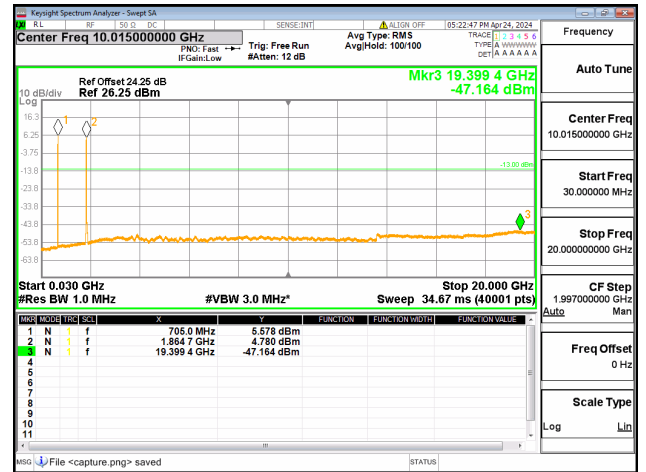
2A-12A / 20+10MHz / QPSK / Low+Low CH / 100@0-50@0



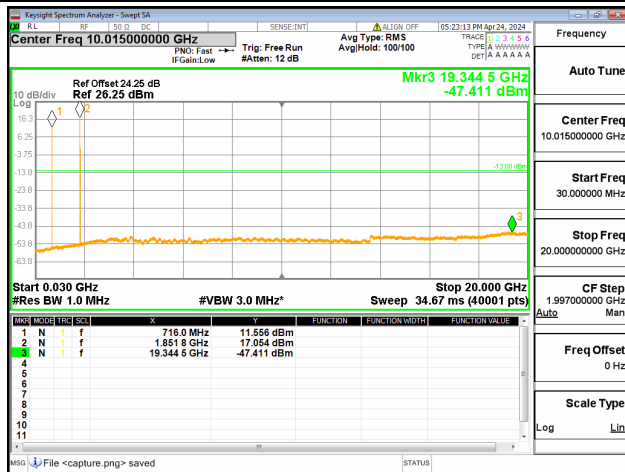
2A-12A / 20+10MHz / QPSK / Low+Mid CH / 1@0-1@49



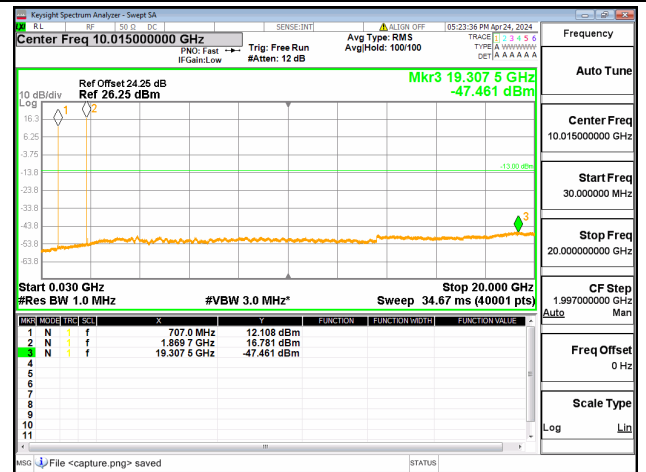
2A-12A / 20+10MHz / QPSK / Low+Mid CH / 1@99-1@0



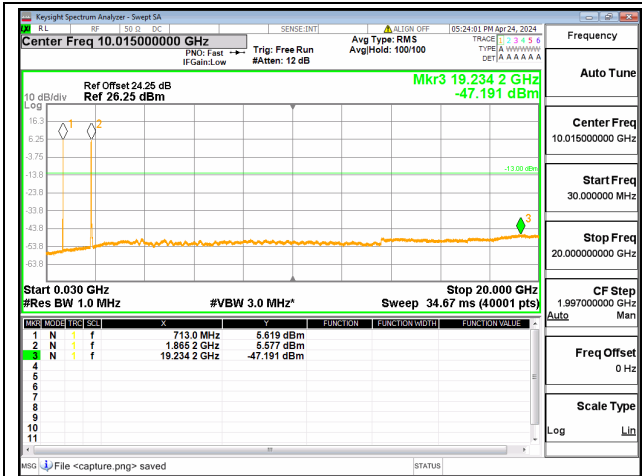
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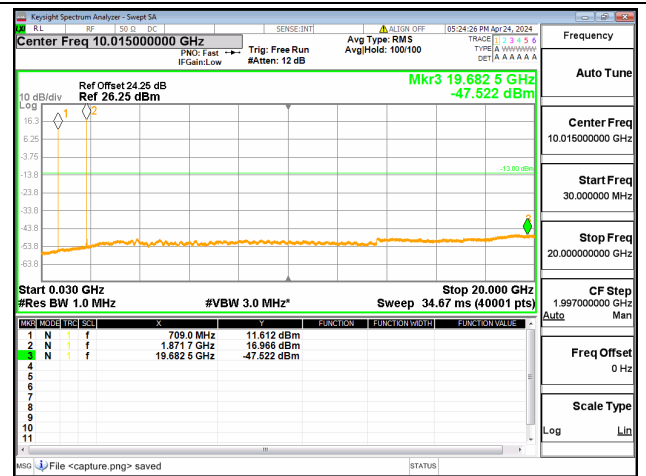
2A-12A / 20+10MHz / QPSK / Low+High CH / 1@0-1@49



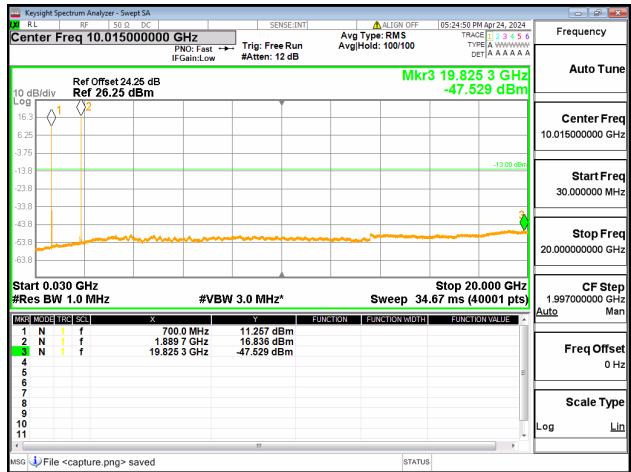
2A-12A / 20+10MHz / QPSK / Low+High CH / 1@99-1@0



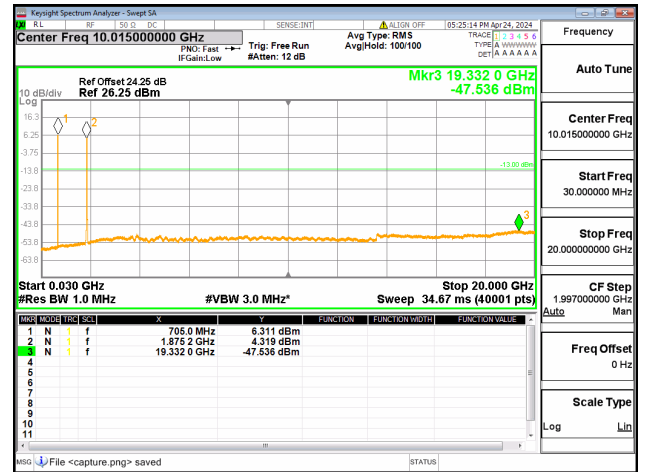
2A-12A / 20+10MHz / QPSK / Low+High CH / 100@0-50@0



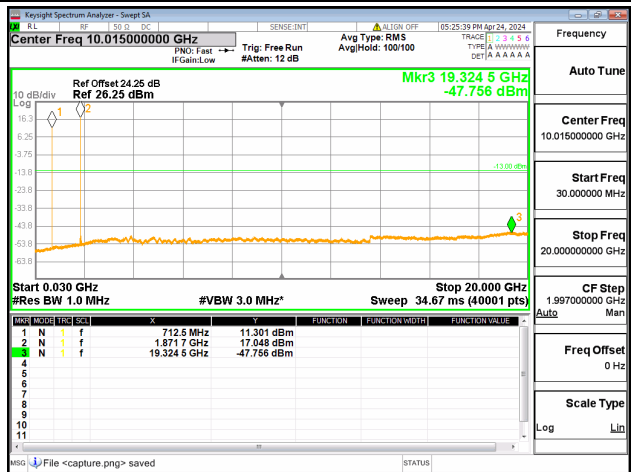
2A-12A / 20+10MHz / QPSK / Mid+Low CH / 1@0-1@49



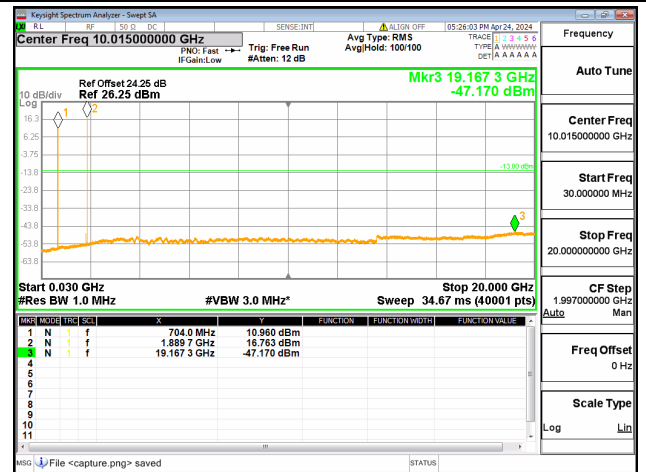
2A-12A / 20+10MHz / QPSK / Mid+Low CH / 1@99-1@0



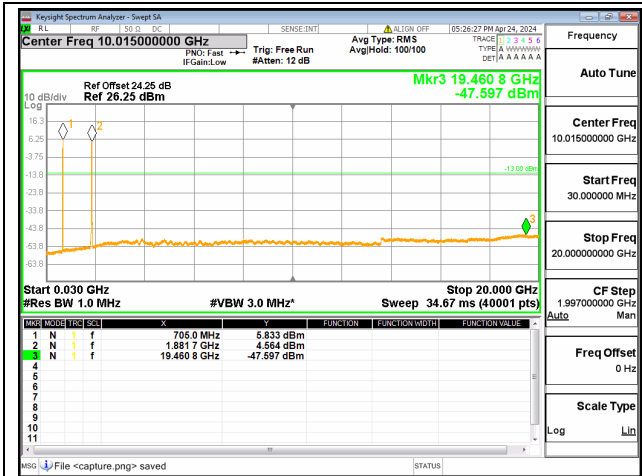
2A-12A / 20+10MHz / QPSK / Mid+Low CH / 100@0-50@0



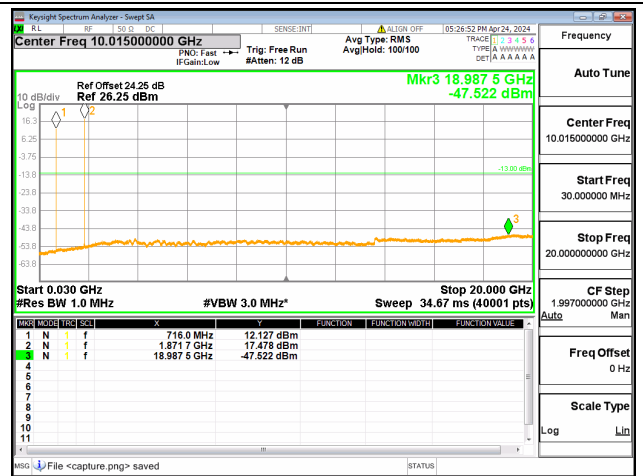
2A-12A / 20+10MHz / QPSK / Mid+Mid CH / 1@0-1@49



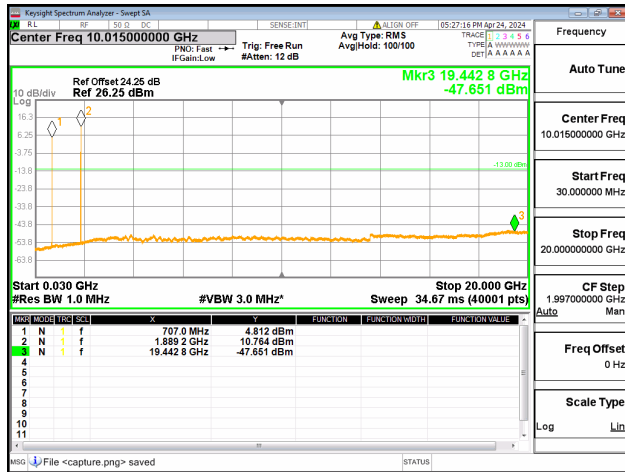
2A-12A / 20+10MHz / QPSK / Mid+Mid CH / 1@99-1@0



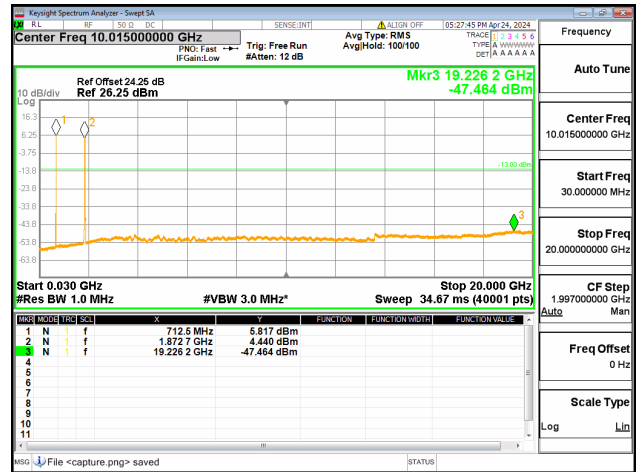
2A-12A / 20+10MHz / QPSK / Mid+Mid CH / 100@0-50@0



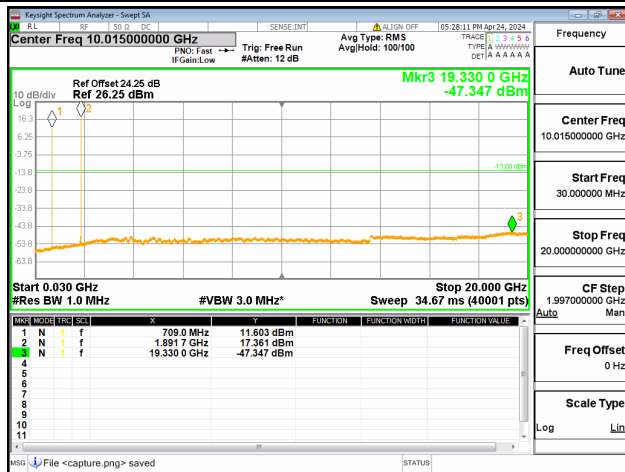
2A-12A / 20+10MHz / QPSK / Mid+High CH / 1@0-1@49



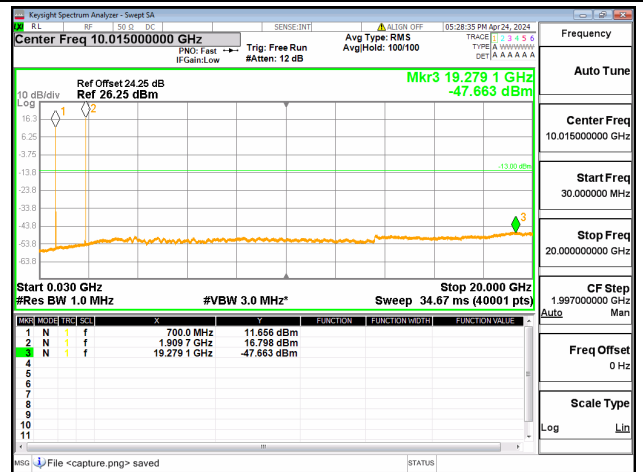
2A-12A / 20+10MHz / QPSK / Mid+High CH / 1@99-1@0



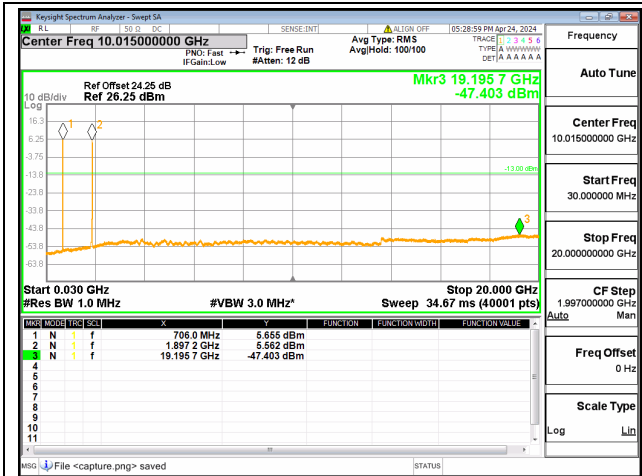
2A-12A / 20+10MHz / QPSK / Mid+High CH / 100@0-50@0



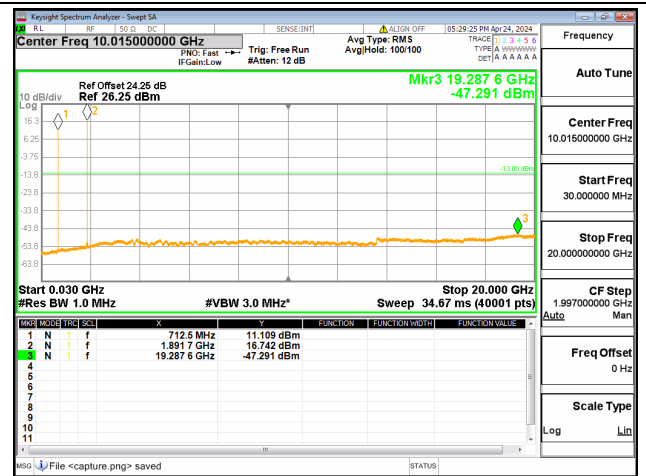
2A-12A / 20+10MHz / QPSK / High+Low CH / 1@0-1@49



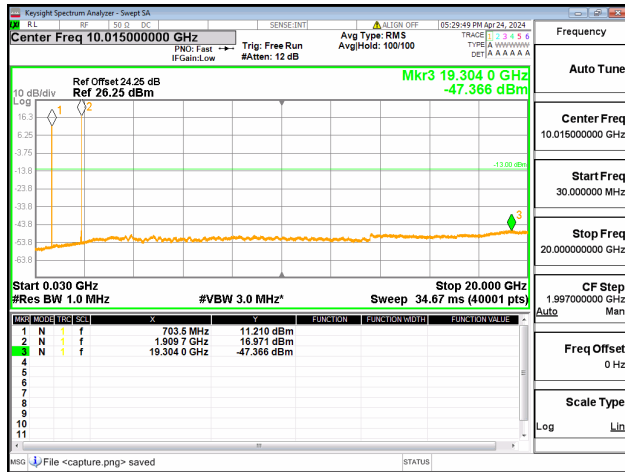
2A-12A / 20+10MHz / QPSK / High+Low CH / 1@99-1@0



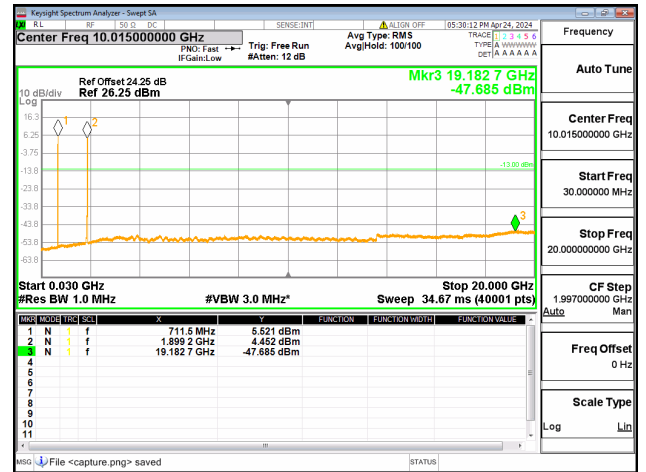
2A-12A / 20+10MHz / QPSK / High+Low CH / 100@0-50@0



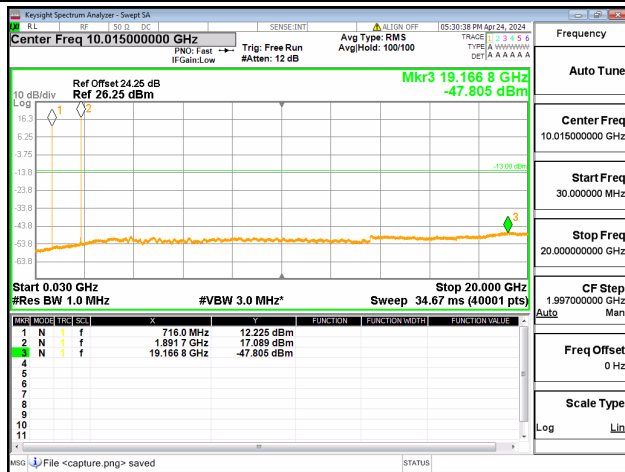
2A-12A / 20+10MHz / QPSK / High+Mid CH / 1@0-1@49



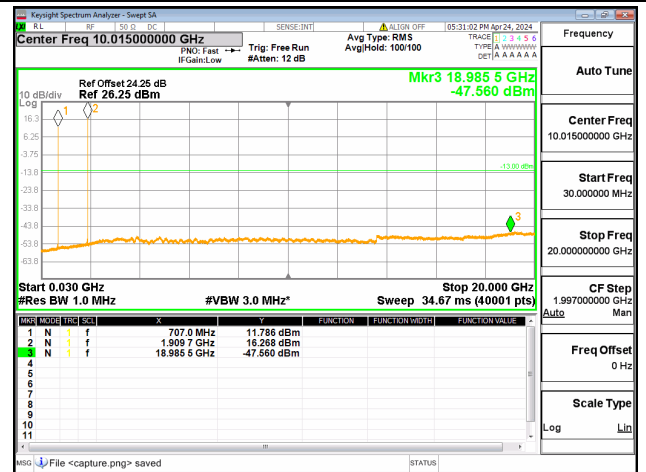
2A-12A / 20+10MHz / QPSK / High+Mid CH / 1@99-1@0



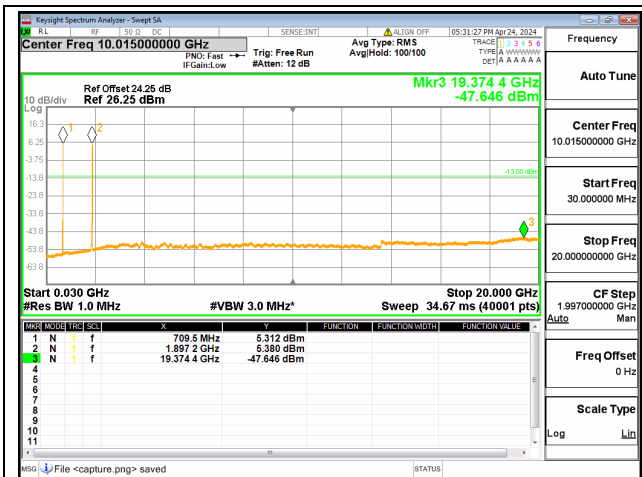
2A-12A / 20+10MHz / QPSK / High+Mid CH / 100@0-50@0



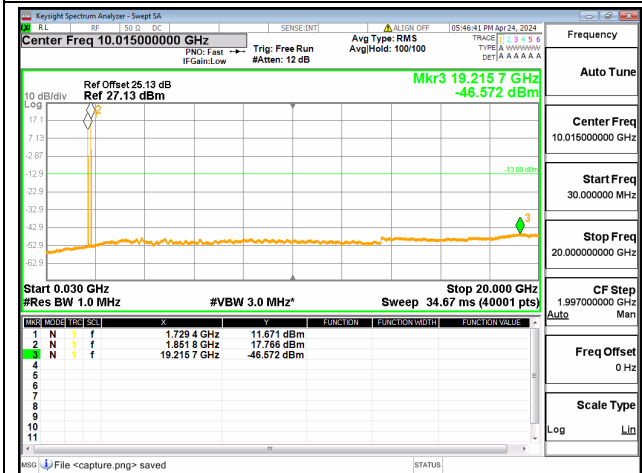
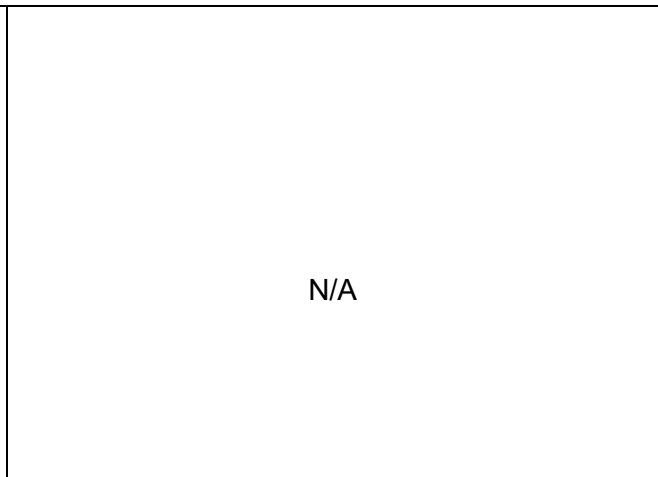
2A-12A / 20+10MHz / QPSK / High+High CH / 1@0-1@49



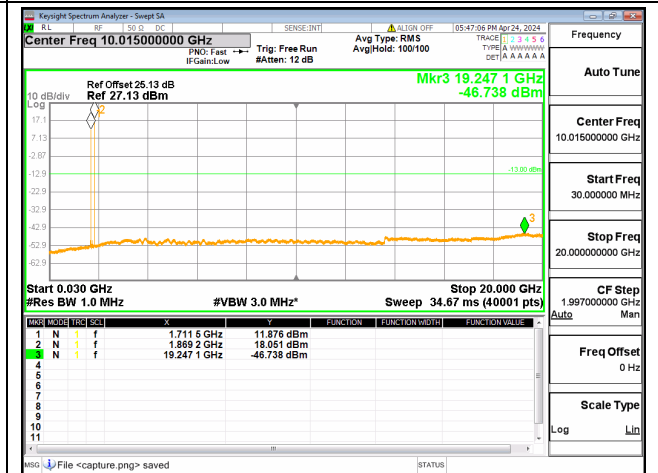
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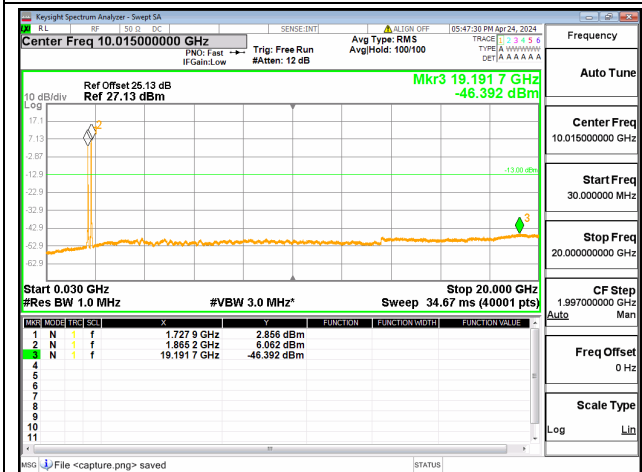
2A-12A / 20+10MHz / QPSK / High+High CH /
100@0-50@0



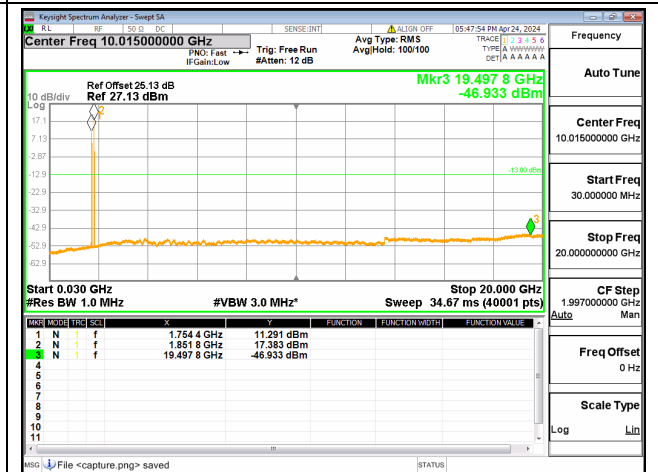
2A-66A / 20+20MHz / QPSK / Low+Low CH /
1@0-1@99



2A-66A / 20+20MHz / QPSK / Low+Low CH /
1@99-1@0



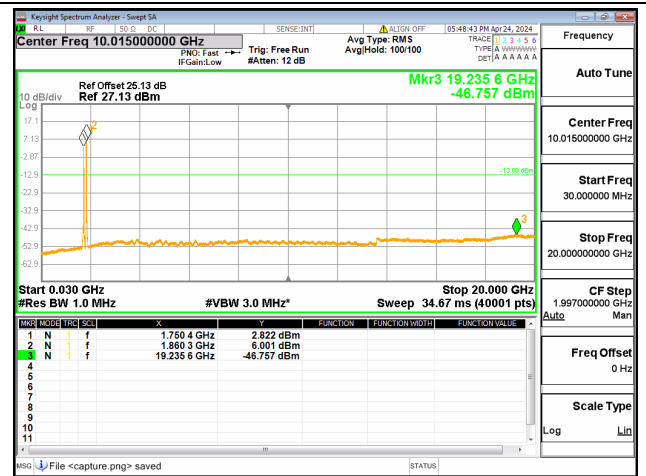
2A-66A / 20+20MHz / QPSK / Low+Low CH /
100@0-100@0



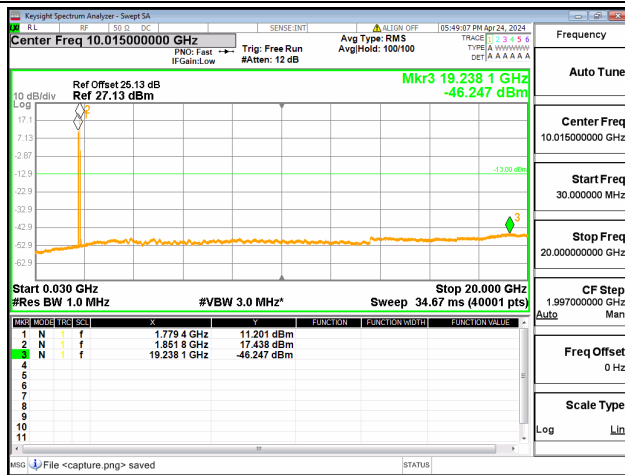
2A-66A / 20+20MHz / QPSK / Low+Mid CH /
1@0-1@99



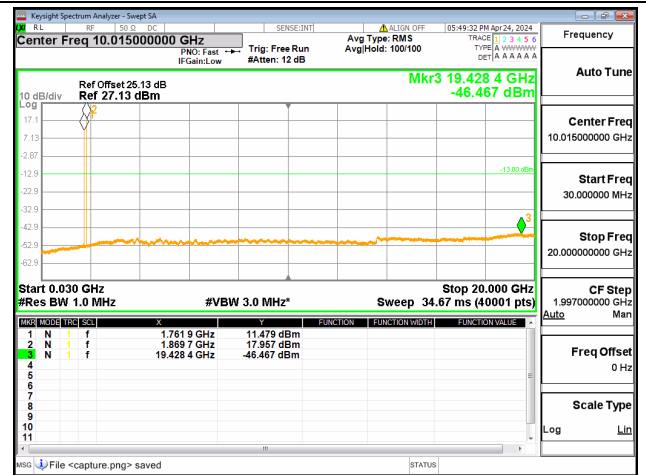
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1@99-1@0



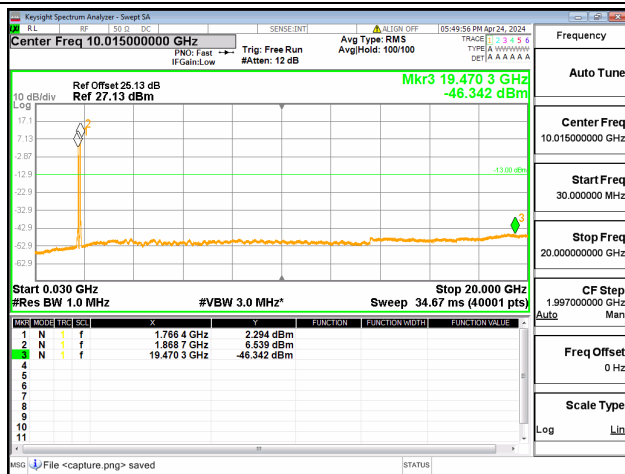
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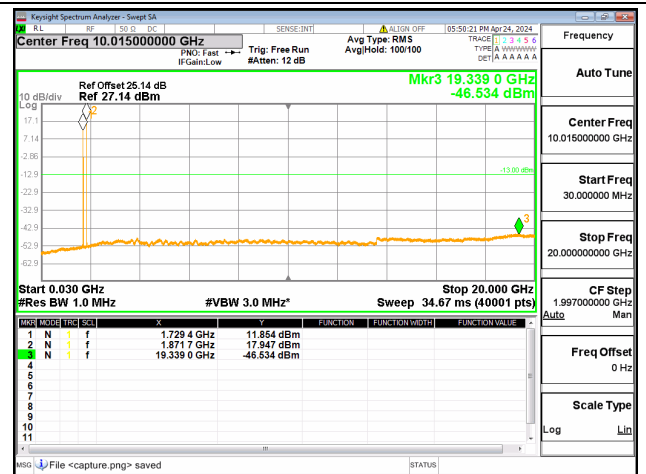
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1@0-1@99



2A-66A / 20+20MHz / QPSK / Low+High CH /
1@99-1@0



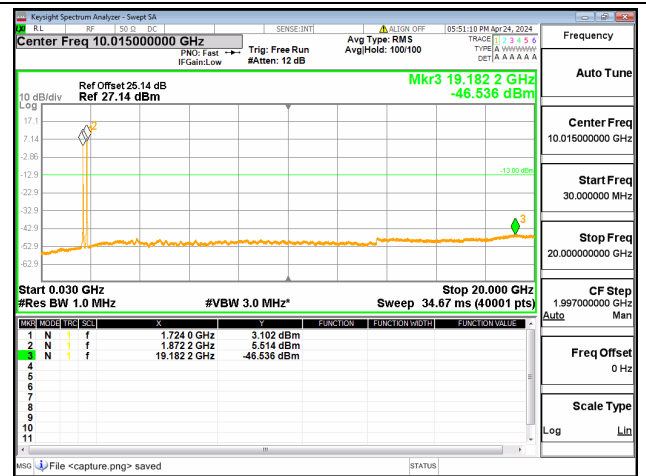
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100@0-100@0



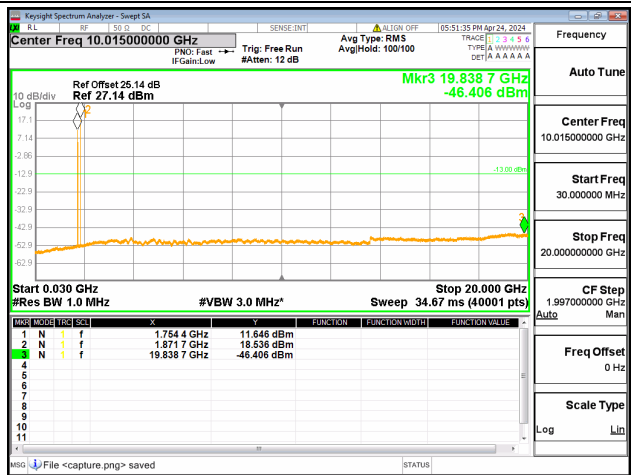
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1@0-1@99



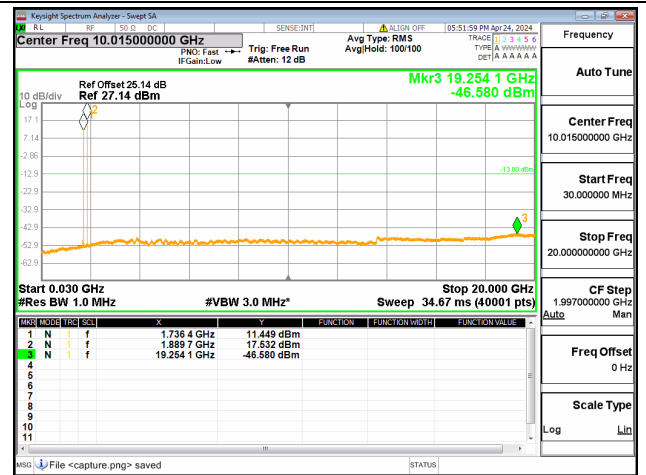
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1@99-1@0



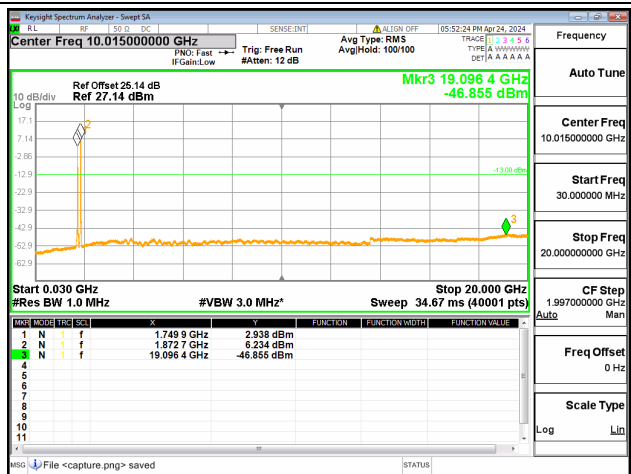
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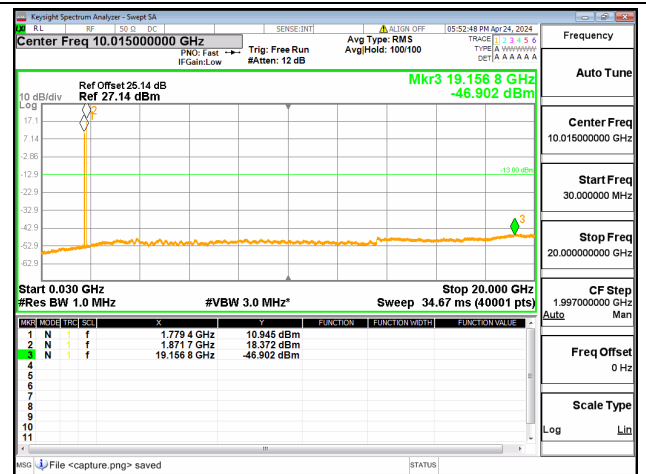
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1@0-1@99



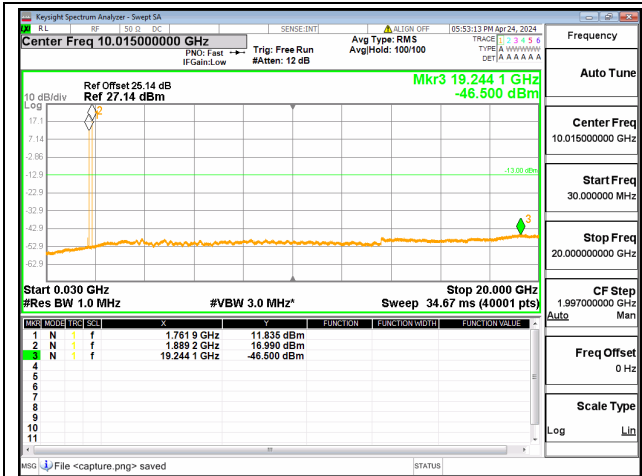
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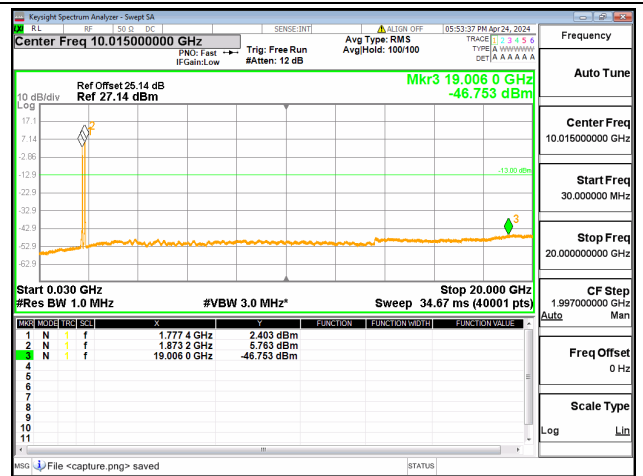
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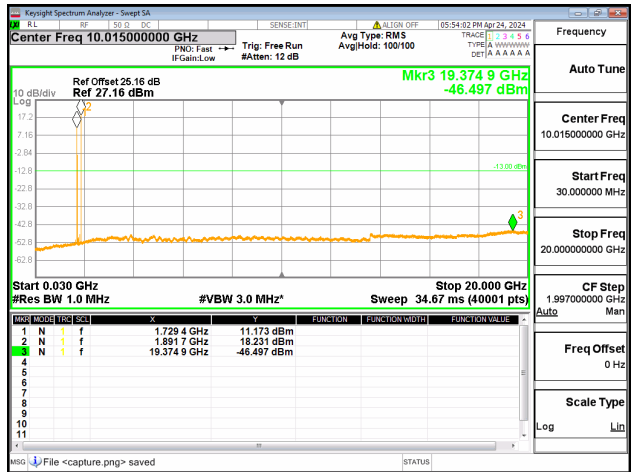
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1@0-1@99



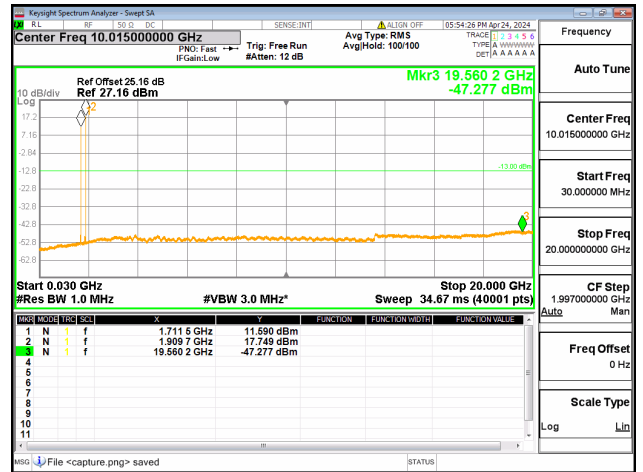
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1@99-1@0



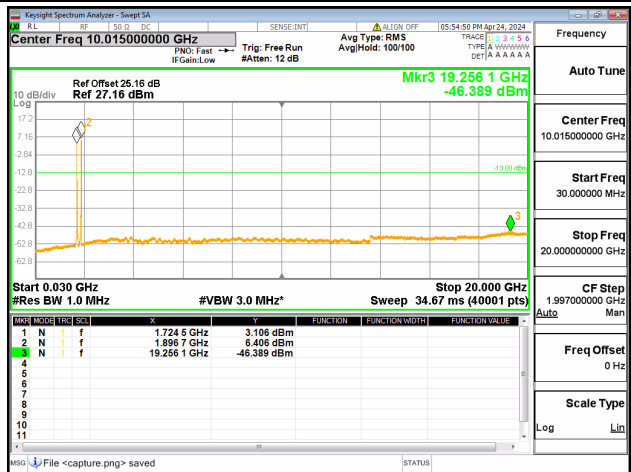
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100@0-100@0



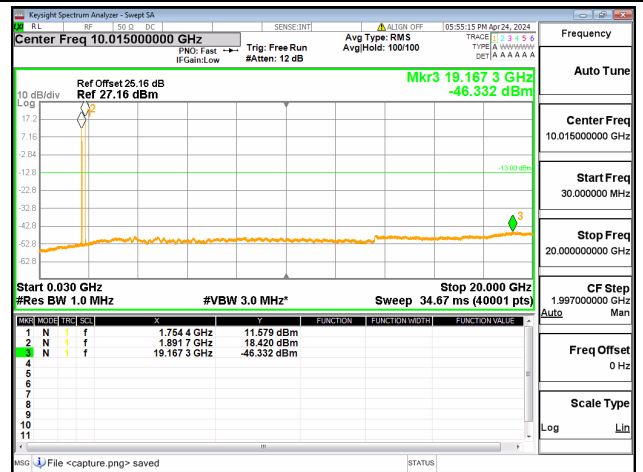
2A-66A / 20+20MHz / QPSK / High+Low CH /
1@0-1@99



2A-66A / 20+20MHz / QPSK / High+Low CH /
1@99-1@0



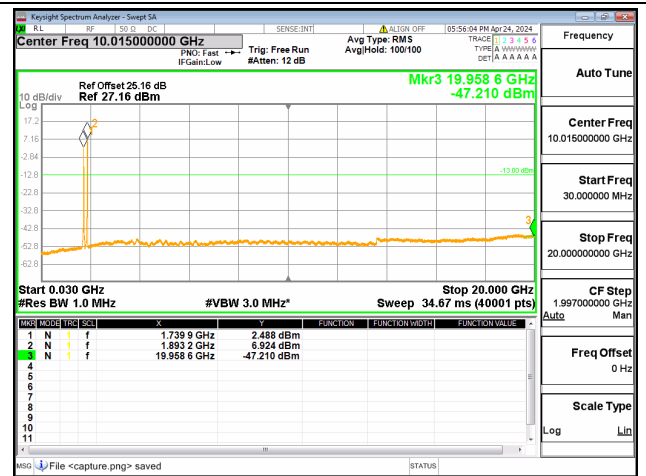
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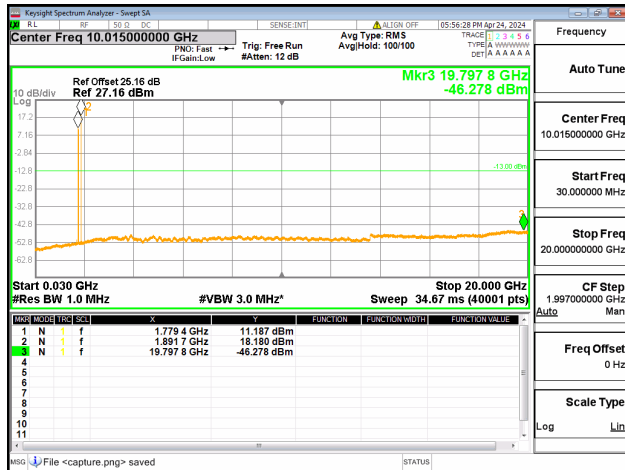
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1@0-1@99



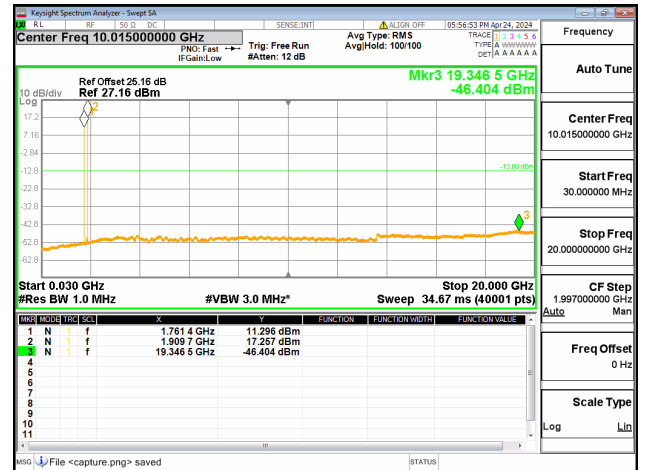
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1@99-1@0



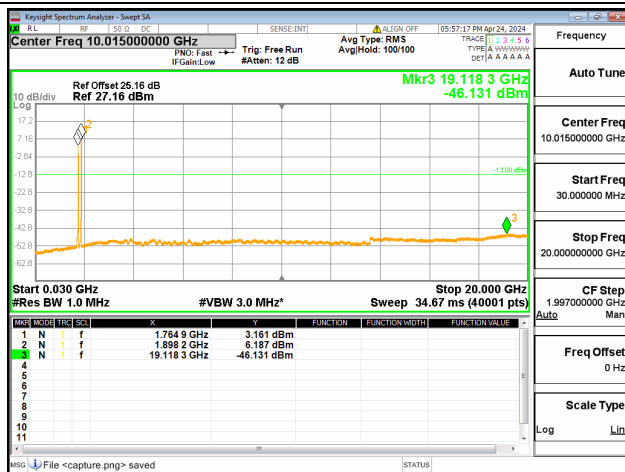
2A-66A / 20+20MHz / QPSK / High+Mid CH /
100@0-100@0



2A-66A / 20+20MHz / QPSK / High+High CH /
1@0-1@99

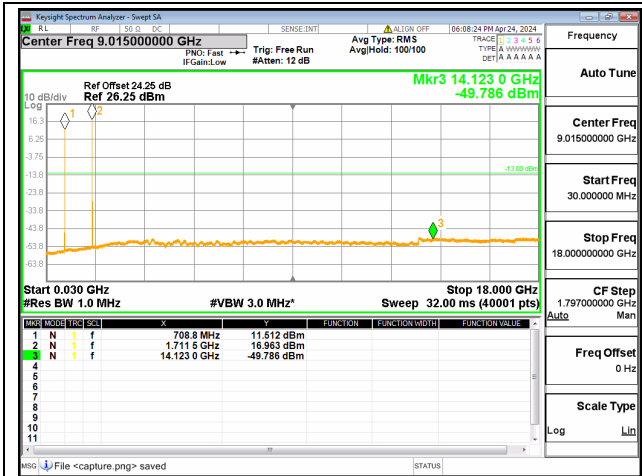


2A-66A / 20+20MHz / QPSK / High+High CH /
1@99-1@0

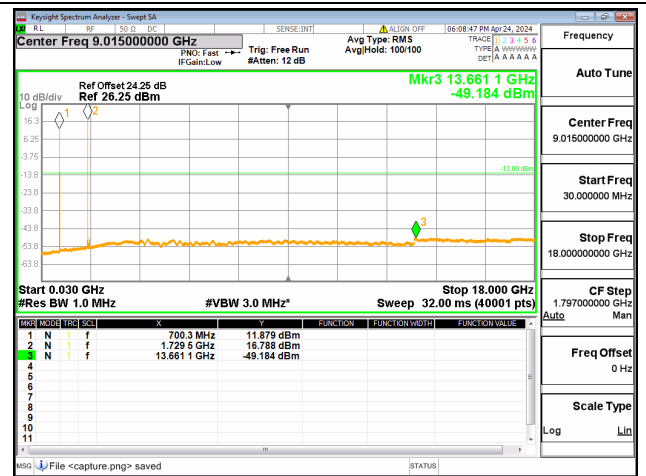


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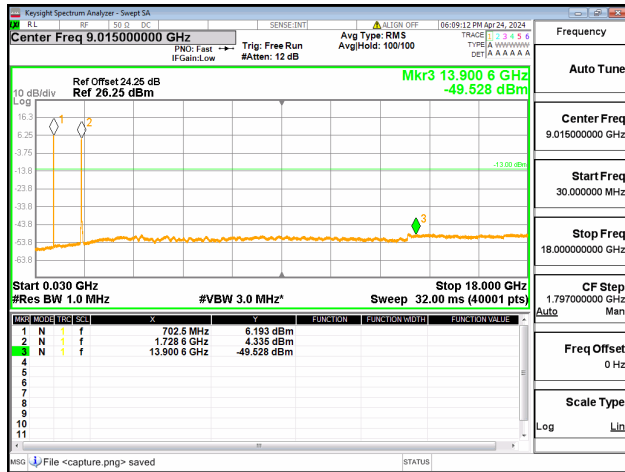
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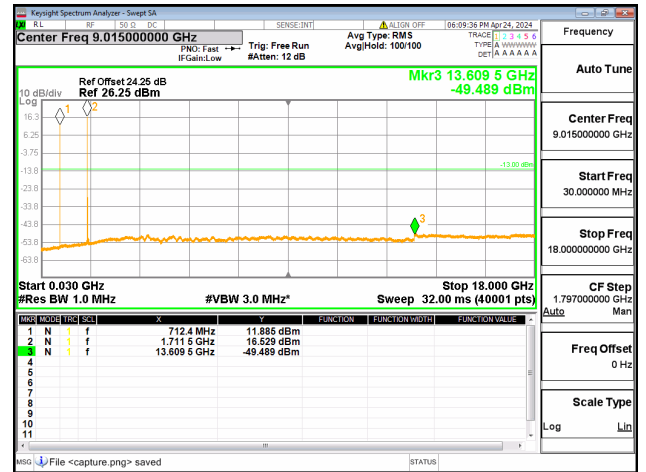
4A-12A / 20+10MHz / QPSK / Low+Low CH /
1@0-1@49



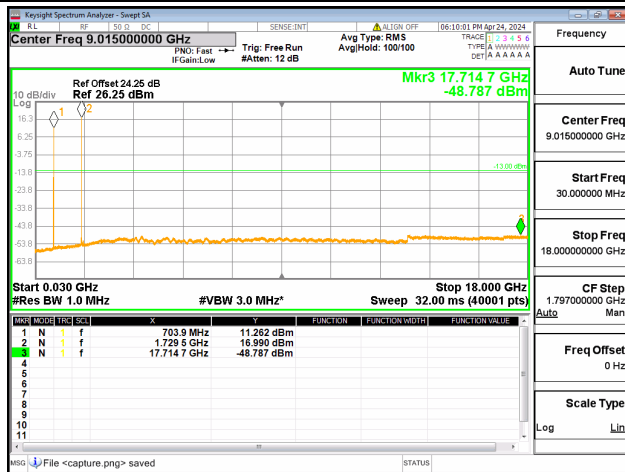
4A-12A / 20+10MHz / QPSK / Low+Low CH /
1@99-1@0



4A-12A / 20+10MHz / QPSK / Low+Low CH /
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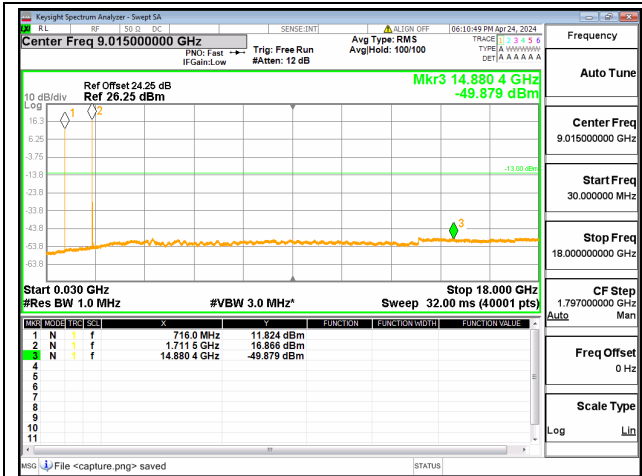
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1@0-1@49



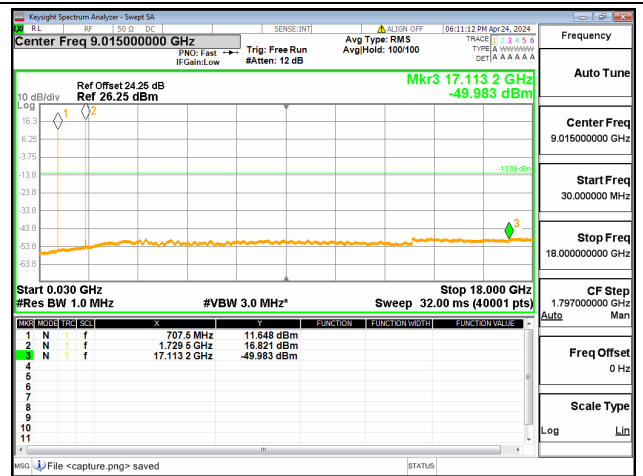
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1@99-1@0



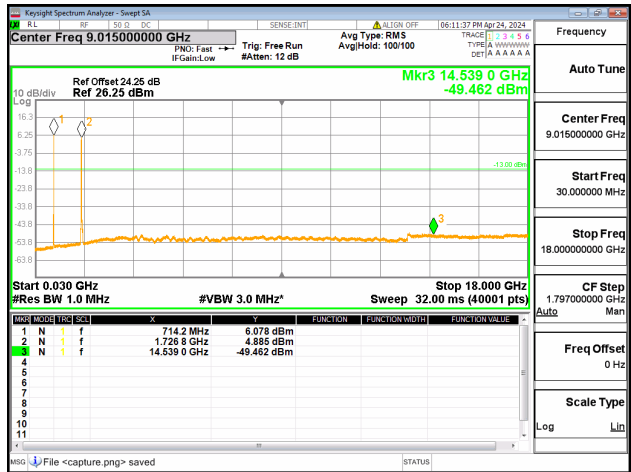
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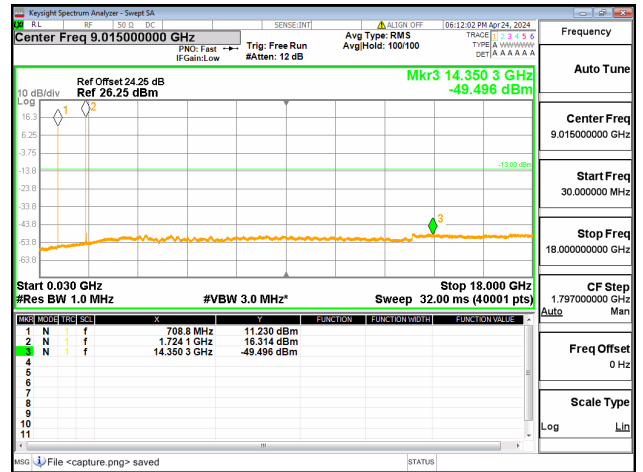
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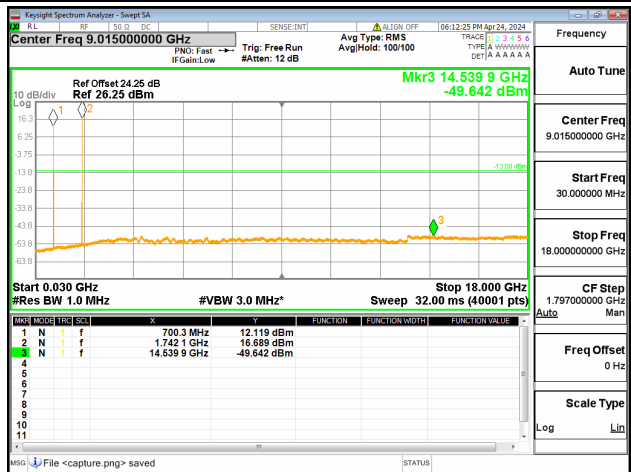
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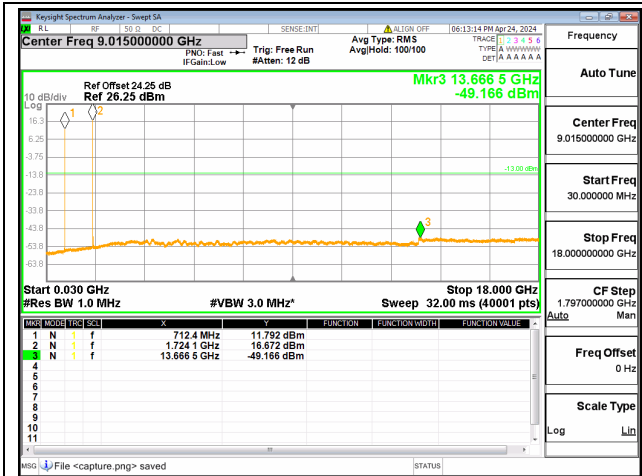
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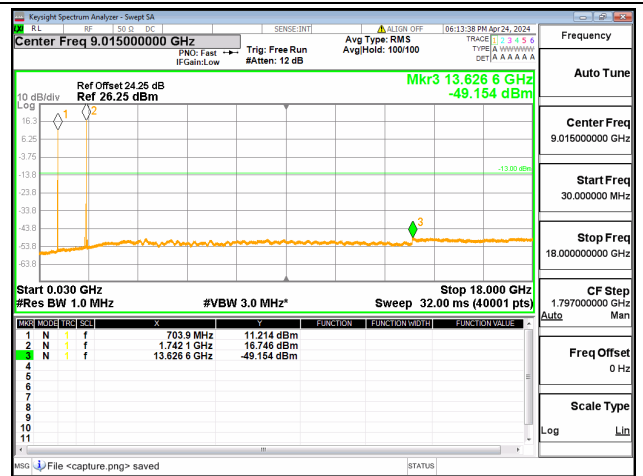
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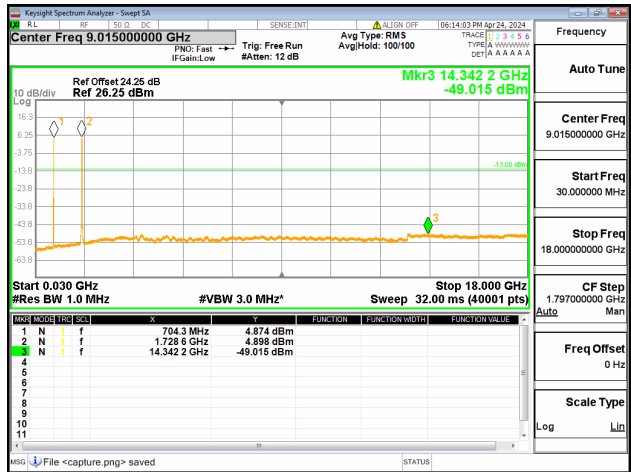
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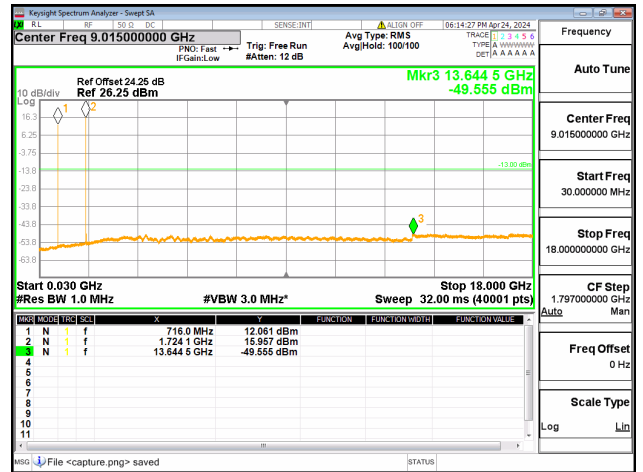
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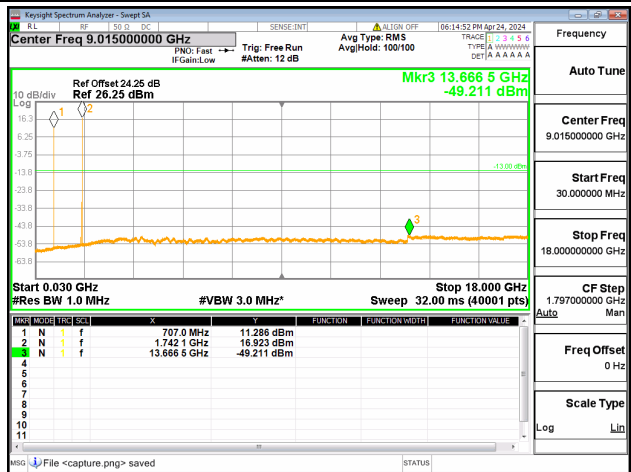
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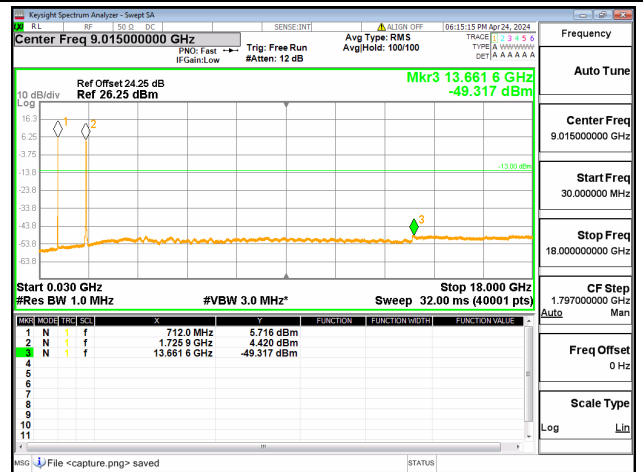
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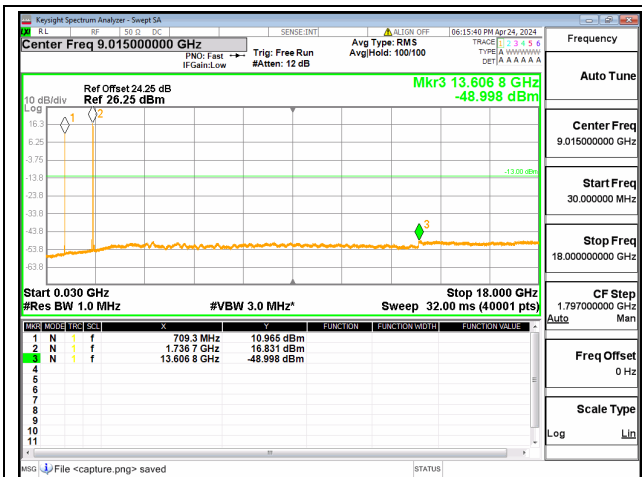
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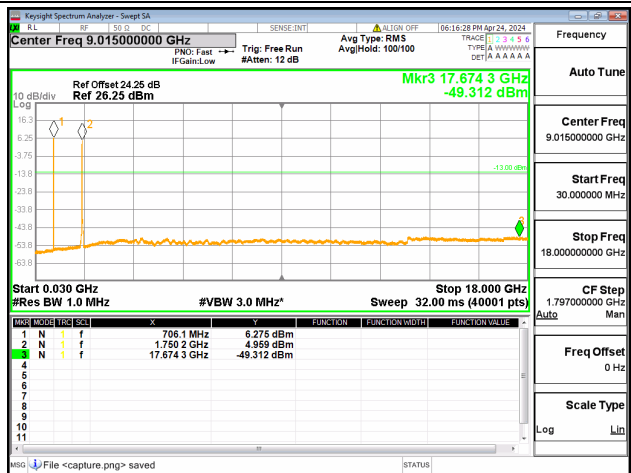
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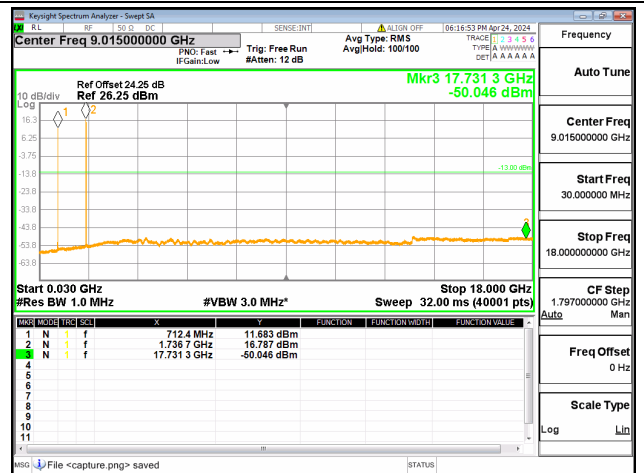
4A-12A / 20+10MHz / QPSK / High+Low CH / 1@0-1@49



4A-12A / 20+10MHz / QPSK / High+Low CH / 1@99-1@0



4A-12A / 20+10MHz / QPSK / High+Low CH / 100@0-50@0



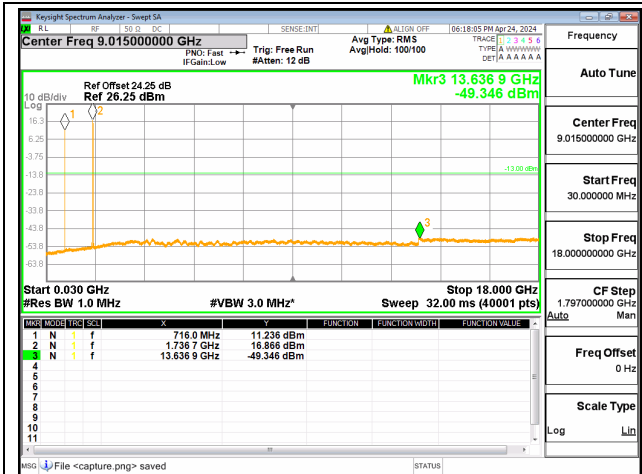
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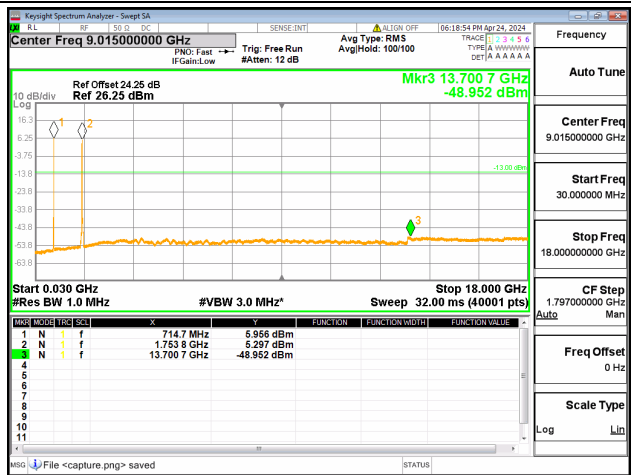
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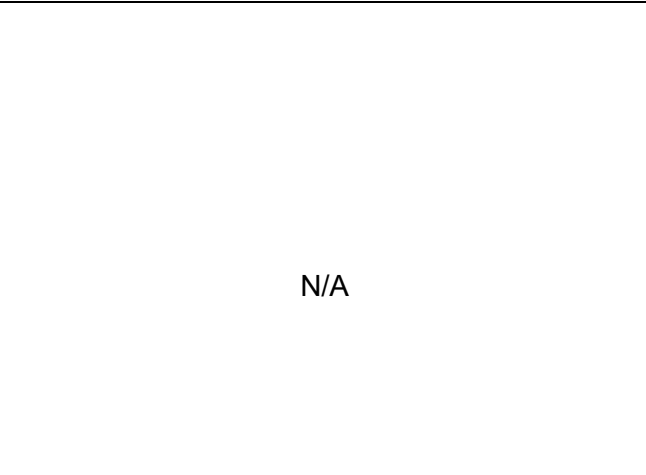
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1@0-1@49



4A-12A / 20+10MHz / QPSK / High+High CH /
1@99-1@0



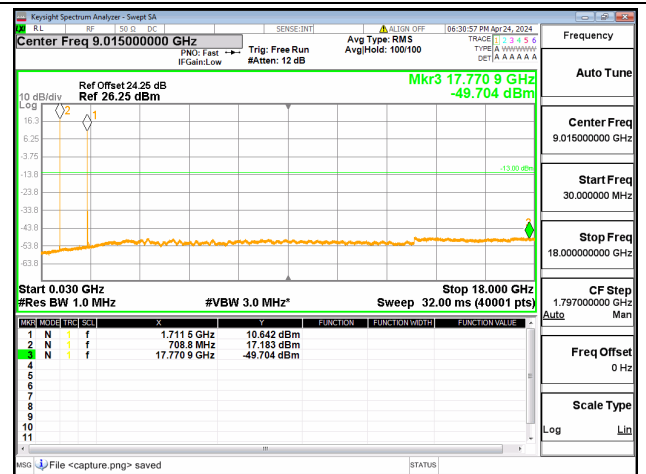
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100@0-50@0



N/A



12A-66A / 10+20MHz / QPSK / Low+Low CH /
1@0-1@99



12A-66A / 10+20MHz / QPSK / Low+Low CH /
1@49-1@0