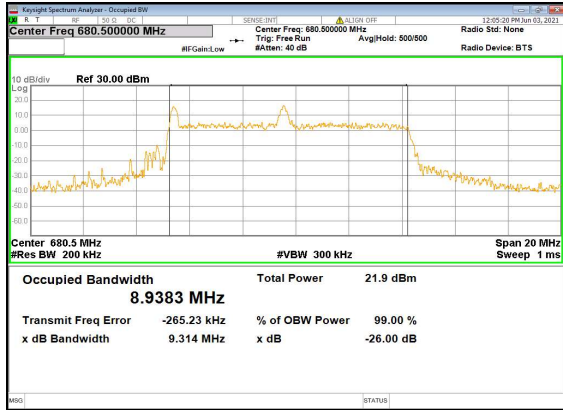
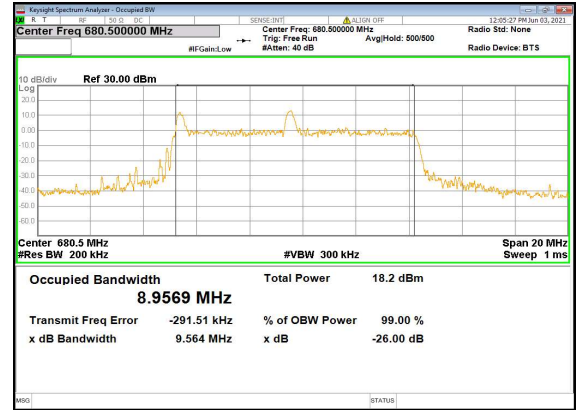




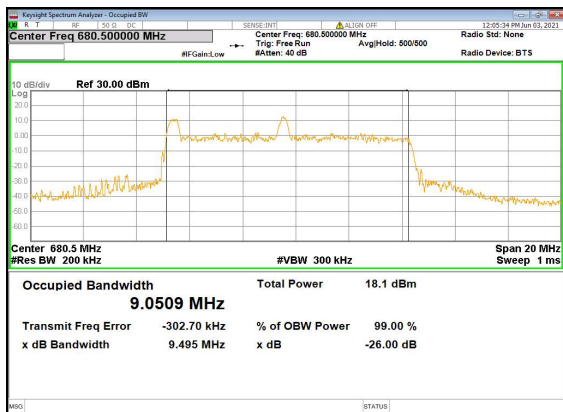
N71(10M)\_DFT-s-OFDM\_PI\_2-BPSK\_Outer\_Full  
\_Mid\_CH



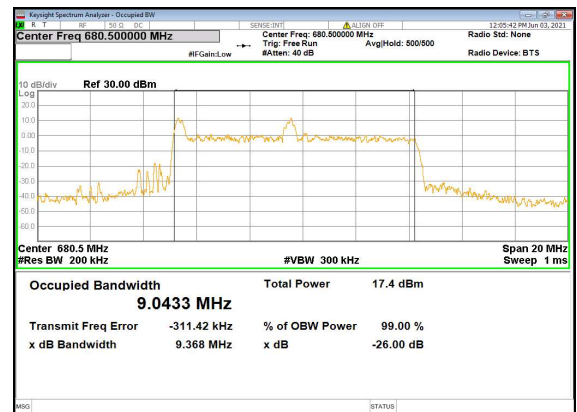
N71(10M)\_DFT-s-OFDM\_QPSK\_Outer\_Full  
\_Mid\_CH



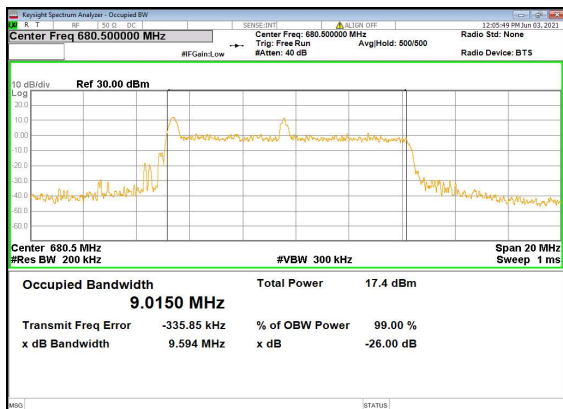
N71(10M)\_DFT-s-OFDM\_16  
QAM\_Outer\_Full\_Mid\_CH



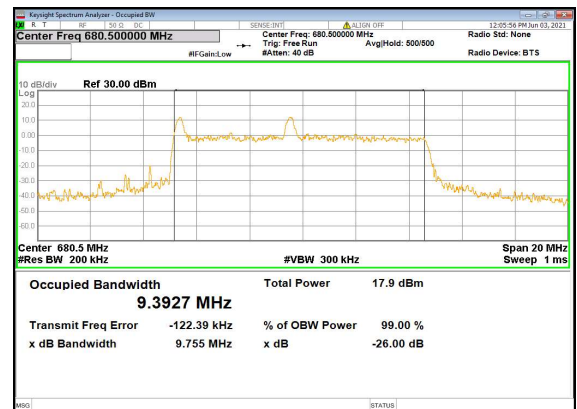
N71(10M)\_DFT-s-OFDM\_64  
QAM\_Outer\_Full\_Mid\_CH



N71(10M)\_DFT-s-OFDM\_256  
QAM\_Outer\_Full\_Mid\_CH

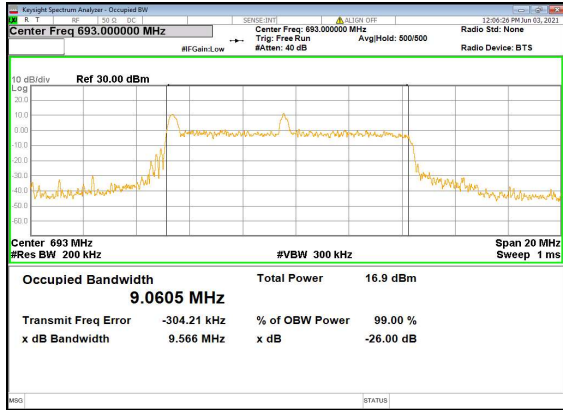


N71(10M)\_CP-OFDM\_QPSK\_Outer\_Full\_Mi  
d\_CH

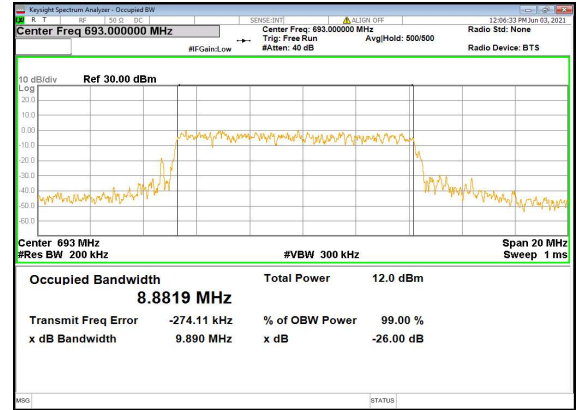




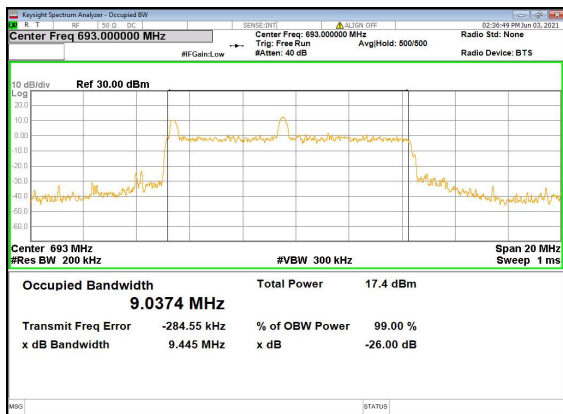
N71(10M)\_DFT-s-OFDM\_PI\_2-BPSK\_Outer\_Full\_High\_CH



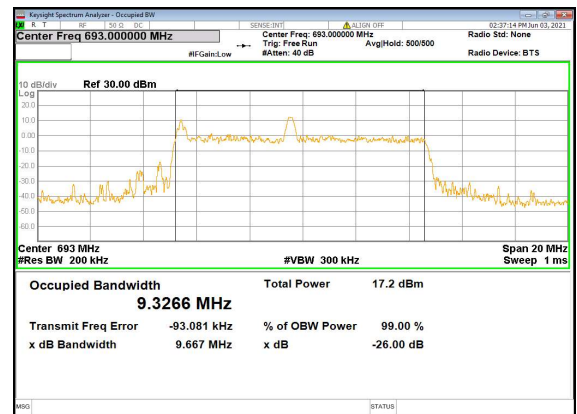
N71(10M)\_DFT-s-OFDM\_QPSK\_Outer\_Full\_High\_CH



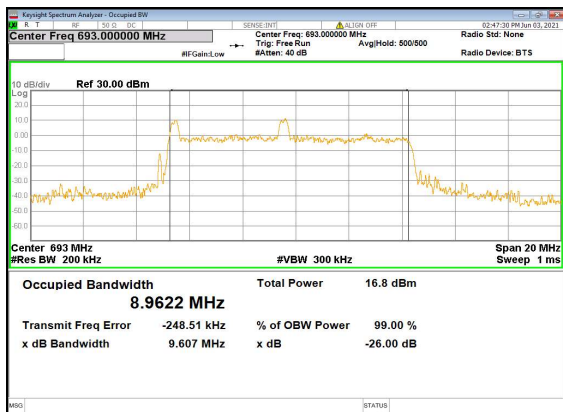
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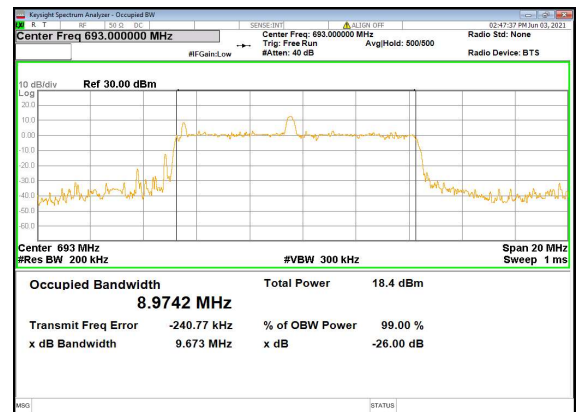
N71(10M)\_DFT-s-OFDM\_64\_QAM\_Outer\_Full\_High\_CH



N71(10M)\_DFT-s-OFDM\_256\_QAM\_Outer\_Full\_High\_CH

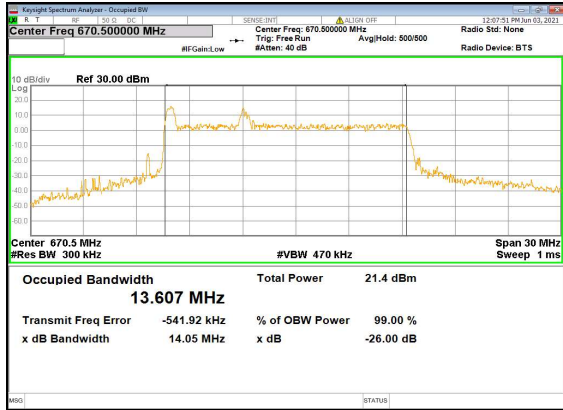


N71(10M)\_CP-OFDM\_QPSK\_Outer\_Full\_High\_CH

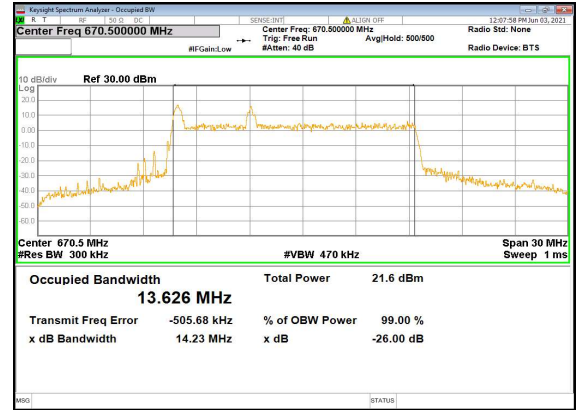




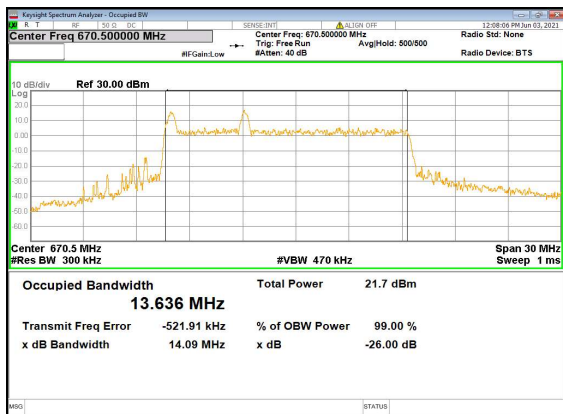
N71(15M)\_DFT-s-OFDM\_PI\_2-BPSK\_Outer\_Full  
\_Low\_CH



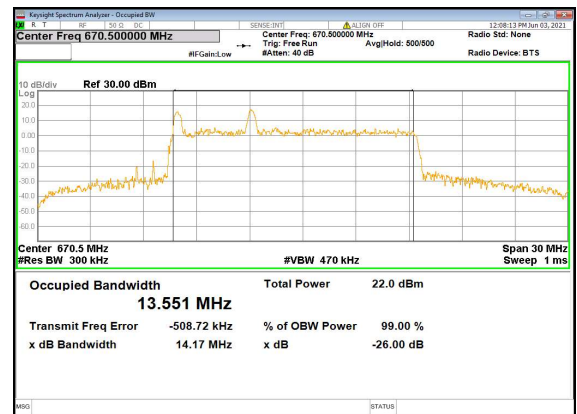
N71(15M)\_DFT-s-OFDM\_QPSK\_Outer\_Full  
Low\_CH



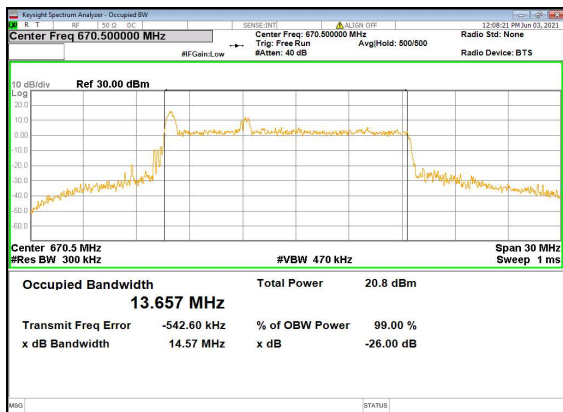
N71(15M)\_DFT-s-OFDM\_16  
QAM\_Outer\_Full\_Low\_CH



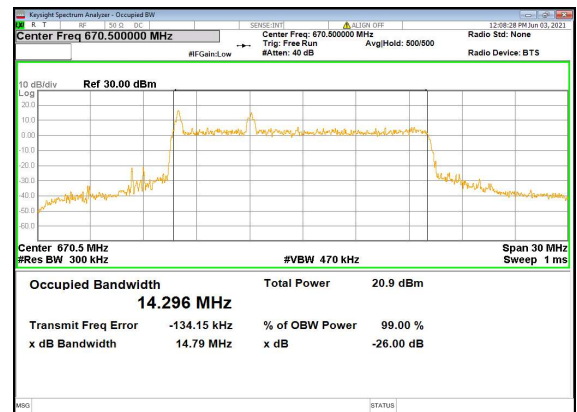
N71(15M)\_DFT-s-OFDM\_64  
QAM\_Outer\_Full\_Low\_CH



N71(15M)\_DFT-s-OFDM\_256  
QAM\_Outer\_Full\_Low\_CH

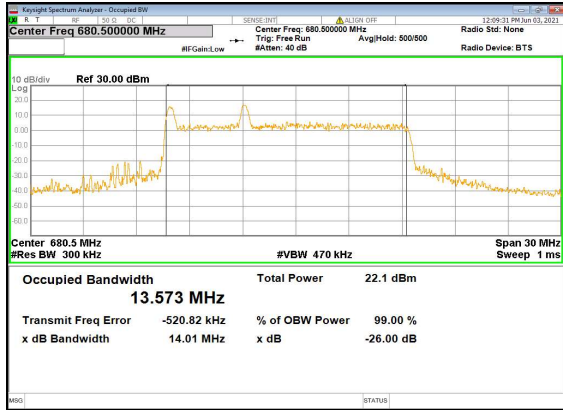


N71(15M)\_CP-OFDM\_QPSK\_Outer\_Full\_Lo  
w\_CH

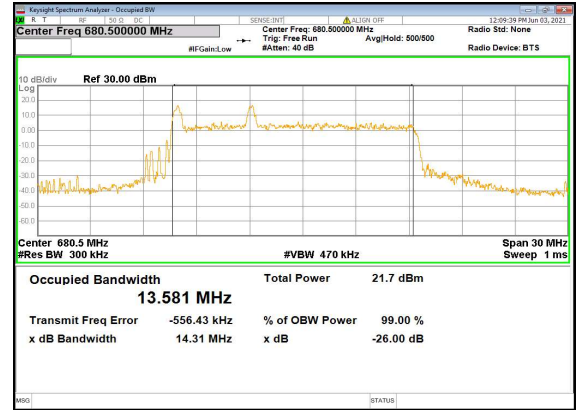




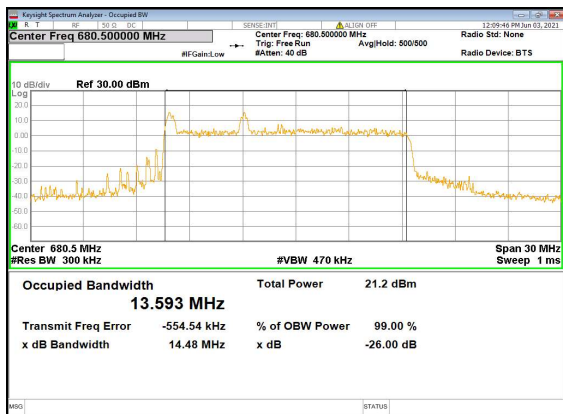
N71(15M)\_DFT-s-OFDM\_PI\_2-BPSK\_Outer\_Full  
\_Mid\_CH



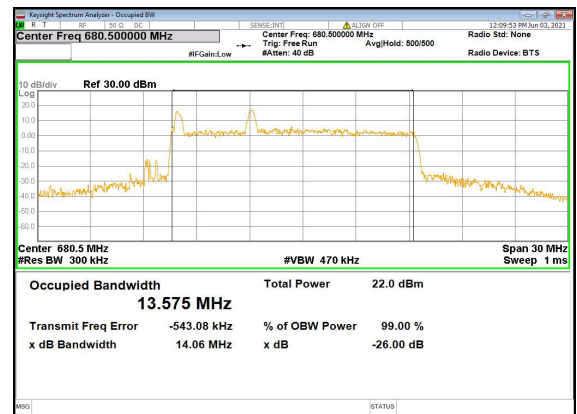
N71(15M)\_DFT-s-OFDM\_QPSK\_Outer\_Full  
\_Mid\_CH



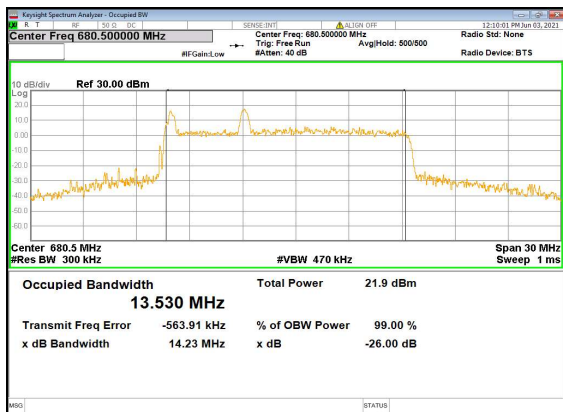
N71(15M)\_DFT-s-OFDM\_16  
QAM\_Outer\_Full\_Mid\_CH



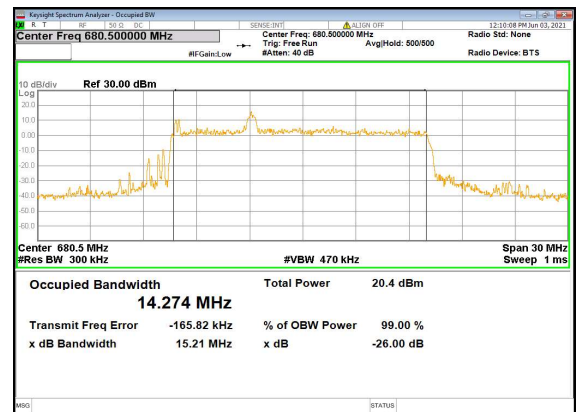
N71(15M)\_DFT-s-OFDM\_64  
QAM\_Outer\_Full\_Mid\_CH



N71(15M)\_DFT-s-OFDM\_256  
QAM\_Outer\_Full\_Mid\_CH

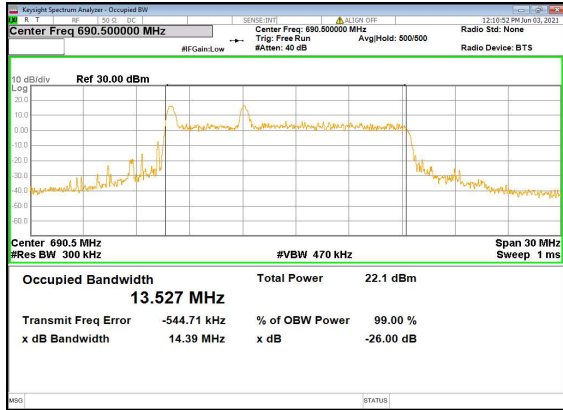


N71(15M)\_CP-OFDM\_QPSK\_Outer\_Full\_Mi  
d\_CH

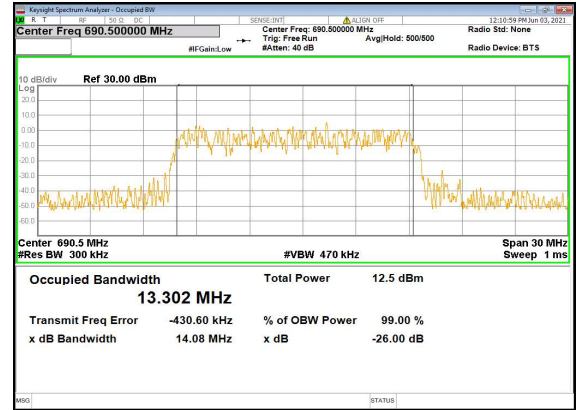




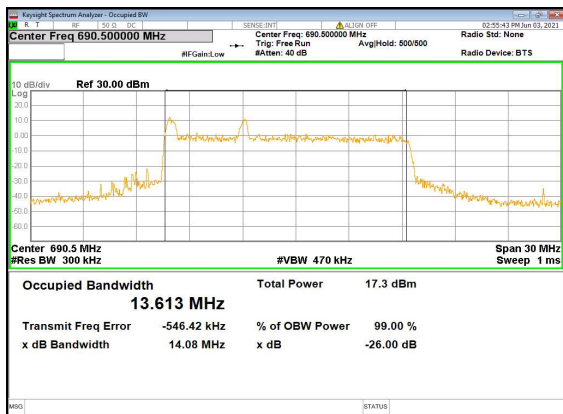
N71(15M)\_DFT-s-OFDM\_PI\_2-BPSK\_Outer\_Full\_High\_CH



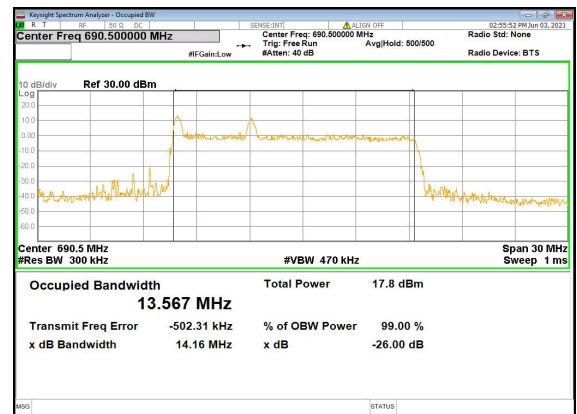
N71(15M)\_DFT-s-OFDM\_QPSK\_Outer\_Full\_High\_CH



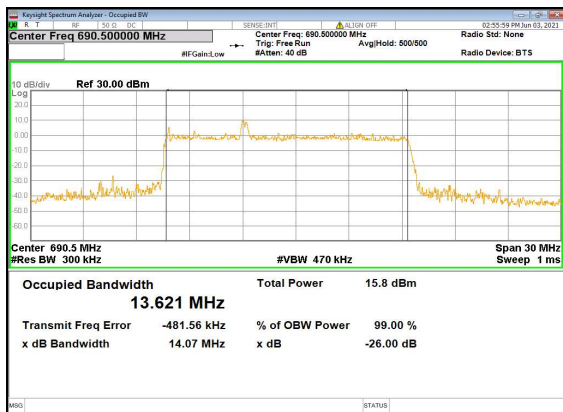
N71(15M)\_DFT-s-OFDM\_16\_QAM\_Outer\_Full\_High\_CH



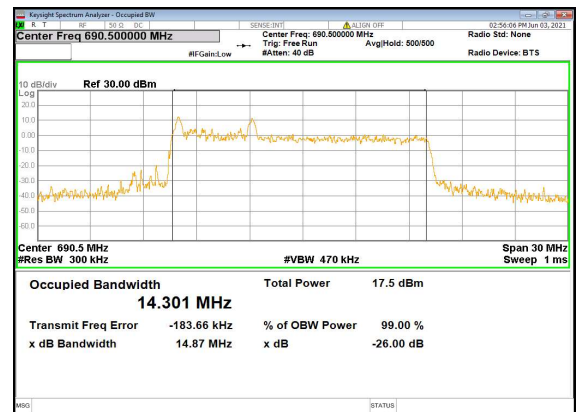
N71(15M)\_DFT-s-OFDM\_64\_QAM\_Outer\_Full\_High\_CH



N71(15M)\_DFT-s-OFDM\_256\_QAM\_Outer\_Full\_High\_CH

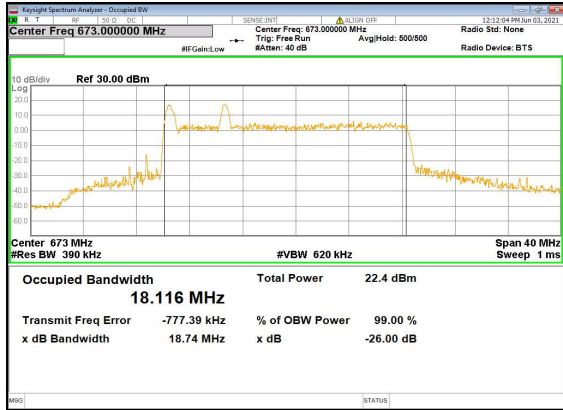


N71(15M)\_CP-OFDM\_QPSK\_Outer\_Full\_High\_CH

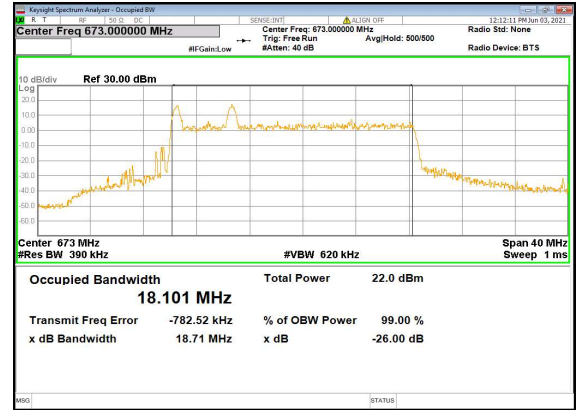




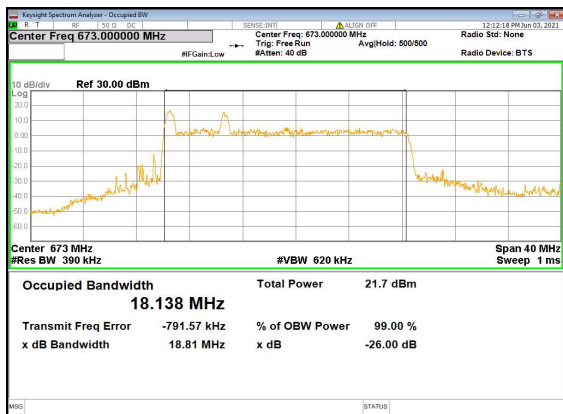
N71(20M)\_DFT-s-OFDM\_PI\_2-BPSK\_Outer\_Full  
\_Low\_CH



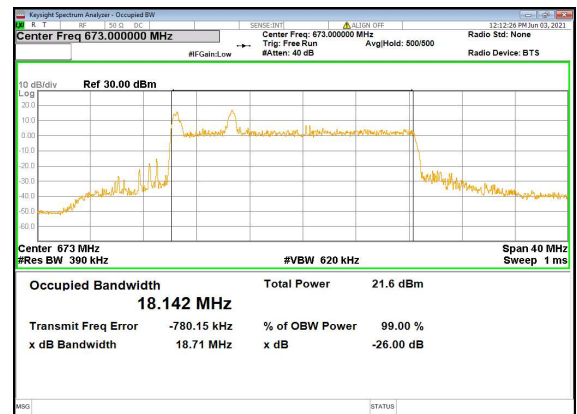
N71(20M)\_DFT-s-OFDM\_QPSK\_Outer\_Full  
Low\_CH



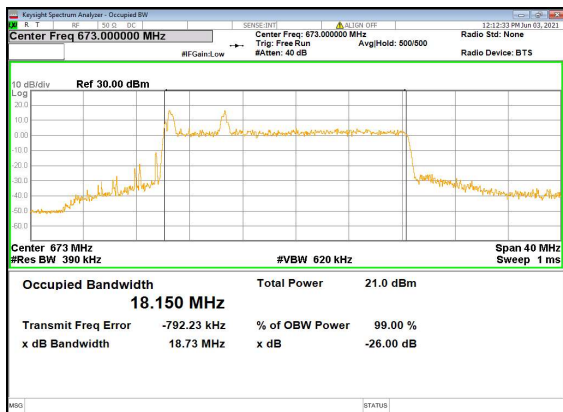
N71(20M)\_DFT-s-OFDM\_16  
QAM\_Outer\_Full\_Low\_CH



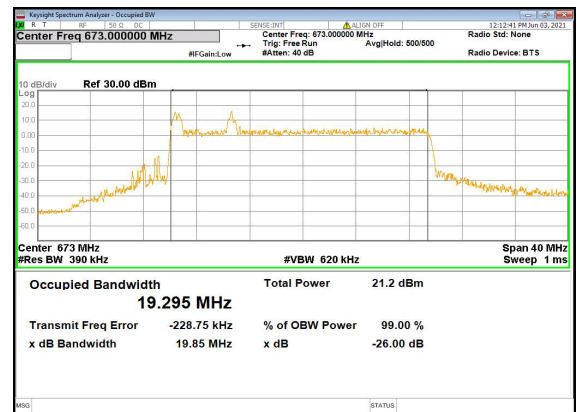
N71(20M)\_DFT-s-OFDM\_64  
QAM\_Outer\_Full\_Low\_CH



N71(20M)\_DFT-s-OFDM\_256  
QAM\_Outer\_Full\_Low\_CH

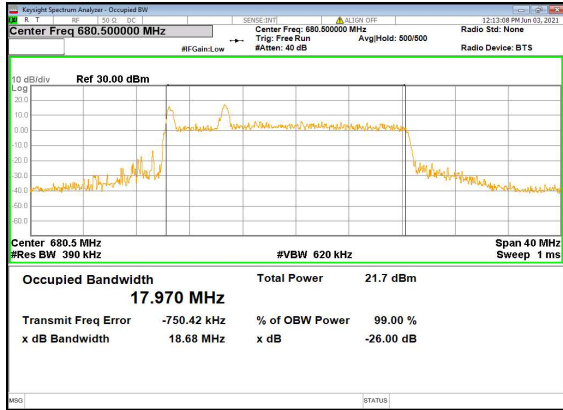


N71(20M)\_CP-OFDM\_QPSK\_Outer\_Full\_Lo  
w\_CH

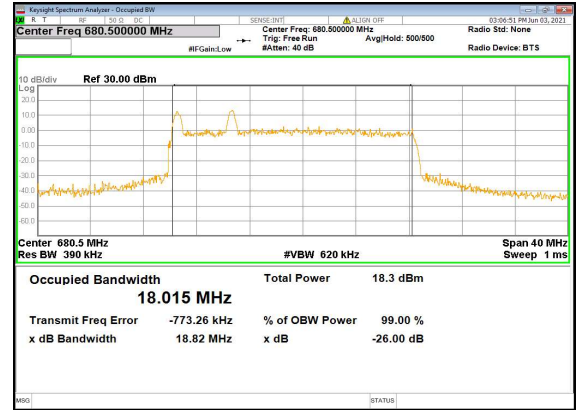




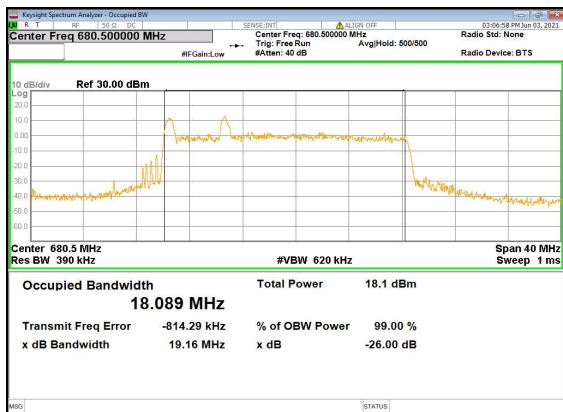
N71(20M)\_DFT-s-OFDM\_PI\_2-BPSK\_Outer\_Full  
\_Mid\_CH



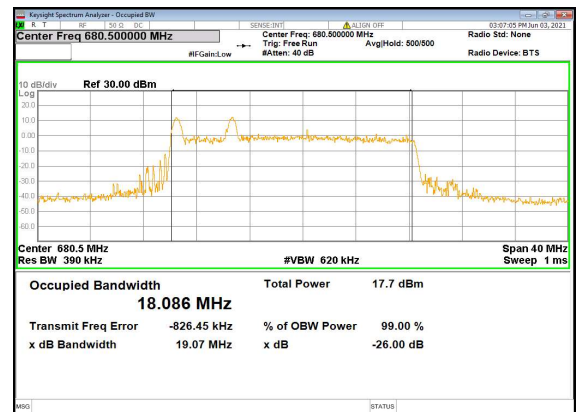
N71(20M)\_DFT-s-OFDM\_QPSK\_Outer\_Full  
\_Mid\_CH



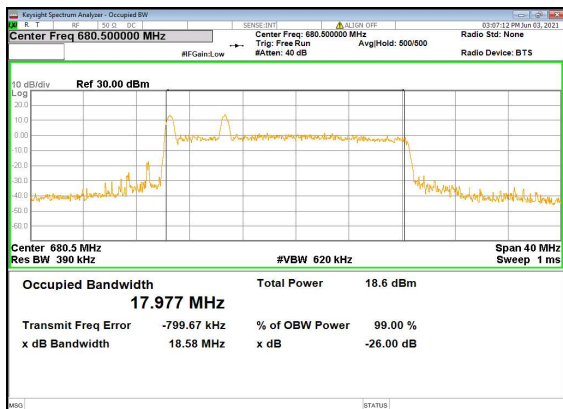
N71(20M)\_DFT-s-OFDM\_16  
QAM\_Outer\_Full\_Mid\_CH



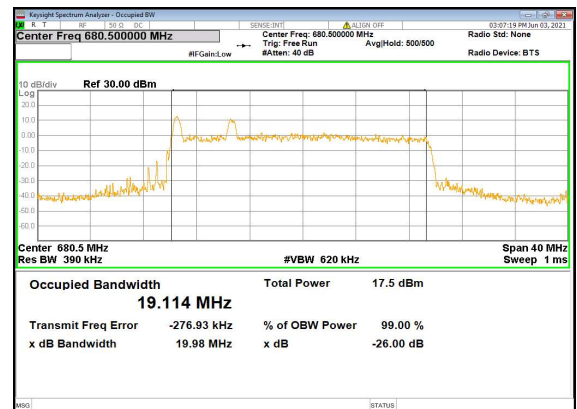
N71(20M)\_DFT-s-OFDM\_64  
QAM\_Outer\_Full\_Mid\_CH



N71(20M)\_DFT-s-OFDM\_256  
QAM\_Outer\_Full\_Mid\_CH

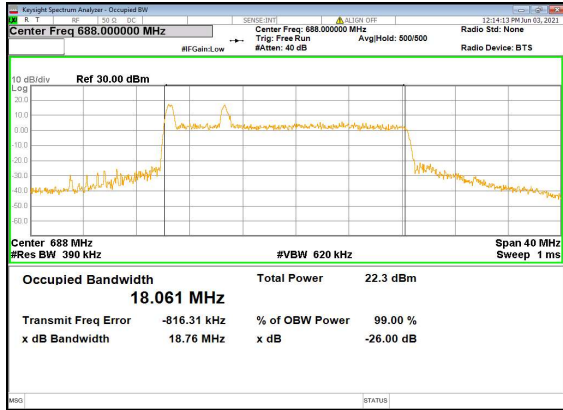


N71(20M)\_CP-OFDM\_QPSK\_Outer\_Full\_Mi  
d\_CH

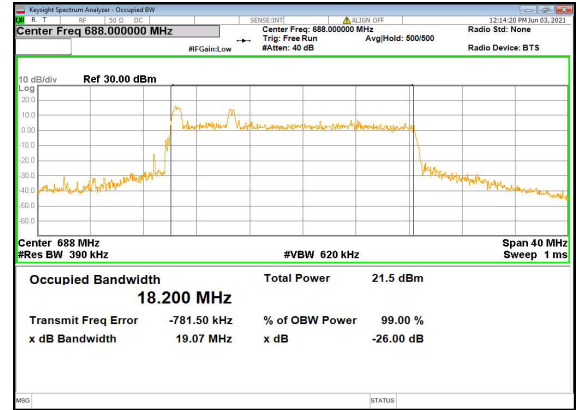




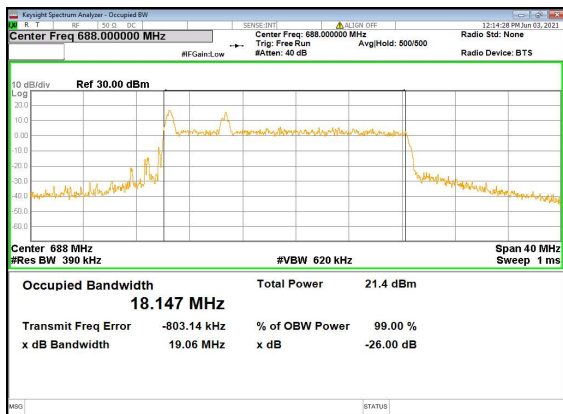
N71(20M)\_DFT-s-OFDM\_PI\_2-BPSK\_Outer\_Full\_High\_CH



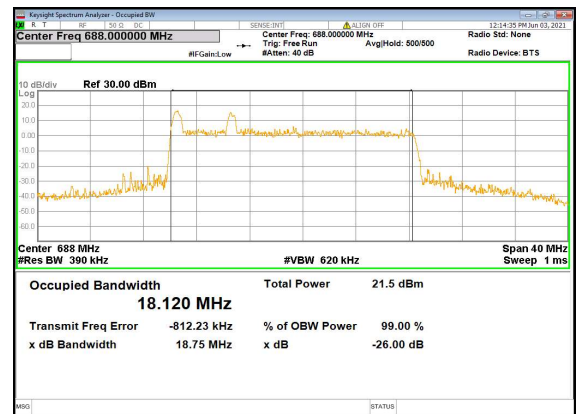
N71(20M)\_DFT-s-OFDM\_QPSK\_Outer\_Full\_High\_CH



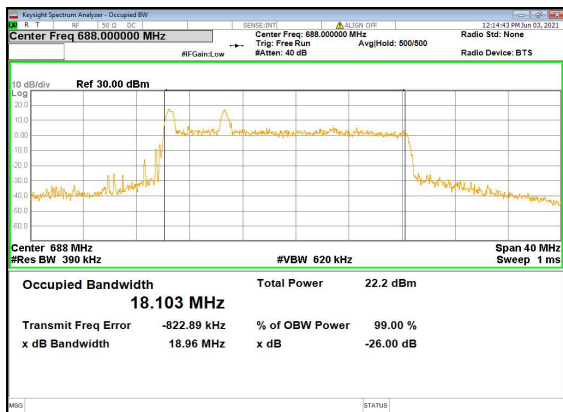
N71(20M)\_DFT-s-OFDM\_16\_QAM\_Outer\_Full\_High\_CH



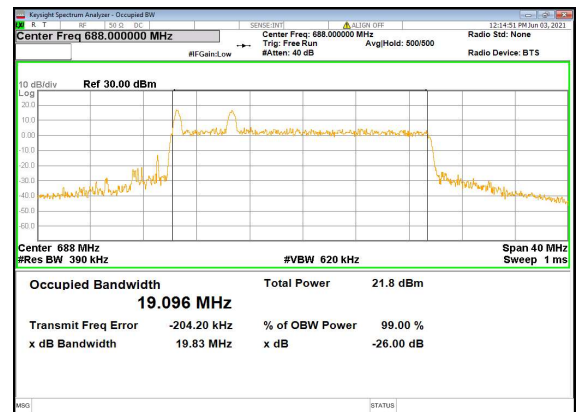
N71(20M)\_DFT-s-OFDM\_64\_QAM\_Outer\_Full\_High\_CH



N71(20M)\_DFT-s-OFDM\_256\_QAM\_Outer\_Full\_High\_CH



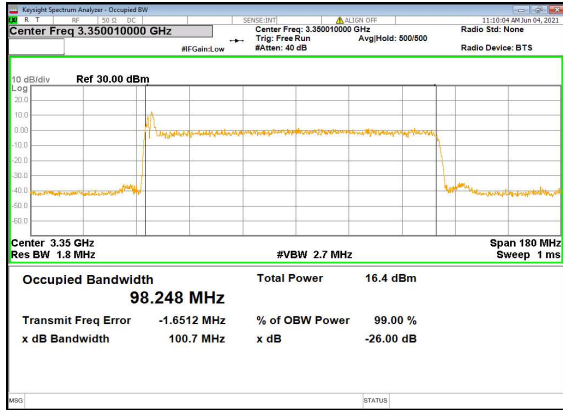
N71(20M)\_CP-OFDM\_QPSK\_Outer\_Full\_High\_CH



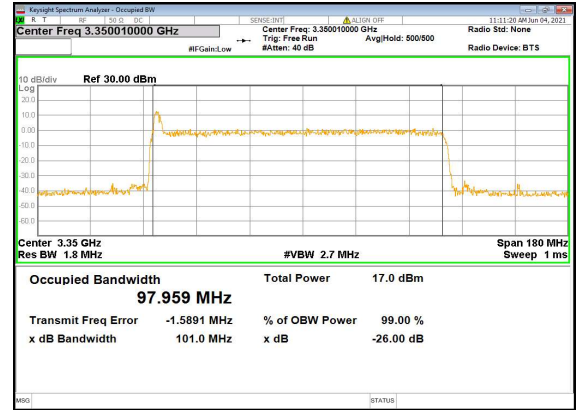




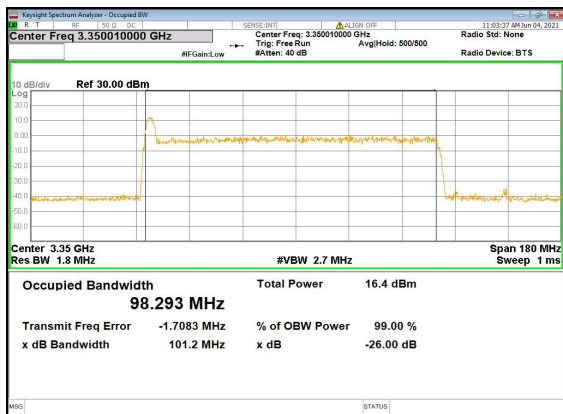
N78(100M)\_DFT-s-OFDM\_PI\_2-BPSK\_Outer\_Fu  
ll\_Mid\_CH



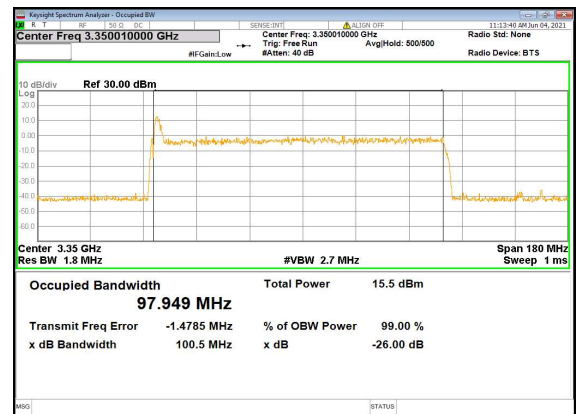
N78(100M)\_DFT-s-OFDM\_QPSK\_Outer\_Full  
\_Mid\_CH



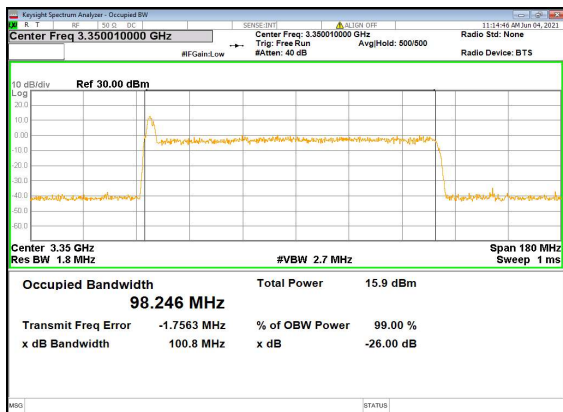
N78(100M)\_DFT-s-OFDM\_16  
QAM\_Outer\_Full\_Mid\_CH



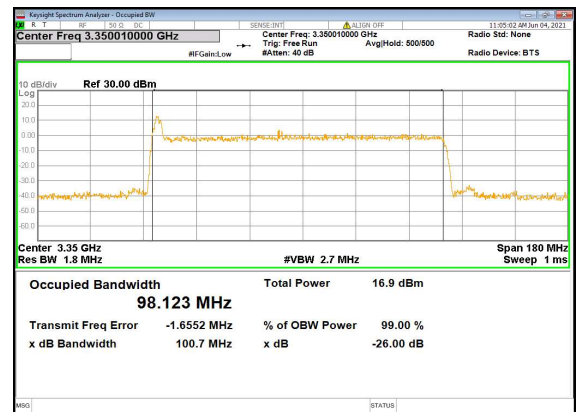
N78(100M)\_DFT-s-OFDM\_64  
QAM\_Outer\_Full\_Mid\_CH



N78(100M)\_DFT-s-OFDM\_256  
QAM\_Outer\_Full\_Mid\_CH



N78(100M)\_CP-OFDM\_QPSK\_Outer\_Full\_M  
id\_CH



## 2.3. Frequency Stability

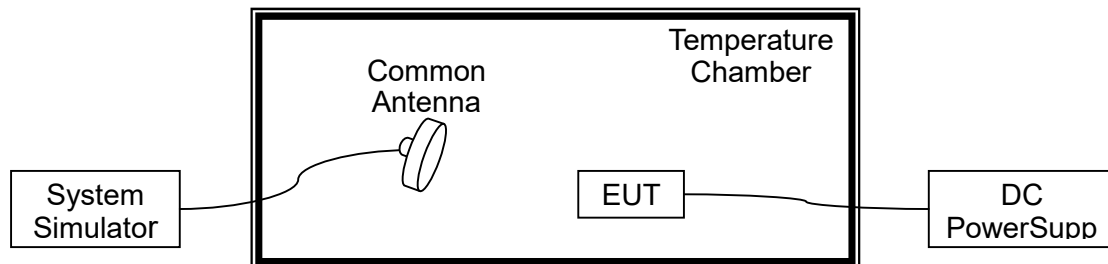
### 2.3.1. Requirement

According to FCC section 2.1055 & 27.54, the frequency stability shall be sufficient to ensure that the fundamental emission stays within the authorized frequency block. According to FCC section 2.1055, the test conditions are:

- (a) The temperature is varied from  $-30^{\circ}\text{C}$  to  $+50^{\circ}\text{C}$  at intervals of not more than  $10^{\circ}\text{C}$ .
- (b) For hand carried battery powered equipment, the primary supply voltage is reduced to the battery operating end point which shall be specified by the manufacture. The supply voltage shall be measured at the input to the cable normally provided with the equipment, or at the power supply terminals if cables are not normally provided.

**Note:** The operating temperature of EUT is from  $0^{\circ}\text{C}$  to  $45^{\circ}\text{C}$ , which are specified by the applicant.

### 2.3.2. Test Description



The EUT which is powered by the DC Power Supply directly, is located in the Temperature Chamber. The EUT is commanded by the System Simulator (SS) to operate at the maximum output power. A call is established between the EUT and the SS via a Common Antenna.

### 2.3.3. Test procedure

KDB 971168 D01v03 Section 9.0 and ANSI/TIA-603-E-2016.

### 2.3.4. Test Result

The nominal, highest and lowest extreme voltages are separately 3.8VDC, 4.2VDC and 3.6VDC, which are specified by the applicant; the normal temperature here used is  $20^{\circ}\text{C}$ .



NR n2, QPSK, Channel 376000, SCS 15kHz, Frequency 1880MHz					
Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	3.8	+20 (Ref)	32	0.017	PASS
100		0	-48	-0.026	
100		+10	52	0.028	
100		+20	27	0.014	
100		+30	-36	-0.019	
100		+40	48	0.026	
100		+50	-36	-0.019	
115	4.2	+20	-28	-0.015	
85	3.6	+20	42	0.022	

NR n41, QPSK, Channel 518598, SCS 30kHz, Frequency 2593MHz					
Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	3.8	+20 (Ref)	-29	-0.011	PASS
100		0	-39	-0.015	
100		+10	28	0.011	
100		+20	-20	-0.008	
100		+30	49	0.019	
100		+40	41	0.016	
100		+50	-36	-0.014	
115	4.2	+20	55	0.021	
85	3.6	+20	-22	-0.008	



NR n66, QPSK, Channel 349000, SCS 15kHz, Frequency 1745MHz					
Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	3.8	+20 (Ref)	-50	-0.029	PASS
100		0	-39	-0.022	
100		+10	30	0.017	
100		+20	25	0.014	
100		+30	44	0.025	
100		+40	-47	-0.027	
100		+50	24	0.014	
115	4.2	+20	-48	-0.028	
85	3.6	+20	20	0.011	

NR n71, QPSK, Channel 136100, SCS 15kHz, Frequency 680.5MHz					
Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	3.8	+20 (Ref)	-13	-0.019	PASS
100		0	17	0.025	
100		+10	-25	-0.037	
100		+20	19	0.028	
100		+30	-19	-0.028	
100		+40	19	0.028	
100		+50	14	0.021	
115	4.2	+20	15	0.022	
85	3.6	+20	-22	-0.032	



NR n78, QPSK, Channel 650000, SCS 30kHz, Frequency 3750MHz Limit =±2.5ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	3.8	+20 (Ref)	17	0.005	PASS
100		0	16	0.004	
100		+10	-14	-0.004	
100		+20	19	0.005	
100		+30	45	0.012	
100		+40	41	0.011	
100		+50	24	0.006	
115		4.2	+20	-23	
85	3.6	+20	44	0.012	