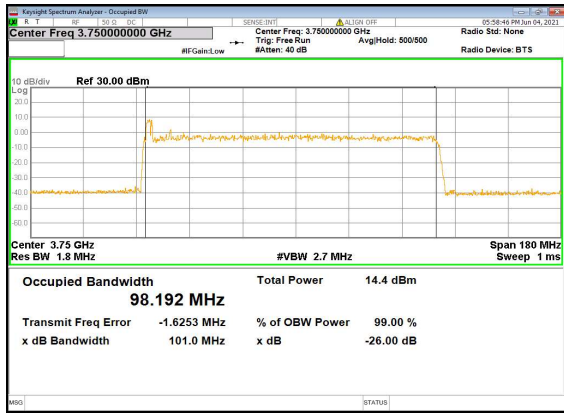
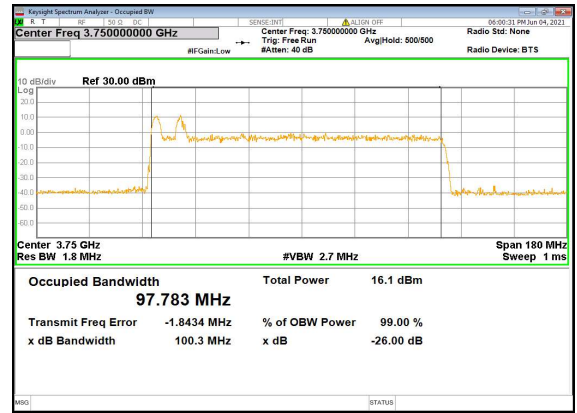




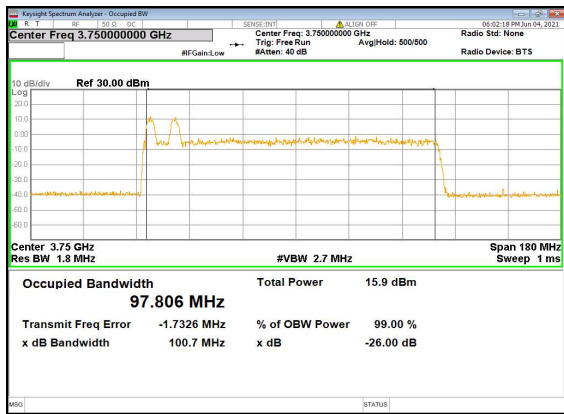
B2_N77(100M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Low_CH



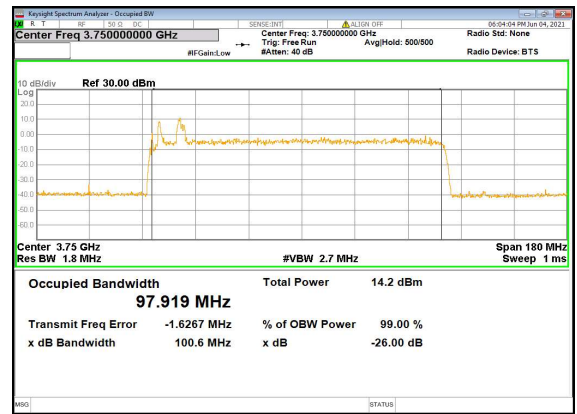
B2_N77(100M)_DFT-s-OFDM_QPSK_Outer_Full_Low_CH



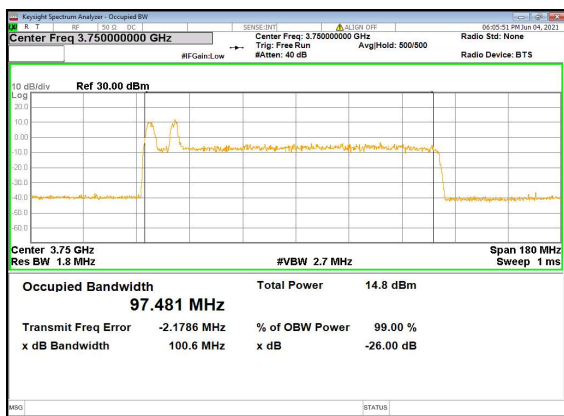
B2_N77(100M)_DFT-s-OFDM_16_QAM_Outer_Full_Low_CH



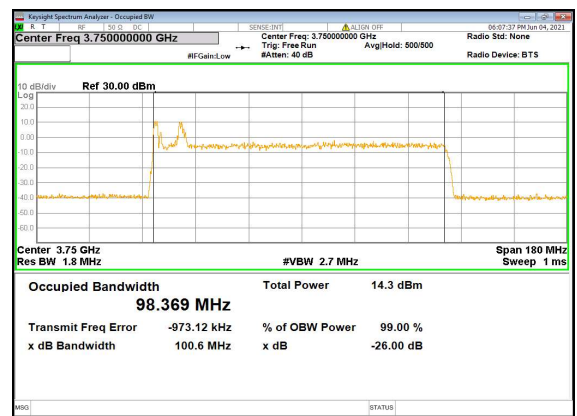
B2_N77(100M)_DFT-s-OFDM_64_QAM_Outer_Full_Low_CH



B2_N77(100M)_DFT-s-OFDM_256_QAM_Outer_Full_Low_CH

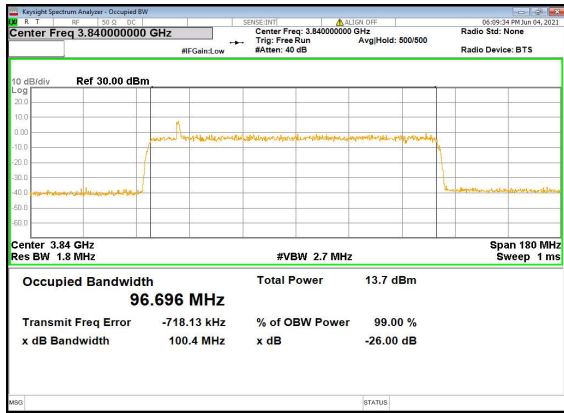


B2_N77(100M)_CP-OFDM_QPSK_Outer_Full_Low_CH

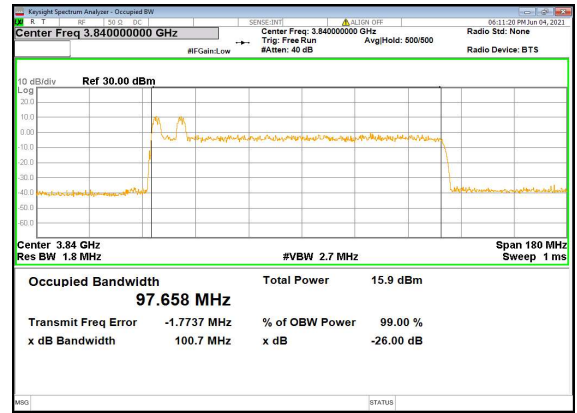




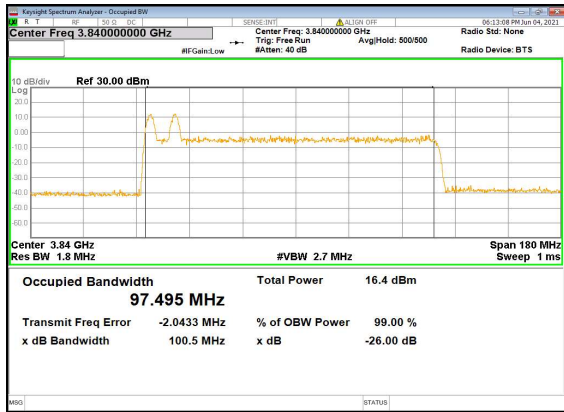
B2_N77(100M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_Mid_CH



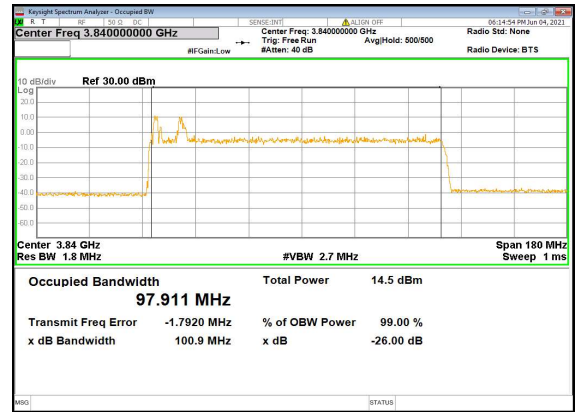
B2_N77(100M)_DFT-s-OFDM_QPSK_Outer_Full_Mid_CH



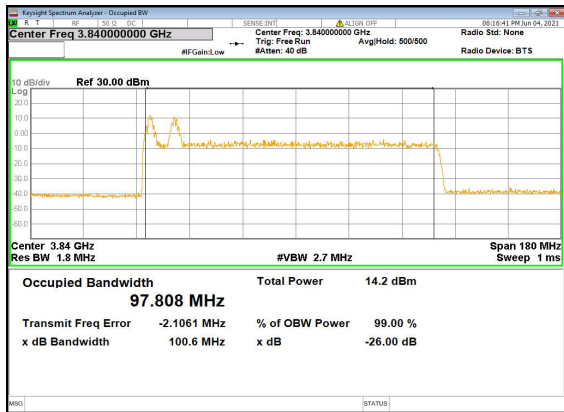
B2_N77(100M)_DFT-s-OFDM_16_QAM_Outer_Full_Mid_CH



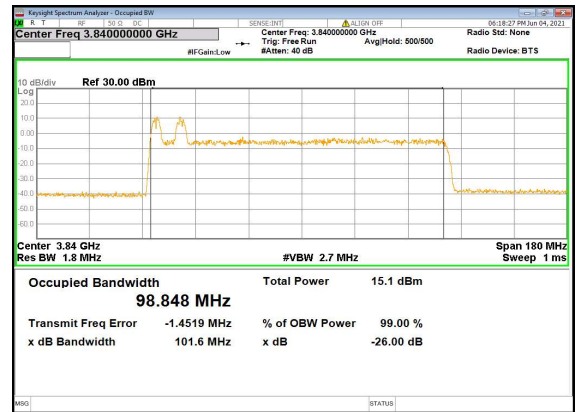
B2_N77(100M)_DFT-s-OFDM_64_QAM_Outer_Full_Mid_CH



B2_N77(100M)_DFT-s-OFDM_256_QAM_Outer_Full_Mid_CH

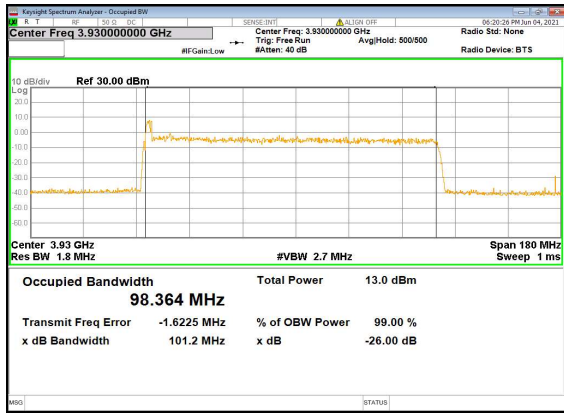


B2_N77(100M)_CP-OFDM_QPSK_Outer_Full_Mid_CH

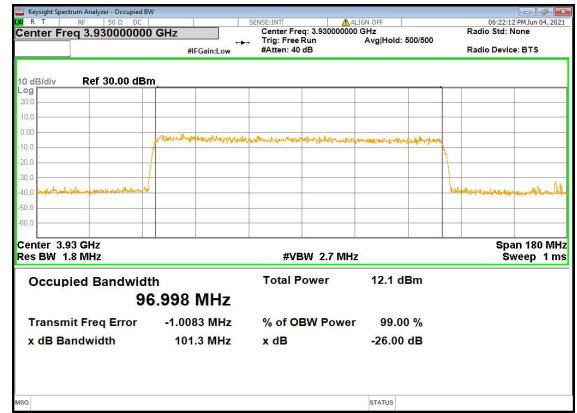




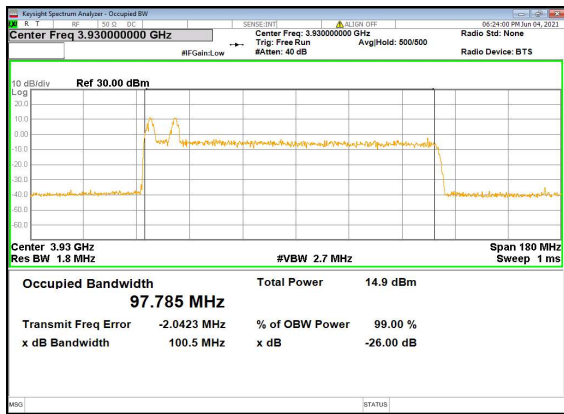
B2_N77(100M)_DFT-s-OFDM_PI_2-BPSK_Outer_Full_High_CH



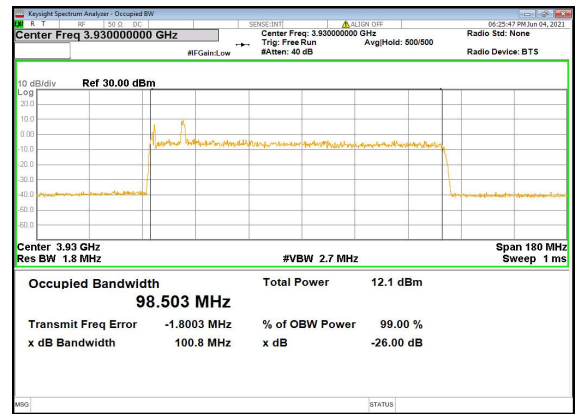
B2_N77(100M)_DFT-s-OFDM_QPSK_Outer_Full_High_CH



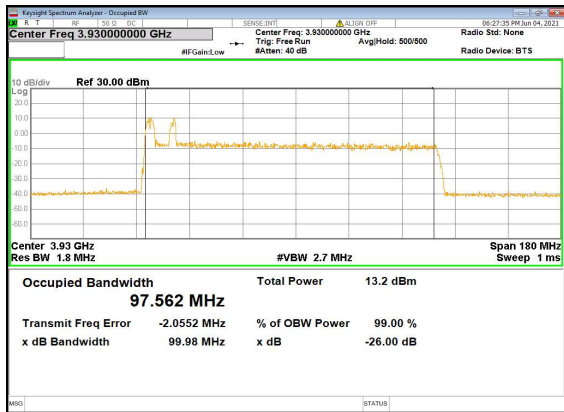
B2_N77(100M)_DFT-s-OFDM_16_QAM_Outer_Full_High_CH



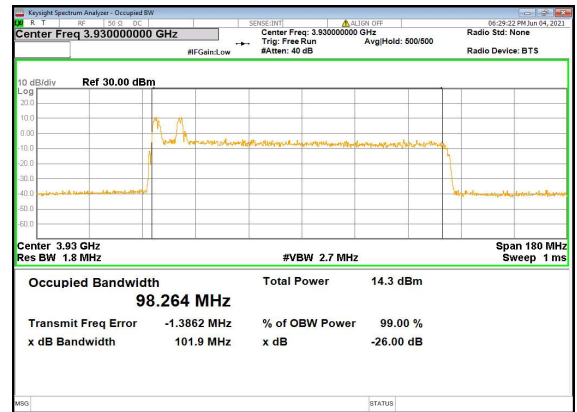
B2_N77(100M)_DFT-s-OFDM_64_QAM_Outer_Full_High_CH



B2_N77(100M)_DFT-s-OFDM_256_QAM_Outer_Full_High_CH



B2_N77(100M)_CP-OFDM_QPSK_Outer_Full_High_CH



2.3. Frequency Stability

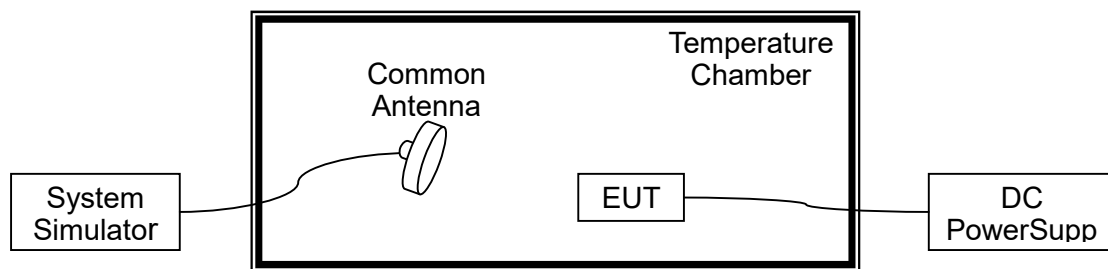
2.3.1. Requirement

According to FCC section 2.1055 & 27.54, the frequency stability shall be sufficient to ensure that the fundamental emission stays within the authorized frequency block. According to FCC section 2.1055, the test conditions are:

- (a) The temperature is varied from -30°C to $+50^{\circ}\text{C}$ at intervals of not more than 10°C .
- (b) For hand carried battery powered equipment, the primary supply voltage is reduced to the battery operating end point which shall be specified by the manufacture. The supply voltage shall be measured at the input to the cable normally provided with the equipment, or at the power supply terminals if cables are not normally provided.

Note: The operating temperature of EUT is from 0°C to 45°C , which are specified by the applicant.

2.3.2. Test Description



The EUT which is powered by the DC Power Supply directly, is located in the Temperature Chamber. The EUT is commanded by the System Simulator (SS) to operate at the maximum output power. A call is established between the EUT and the SS via a Common Antenna.

2.3.3. Test procedure

KDB 971168 D01v03 Section 9.0 and ANSI/TIA-603-E-2016.

2.3.4. Test Result

The nominal, highest and lowest extreme voltages are separately 3.8VDC, 4.2VDC and 3.6VDC, which are specified by the applicant; the normal temperature here used is 20°C .



NR n2, QPSK, Channel 376000, SCS 15kHz, Frequency 1880MHz					
Limit =±1ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	3.8	+20 (Ref)	-41	-0.022	PASS
100		0	-43	-0.023	
100		+10	21	0.011	
100		+20	-28	-0.015	
100		+30	48	0.026	
100		+40	-31	-0.016	
100		+50	34	0.018	
115	4.2	+20	-32	-0.017	
85	3.6	+20	38	0.020	

NR n5, QPSK, Channel 167300, SCS 15kHz, Frequency 836.5MHz					
Limit =±1ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	3.8	+20 (Ref)	45	0.054	PASS
100		0	-44	-0.053	
100		+10	-32	-0.038	
100		+20	46	0.055	
100		+30	16	0.019	
100		+40	-39	-0.047	
100		+50	15	0.018	
115	4.2	+20	-28	-0.033	
85	3.6	+20	40	0.048	



NR n66, QPSK, Channel 349000, SCS 15kHz, Frequency 1745MHz					
Limit = \pm 1ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	3.8	+20 (Ref)	36	0.021	PASS
100		0	-50	-0.029	
100		+10	22	0.013	
100		+20	44	0.025	
100		+30	42	0.024	
100		+40	16	0.009	
100		+50	-13	-0.007	
115	4.2	+20	22	0.013	
85	3.6	+20	31	0.018	

NR n77, QPSK, Channel 650000, SCS 30kHz, Frequency 3750MHz					
Limit = \pm 1ppm					
Voltage (%)	Power (VDC)	Temp (°C)	Fre. Dev. (Hz)	Deviation (ppm)	Result
100	3.8	+20 (Ref)	-39	-0.010	PASS
100		0	30	0.008	
100		+10	15	0.004	
100		+20	27	0.007	
100		+30	33	0.009	
100		+40	-24	-0.006	
100		+50	42	0.011	
115	4.2	+20	-18	-0.005	
85	3.6	+20	-50	-0.013	

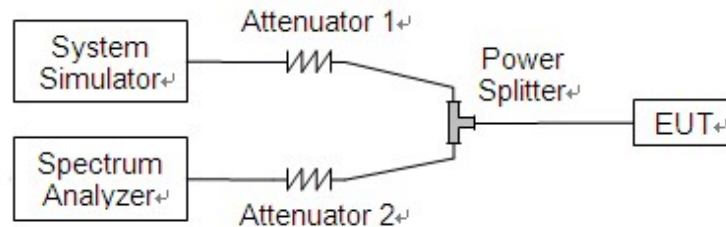
2.4. Peak to Average Ratio

2.4.1. Requirement

According to FCC section 24.232(d), the peak to average ratio (PAR) of the transmission may not exceed 13dB.

2.4.2. Test Description

Test Set:



The EUT is coupled to the Spectrum Analyzer (SA) and the System Simulator (SS) with Attenuators through the Power Splitter; the RF load attached to the EUT antenna terminal is 50 Ohm; the path loss as the factor is calibrated to correct the reading. The EUT is commanded by the SS to operate at the maximum output power. A call is established between the EUT and the SS.

2.4.3. Test procedure

KDB 971168 D01v03 Section 5.7 and ANSI/TIA-603-E-2016.

2.4.4. Test Result

Record the maximum PAPR level associated with a probability of 0.1%.



66A_N2					
BW(MHz)	Channel Level	Modulation	Peak to Average Radio(dB)	Limit (dB)	Verdict
5	Low	BPSK	7.42	<=13	PASS
5	Low	QPSK	7.44	<=13	PASS
5	Mid	BPSK	7.46	<=13	PASS
5	Mid	QPSK	7.43	<=13	PASS
5	High	BPSK	7.44	<=13	PASS
5	High	QPSK	7.46	<=13	PASS
10	Low	BPSK	7.51	<=13	PASS
10	Low	QPSK	6.64	<=13	PASS
10	Mid	BPSK	7.56	<=13	PASS
10	Mid	QPSK	6.65	<=13	PASS
10	High	BPSK	7.5	<=13	PASS
10	High	QPSK	6.7	<=13	PASS
15	Low	BPSK	7.75	<=13	PASS
15	Low	QPSK	7.07	<=13	PASS
15	Mid	BPSK	7.76	<=13	PASS
15	Mid	QPSK	7.19	<=13	PASS
15	High	BPSK	7.85	<=13	PASS
15	High	QPSK	7.24	<=13	PASS
20	Low	BPSK	7.53	<=13	PASS
20	Low	QPSK	6.59	<=13	PASS
20	Mid	BPSK	7.52	<=13	PASS
20	Mid	QPSK	6.75	<=13	PASS
20	High	BPSK	7.61	<=13	PASS
20	High	QPSK	6.73	<=13	PASS



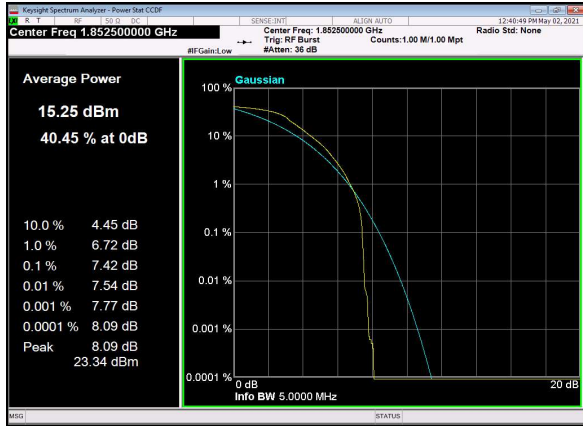
13A_N66					
BW(MHz)	Channel Level	Modulation	Peak to Average Radio(dB)	Limit (dB)	Verdict
5	Low	BPSK	7.42	<=13	PASS
5	Low	QPSK	7.41	<=13	PASS
5	Mid	BPSK	7.43	<=13	PASS
5	Mid	QPSK	7.44	<=13	PASS
5	High	BPSK	7.44	<=13	PASS
5	High	QPSK	7.45	<=13	PASS
10	Low	BPSK	7.52	<=13	PASS
10	Low	QPSK	6.66	<=13	PASS
10	Mid	BPSK	7.51	<=13	PASS
10	Mid	QPSK	6.67	<=13	PASS
10	High	BPSK	7.49	<=13	PASS
10	High	QPSK	6.78	<=13	PASS
15	Low	BPSK	7.72	<=13	PASS
15	Low	QPSK	7.17	<=13	PASS
15	Mid	BPSK	7.72	<=13	PASS
15	Mid	QPSK	7.1	<=13	PASS
15	High	BPSK	7.66	<=13	PASS
15	High	QPSK	7.13	<=13	PASS
20	Low	BPSK	7.42	<=13	PASS
20	Low	QPSK	6.72	<=13	PASS
20	Mid	BPSK	7.5	<=13	PASS
20	Mid	QPSK	6.64	<=13	PASS
20	High	BPSK	7.54	<=13	PASS
20	High	QPSK	6.61	<=13	PASS



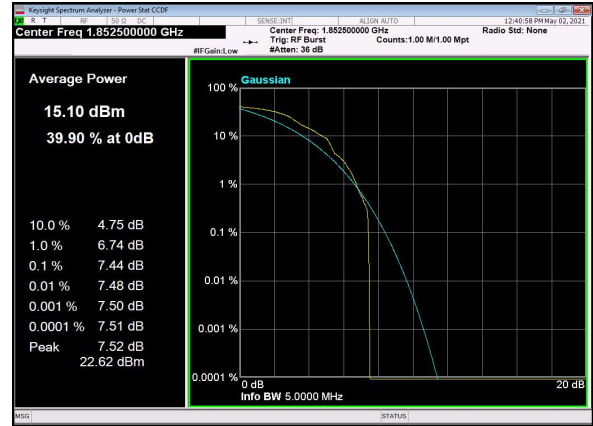
13A_N77					
BW(MHz)	Channel Level	Modulation	Peak to Average Radio(dB)	Limit (dB)	Verdict
100	Low	BPSK	9.55	<=13	PASS
100	Low	QPSK	9.70	<=13	PASS
100	Mid	BPSK	9.37	<=13	PASS
100	Mid	QPSK	3.03	<=13	PASS
100	High	BPSK	9.48	<=13	PASS
100	High	QPSK	9.61	<=13	PASS



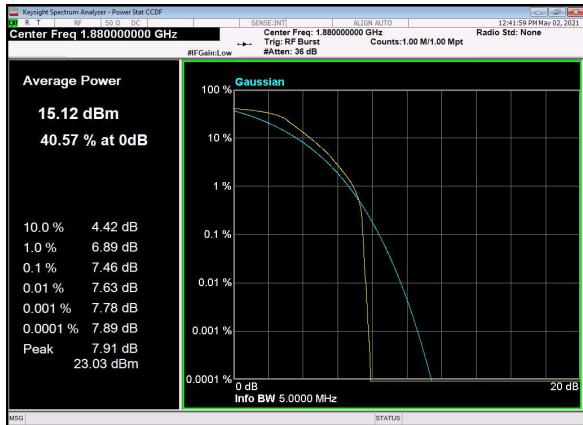
B66_N2(5M)_DFT-s-OFDM_PI_2-BPSK_Out
er_Full_Low_CH



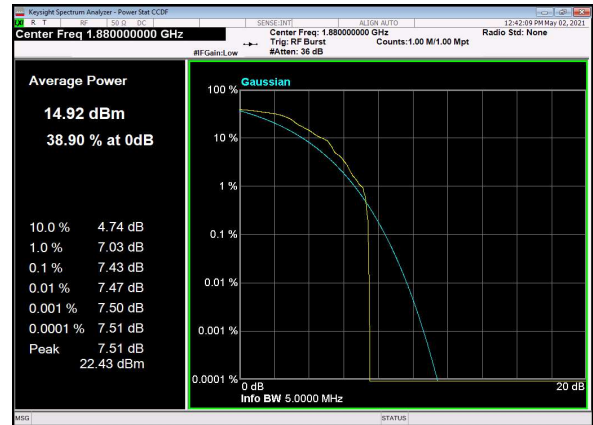
B66_N2(5M)_DFT-s-OFDM_PI_2-BPSK_Edge_1
RB_Left_Low_CH



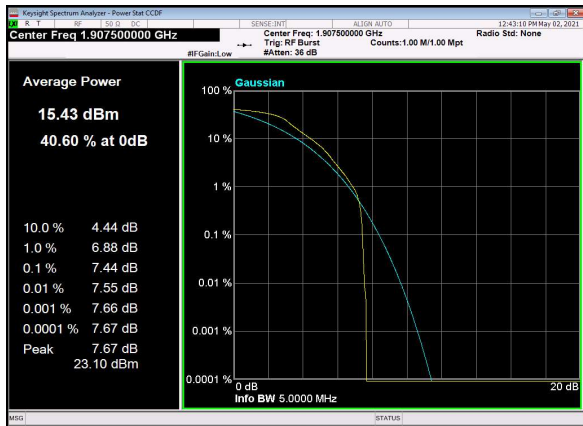
B66_N2(5M)_DFT-s-OFDM_PI_2-BPSK_Out
er_Full_Mid_CH



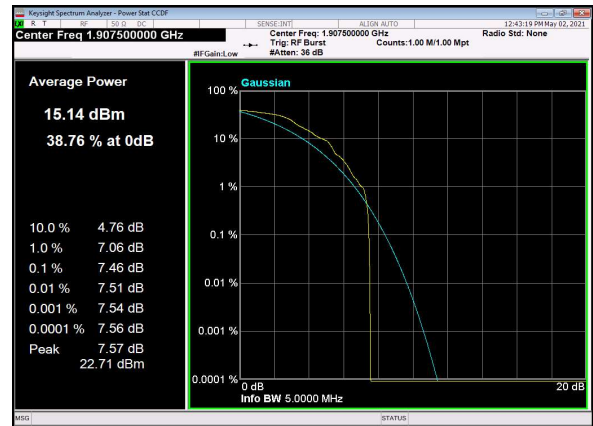
B66_N2(5M)_DFT-s-OFDM_PI_2-BPSK_Edge_1
RB_Left_Mid_CH



B66_N2(5M)_DFT-s-OFDM_PI_2-BPSK_Out
er_Full_High_CH

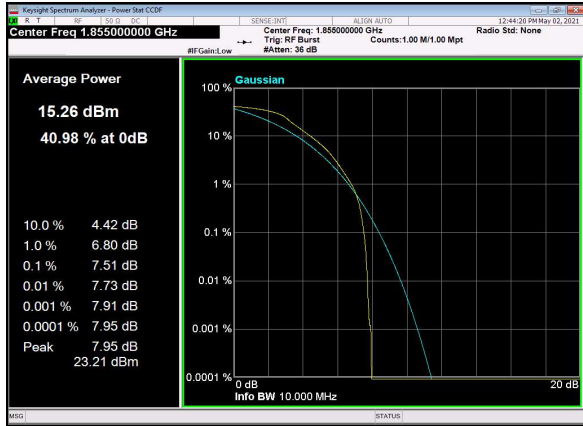


B66_N2(5M)_DFT-s-OFDM_PI_2-BPSK_Edge_1
RB_Left_High_CH

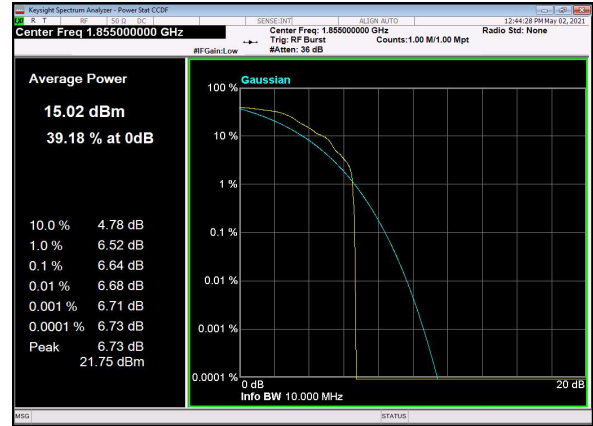




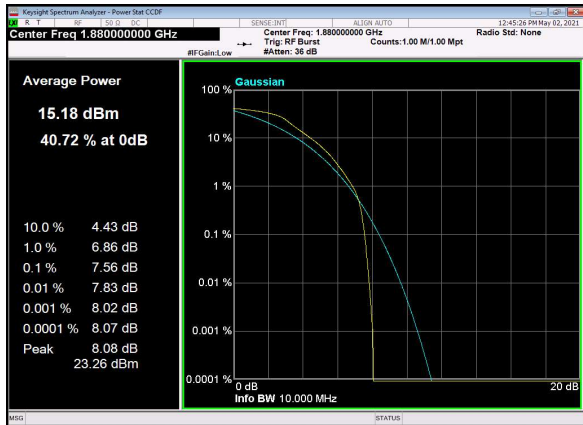
B66_N2(10M)_DFT-s-OFDM_PI_2-BPSK_Ou
ter_Full_Low_CH



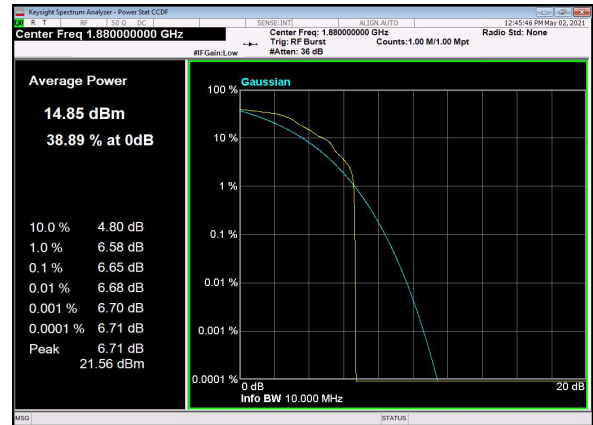
B66_N2(10M)_DFT-s-OFDM_PI_2-BPSK_Edge
1RB_Left_Low_CH



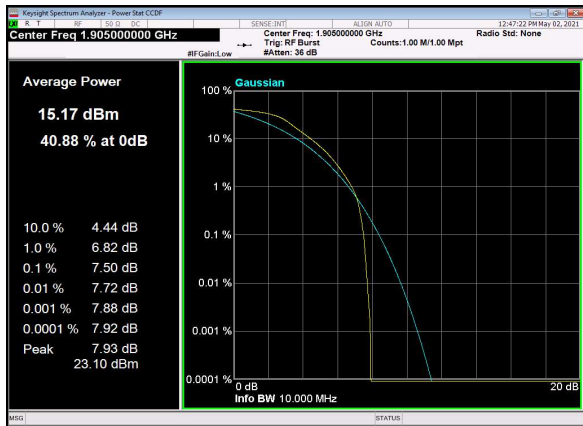
B66_N2(10M)_DFT-s-OFDM_PI_2-BPSK_Ou
ter_Full_Mid_CH



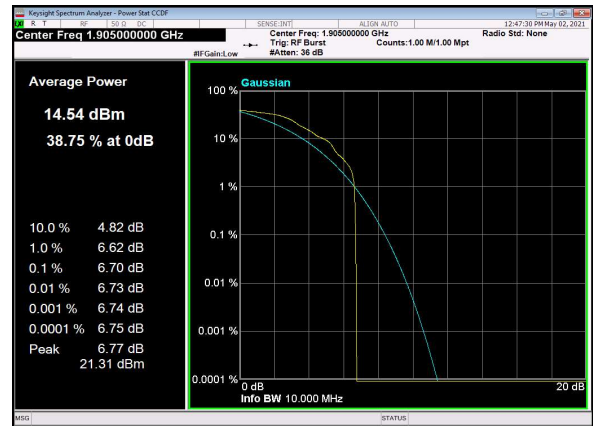
B66_N2(10M)_DFT-s-OFDM_PI_2-BPSK_Edge
1RB_Left_Mid_CH



B66_N2(10M)_DFT-s-OFDM_PI_2-BPSK_Ou
ter_Full_High_CH

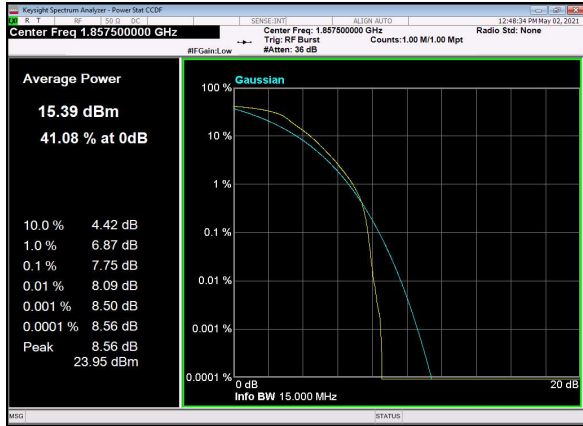


B66_N2(10M)_DFT-s-OFDM_PI_2-BPSK_Edge
1RB_Left_High_CH

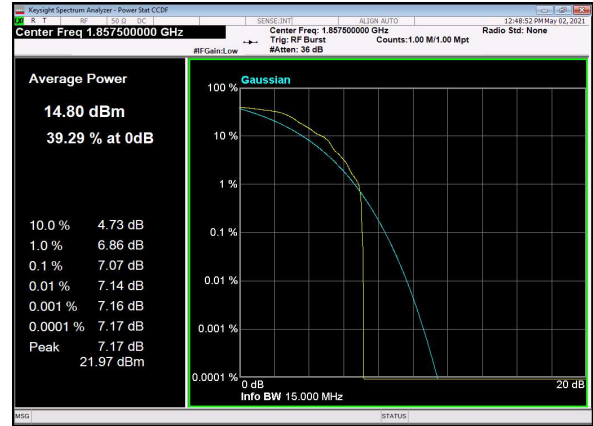




B66_N2(15M)_DFT-s-OFDM_PI_2-BPSK_Ou
ter_Full_Low_CH



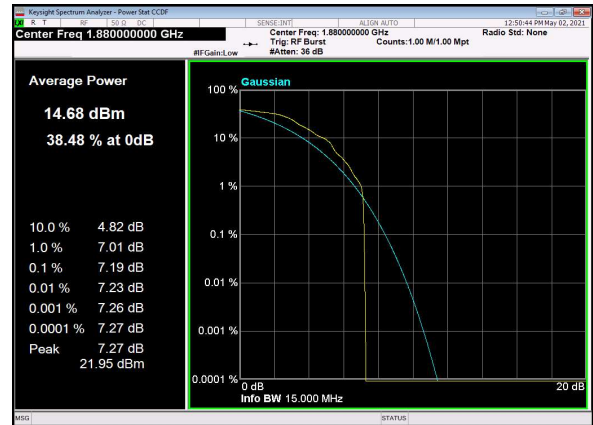
B66_N2(15M)_DFT-s-OFDM_PI_2-BPSK_Edge
1RB_Left_Low_CH



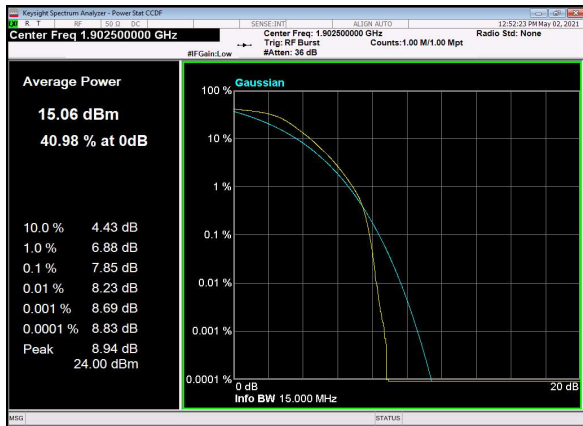
B66_N2(15M)_DFT-s-OFDM_PI_2-BPSK_Ou
ter_Full_Mid_CH



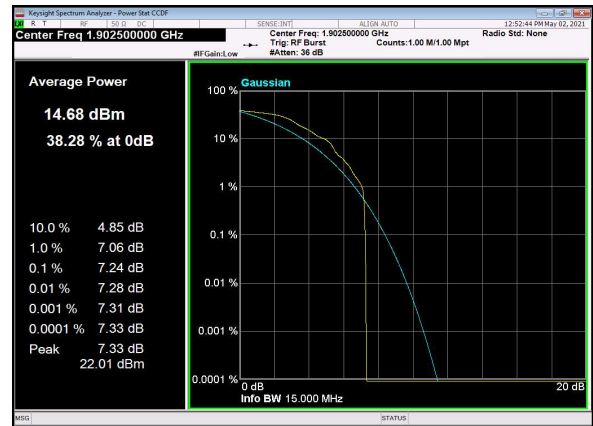
B66_N2(15M)_DFT-s-OFDM_PI_2-BPSK_Edge
1RB_Left_Mid_CH



B66_N2(15M)_DFT-s-OFDM_PI_2-BPSK_Ou
ter_Full_High_CH

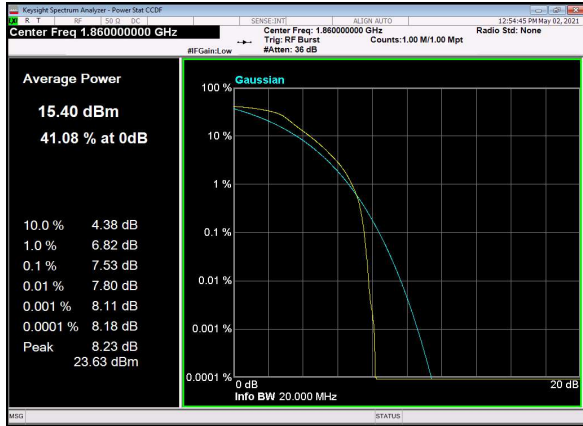


B66_N2(15M)_DFT-s-OFDM_PI_2-BPSK_Edge
1RB_Left_High_CH

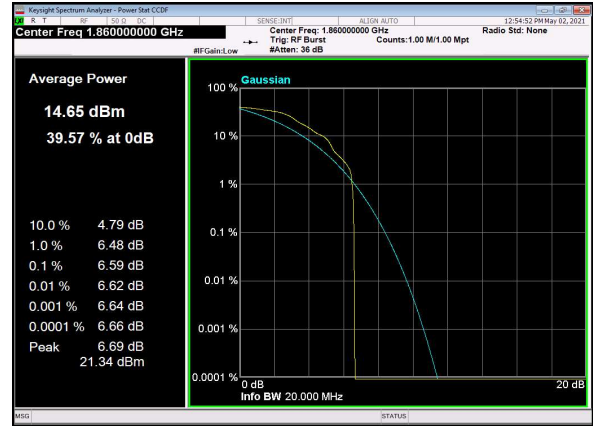




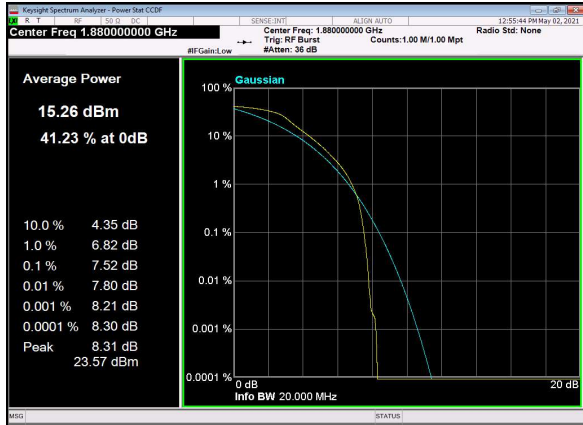
B66_N2(20M)_DFT-s-OFDM_PI_2-BPSK_Ou
ter_Full_Low_CH



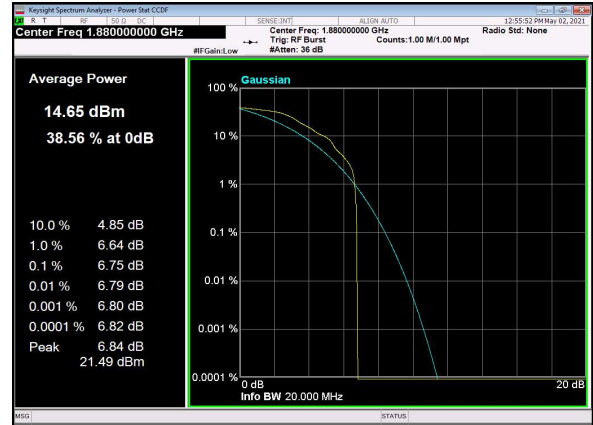
B66_N2(20M)_DFT-s-OFDM_PI_2-BPSK_Edge
1RB_Left_Low_CH



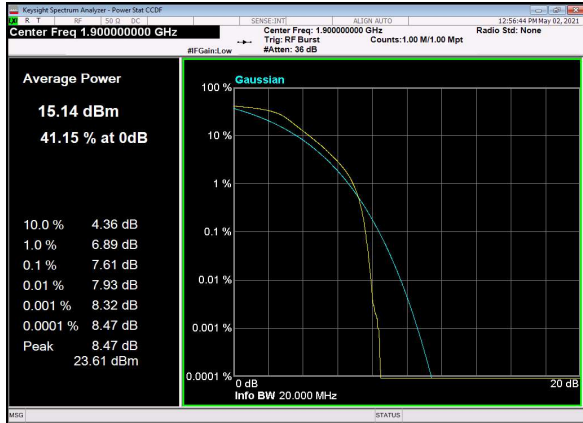
B66_N2(20M)_DFT-s-OFDM_PI_2-BPSK_Ou
ter_Full_Mid_CH



B66_N2(20M)_DFT-s-OFDM_PI_2-BPSK_Edge
1RB_Left_Mid_CH



B66_N2(20M)_DFT-s-OFDM_PI_2-BPSK_Ou
ter_Full_High_CH



B66_N2(20M)_DFT-s-OFDM_PI_2-BPSK_Edge
1RB_Left_High_CH

