

3. 6 dB Bandwidth & 99 % Bandwidth

3.1. Test Setup



3.2. Limit

3.2.1. FCC

According to §15.247(a)(2), systems using digital modulation techniques may operate in the 902-928 MHz, 2 400-2 483.5 MHz, and 5 725-5 850 MHz bands. The minimum 6 dB bandwidth shall be at least 500 kHz.

3.2.2. IC

According to RSS-247 Issue 2, 5.2(a), the minimum 6 dB bandwidth shall be 500 kHz.

3.3. Test Procedure

All data rates and modes were investigated for this test. The full data for the worst case data rate are reported in this section.

3.3.1. 6 dB Bandwidth

The test follows section 11.8 DTS bandwidth of ANSI C63.10-2013.

Tests performed using section 11.8.1 Option 1.

- Option 1:

1. Set RBW to = 100 kHz.
2. Set the VBW \geq [3 x RBW].
3. Detector = peak.
4. Trace mode = max hold.
5. Sweep = auto couple.
6. Allow the trace to stabilize.
7. Measure the maximum width of the emission that is constrained by the frequencies associated with the two outermost amplitude points (upper and lower frequencies) that are attenuated by 6 dB relative to the maximum level measured in the fundamental emission.

3.3.2. 99 % Bandwidth

The following conditions shall be observed for measuring the occupied bandwidth and x dB bandwidth:

- The transmitter shall be operated at its maximum carrier power measured under normal test condition.
- The span of the spectrum analyzer shall be set large enough to capture all products of the modulation process, including the emission skirts, around the carrier frequency, but small enough to avoid having other emissions (e.g. on adjacent channels) within the span.
- The detector of the spectrum analyzer shall be set to "Sample". However, a peak, or peak hold, may be used in place of the sampling detector since this usually produces a wider bandwidth than the actual bandwidth (worst-case measurement). Use of a peak hold (or "Max Hold") may be necessary to determine the occupied / x dB bandwidth if the device is not transmitting continuously.
- The resolution bandwidth (RBW) shall be in the range of 1 % to 5 % of the actual occupied / x dB bandwidth and the video bandwidth (VBW) shall not be smaller than three times the RBW value. Video averaging is not permitted.

Note: It may be necessary to repeat the measurement a few times until the RBW and VBW are in compliance with the above requirement.

For the 99 % emission bandwidth, the trace data points are recovered and directly summed in linear power level terms. The recovered amplitude data points, beginning at the lowest frequency, are placed in a running sum until 0.5 % of the total is reached, and that frequency recorded. The process is repeated for the highest frequency data points (starting at the highest frequency, at the right side of the span, and going down in frequency). This frequency is then recorded. The difference between the two recorded frequencies is the occupied bandwidth (or the 99 % emission bandwidth).

3.4. Test Results

Ambient temperature : (23 ± 1) °C
 Relative humidity : 47 % R.H.

- 6 dB Bandwidth

Mode	Voltage	Channel	Frequency (MHz)	6 dB Bandwidth (MHz)		Minimum Bandwidth (kHz)
				PHY 1M	PHY 2M	
GFSK	DC 10.2 V	Low	2 402	0.767	1.201	500
		Middle	2 440	0.767	1.172	
		High	2 480	0.753	1.172	
	DC 27.6 V	Low	2 402	0.767	1.187	
		Middle	2 440	0.753	1.172	
		High	2 480	0.738	1.172	

- 99 % Bandwidth

Mode	Voltage	Channel	Frequency (MHz)	99% Bandwidth (MHz)	
				PHY 1M	PHY 2M
GFSK	DC 10.2 V	Low	2 402	1.056	2.062
		Middle	2 440	1.056	2.062
		High	2 480	1.064	2.069
	DC 27.6 V	Low	2 402	1.064	2.062
		Middle	2 440	1.056	2.062
		High	2 480	1.064	2.077

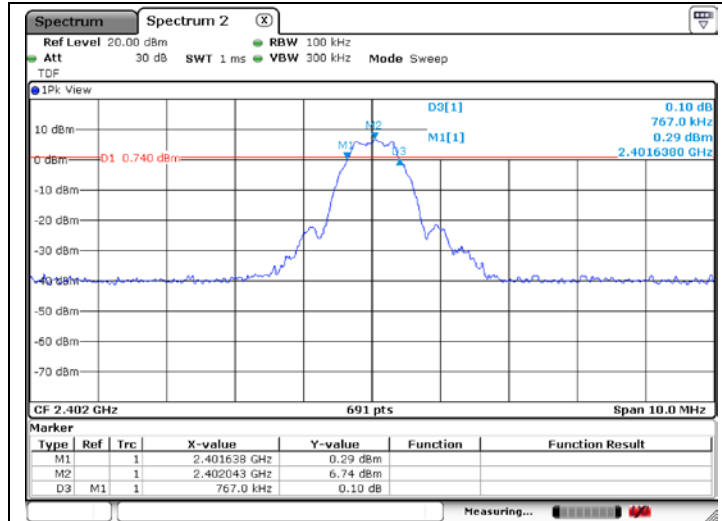
- Test plots

- 6 dB Bandwidth

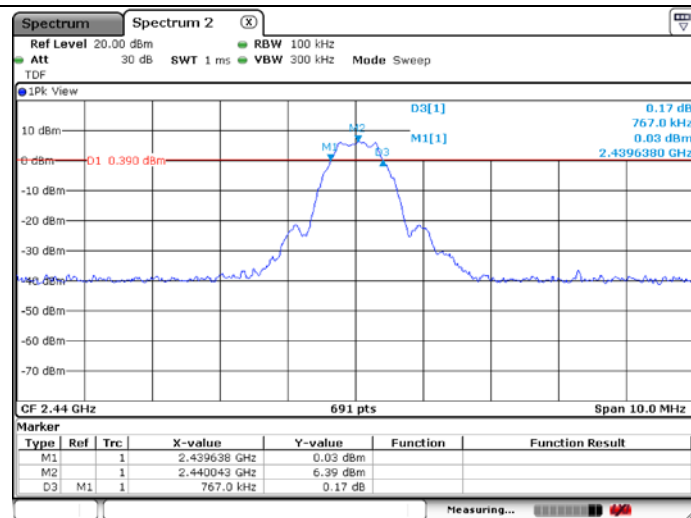
Test voltage: DC 10.2 V

PHY 1M (255 bytes)

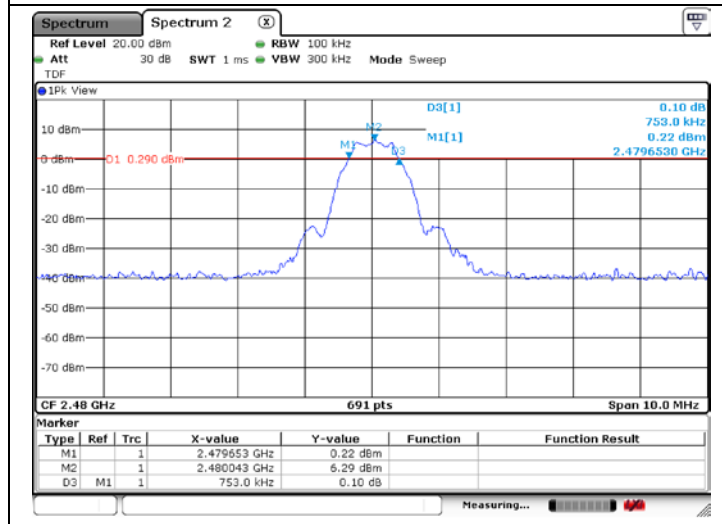
Low Channel



Middle Channel

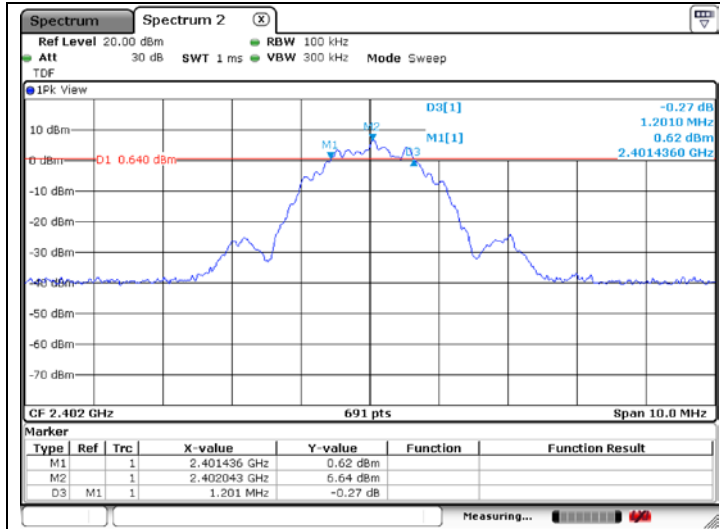


High Channel

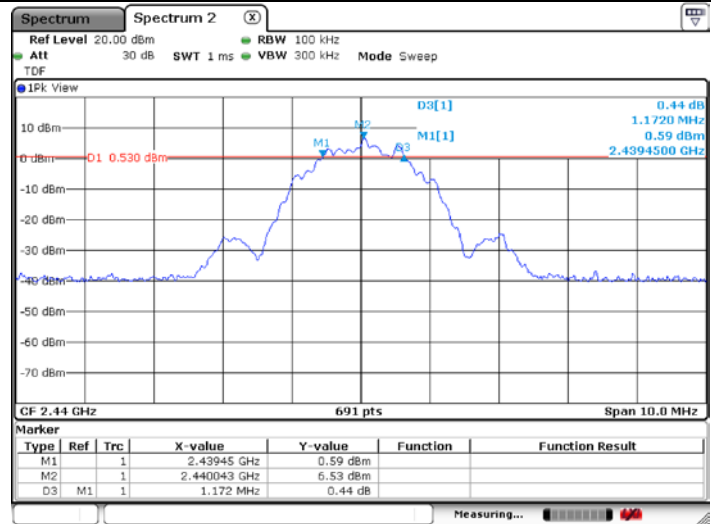


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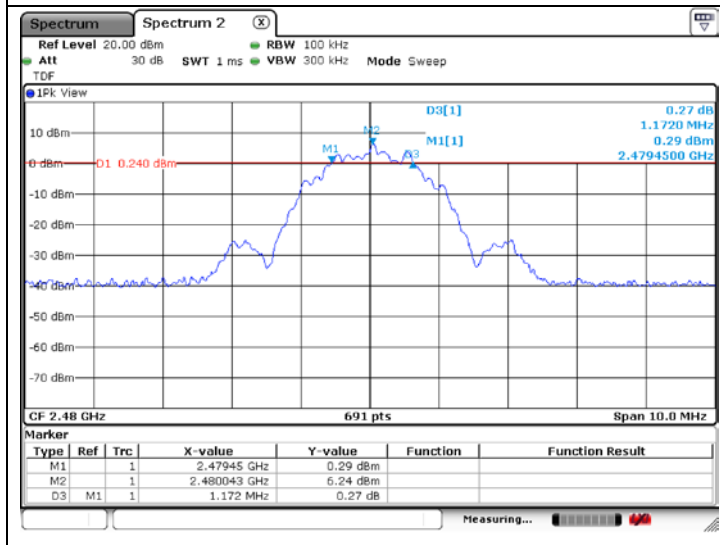
Low Channel



Middle Channel



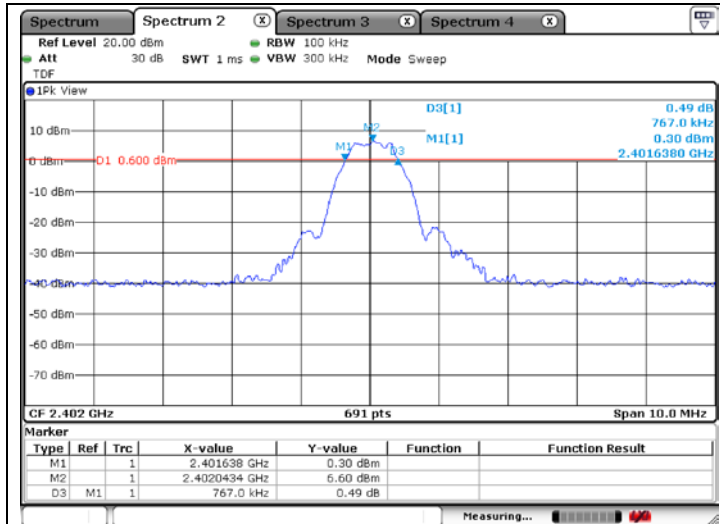
High Channel



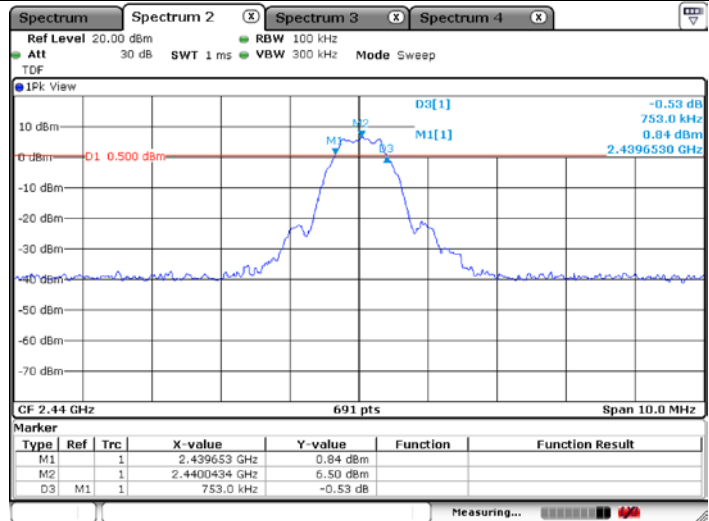
Test voltage: DC 27.6 V

PHY 1M (255 bytes)

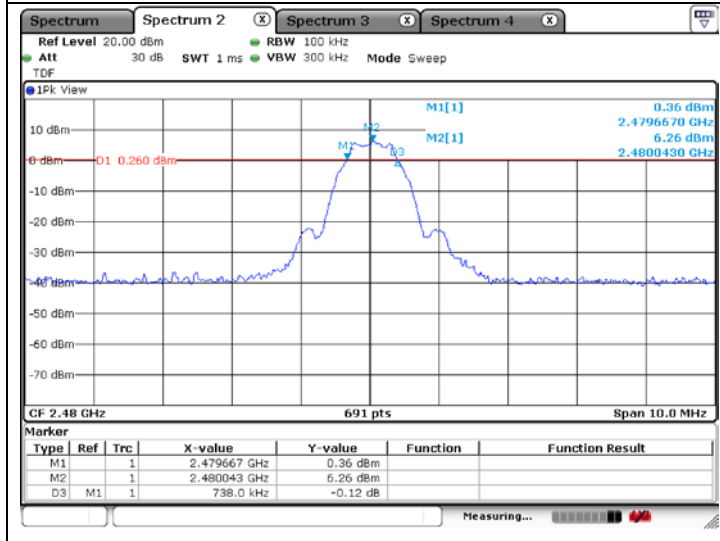
Low Channel



Middle Channel

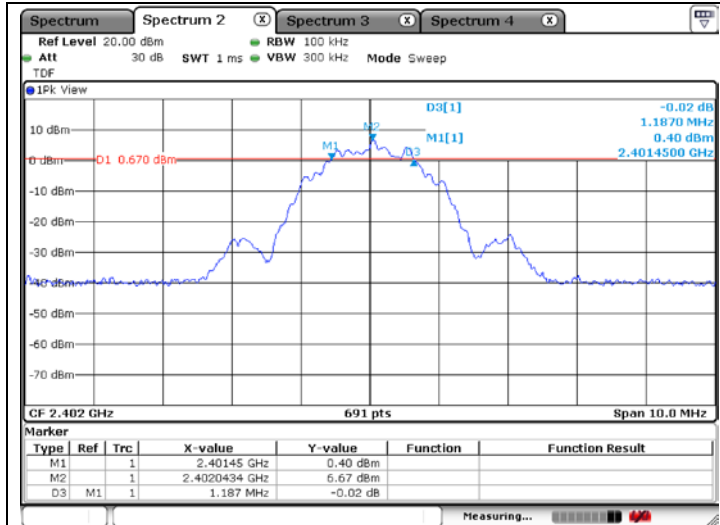


High Channel

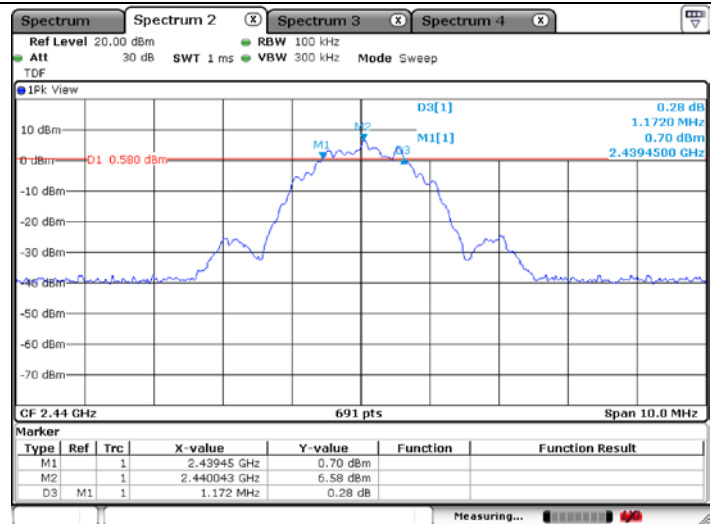


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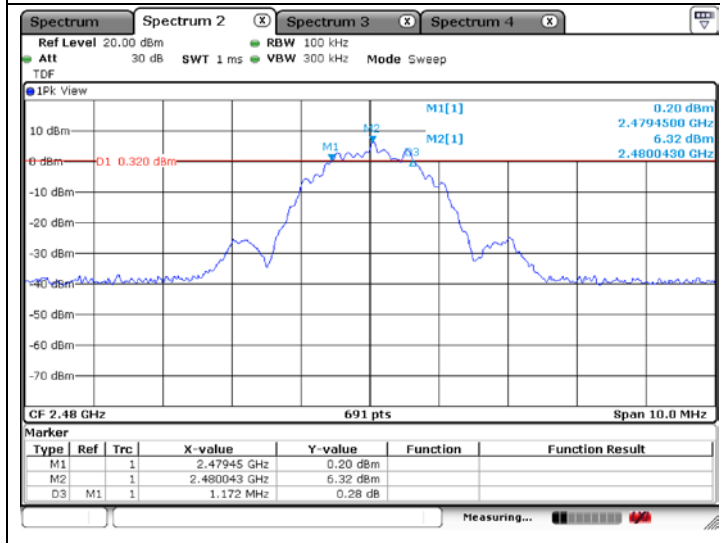
Low Channel



Middle Channel



High Channel

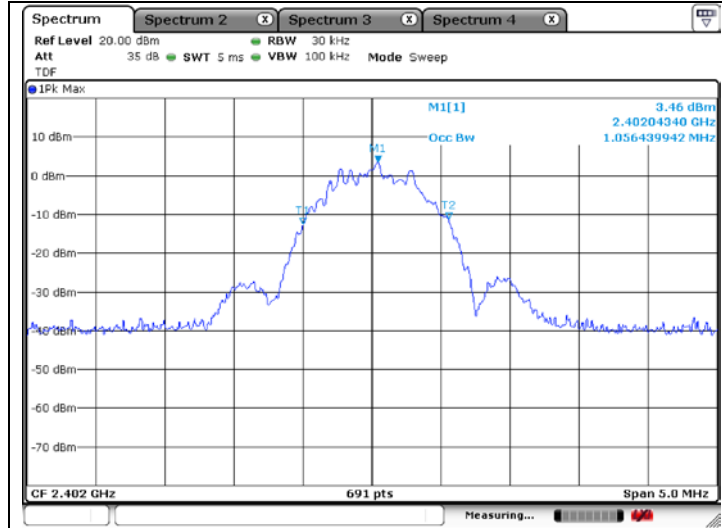


- 99 % Bandwidth

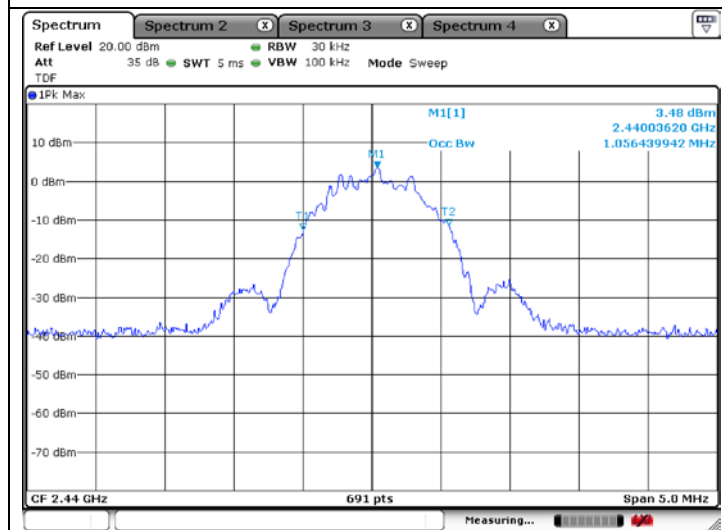
Test voltage: DC 10.2 V

PHY 1M (255 bytes)

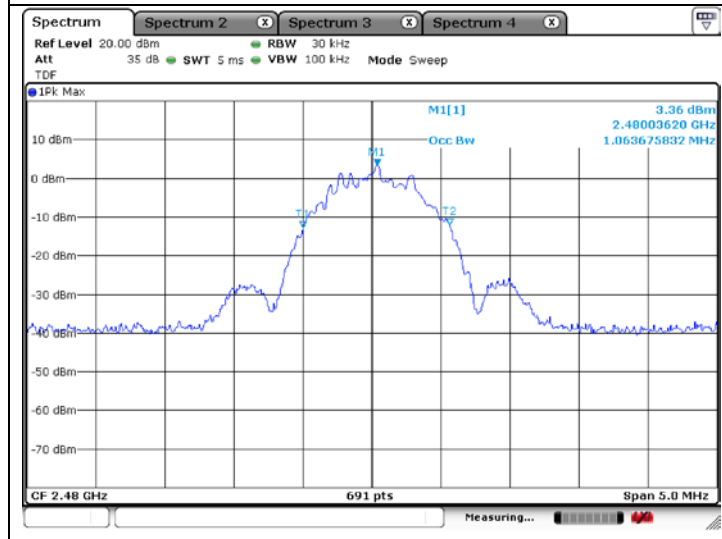
Low Channel



Middle Channel

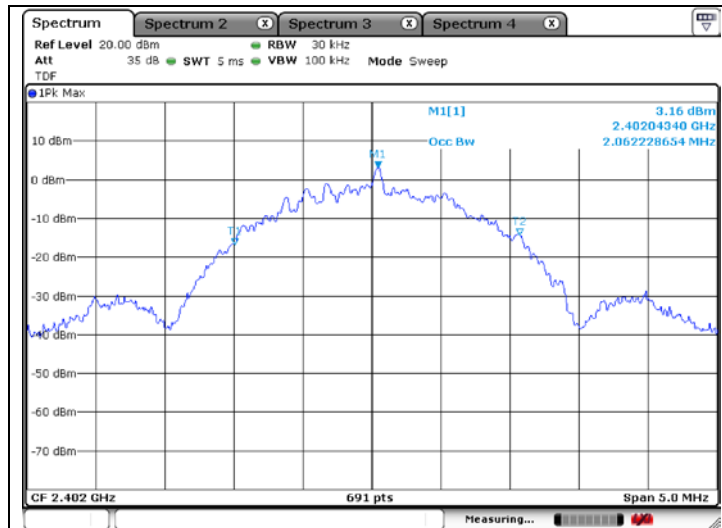


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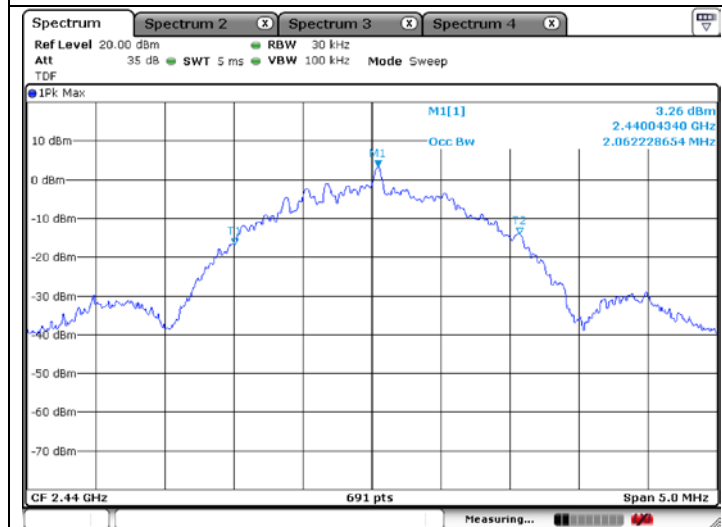


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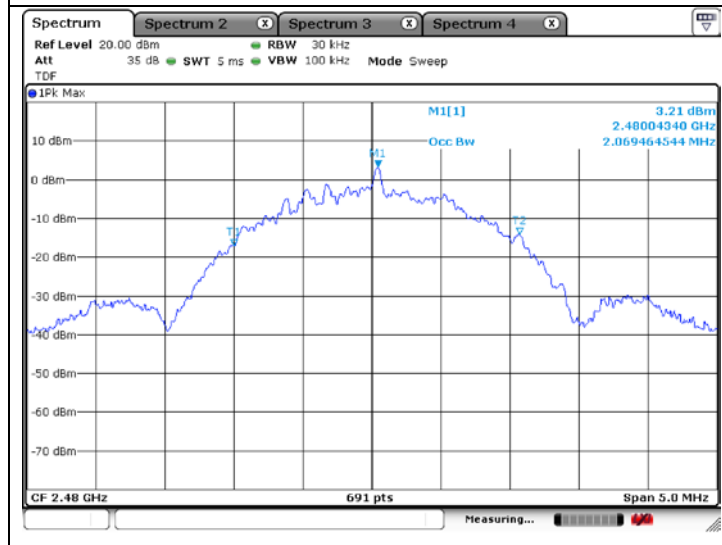
Low Channel



Middle Channel



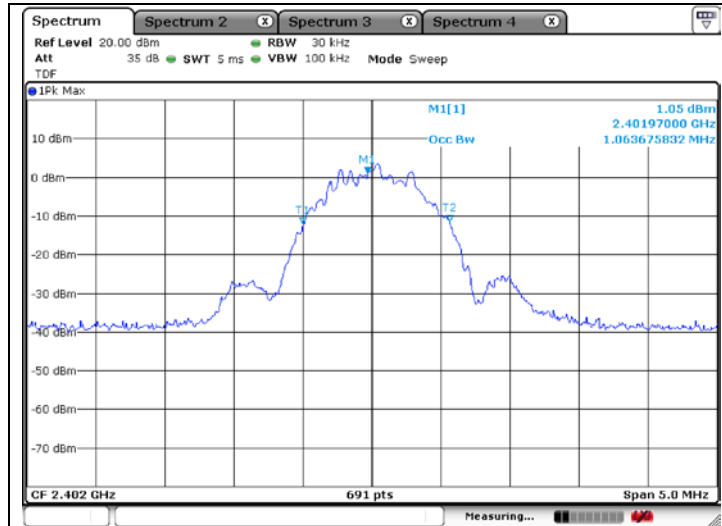
High Channel



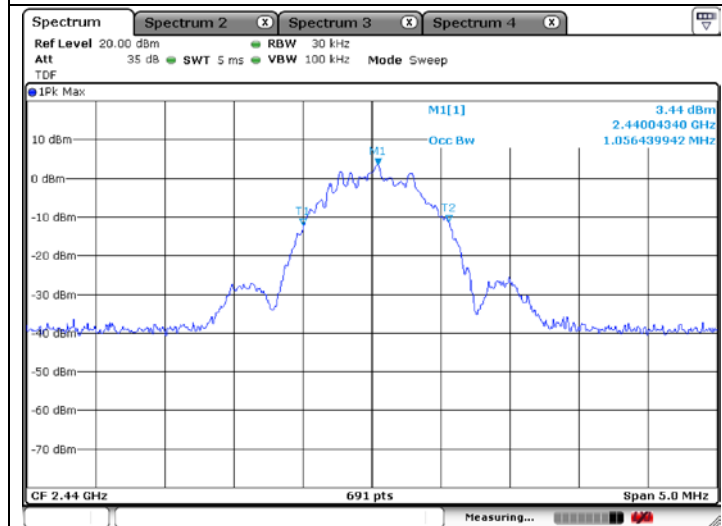
Test voltage: DC 27.6 V

PHY 1M (255 bytes)

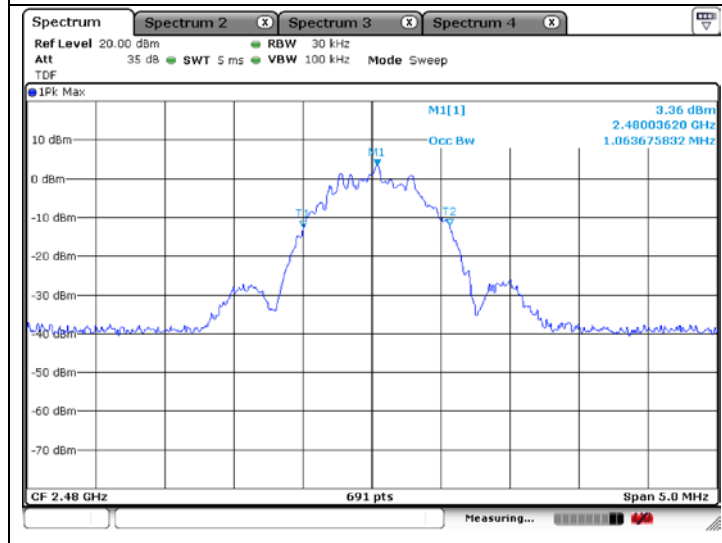
Low Channel



Middle Channel

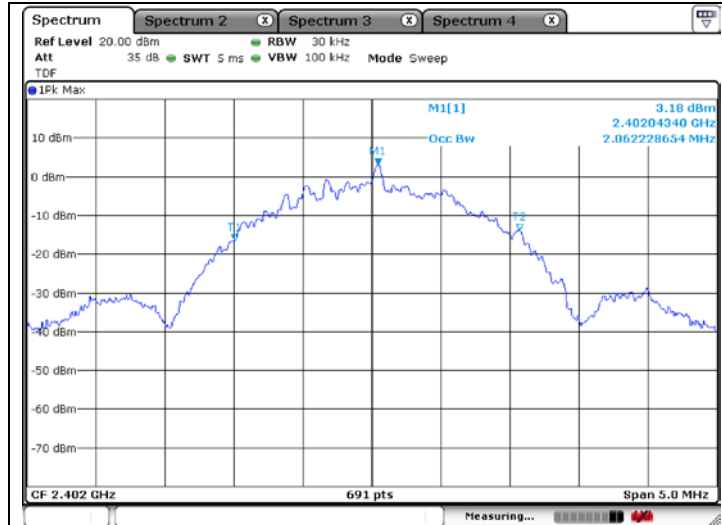


High Channel

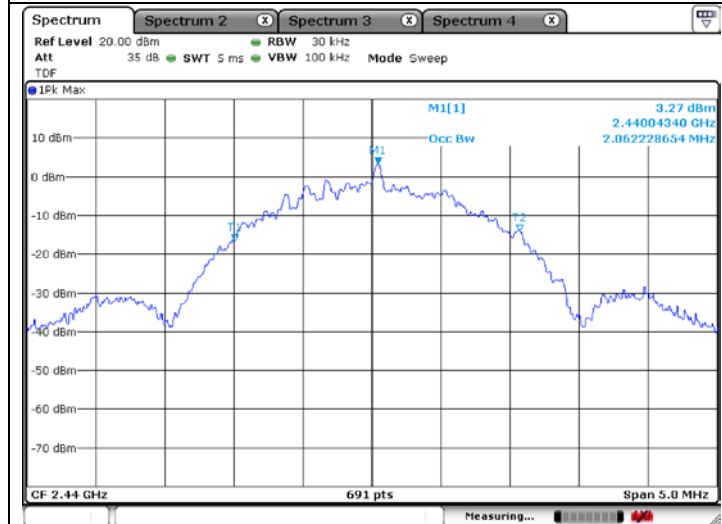


PHY 2M (255 bytes)

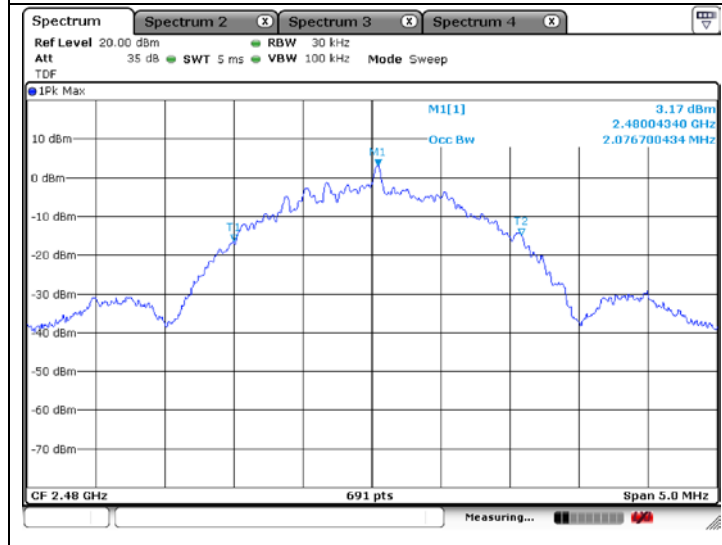
Low Channel



Middle Channel

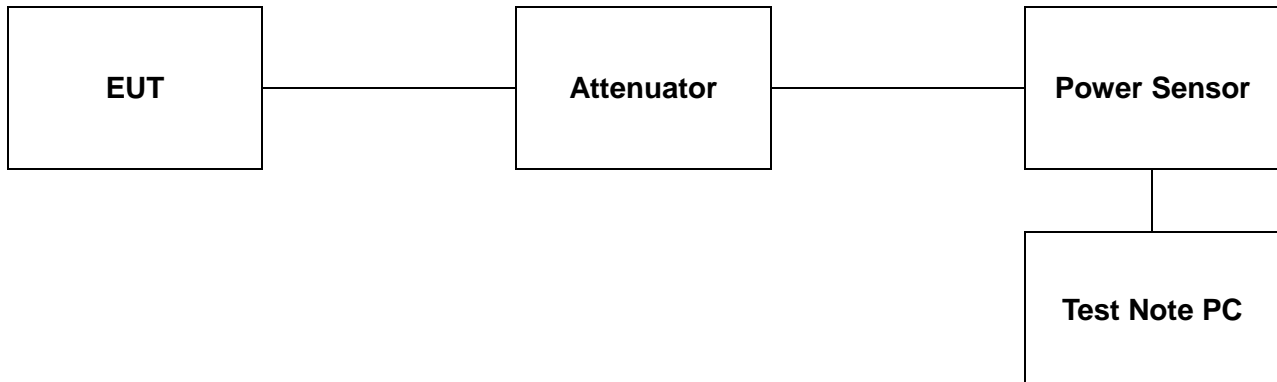


High Channel



4. Maximum Peak Conducted Output Power

4.1. Test Setup



4.2. Limit

4.2.1. FCC

According to §15.247(b)(3), for systems using digital modulation in the 902-928 MHz, 2 400-2 483.5 MHz, and 5 725-5 850 MHz bands: 1 Watt. As an alternative to a peak power measurement, compliance with the one Watt limit can be based on a measurement of the maximum conducted output power. Maximum Conducted Output Power is defined as the total transmit power delivered to all antennas and antenna elements averaged across all symbols in the signaling alphabet when the transmitter is operating at its maximum power control level. Power must be summed across all antennas and antenna elements. The average must not include any time intervals during which the transmitter is off or is transmitting at a reduced power level. If multiple modes of operation are possible (e.g., alternative modulation methods), the maximum conducted output power is the highest total transmit power occurring in any mode.

According to §15.247(b)(4), the conducted output power limit specified in paragraph (b) of this section is based on the use of antennas with directional gains that do not exceed 6 dBi. Except as shown in paragraph (c) of this section, if transmitting antennas of directional gain greater than 6 dBi are used, the conducted output power from the intentional radiator shall be reduced below the stated values in paragraph (b)(1), (b)(2), and (b)(3) of this section, as appropriate, by the amount in dB that the directional gain of the antenna exceeds 6 dBi.

4.2.2. IC

According to RSS-247 Issue 2, 5.4(d), for DTSs employing digital modulation techniques operating in the bands 902-928 MHz and 2 400-2 483.5 MHz, the maximum peak conducted output power shall not exceed 1 W. The e.i.r.p. shall not exceed 4 W, except as provided in section 5.4(e),

As an alternative to a peak measurement, compliance can be based on a measurement of the maximum conducted output power. The maximum conducted output power is the total transmit power delivered to all antennas and antenna elements, averaged across all symbols in the signalling alphabet when the transmitter is operating at its maximum power control level. Power must be summed across all antennas and antenna elements. The average must not include any time intervals during which the transmitter is off or transmitting at a reduced power level. If multiple modes of operation are implemented, the maximum conducted output power is the highest total transmit power occurring in any mode.

4.3. Test Procedure

The test follows section 11.9.1.3 of ANSI C63.10-2013.

PKPM1 Peak-reading power meter method

- The maximum peak conducted output power may be measured using a broadband peak RF power meter. The power meter shall have a video bandwidth that is greater than or equal to the DTS bandwidth and shall utilize a fast-responding diode detector.

The test follows section 11.9.2.3.2 of ANSI C63.10-2013.

Method AVGPM-G (Measurement using a gated RF average-reading power meter)

- Alternatively, measurements may be performed using a wideband gated RF power meter provided that the gate parameters are adjusted such that the power is measured only when the EUT is transmitting at its maximum power control level. Since this measurement is made only during the ON time of the transmitter, no duty cycle correction is required.

Test program: (S/W name: R&S Power Viewer, Version: 3.2.0)

1. Initially overall offset for attenuator and cable loss is measured per frequency.
2. Measured offset is inserted in test program in advance of measurement for output power.
3. Power for each frequency (channel) of device is investigated as final result.
4. Final result reported on this section from R&S power viewer program includes with several factors and test program shows only final result.

4.4. Test Results

Ambient temperature : (23 ± 1) °C
 Relative humidity : 47 % R.H.

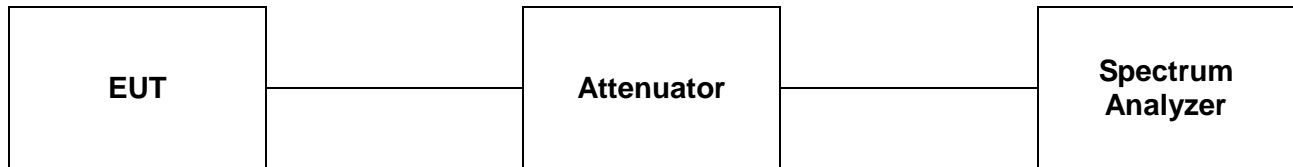
Mode	Voltage	Channel	Frequency (MHz)	Average Power Result (dB m)		Peak Power Result (dB m)		Limit (dB m)
				PHY 1M	PHY 2M	PHY 1M	PHY 2M	
GFSK	DC 10.2 V	Low	2 402	<u>7.27</u>	<u>7.10</u>	<u>7.40</u>	<u>7.40</u>	30
		Middle	2 440	7.18	7.02	7.33	7.33	
		High	2 480	6.75	6.56	6.89	6.90	
	DC 27.6 V	Low	2 402	<u>7.25</u>	7.13	<u>7.42</u>	<u>7.41</u>	
		Middle	2 440	7.16	<u>7.16</u>	7.33	7.32	
		High	2 480	6.79	6.70	6.91	6.93	

Remark;

According to ANSI C63.10-2013 Clause 5.13 c),
 the test voltages are $12 \times (100-15) \% = 10.2 \text{ V}$, $24 \times (100+15) \% = 27.6 \text{ V}$.

5. Power Spectral Density

5.1. Test Setup



5.2. Limit

5.2.1 FCC

According to §15.247(e), for digitally modulated systems, the power spectral density conducted from the intentional radiator to the antenna shall not be greater than 8 dBm in any 3 kHz band during any time interval of continuous transmission. This power spectral density shall be determined in accordance with the provisions of paragraph (b) of this section. The same method of determining the conducted output power shall be used to determine the power spectral density.

5.2.2 IC

According to RSS-247 Issue 2, 5.2(b), the transmitter power spectral density conducted from the transmitter to the antenna shall not be greater than 8 dBm in any 3 kHz band during any time interval of continuous transmission. This power spectral density shall be determined in accordance with the provisions of section 5.4(d), (i.e. the power spectral density shall be determined using the same method as is used to determine the conducted output power).

5.3. Test Procedure

The measurements are recorded using the PKPSD measurement procedure in section 11.10.2 of ANSI C63.10-2013.

- This procedure shall be used if maximum peak conducted output power was used to demonstrate compliance, and is optional if the maximum conducted (average) output power was used to demonstrate compliance.

1. Set analyzer center frequency to DTS channel center frequency.
2. Set the span to 1.5 times the DTS bandwidth.
3. Set the RBW to $3 \text{ kHz} \leq \text{RBW} \leq 100 \text{ kHz}$.
4. Set the VBW $\geq [3 \times \text{RBW}]$.
5. Detector = peak.
6. Sweep time = auto couple.
7. Trace mode = max hold.
8. Allow trace to fully stabilize.
9. Use the peak marker function to determine the maximum amplitude level within the RBW.
10. If measured value exceeds requirement, then reduce RBW (but no less than 3 kHz) and repeat.

5.4. Test Results

Ambient temperature : (23 ± 1) °C
 Relative humidity : 47 % R.H.

Mode	Voltage	Channel	Frequency (MHz)	Measured PSD (dB m/3 kHz)		Limit (dB m/3 kHz)
				PHY 1M	PHY 2M	
GFSK	DC 10.2 V	Low	2 402	-9.19	-10.85	8
		Middle	2 440	-9.59	-11.09	
		High	2 480	-9.84	-11.33	
	DC 27.6 V	Low	2 402	-9.29	-11.09	
		Middle	2 440	-9.79	-11.33	
		High	2 480	-9.81	-11.49	

Remark;

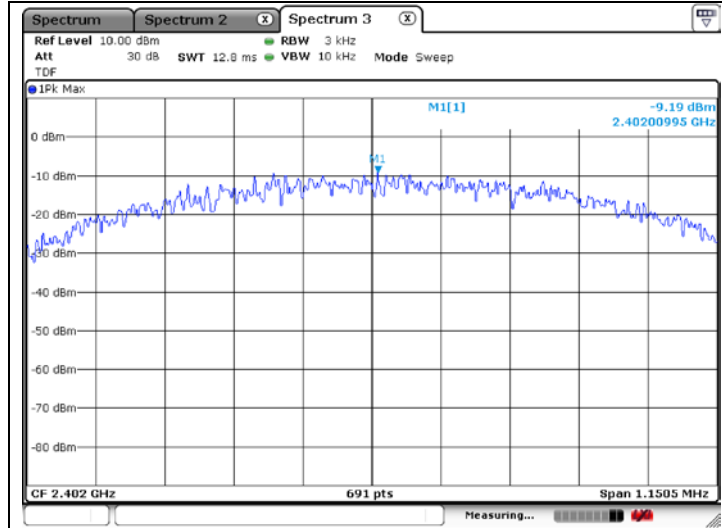
According to ANSI C63.10-2013 Clause 5.13 c),
 the test voltages are $12 \cdot (100-15) \% = 10.2 \text{ V}$, $24 \cdot (100+15) \% = 27.6 \text{ V}$.

- Test plots

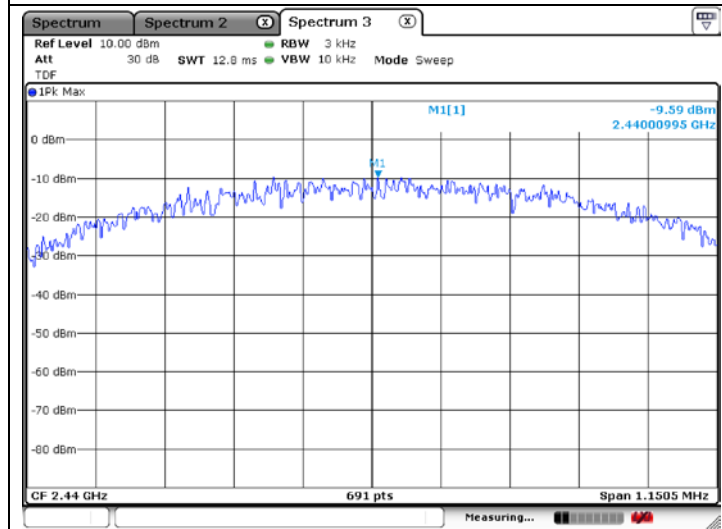
Test voltage: DC 10.2 V

PHY 1M (255 bytes)

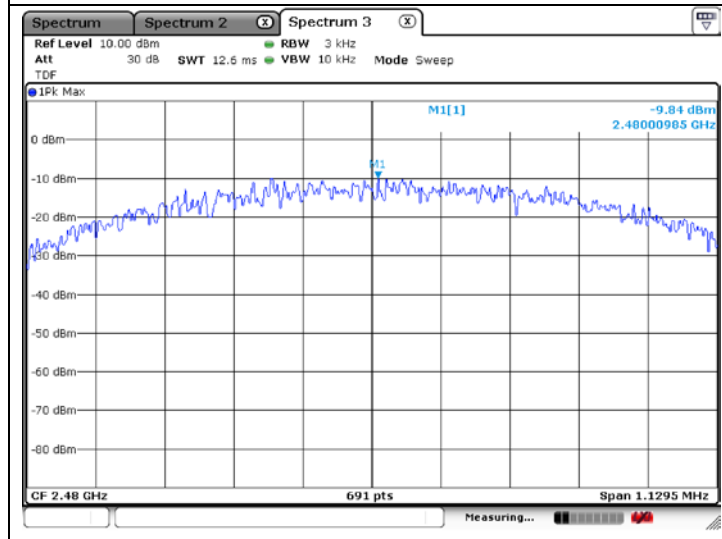
Low Channel



Middle Channel

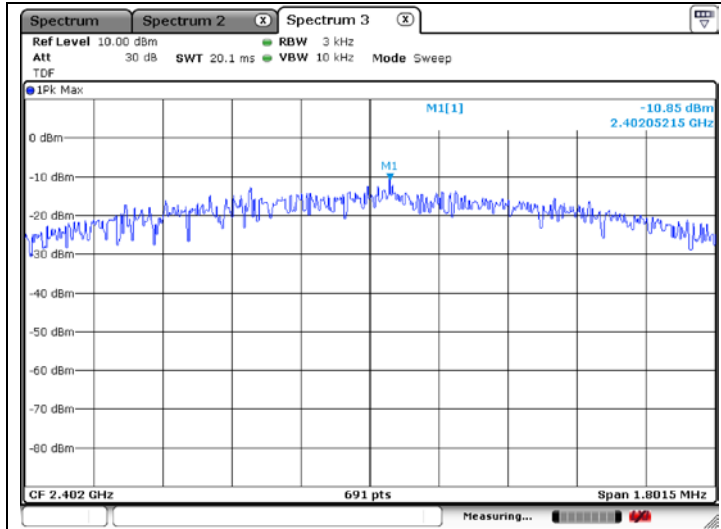


High Channel

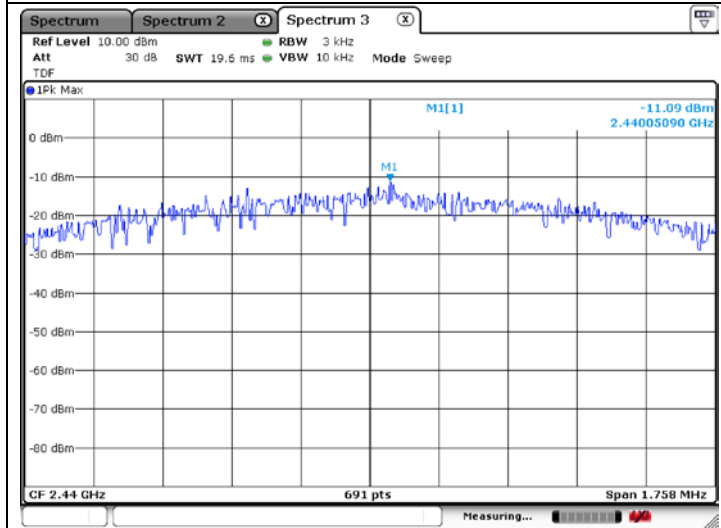


PHY 2M (255 bytes)

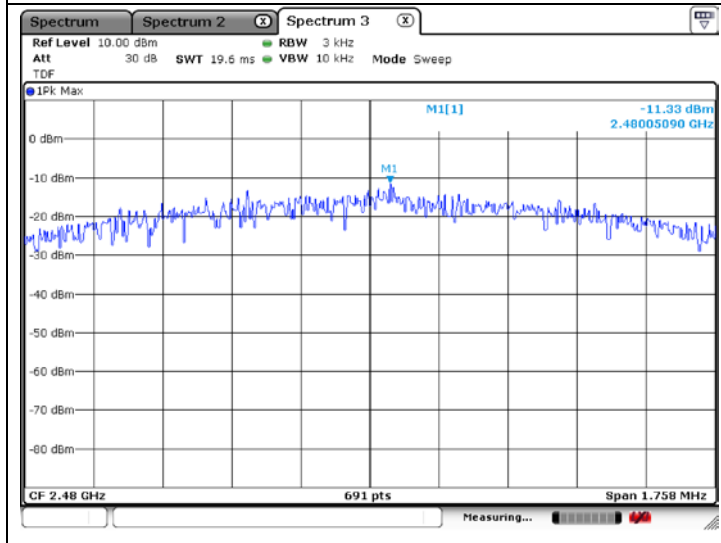
Low Channel



Middle Channel



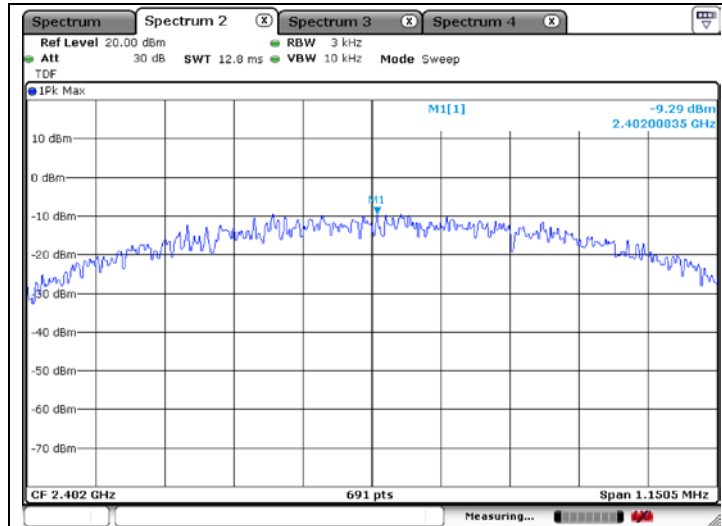
High Channel



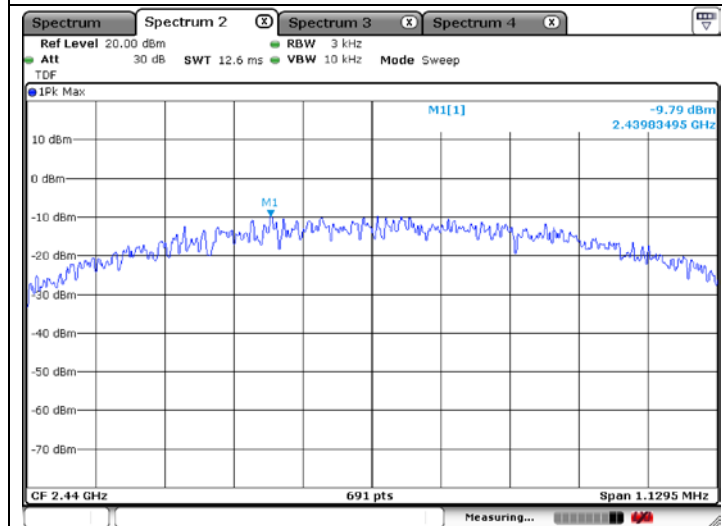
Test voltage: DC 27.6 V

PHY 1M (255 bytes)

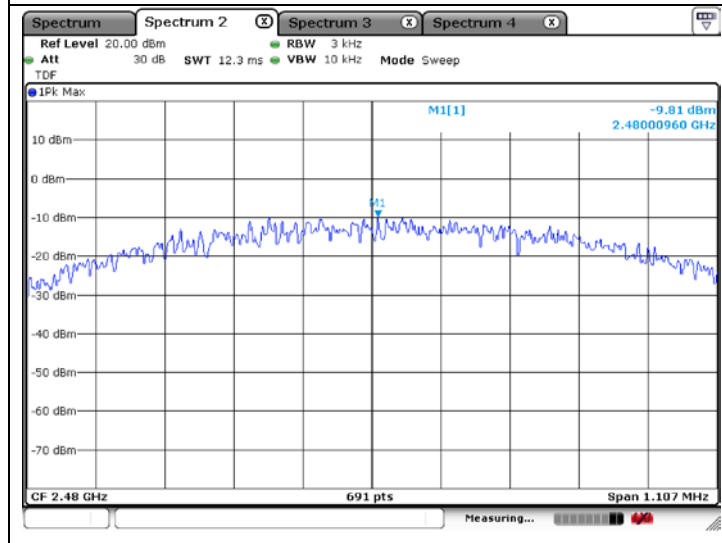
Low Channel



Middle Channel

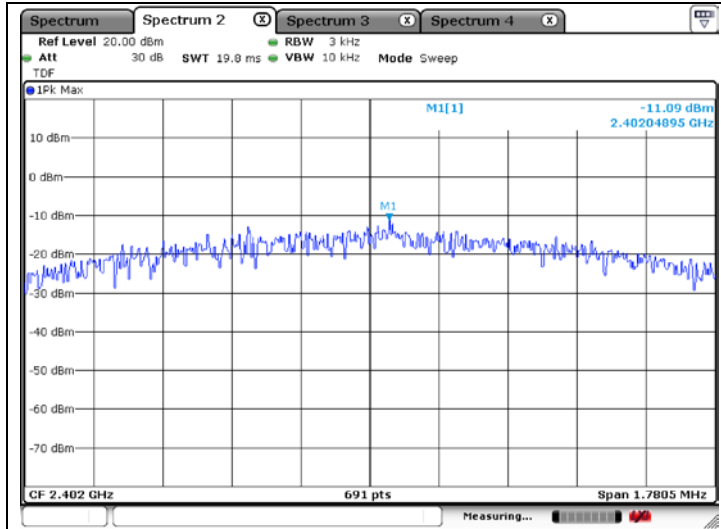


High Channel

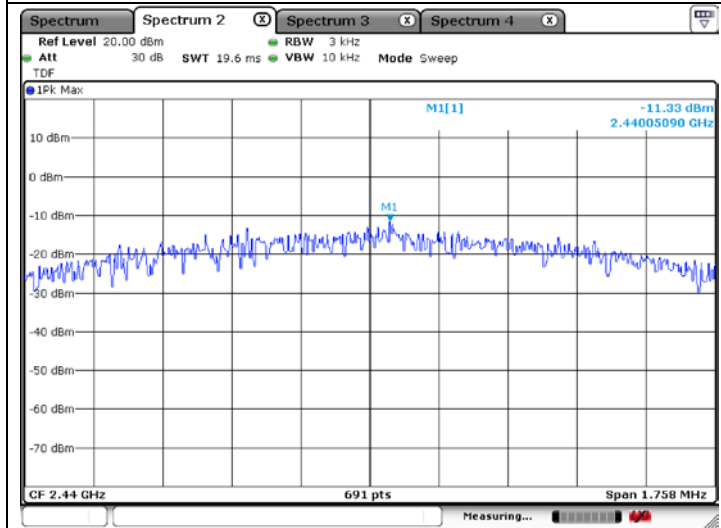


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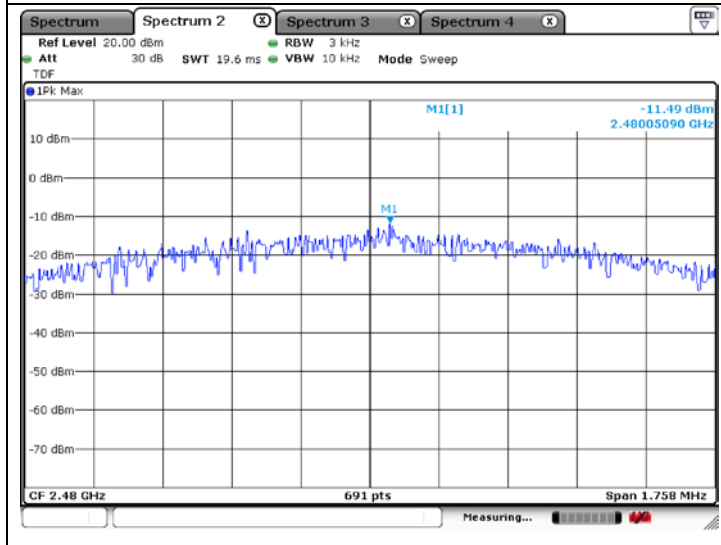
Low Channel



Middle Channel



High Channel



6. Antenna Requirement

6.1. Standard Applicable

For intentional device, according to FCC 47 CFR Section §15.203, an intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. And according to FCC 47 CFR Section §15.247(b) if transmitting antennas of directional gain greater than 6 dBi are used, the power shall be reduced by the amount in dB that the gain of the antenna exceeds 6 dBi.

6.2. Antenna Connected Construction

Antenna used in this product is Chip Antenna with gain of 0.66 dBi.

- End of the Test Report -