

# ANNEX D TEST DATA

## For

Project No.:	8225EU012303W
Client:	SHENZHEN ELECTRON TECHNOLOGY CO.,LTD.
Product Description:	Android Tablet
Model No.:	WA1383T
FCC ID:	2ABC5-E0053
Technology:	Bluetooth BDR+EDR
Test Engineer:	<i>Mikoy zhu</i>
Test Date:	2024-02-01

## Test Summary

Item	Result
Duty Cycle	Pass
Bandwidth	Pass
Maximum Conducted Output Power	Pass
Carrier Frequency Separation	Pass
Number of Hopping Frequencies	Pass
Time of Occupancy (Dwell Time)	Pass
Unwanted Emissions In Non-restricted Frequency Bands	Pass

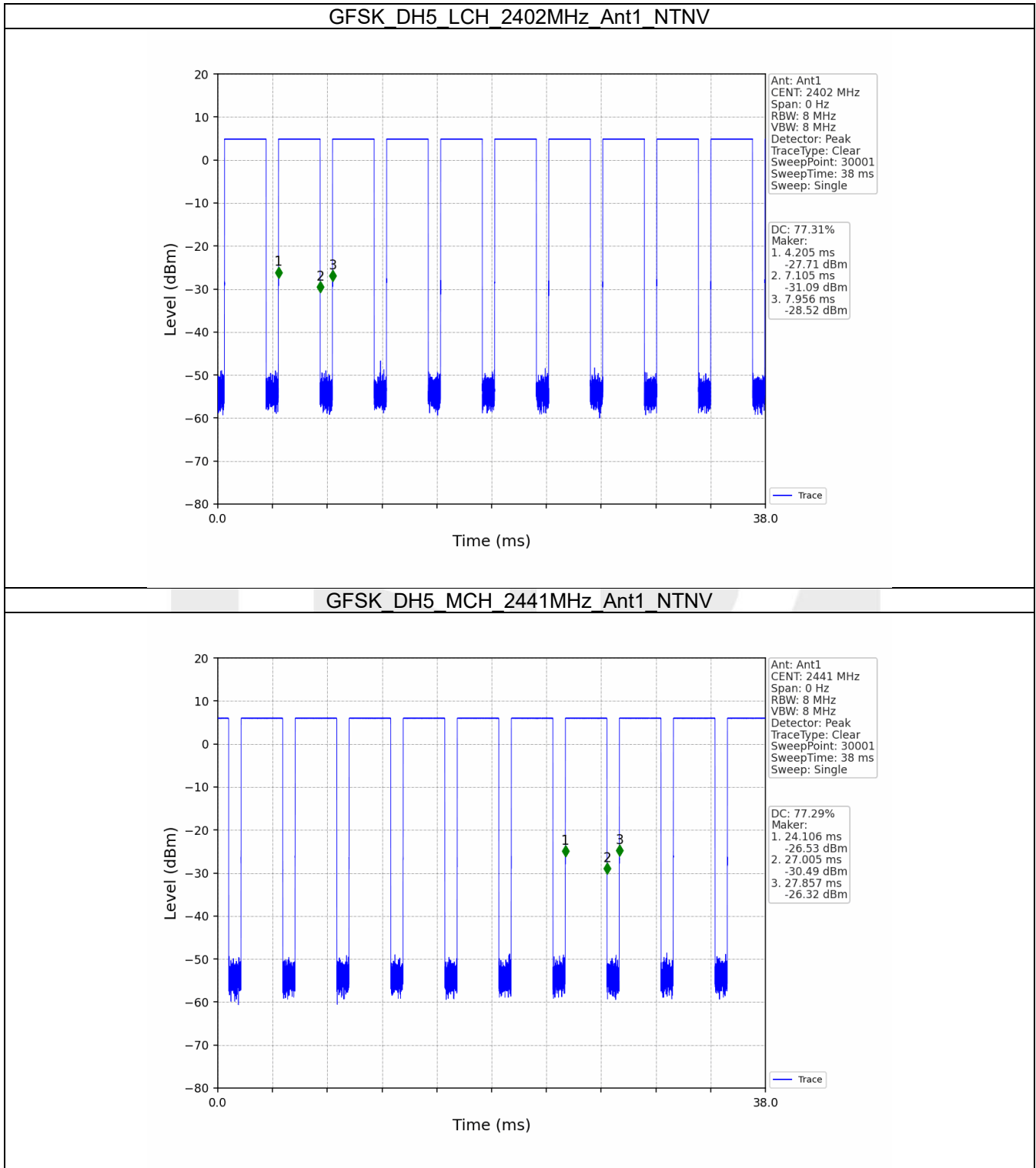
## 1. Duty Cycle

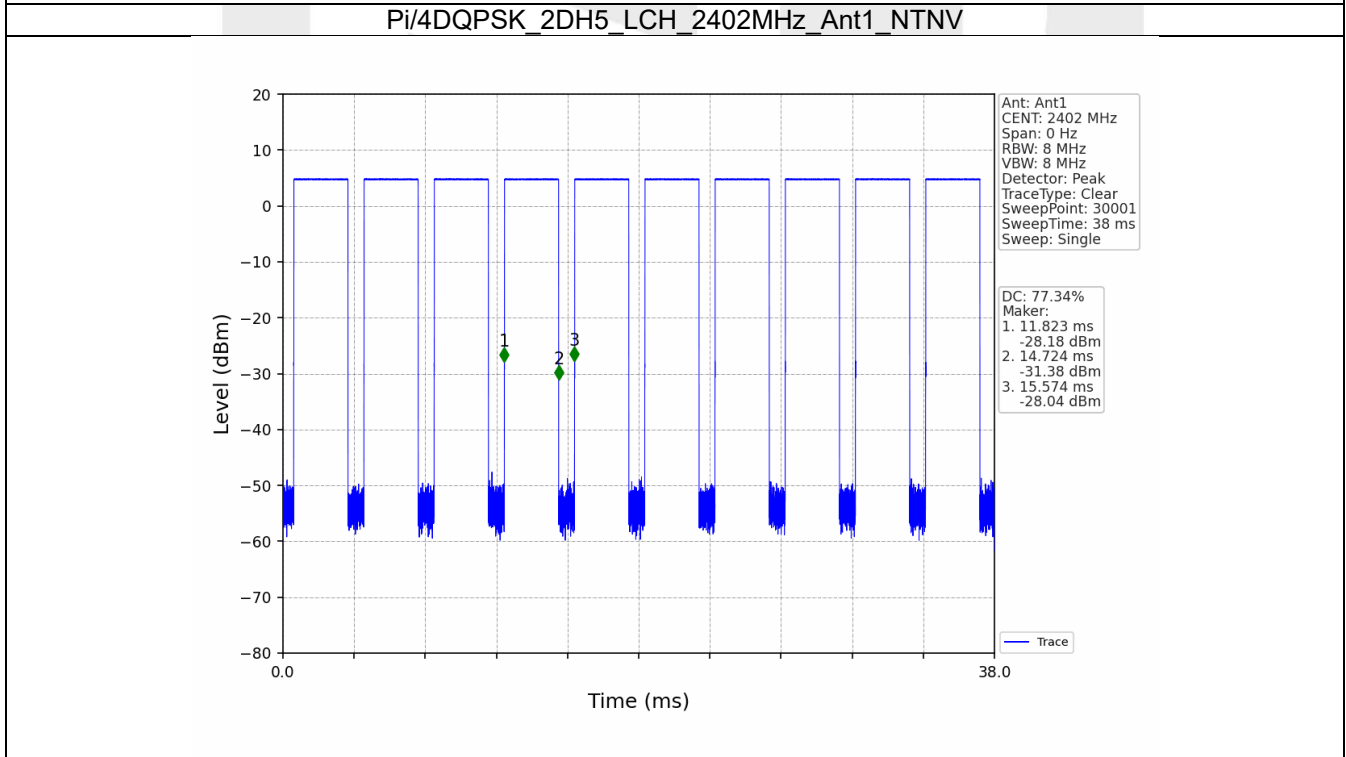
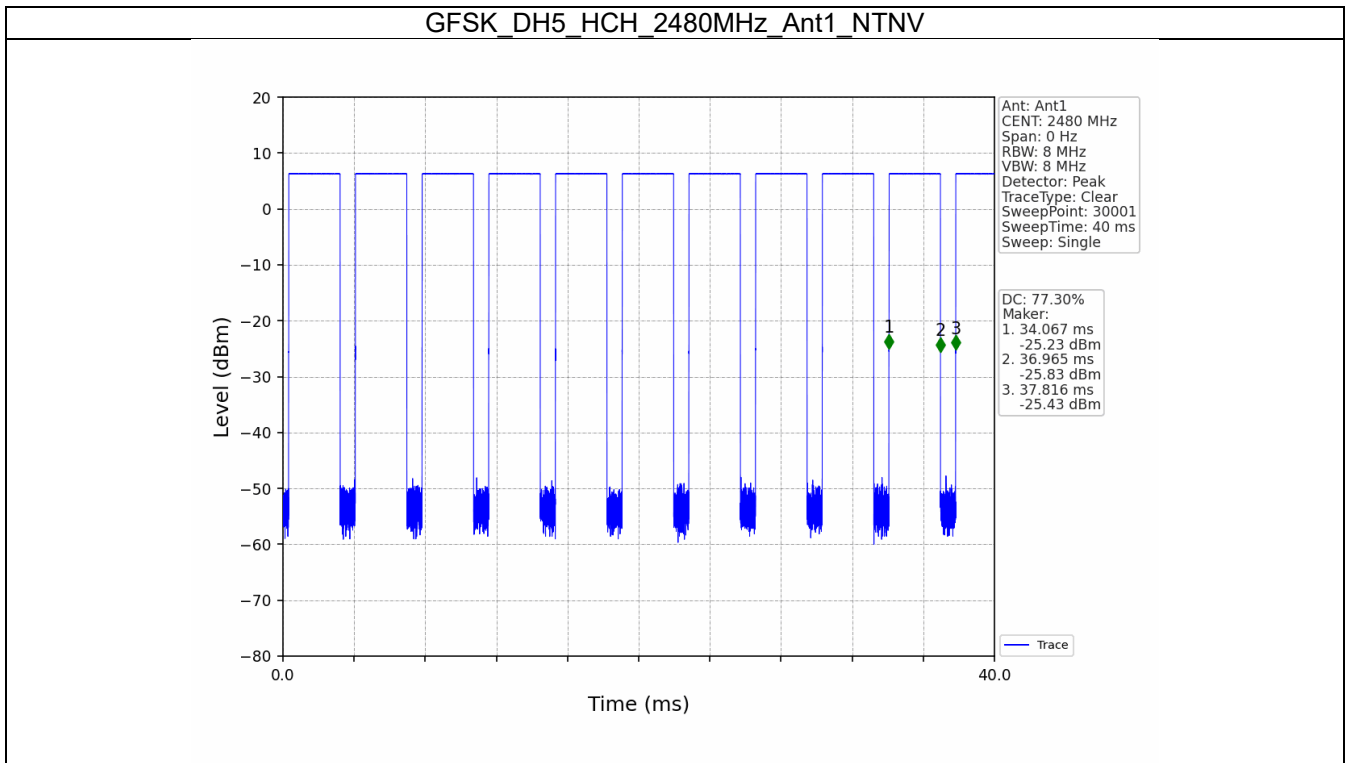
### 1.1 Ant1

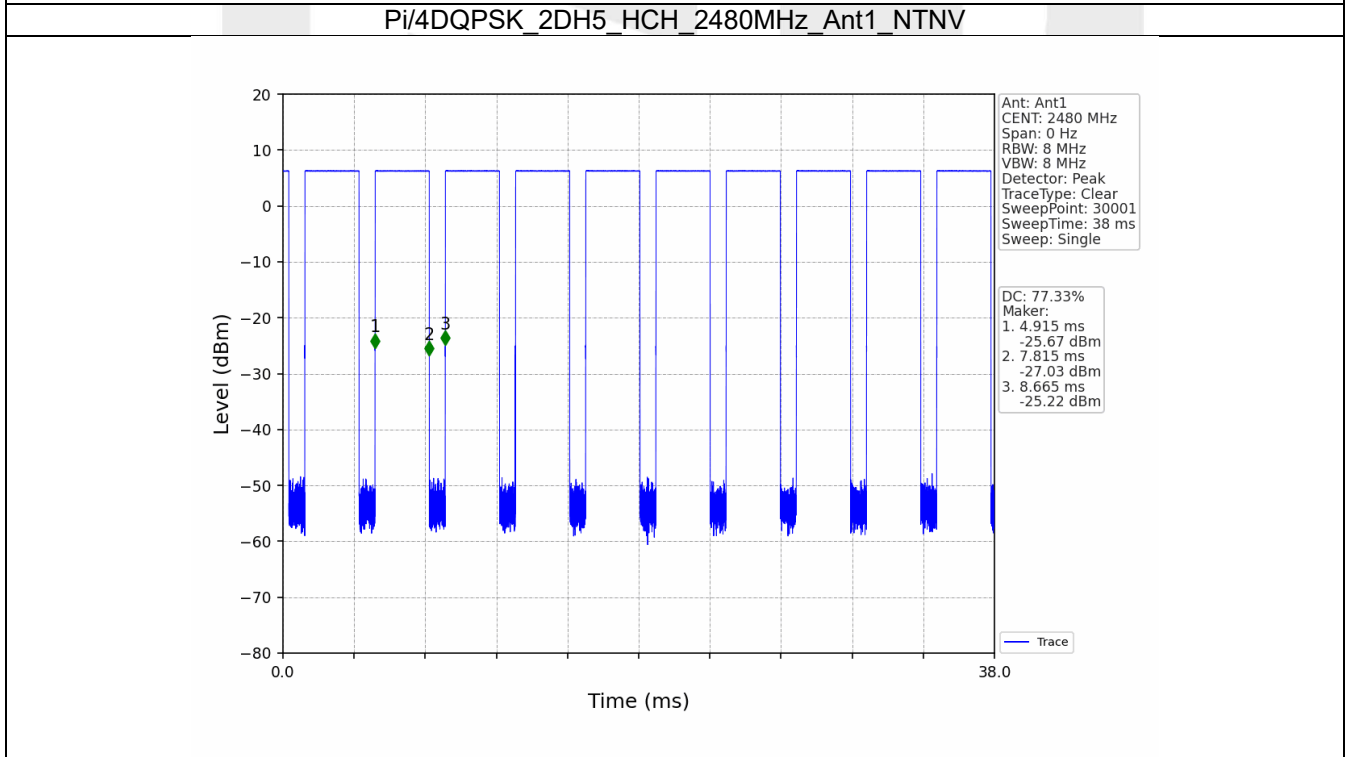
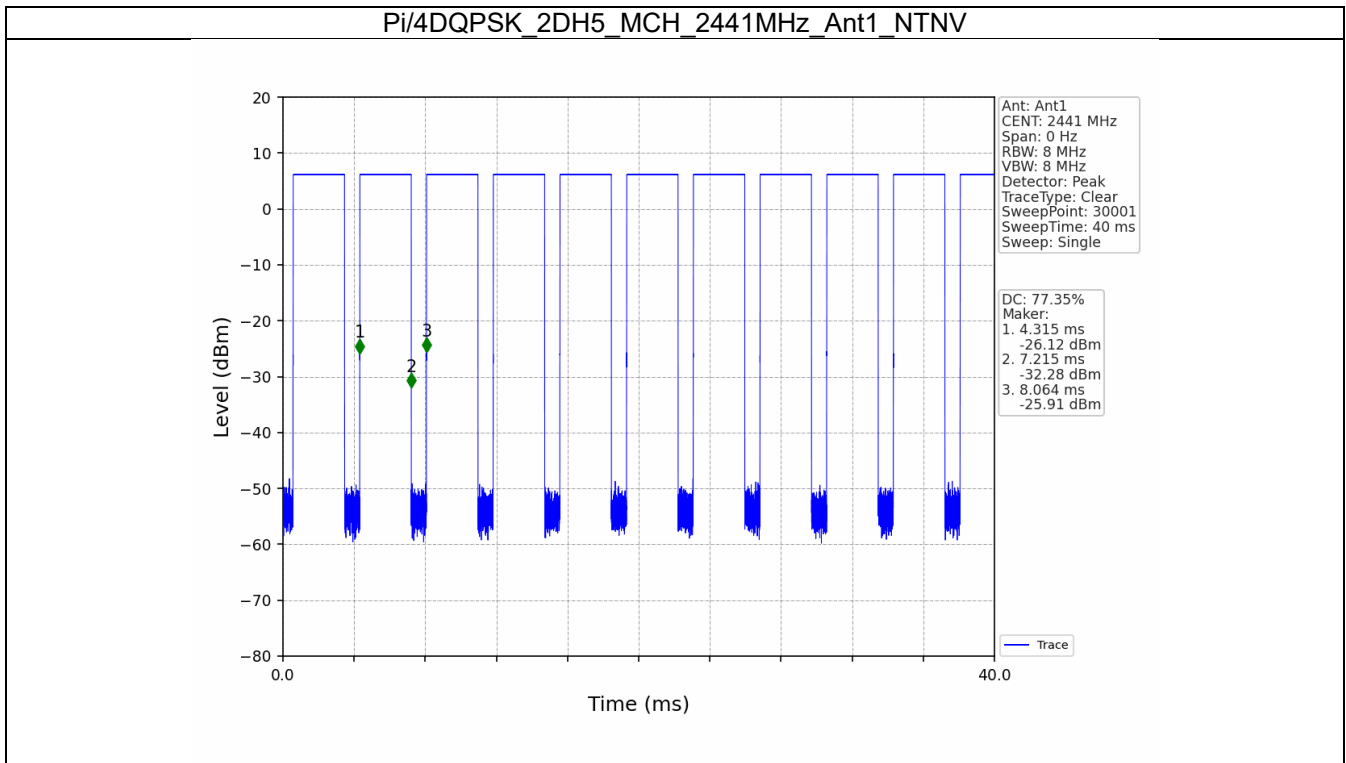
#### 1.1.1 Test Result

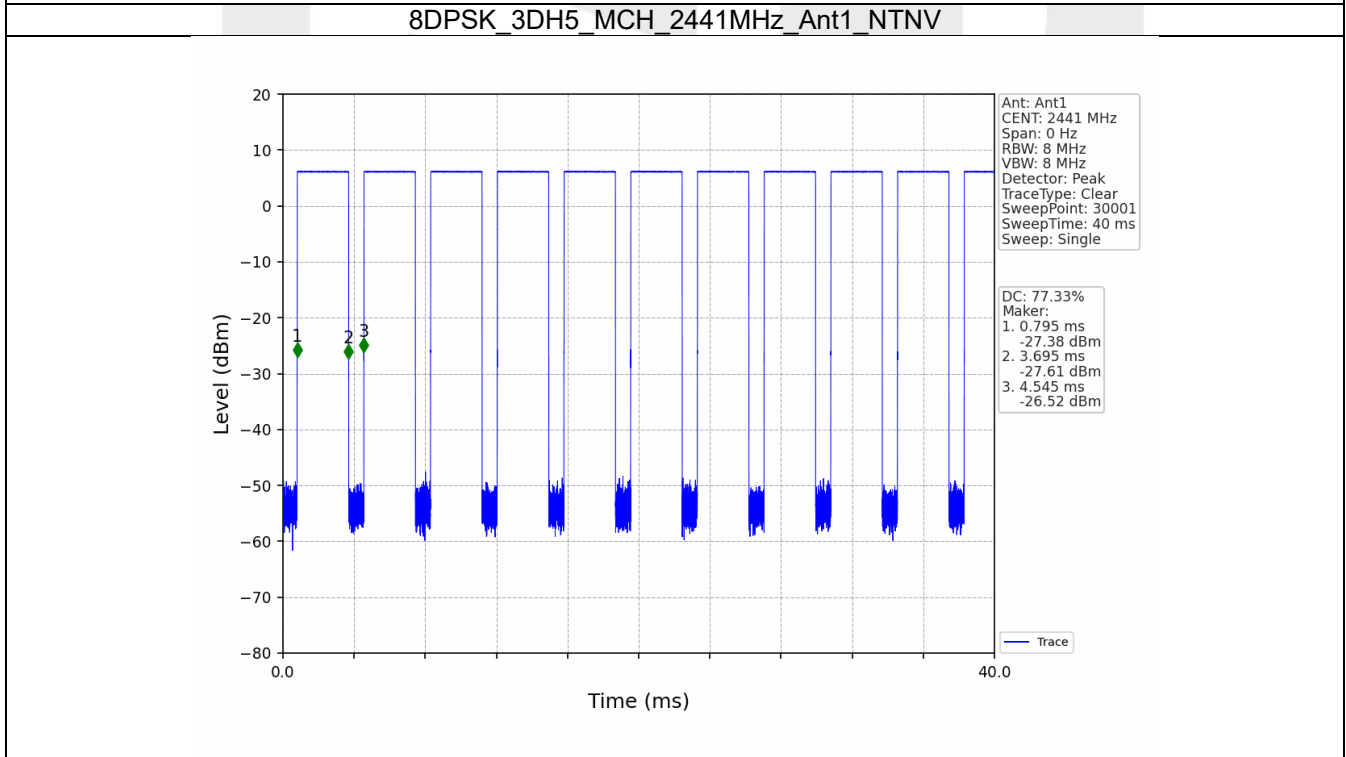
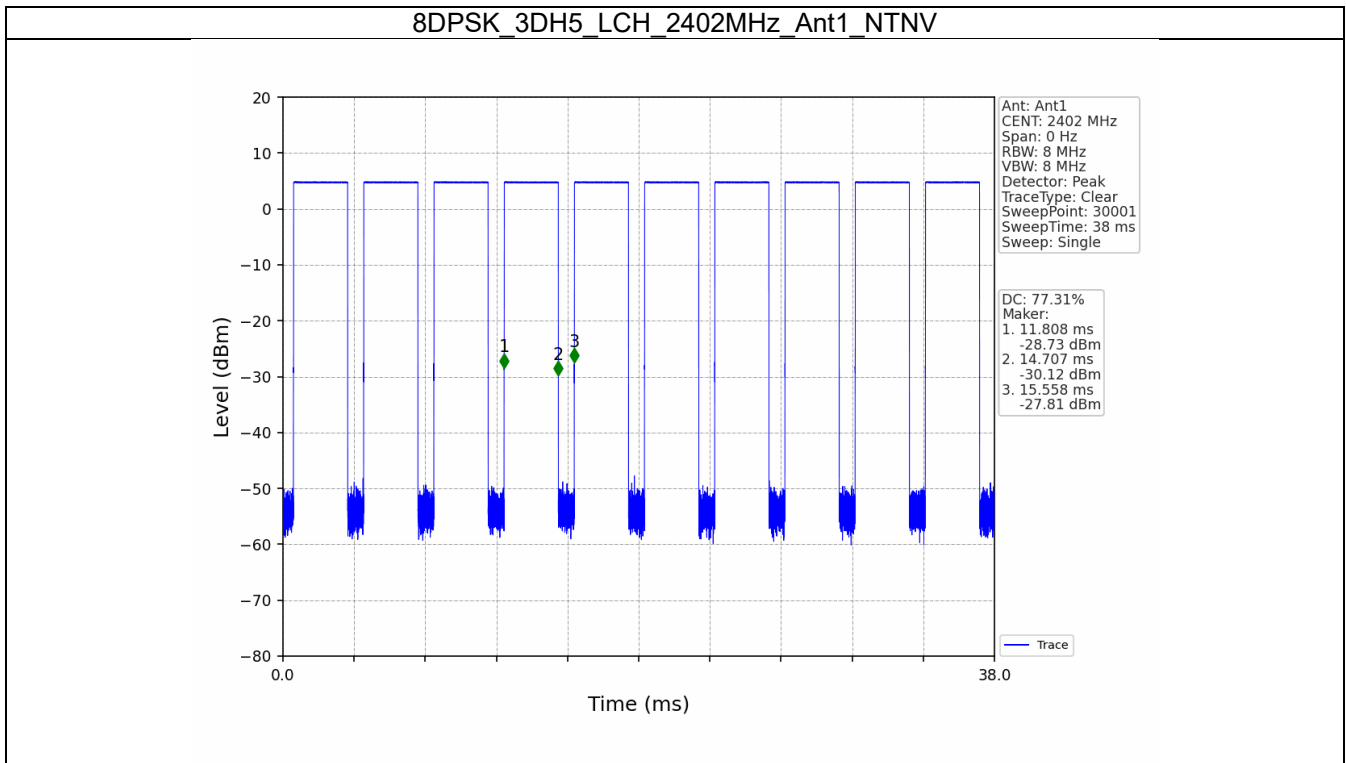
Ant1								
Mode	Tx Type	Frequency (MHz)	Packet Type	T_on (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	Max. DC Variation (%)
GFSK	SISO	2402	DH5	2.900	3.751	77.31	1.12	0.03
		2441	DH5	2.899	3.751	77.29	1.12	0.03
		2480	DH5	2.898	3.749	77.30	1.12	0.04
Pi/4DQPSK	SISO	2402	2DH5	2.901	3.751	77.34	1.12	0.03
		2441	2DH5	2.900	3.749	77.35	1.12	0.04
		2480	2DH5	2.900	3.750	77.33	1.12	0.03
8DPSK	SISO	2402	3DH5	2.899	3.750	77.31	1.12	0.03
		2441	3DH5	2.900	3.750	77.33	1.12	0.03
		2480	3DH5	2.900	3.751	77.31	1.12	0.03

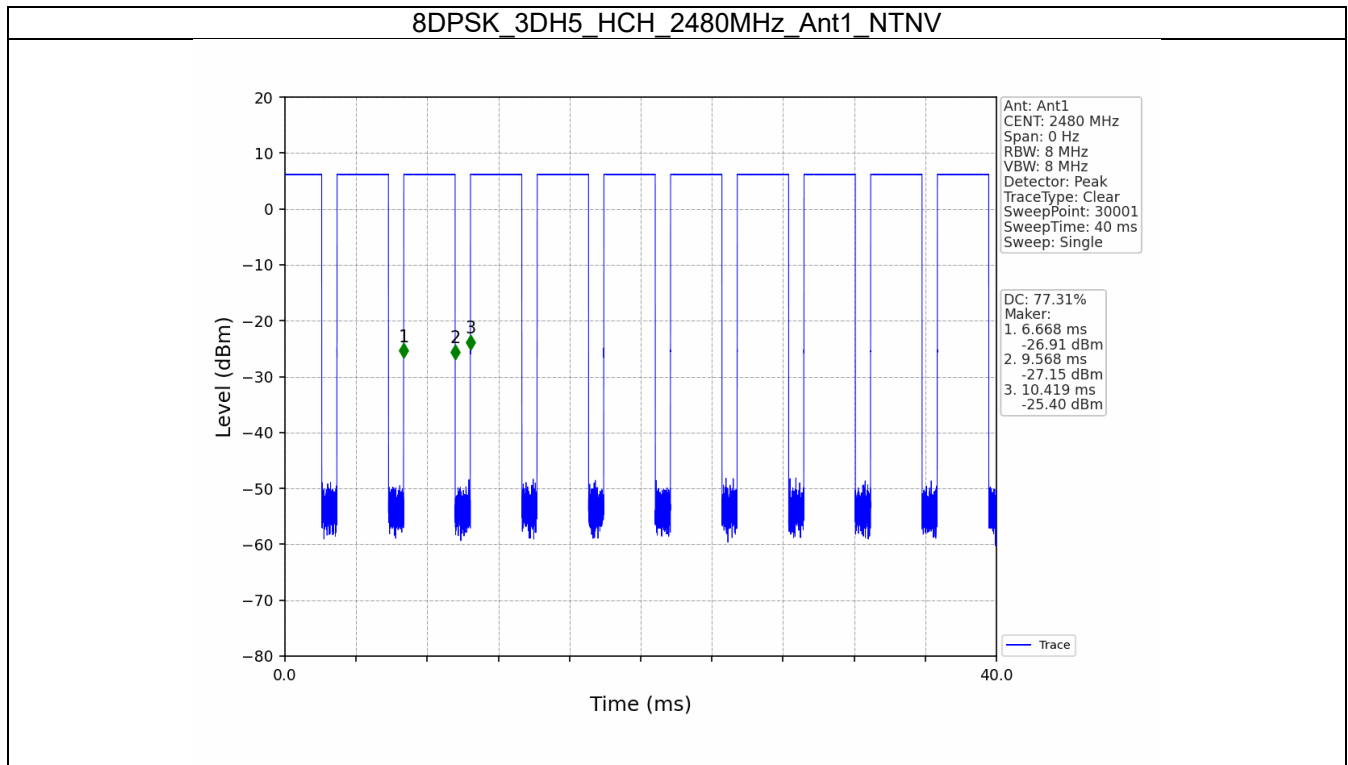
1.1.2 Test Graph













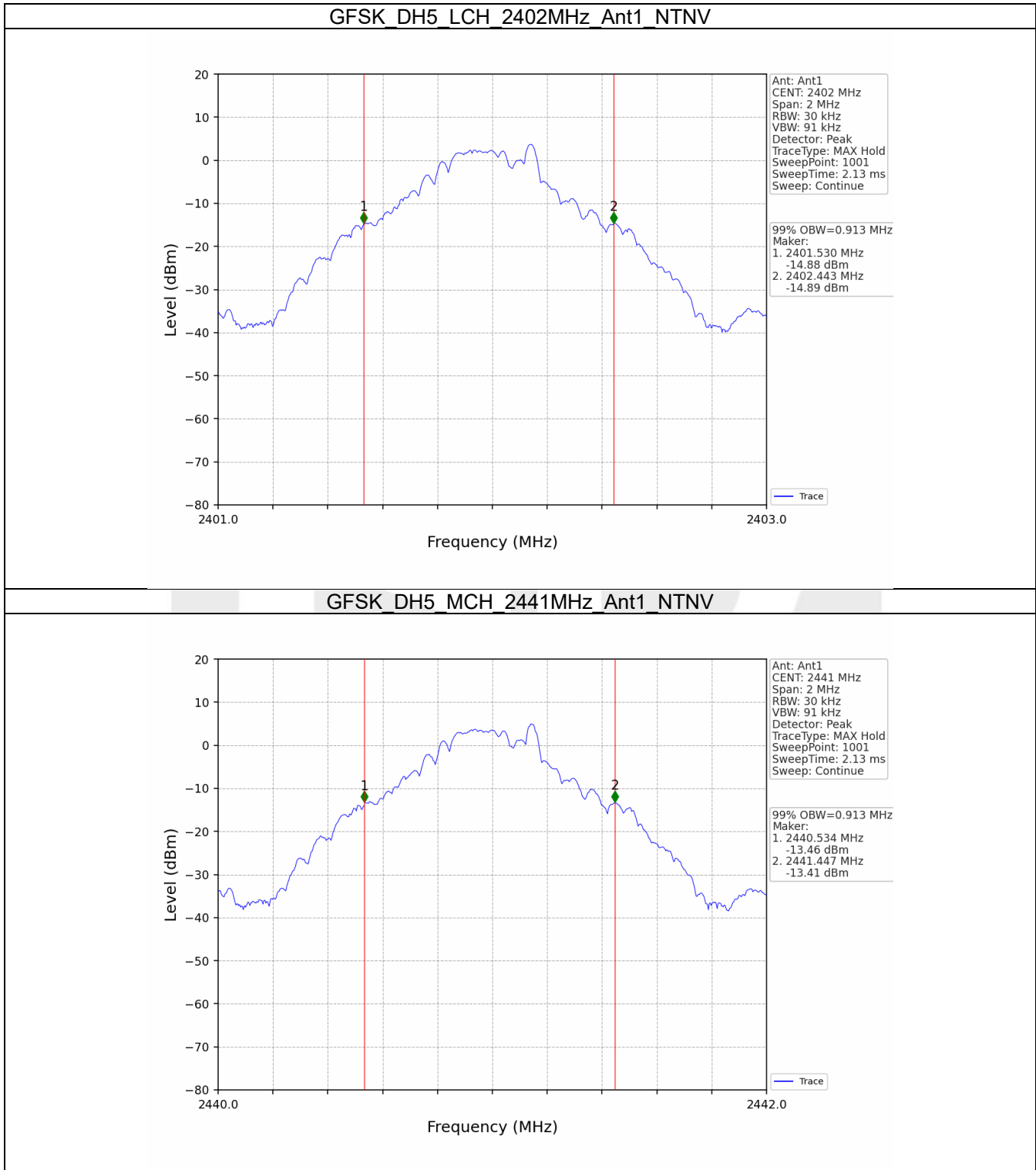
## 2. Bandwidth

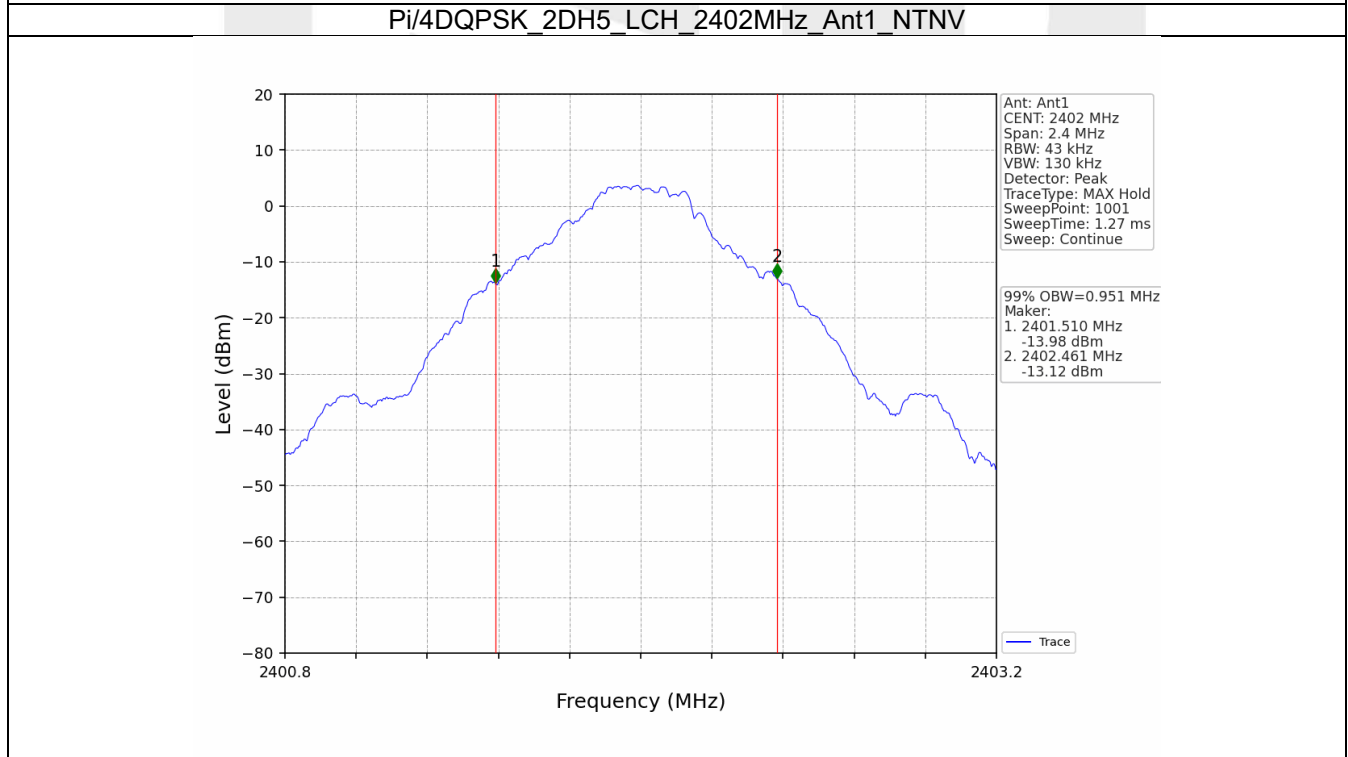
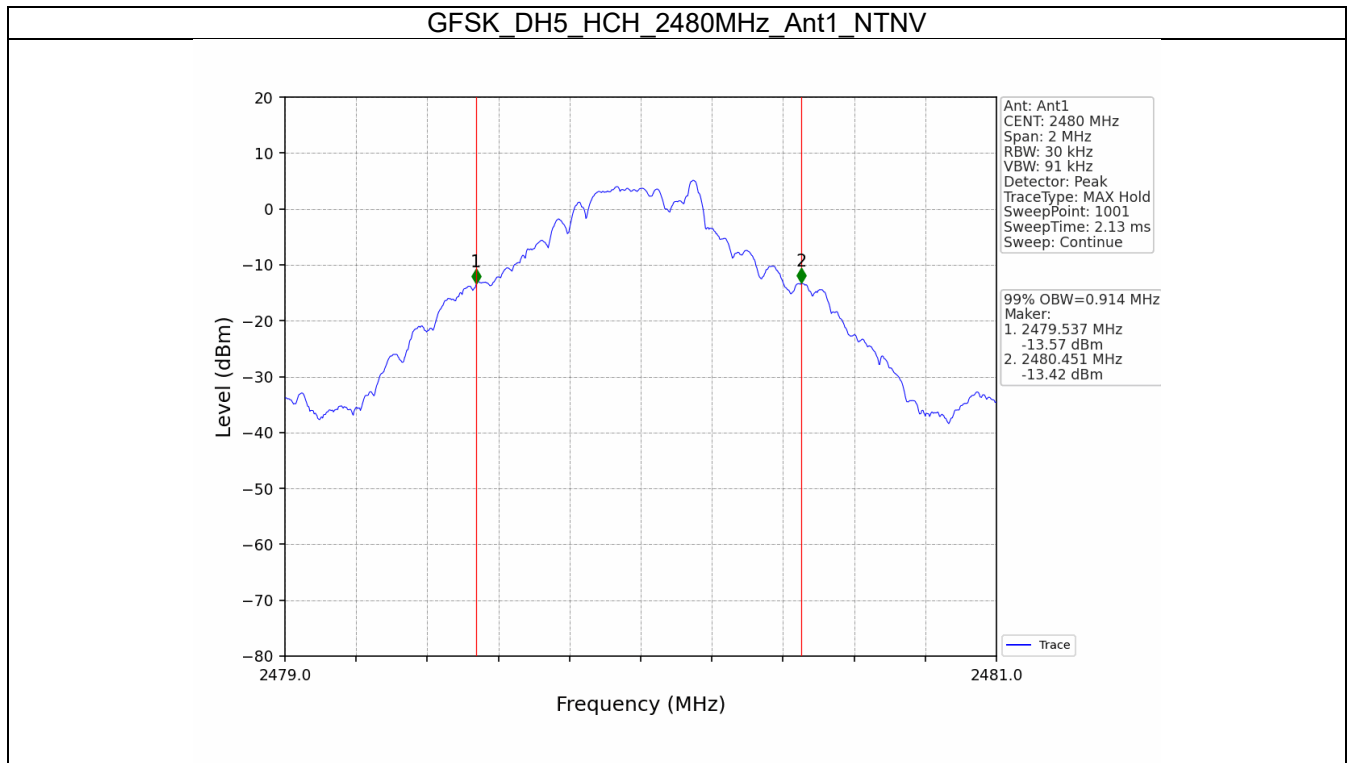
### 2.1 OBW

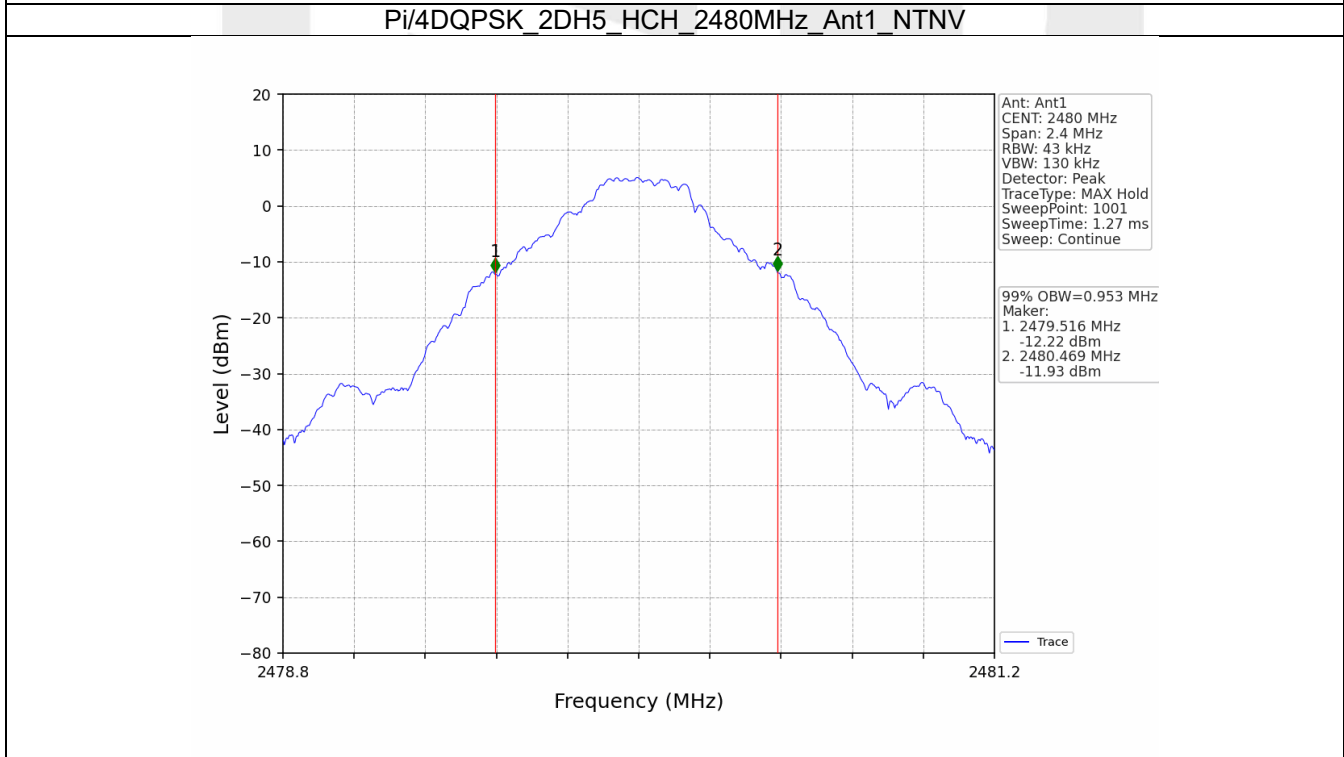
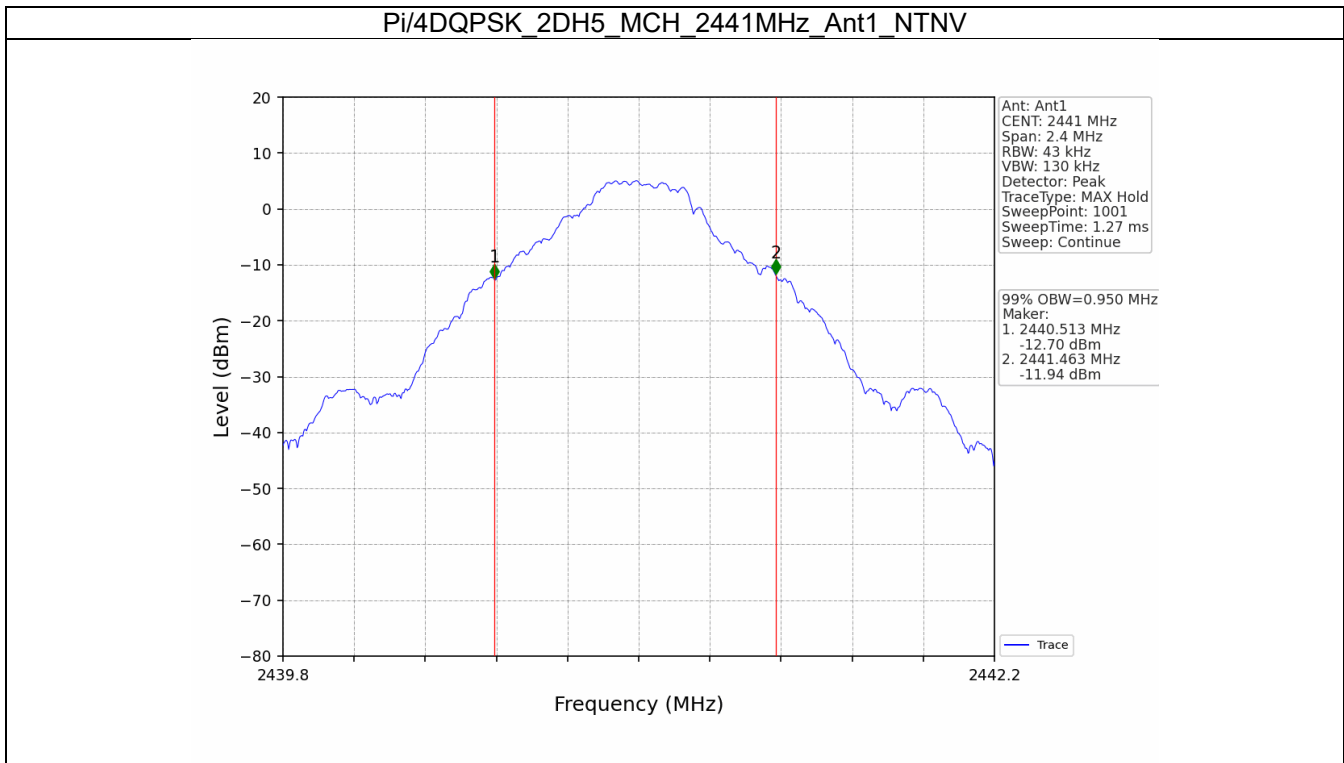
#### 2.1.1 Test Result

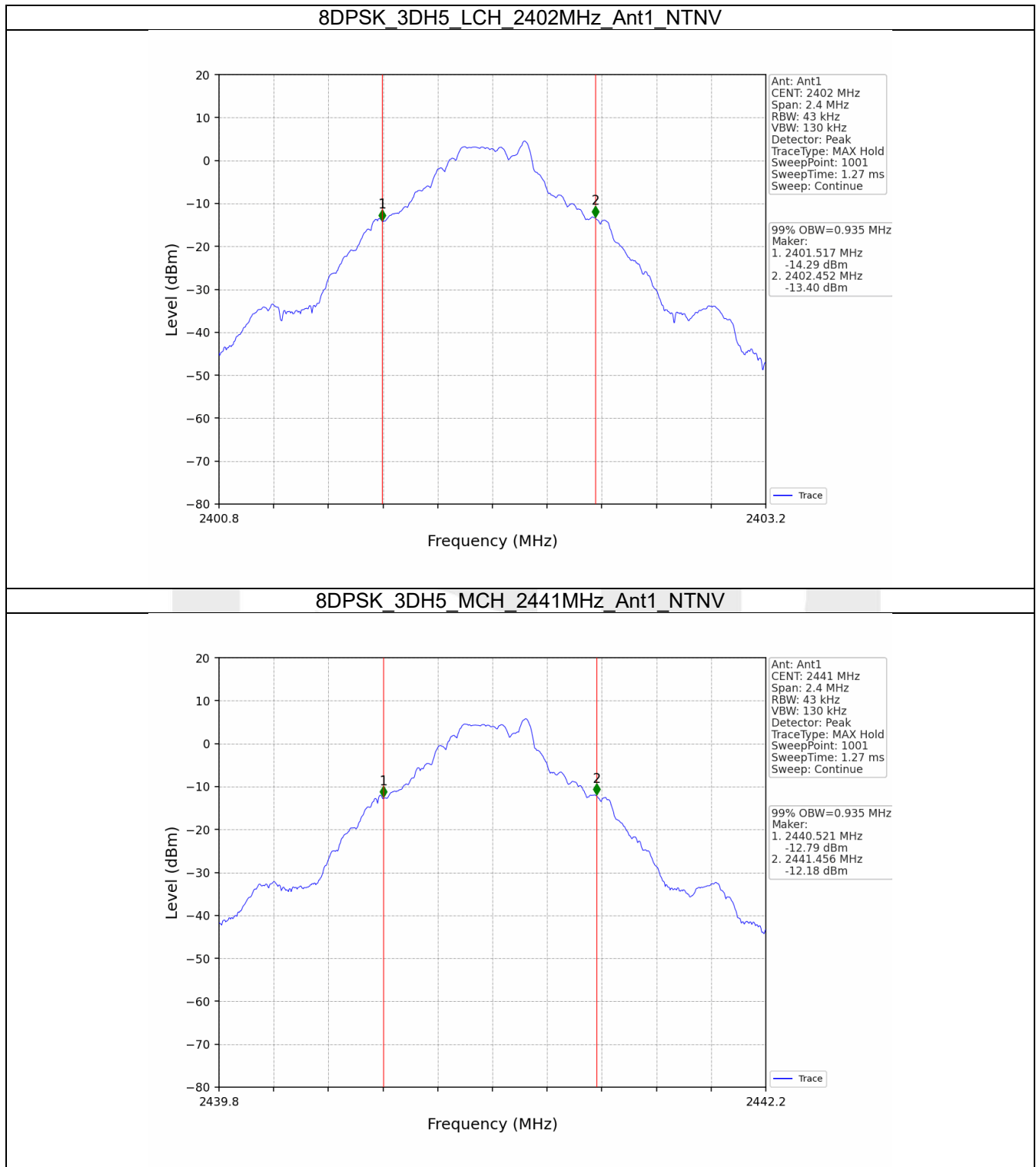
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	99% Occupied Bandwidth (MHz)	Verdict
					Result	
GFSK	SISO	2402	DH5	1	0.913	Pass
		2441	DH5	1	0.913	Pass
		2480	DH5	1	0.914	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	0.951	Pass
		2441	2DH5	1	0.950	Pass
		2480	2DH5	1	0.953	Pass
8DPSK	SISO	2402	3DH5	1	0.935	Pass
		2441	3DH5	1	0.935	Pass
		2480	3DH5	1	0.932	Pass

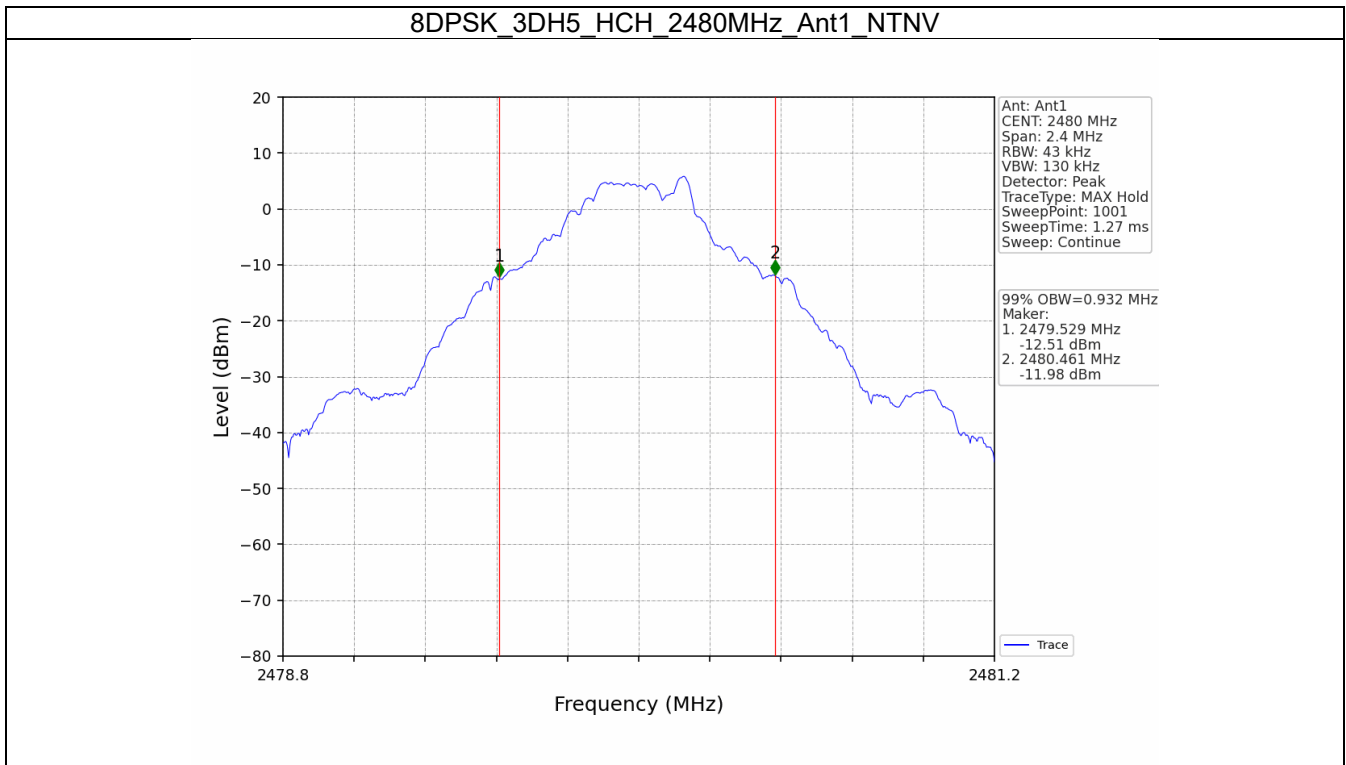
2.1.2 Test Graph









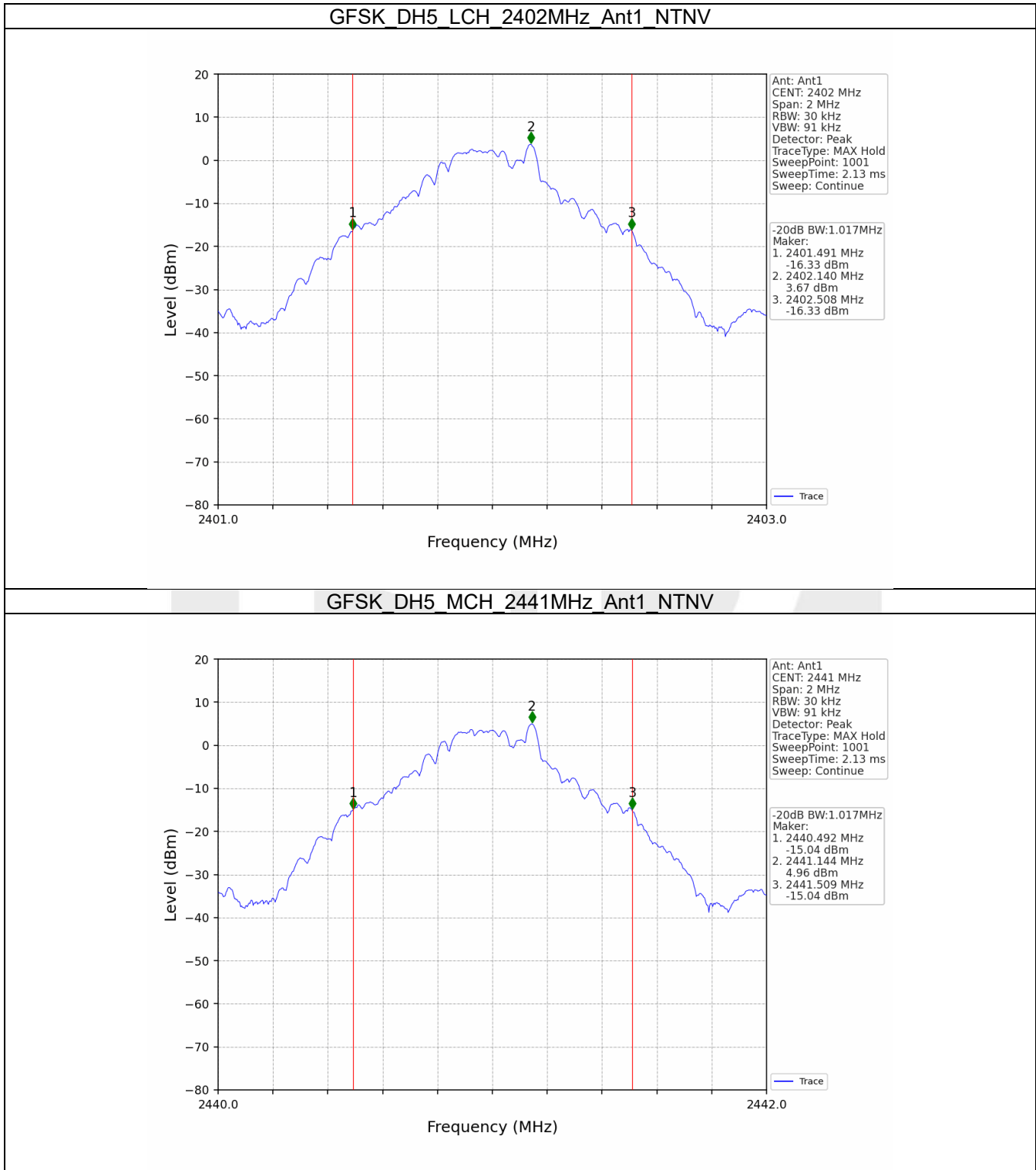


## 2.2 20dB BW

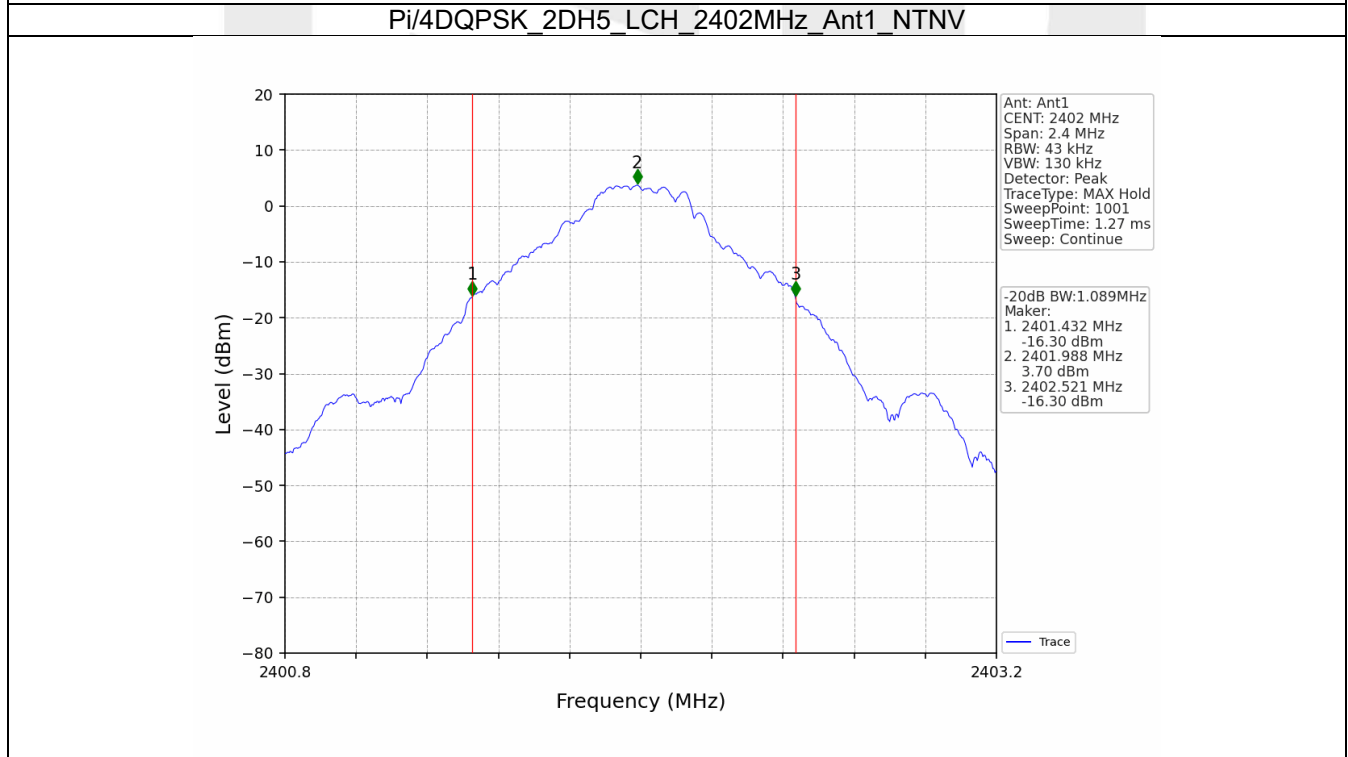
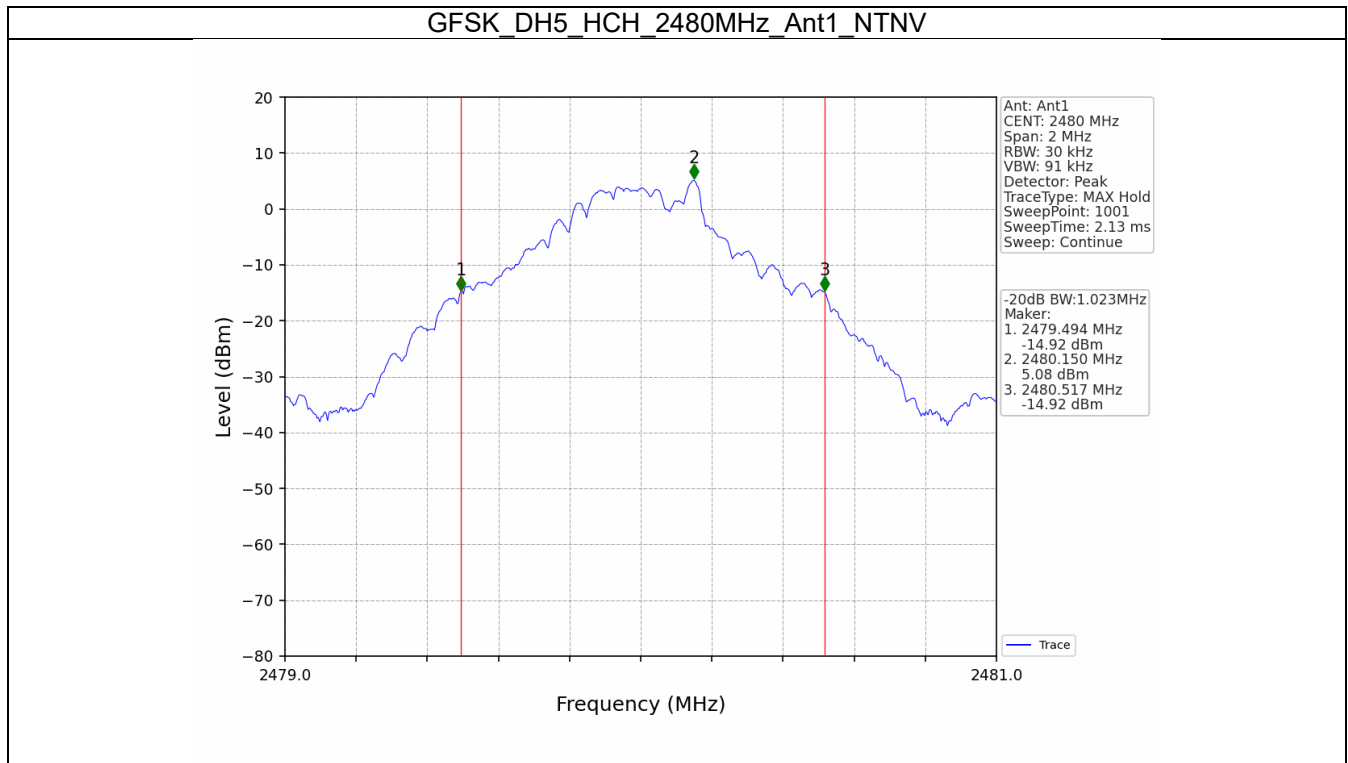
### 2.2.1 Test Result

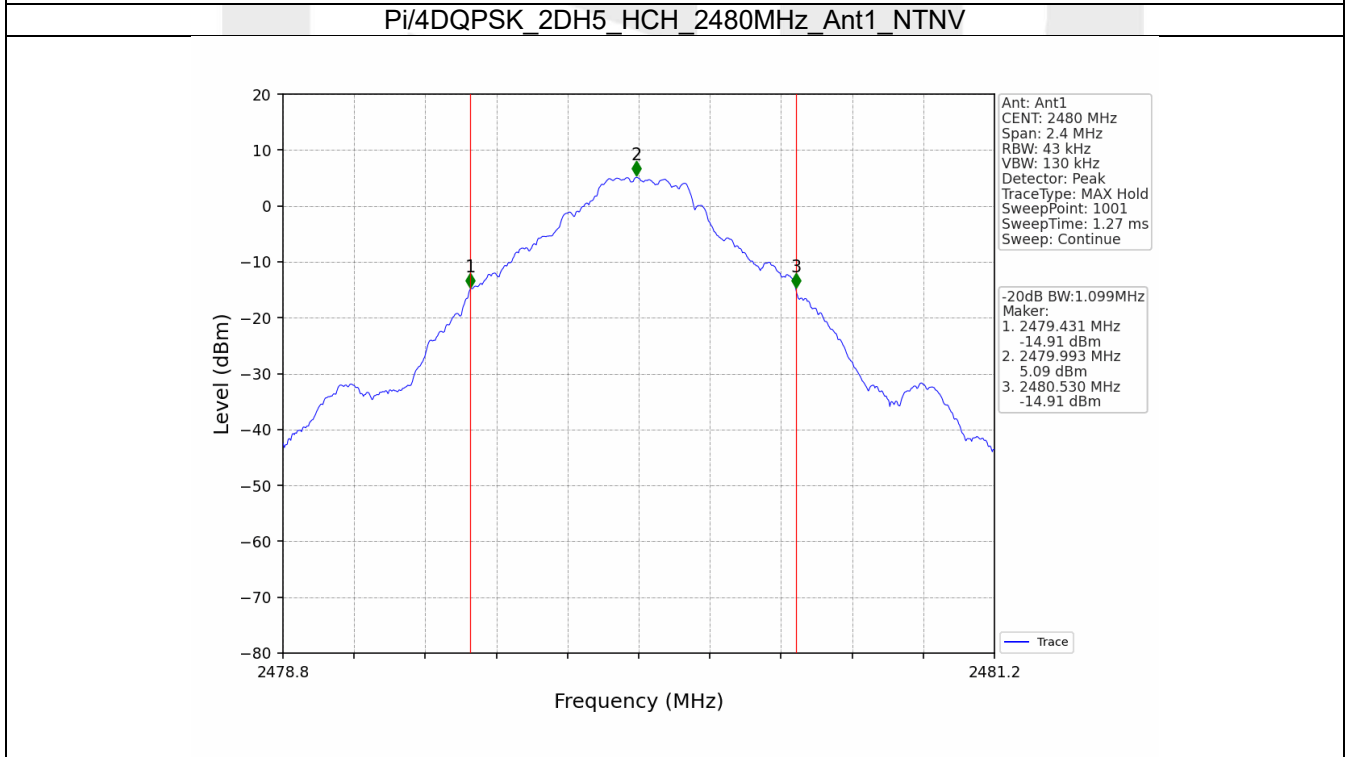
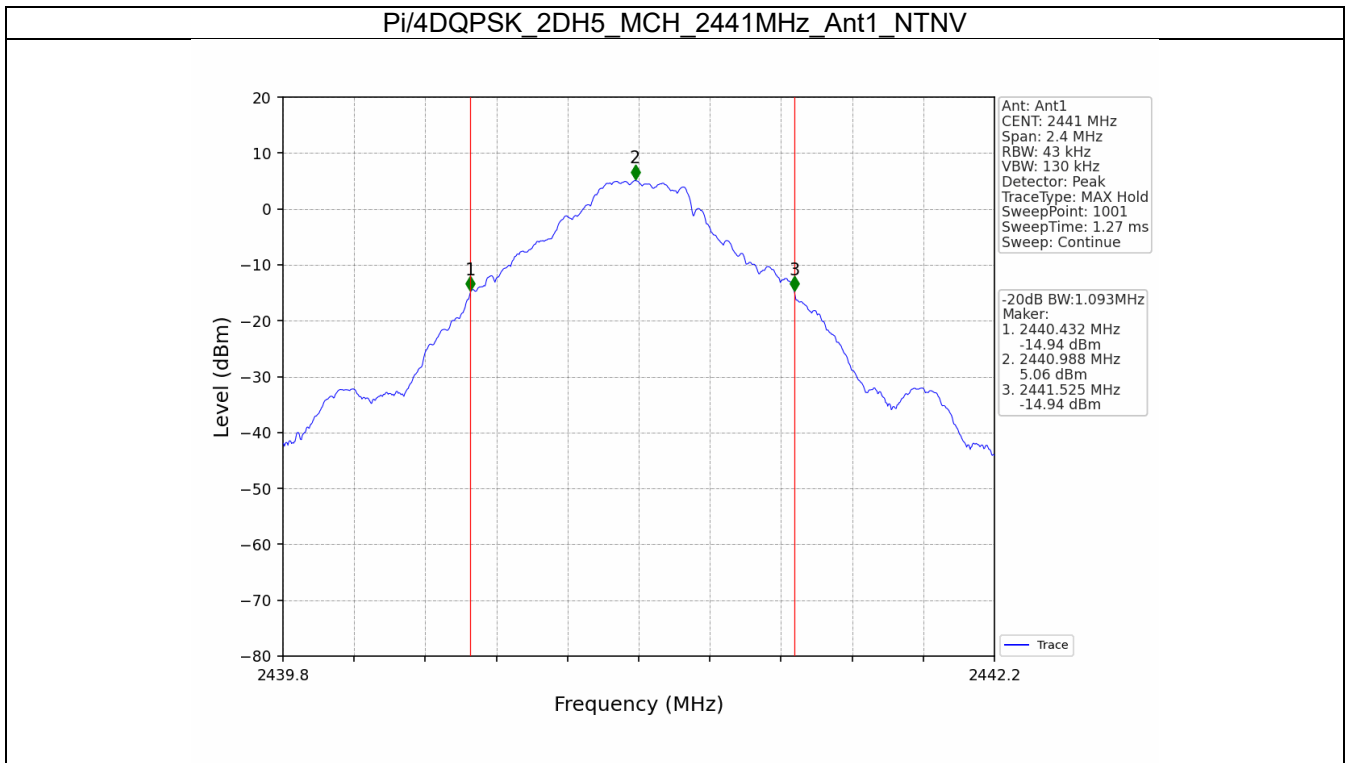
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	20dB Bandwidth (MHz)	Verdict
					Result	
GFSK	SISO	2402	DH5	1	1.017	Pass
		2441	DH5	1	1.017	Pass
		2480	DH5	1	1.023	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	1.089	Pass
		2441	2DH5	1	1.093	Pass
		2480	2DH5	1	1.099	Pass
8DPSK	SISO	2402	3DH5	1	1.047	Pass
		2441	3DH5	1	1.050	Pass
		2480	3DH5	1	1.053	Pass

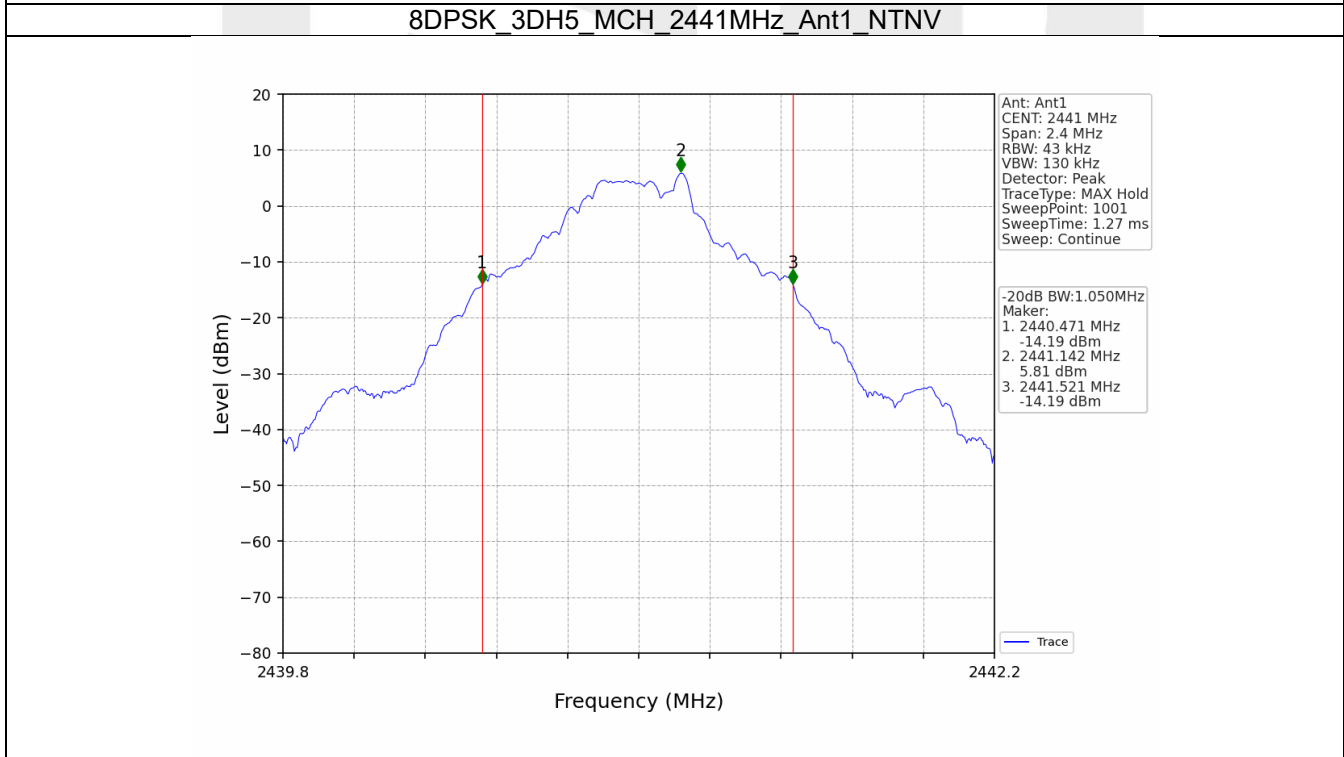
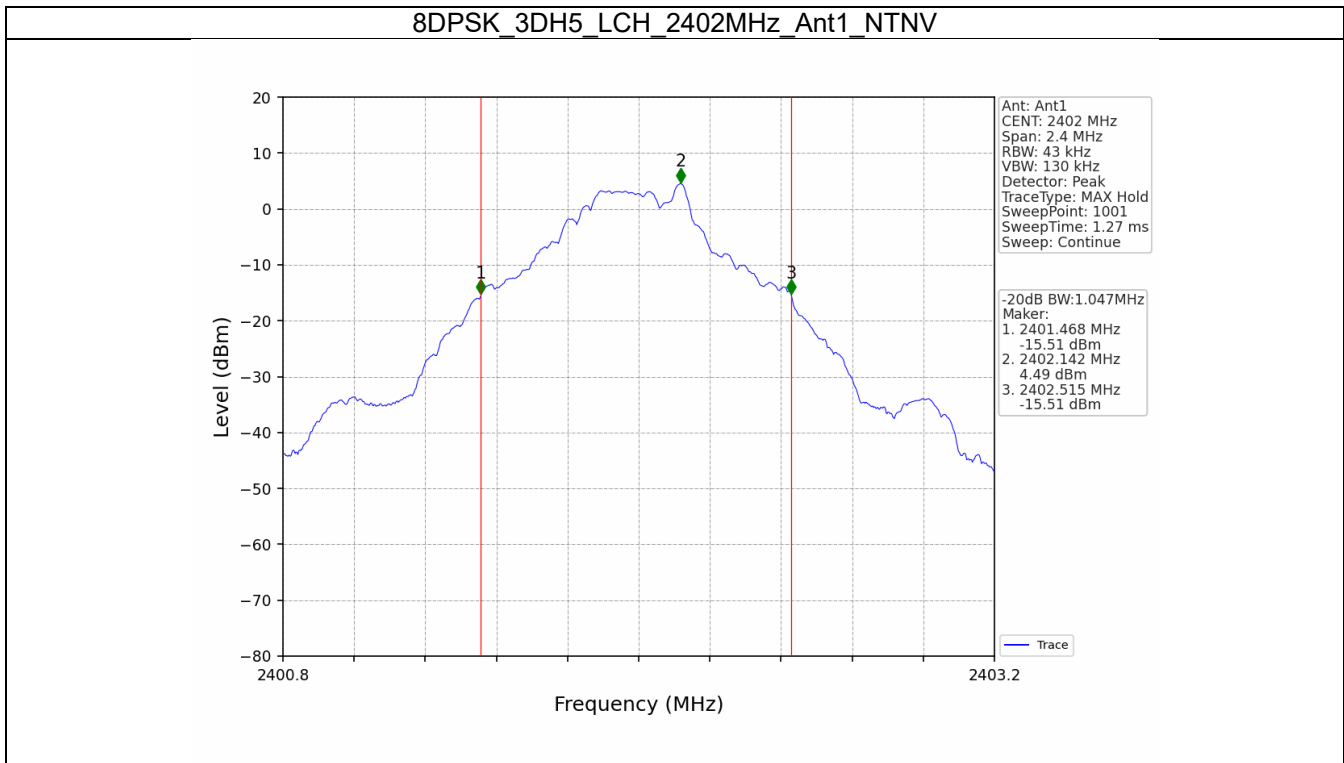
2.2.2 Test Graph

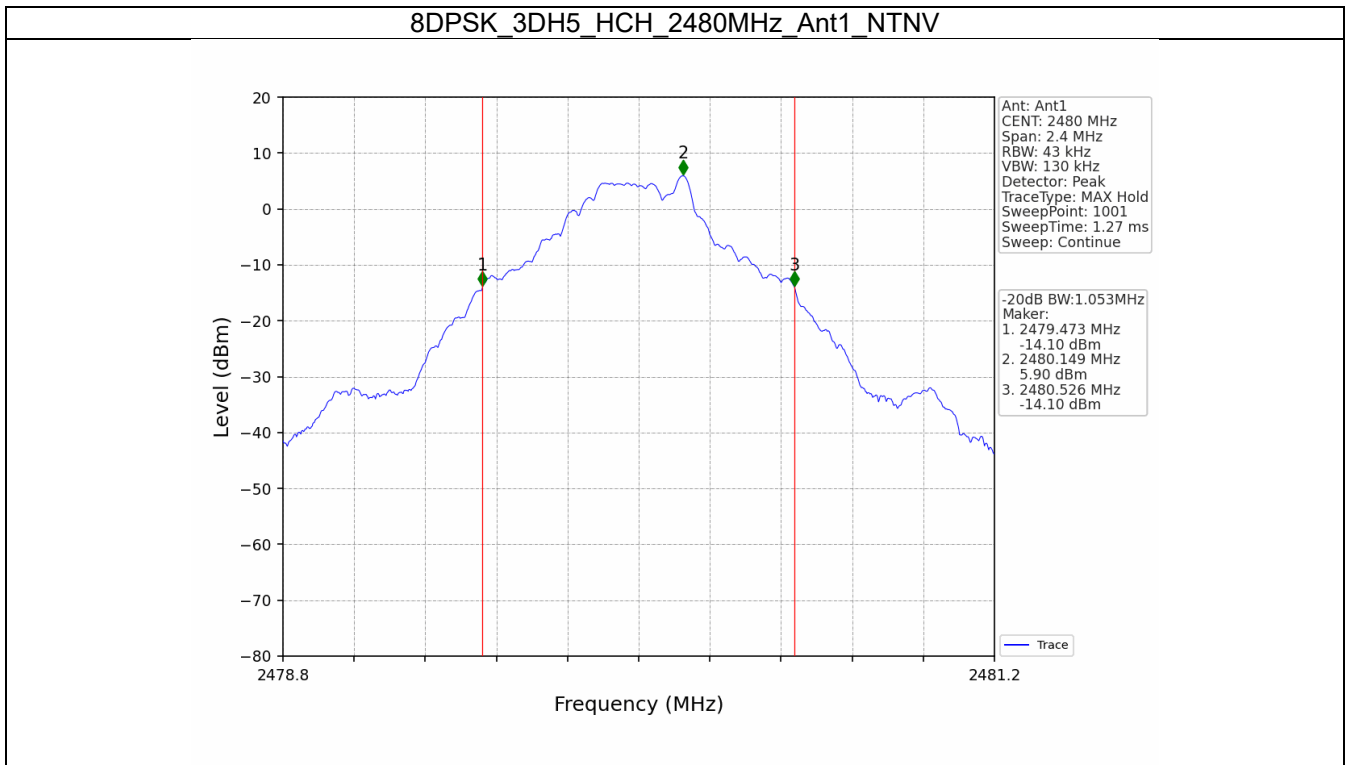












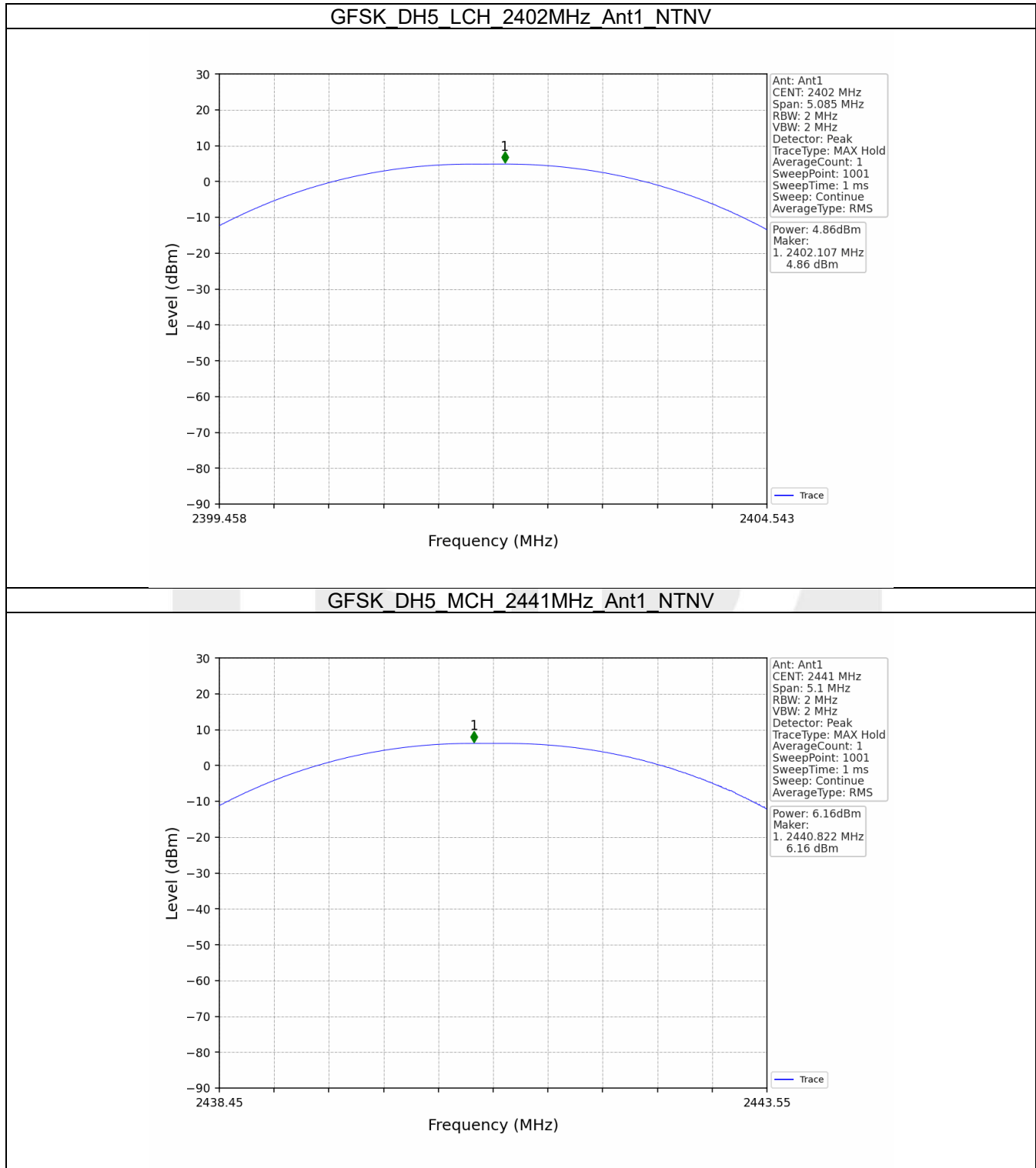
### 3. Maximum Conducted Output Power

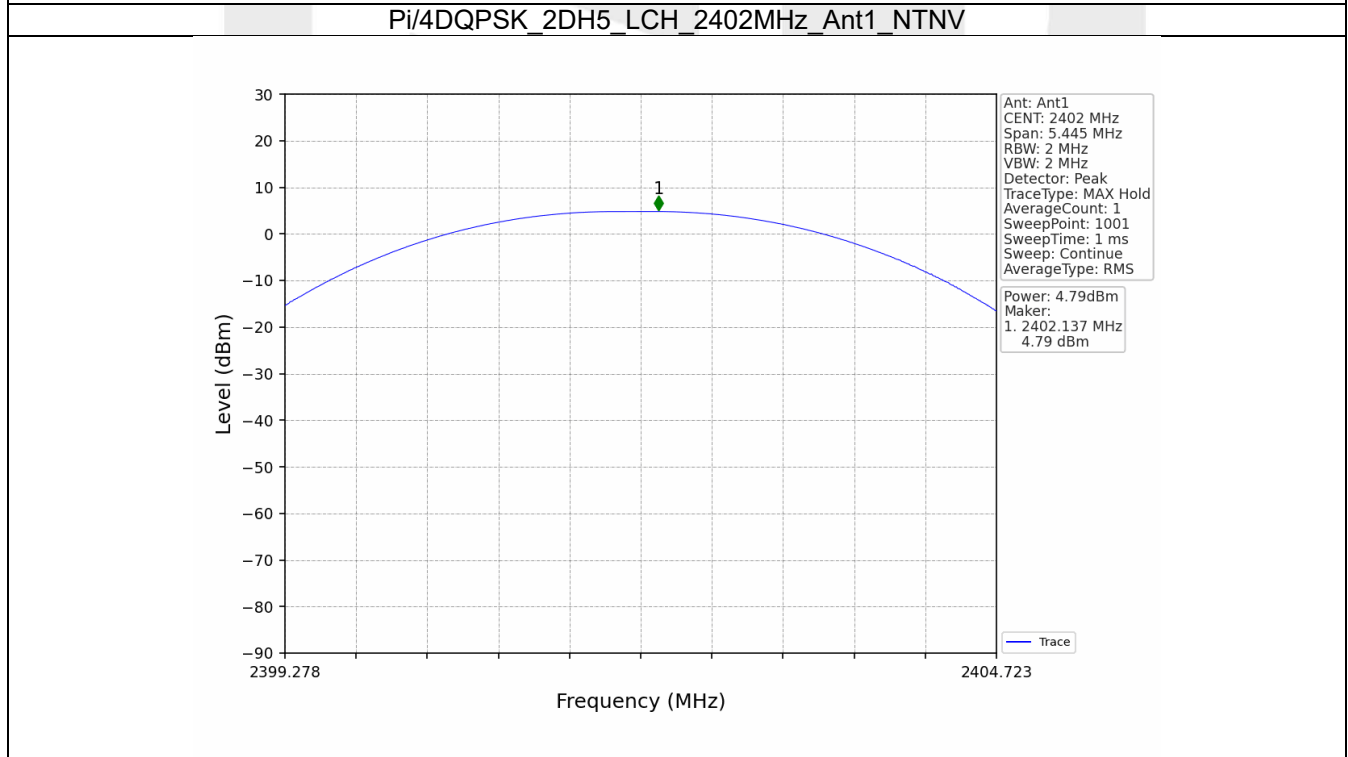
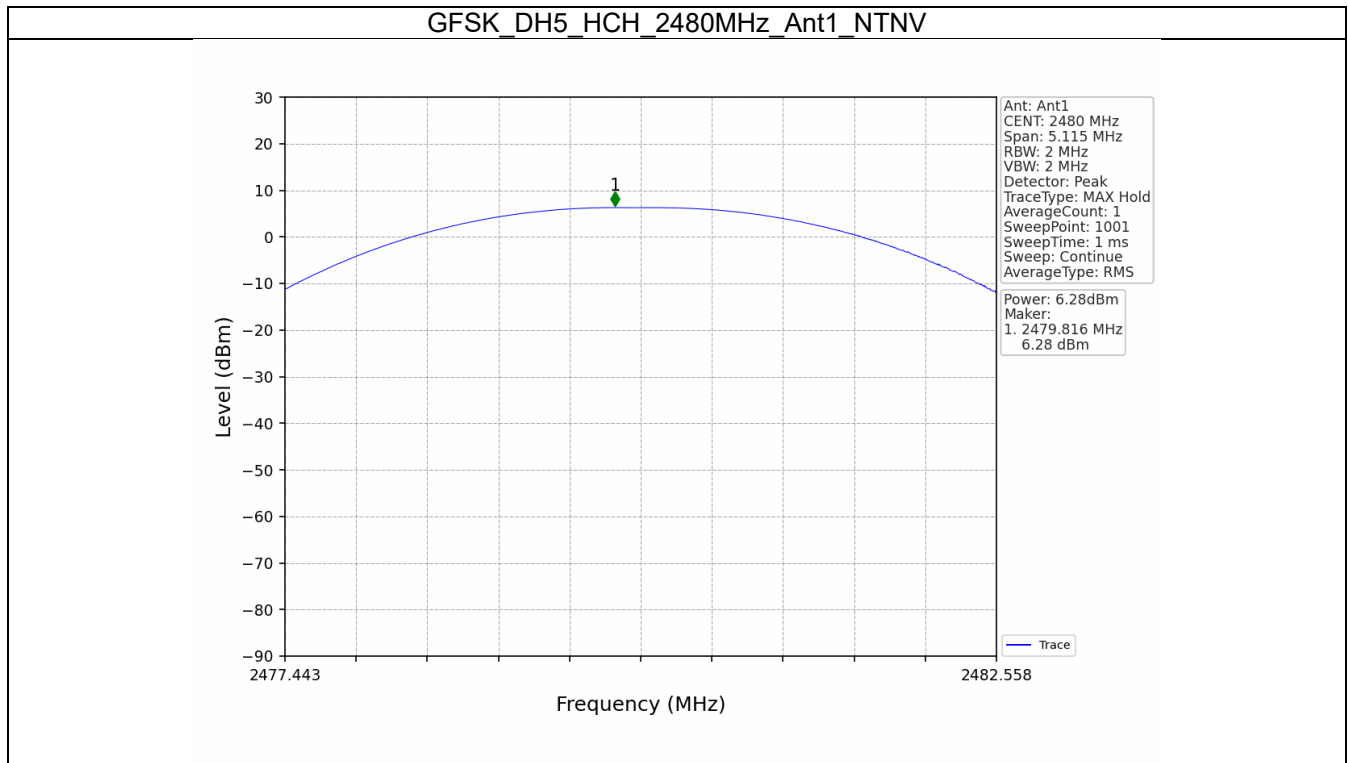
#### 3.1 Power

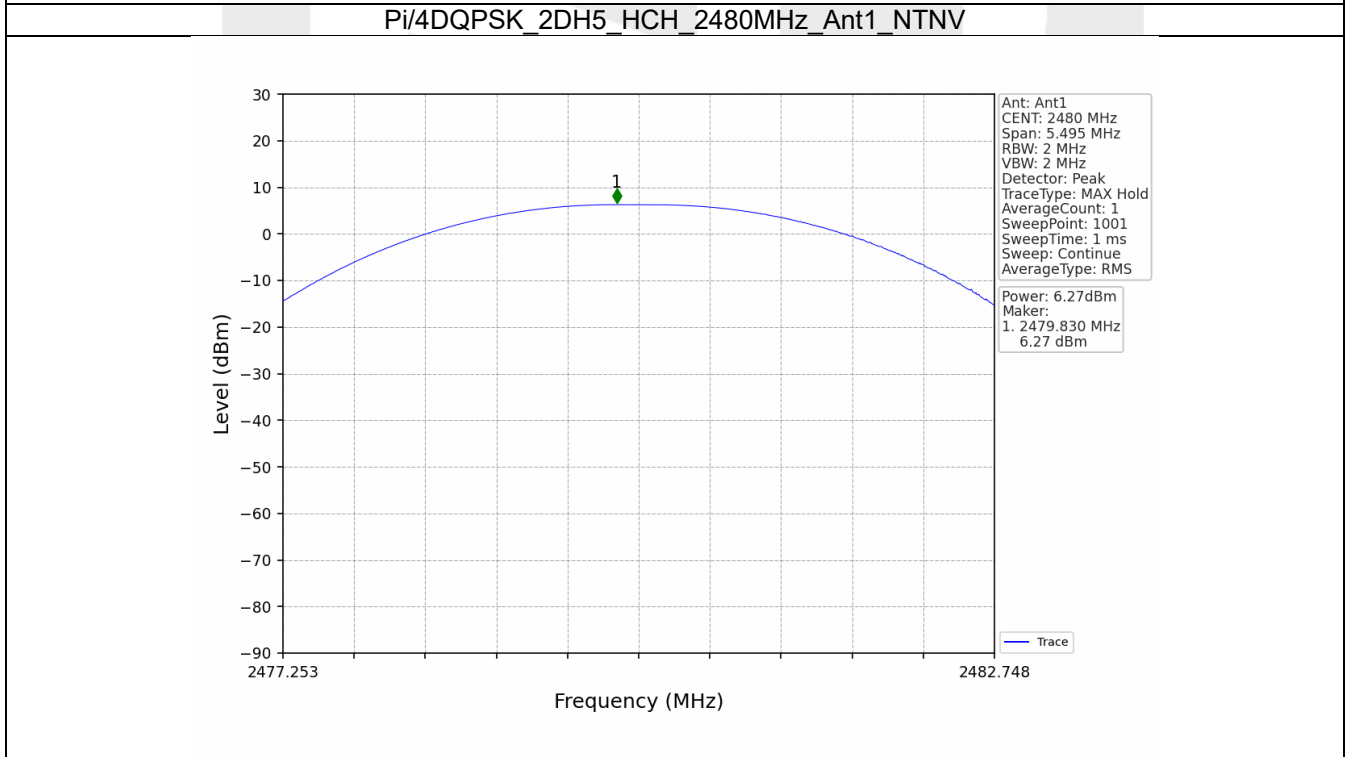
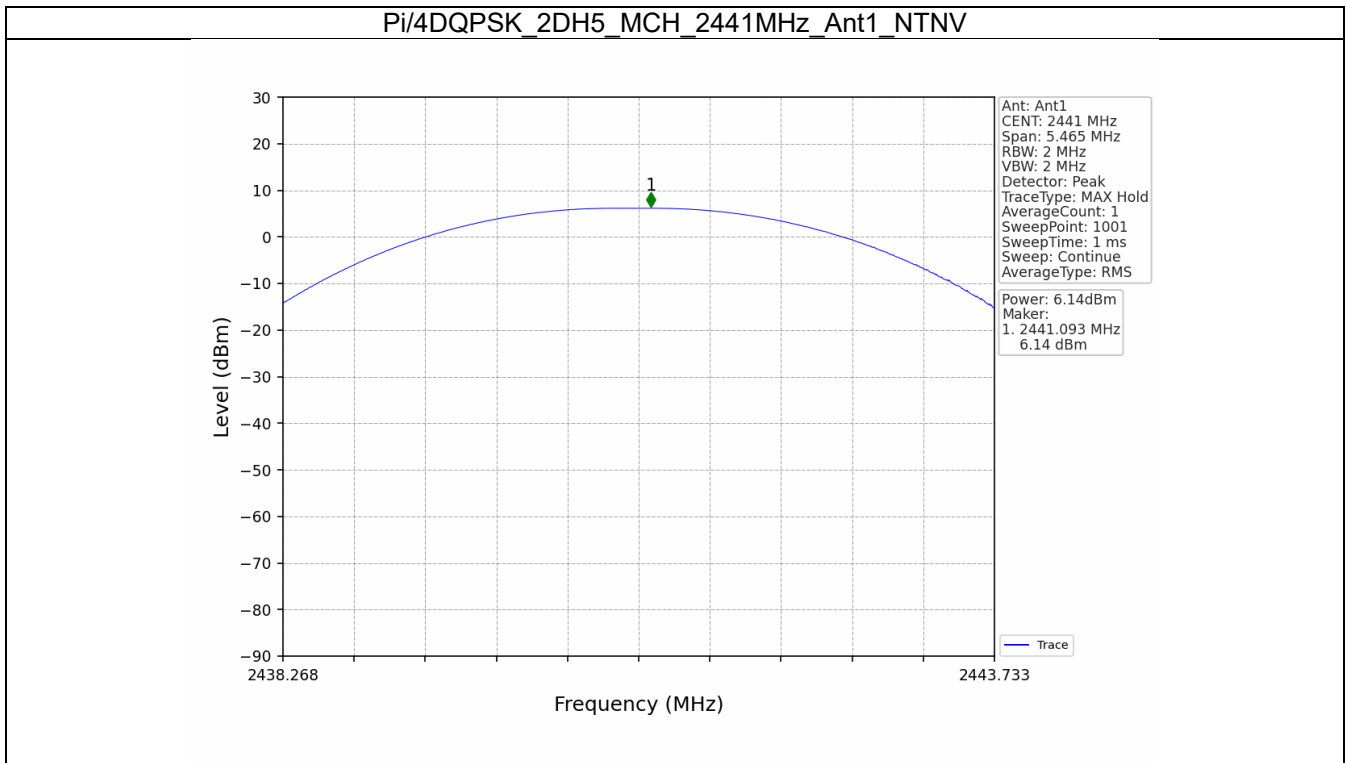
##### 3.1.1 Test Result

Mode	TX Type	Frequency (MHz)	Packet Type	Maximum Peak Conducted Output Power (dBm)		Verdict
				ANT1	Limit	
GFSK	SISO	2402	DH5	4.86	<=20.97	Pass
		2441	DH5	6.16	<=20.97	Pass
		2480	DH5	6.28	<=20.97	Pass
Pi/4DQPSK	SISO	2402	2DH5	4.79	<=20.97	Pass
		2441	2DH5	6.14	<=20.97	Pass
		2480	2DH5	6.27	<=20.97	Pass
8DPSK	SISO	2402	3DH5	4.79	<=20.97	Pass
		2441	3DH5	6.11	<=20.97	Pass
		2480	3DH5	6.23	<=20.97	Pass

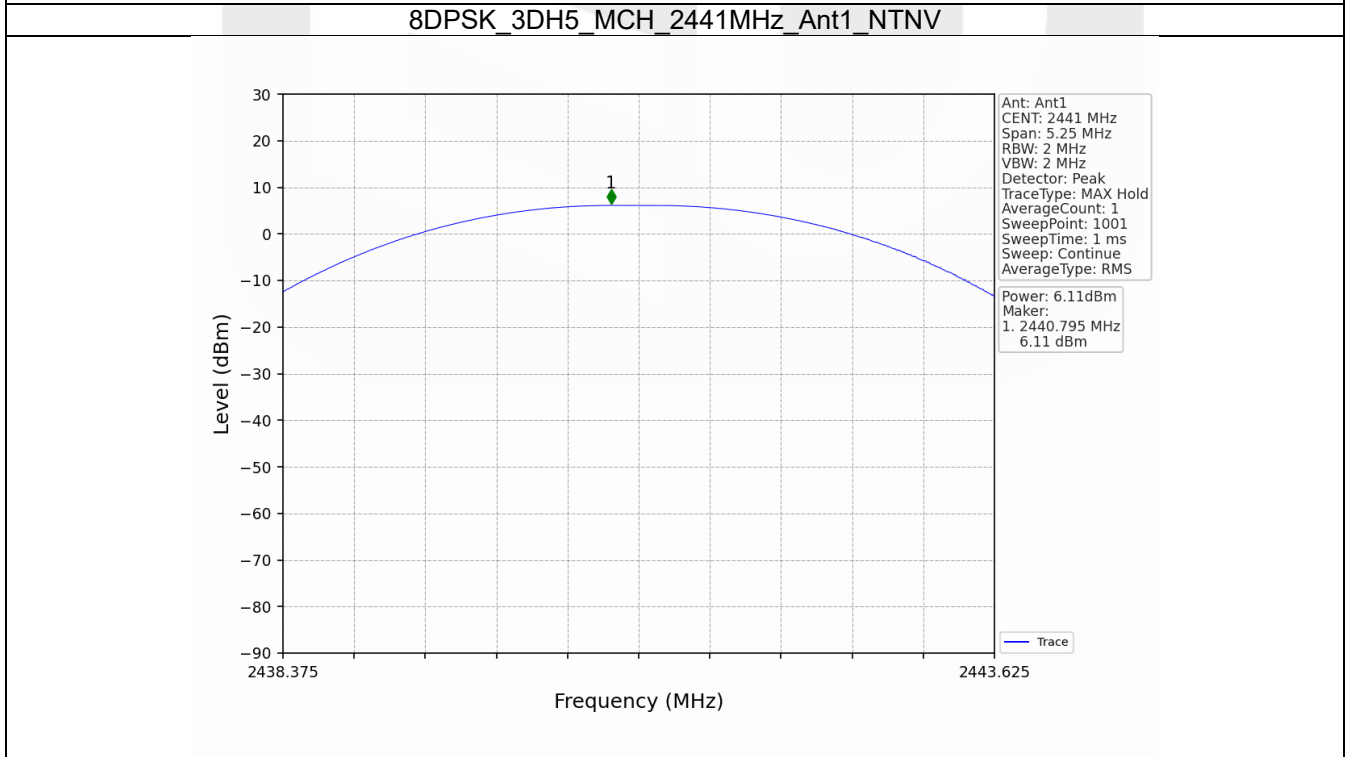
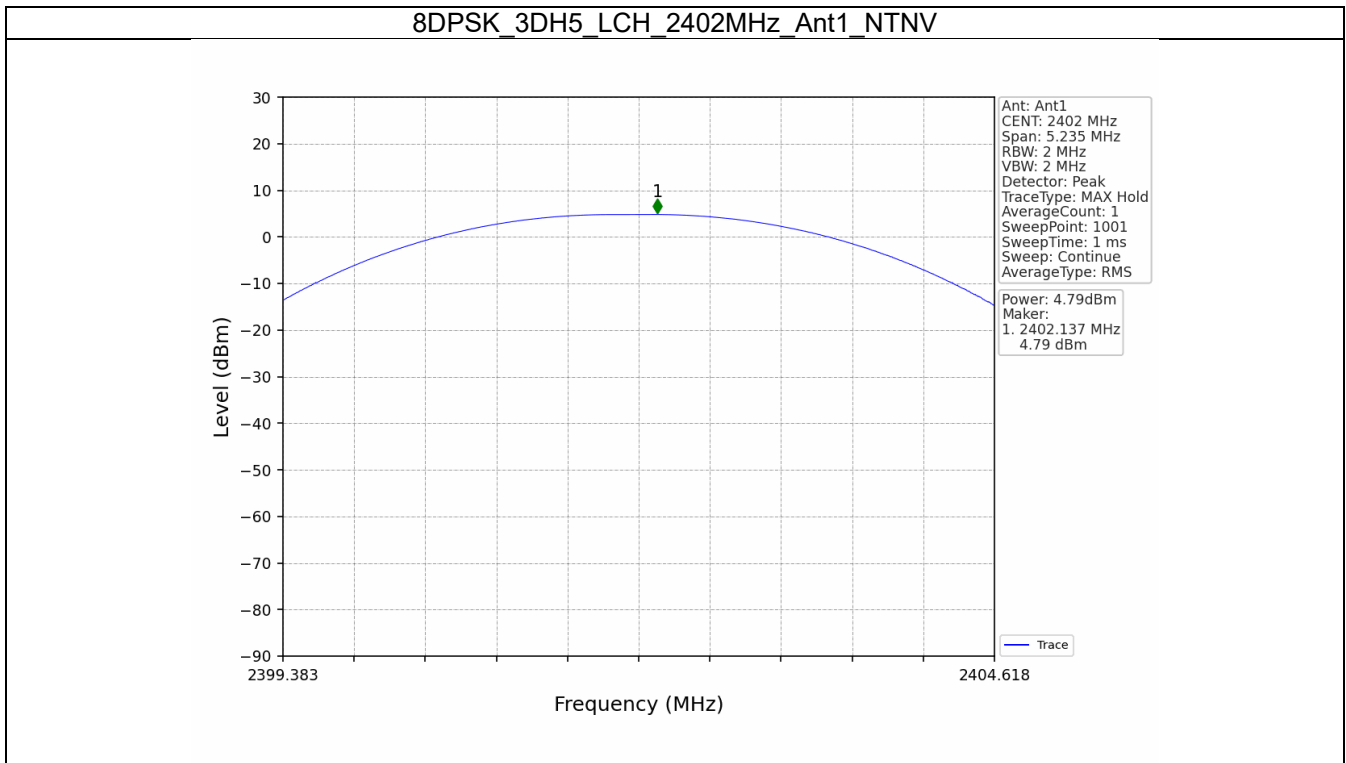
3.1.2 Test Graph

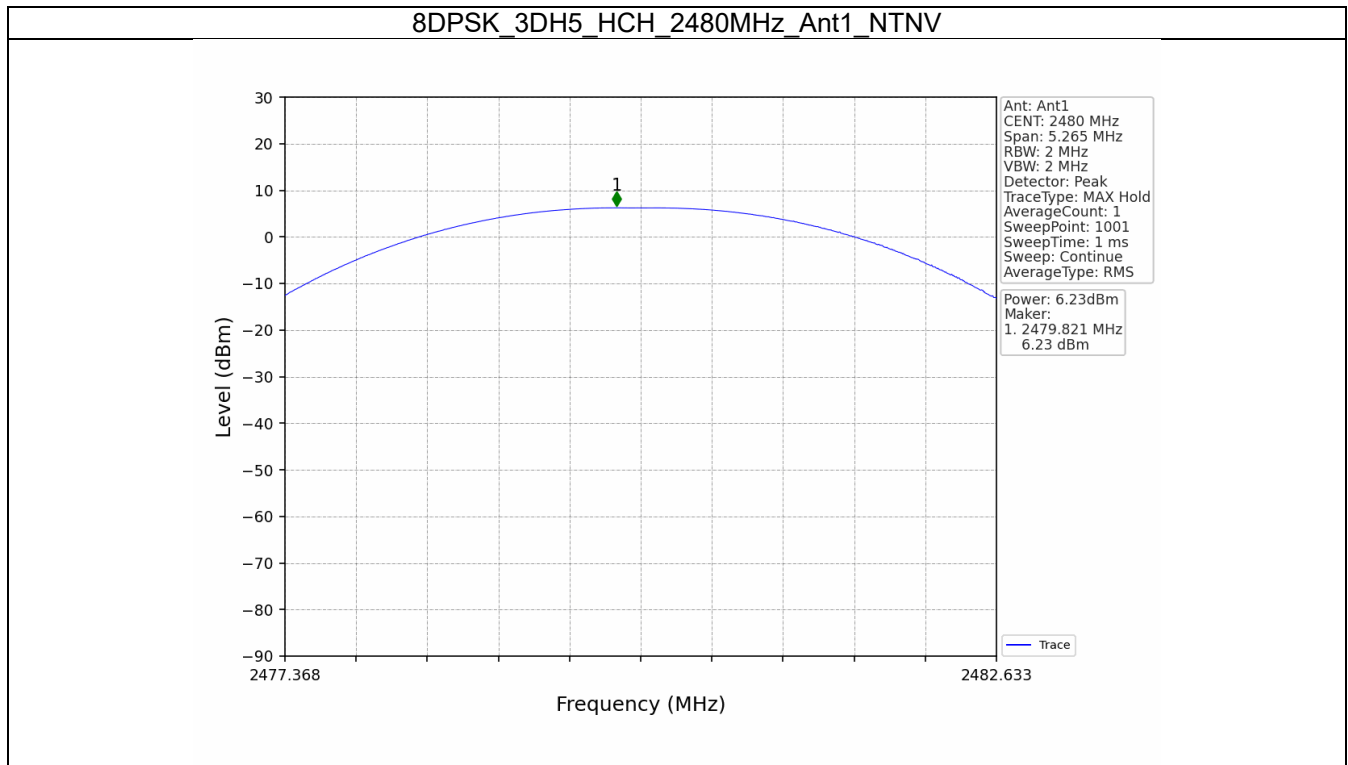












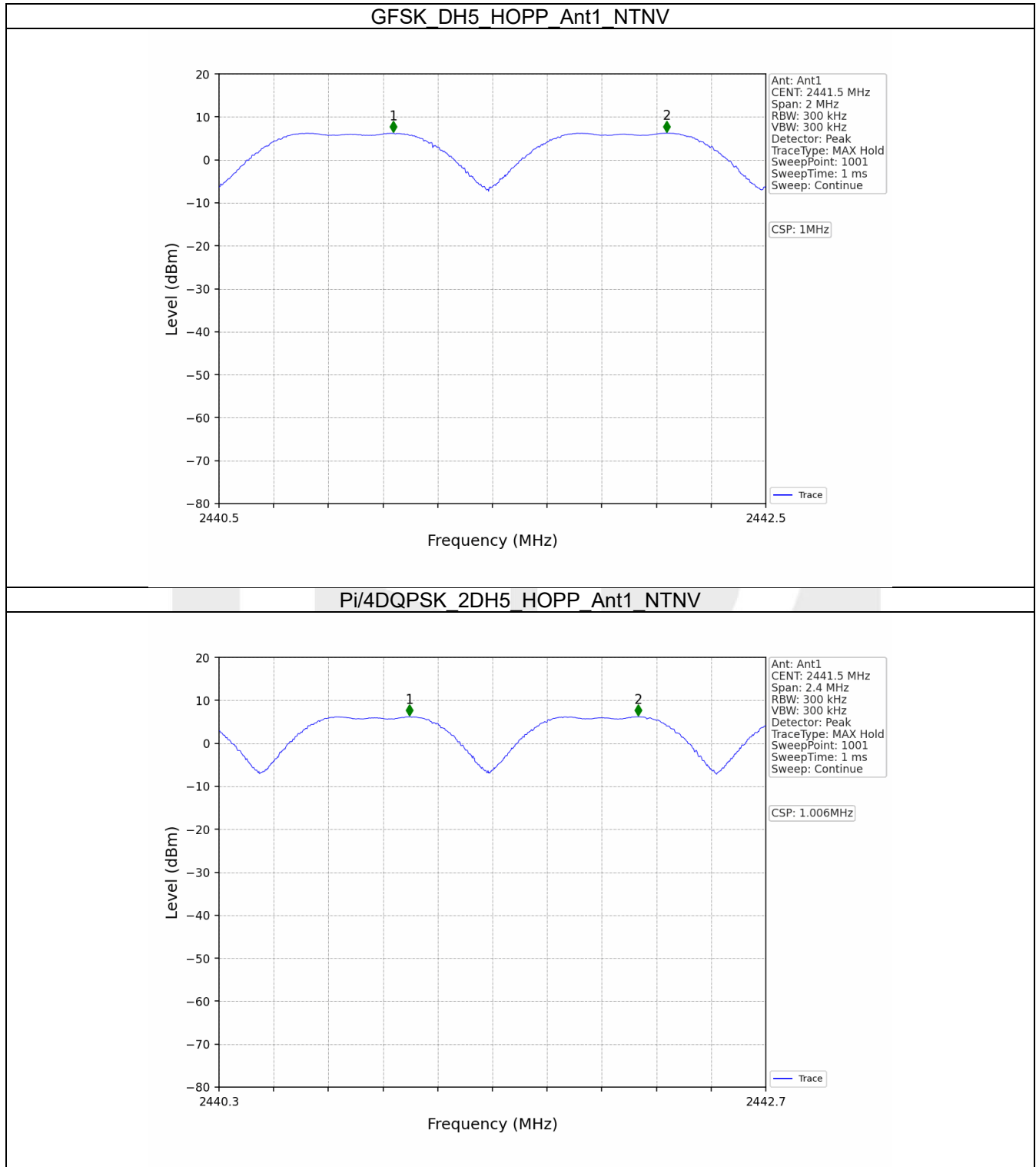
#### 4. Carrier Frequency Separation

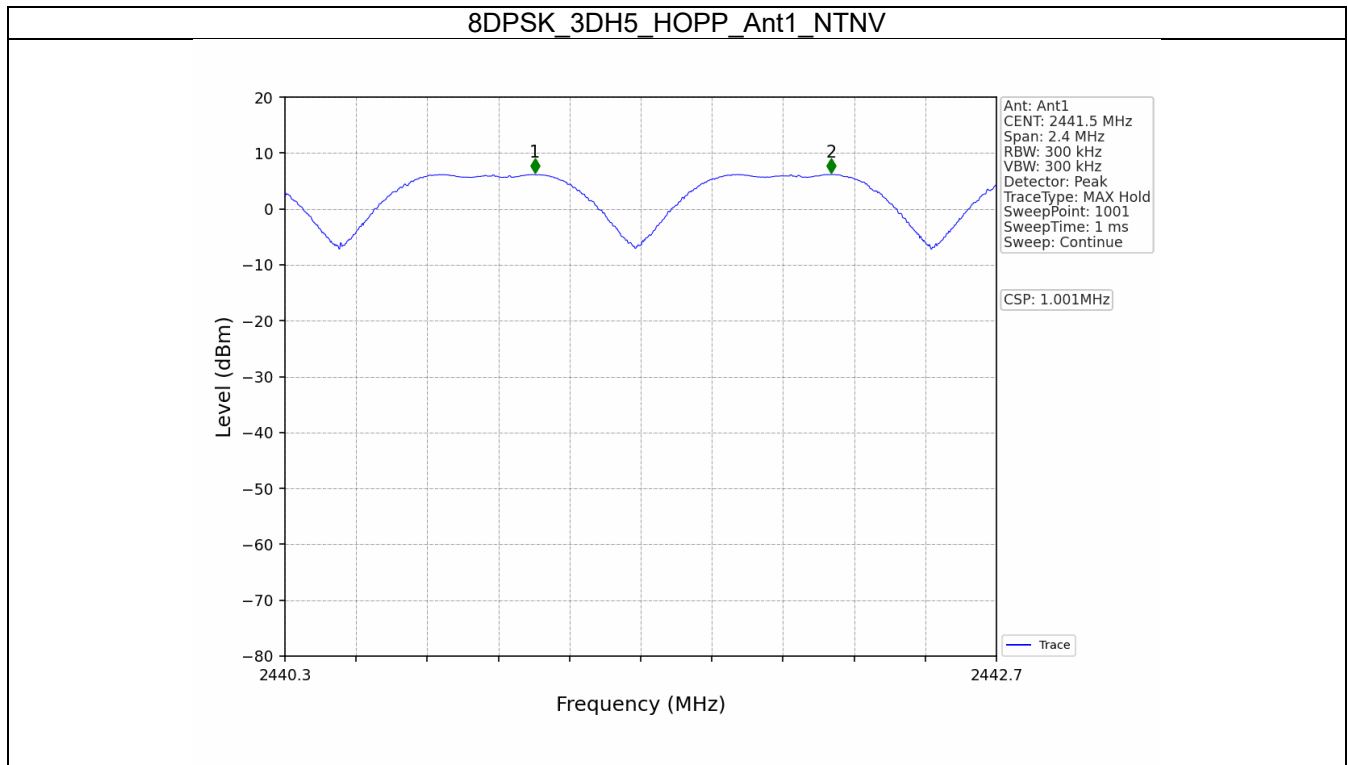
##### 4.1 Ant1

##### 4.1.1 Test Result

Ant1							
Mode	TX Type	Frequency (MHz)	Packet Type	Channel Separation (MHz)	20dB Bandwidth (MHz)	Limit (MHz)	Verdict
GFSK	SISO	HOPP	DH5	1.000	1.023	$\geq 0.682$	Pass
Pi/4DQPSK	SISO	HOPP	2DH5	1.006	1.099	$\geq 0.733$	Pass
8DPSK	SISO	HOPP	3DH5	1.001	1.053	$\geq 0.702$	Pass

4.1.2 Test Graph





## 5. Number of Hopping Frequencies

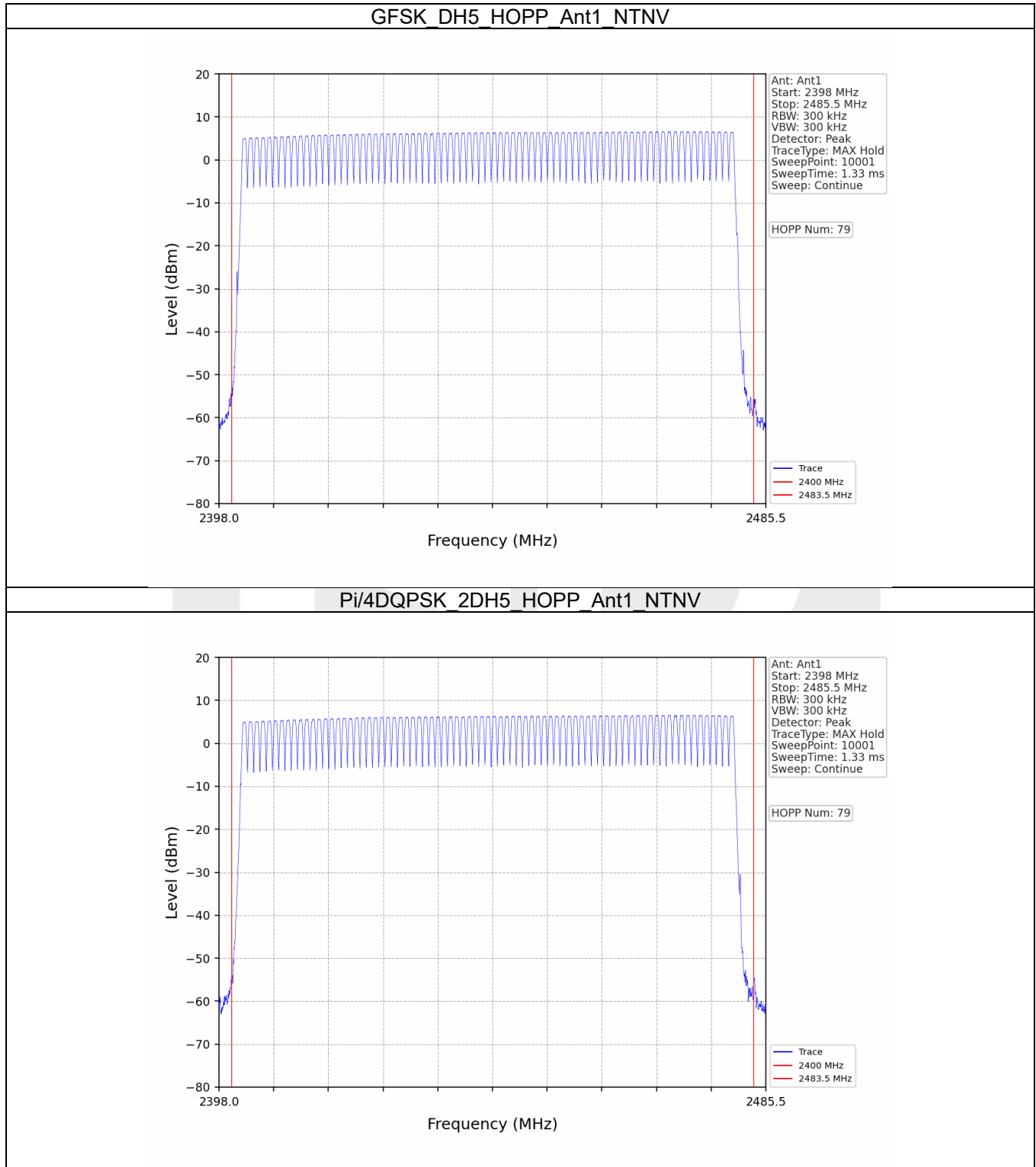
### 5.1 HoppNum

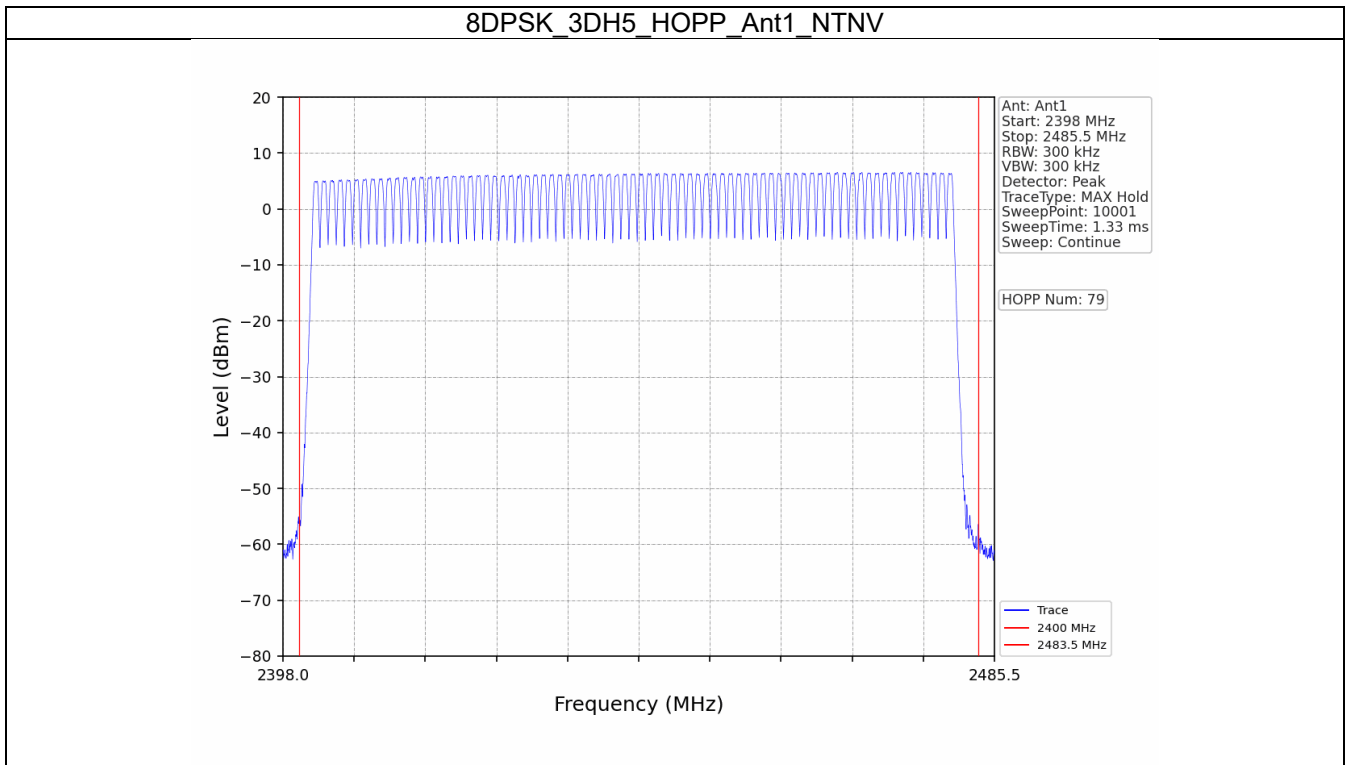
#### 5.1.1 Test Result

Mode	TX Type	Frequency (MHz)	Packet Type	Num of Hopping Frequencies		Verdict
				ANT1	Limit	
GFSK	SISO	HOPP	DH5	79	>=15	Pass
Pi/4DQPSK	SISO	HOPP	2DH5	79	>=15	Pass
8DPSK	SISO	HOPP	3DH5	79	>=15	Pass



5.1.2 Test Graph







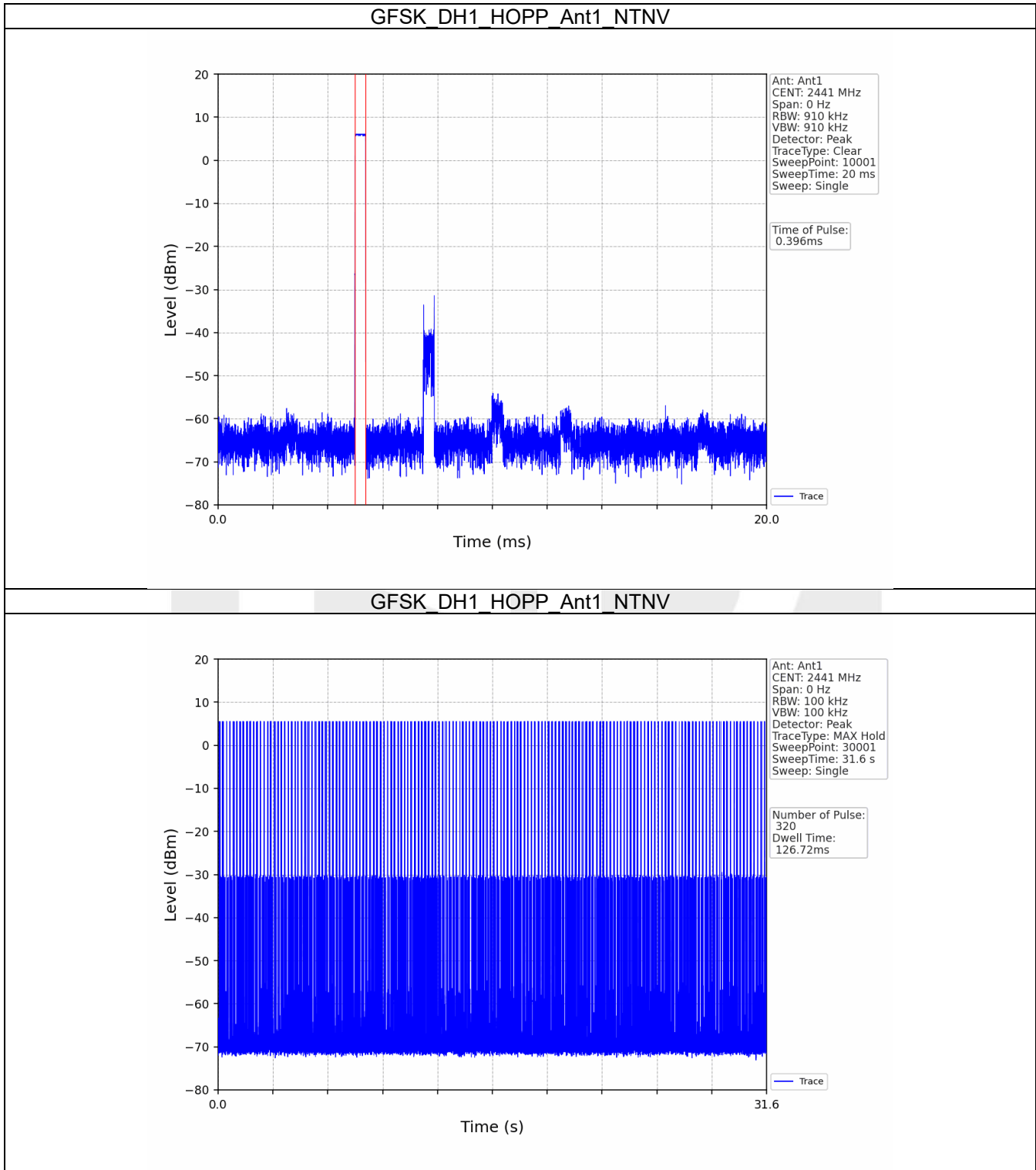
## 6. Time of Occupancy (Dwell Time)

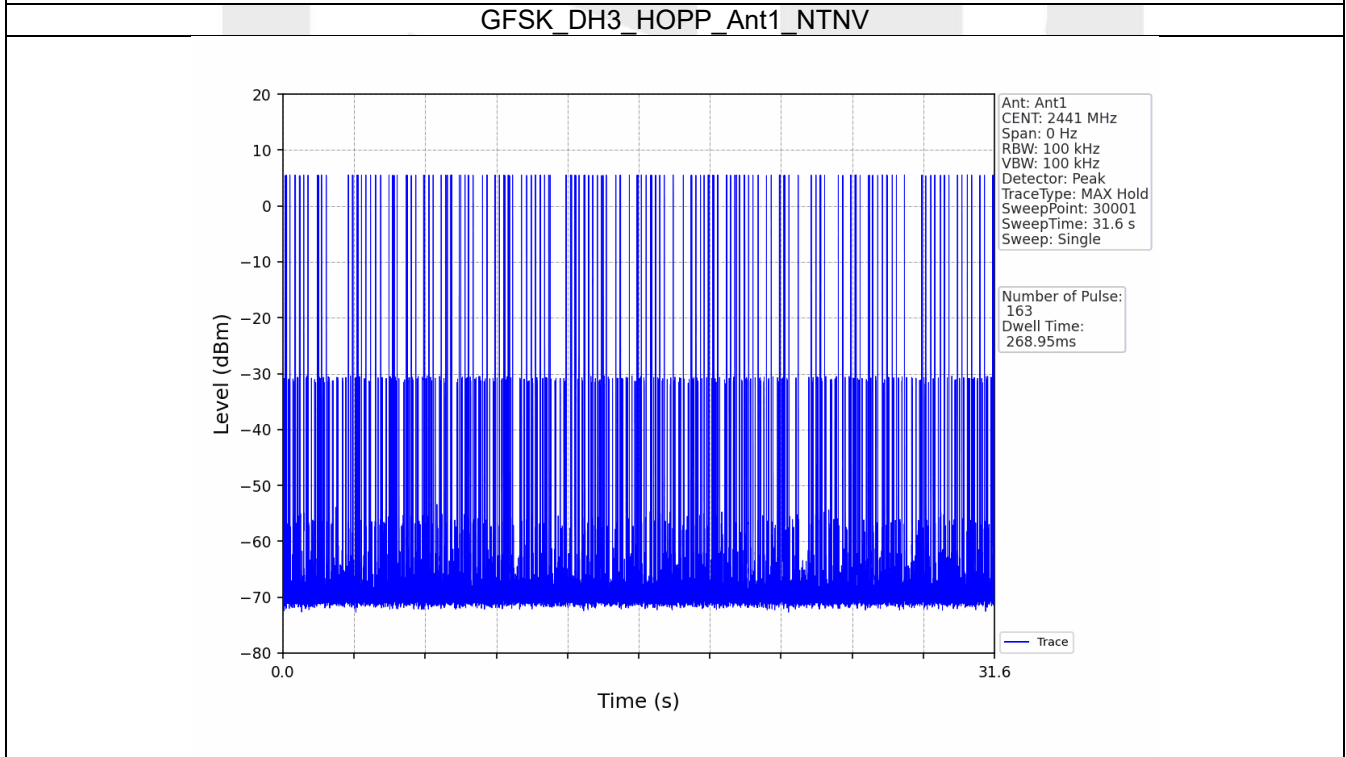
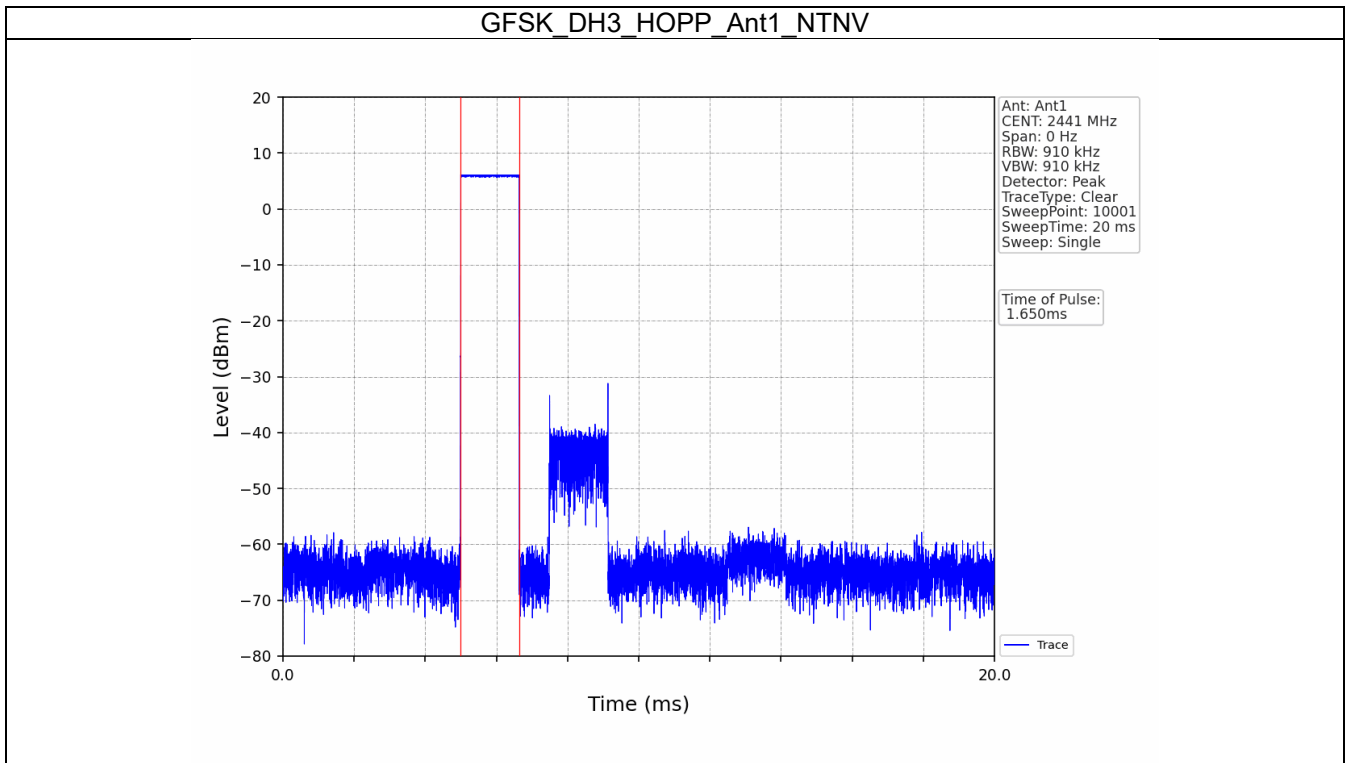
### 6.1 Ant1

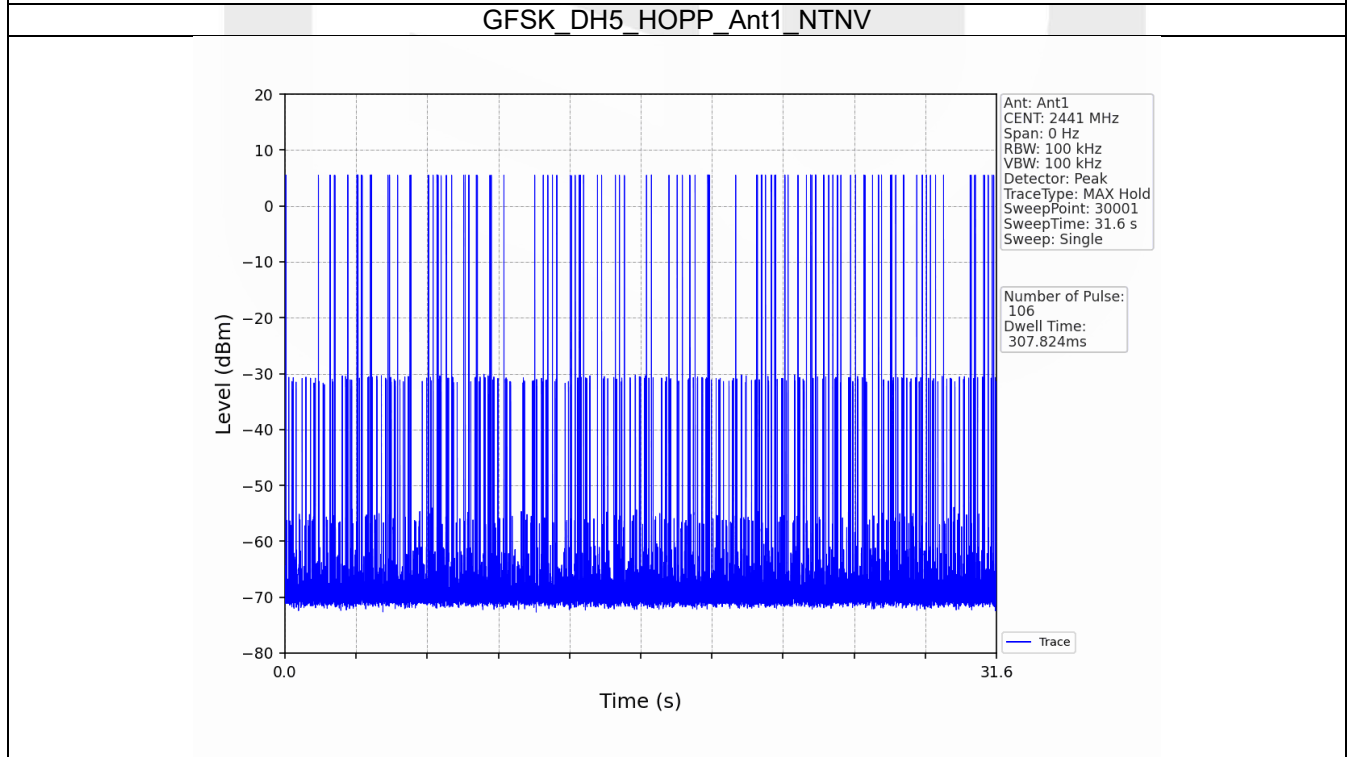
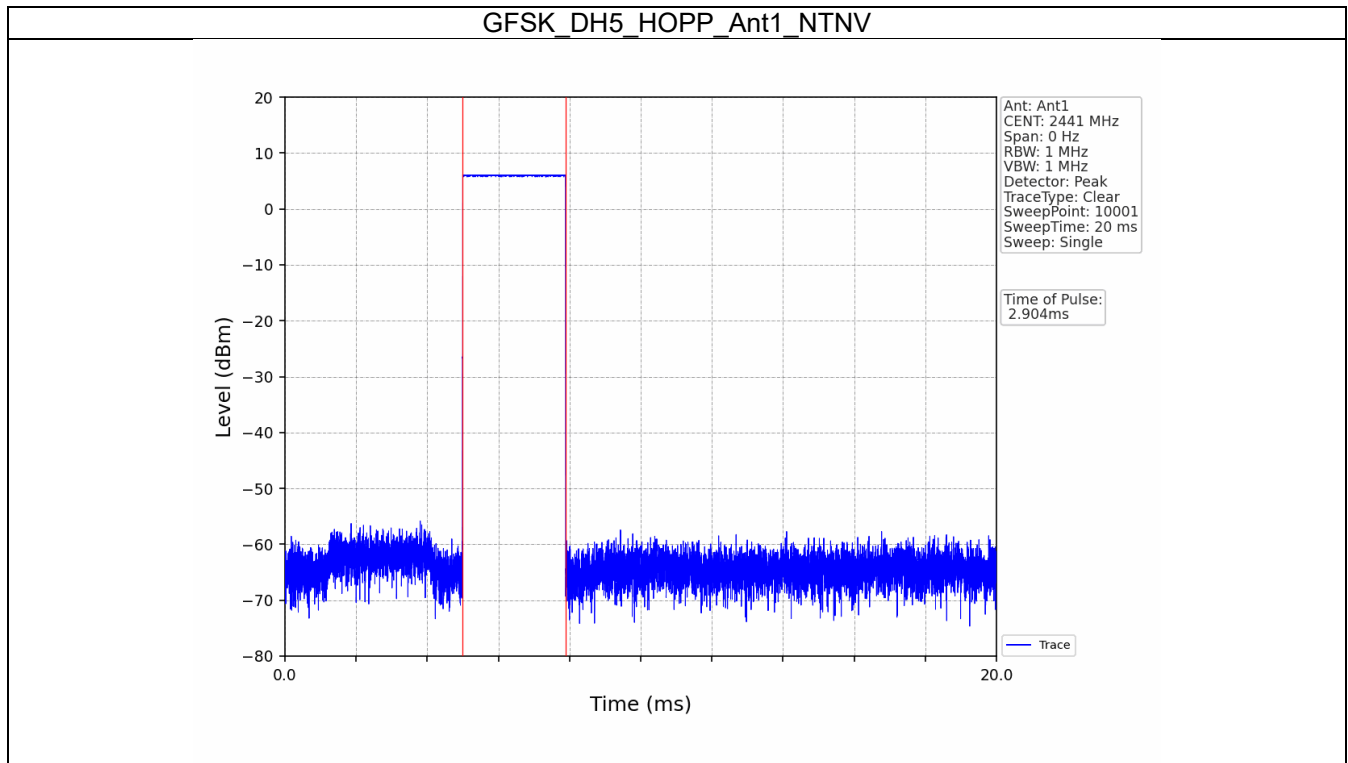
#### 6.1.1 Test Result

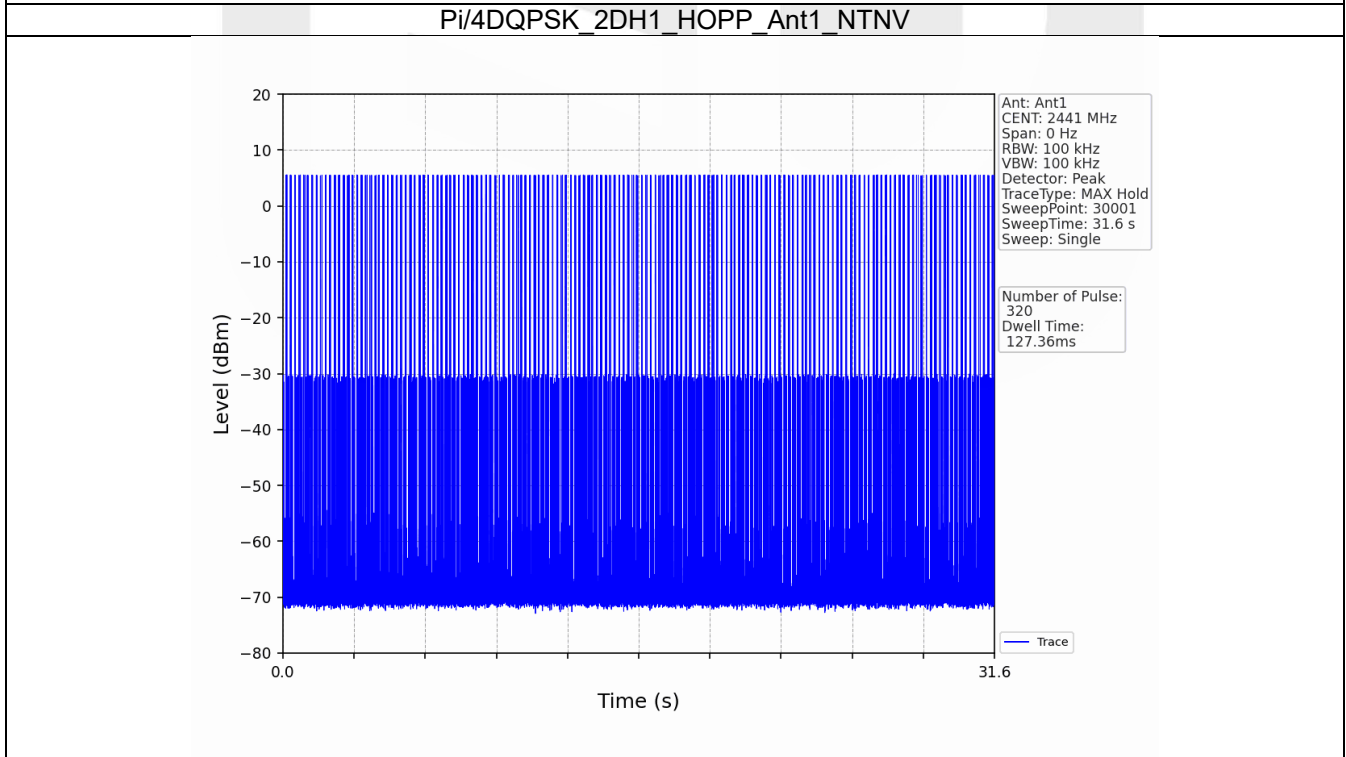
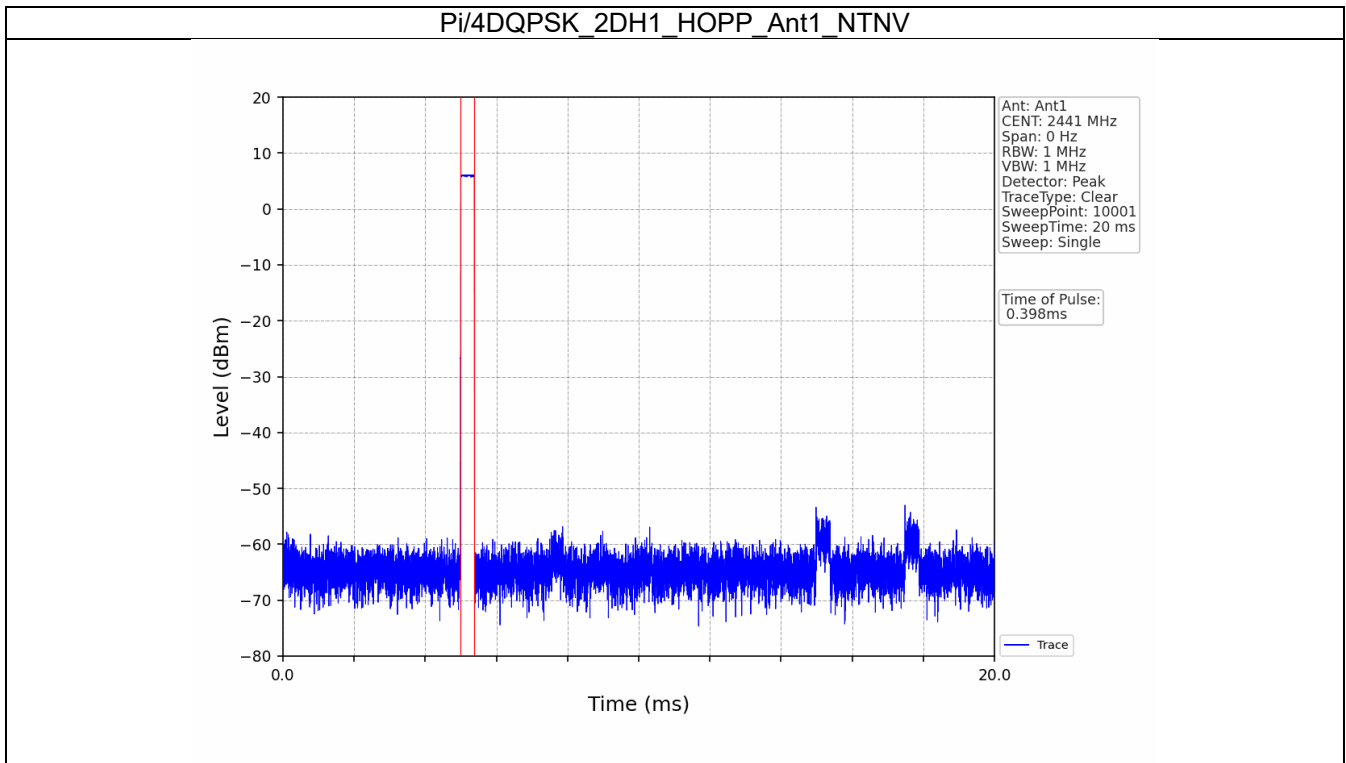
Ant1									
Mode	TX Type	Frequency (MHz)	Packet Type	Duration of Single Pulse (ms)	Observation Period (s)	Num of Pulse in Observation Period	Dwell Time (ms)	Limit (ms)	Verdict
GFSK	SISO	HOPP	DH1	0.396	31.600	320	126.720	<=400	Pass
			DH3	1.650	31.600	163	268.950	<=400	Pass
			DH5	2.904	31.600	106	307.824	<=400	Pass
Pi/4DQPSK	SISO	HOPP	2DH1	0.398	31.600	320	127.360	<=400	Pass
			2DH3	1.648	31.600	157	258.736	<=400	Pass
			2DH5	2.906	31.600	90	261.540	<=400	Pass
8DPSK	SISO	HOPP	3DH1	2.902	31.600	95	275.690	<=400	Pass
			3DH3	1.654	31.600	163	269.602	<=400	Pass
			3DH5	2.902	31.600	109	316.318	<=400	Pass

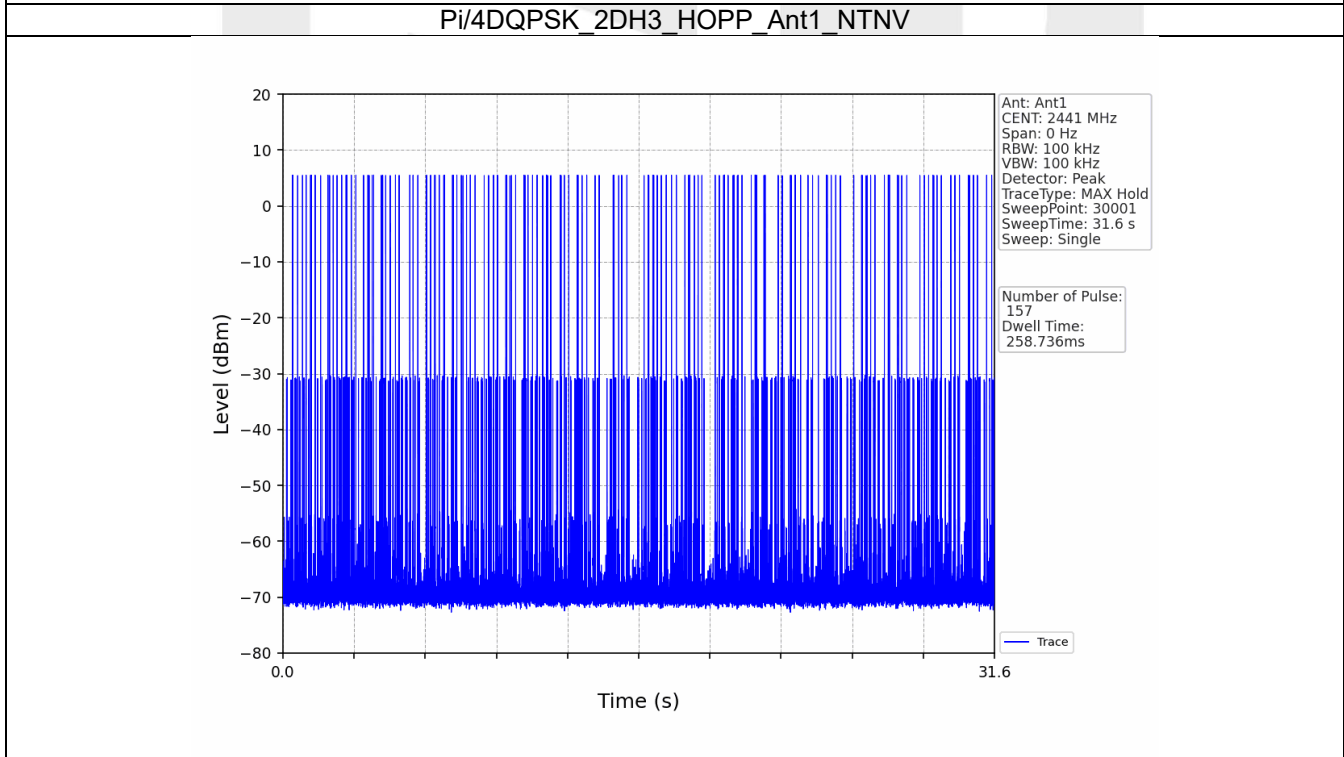
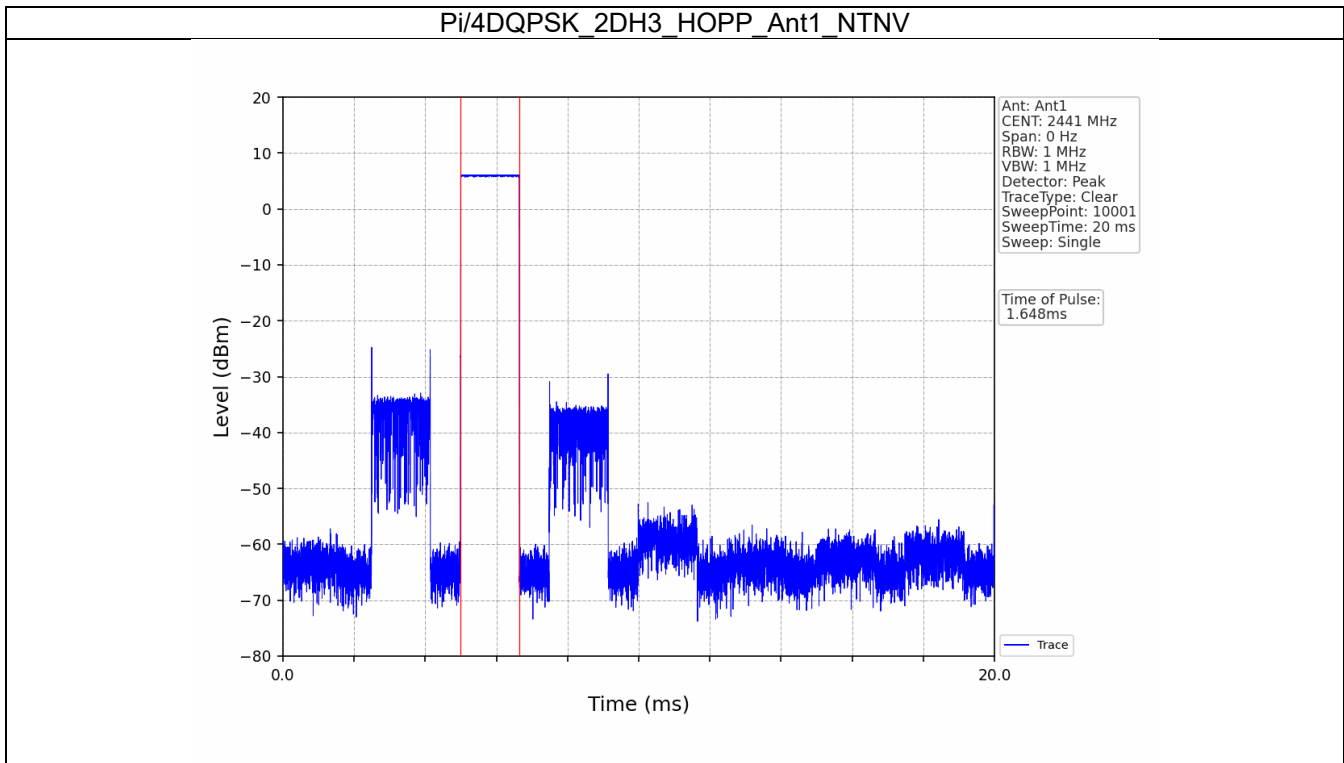
6.1.2 Test Graph

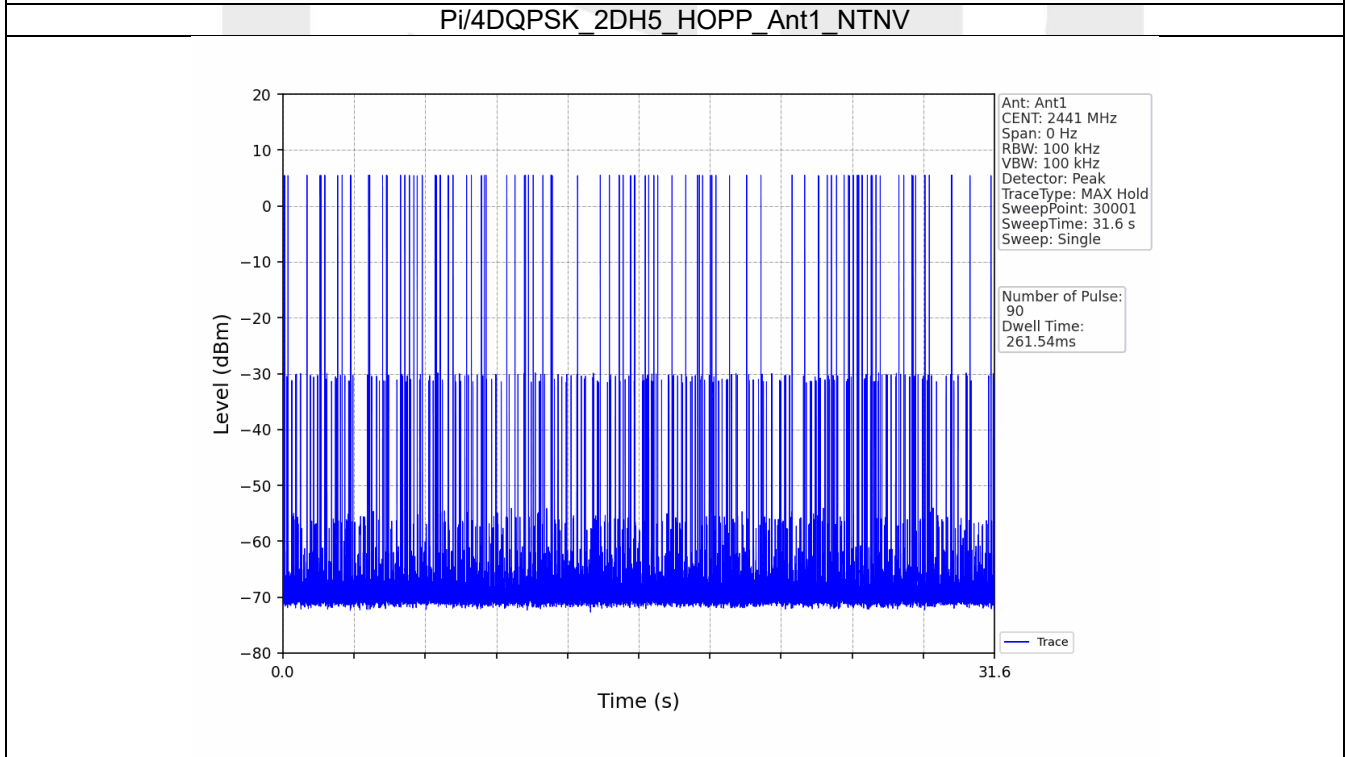
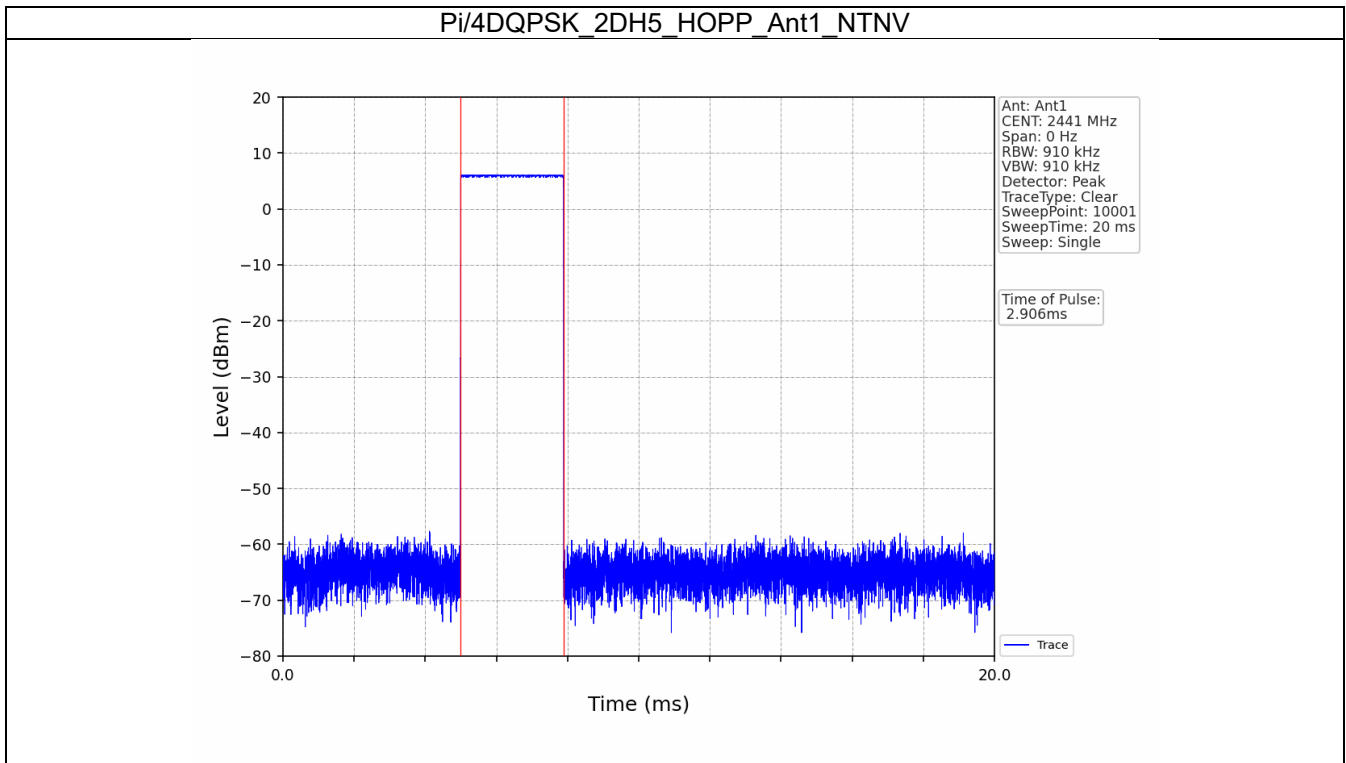


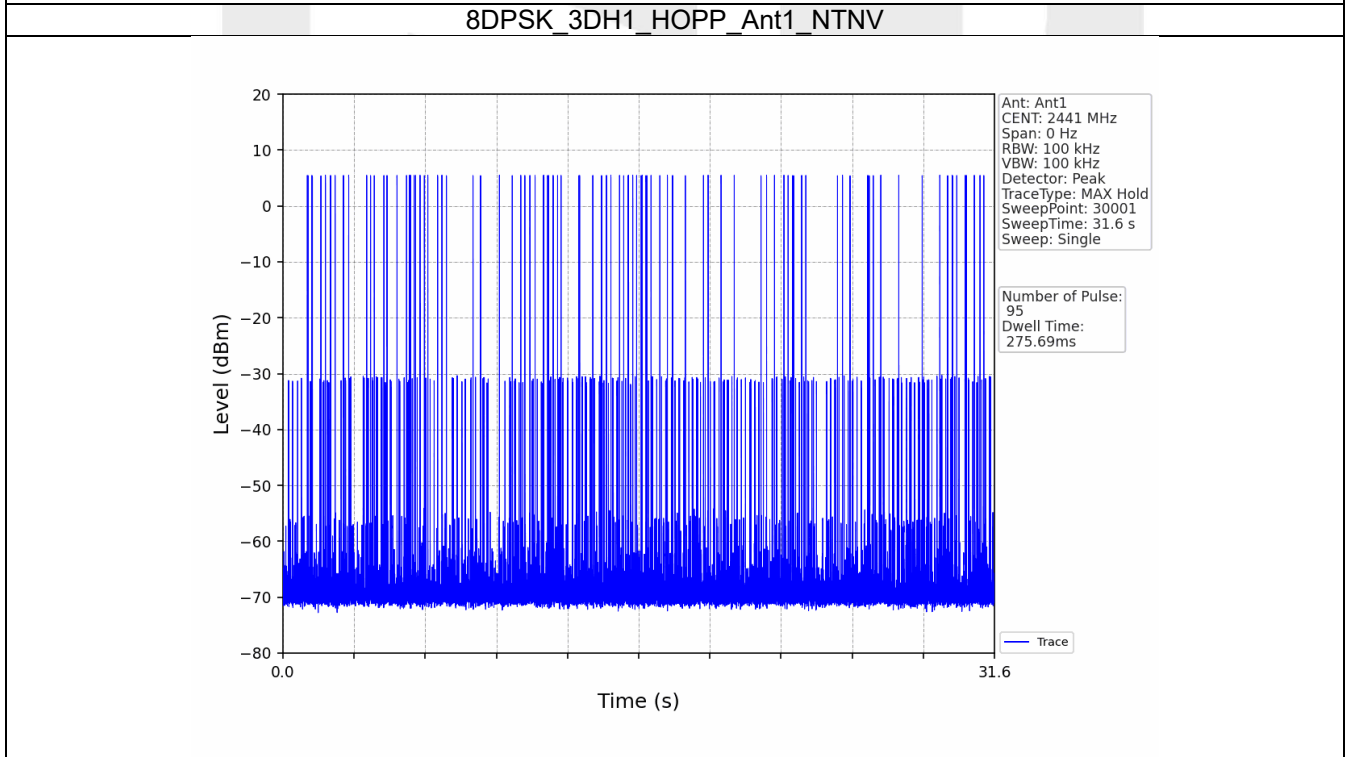
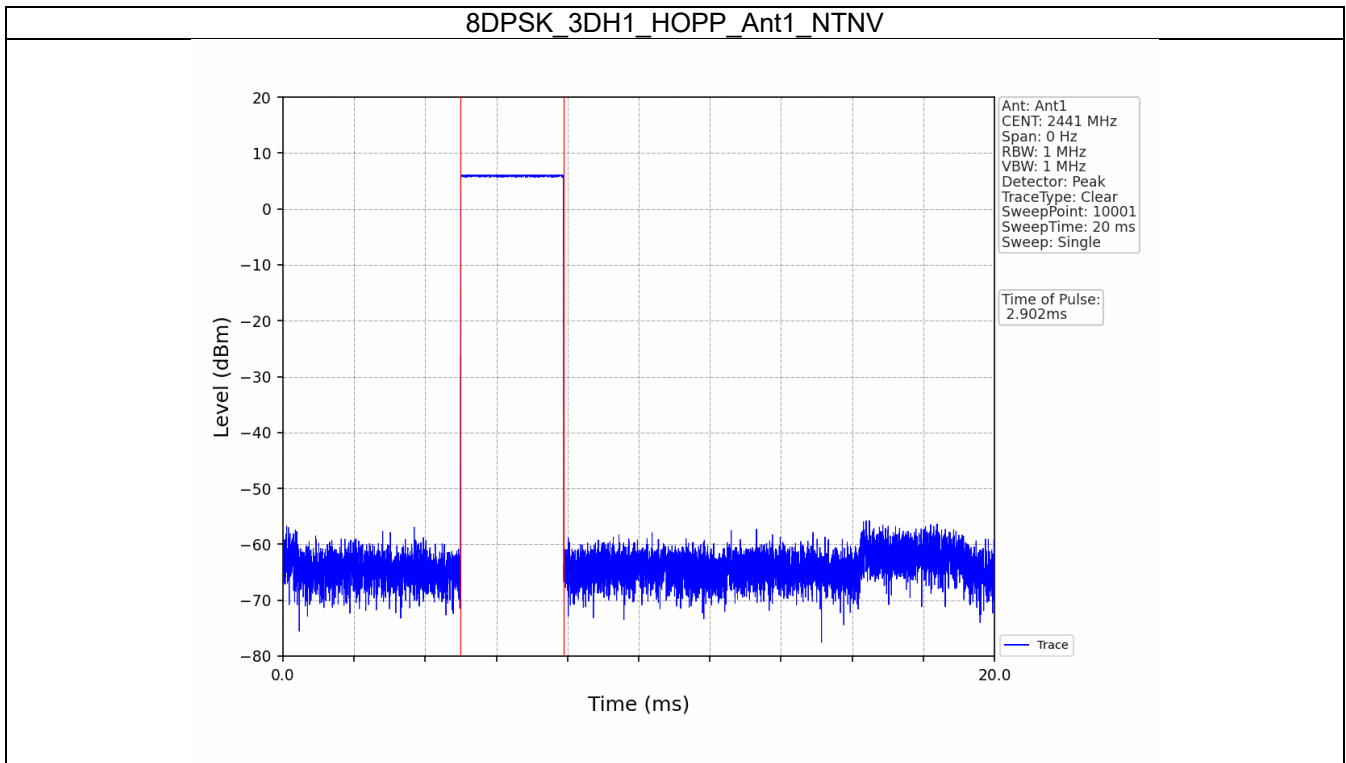




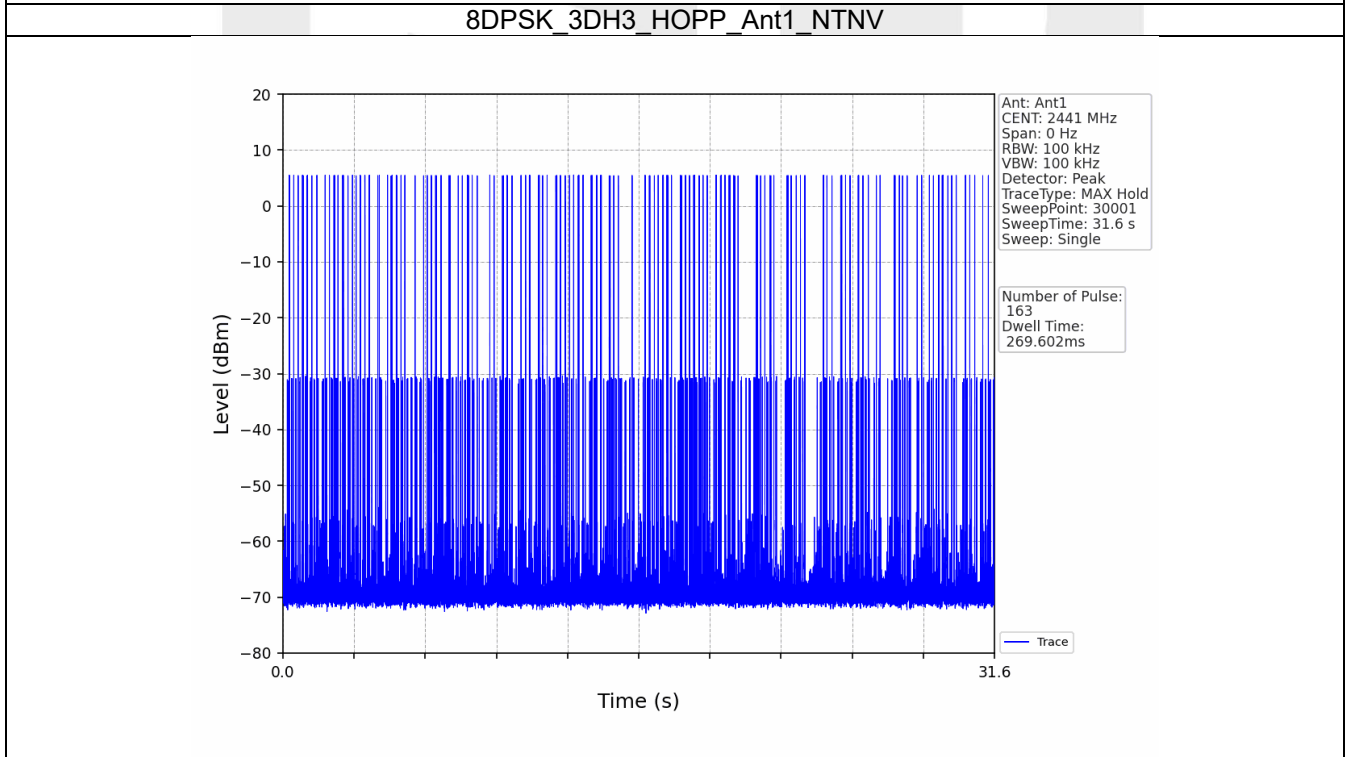
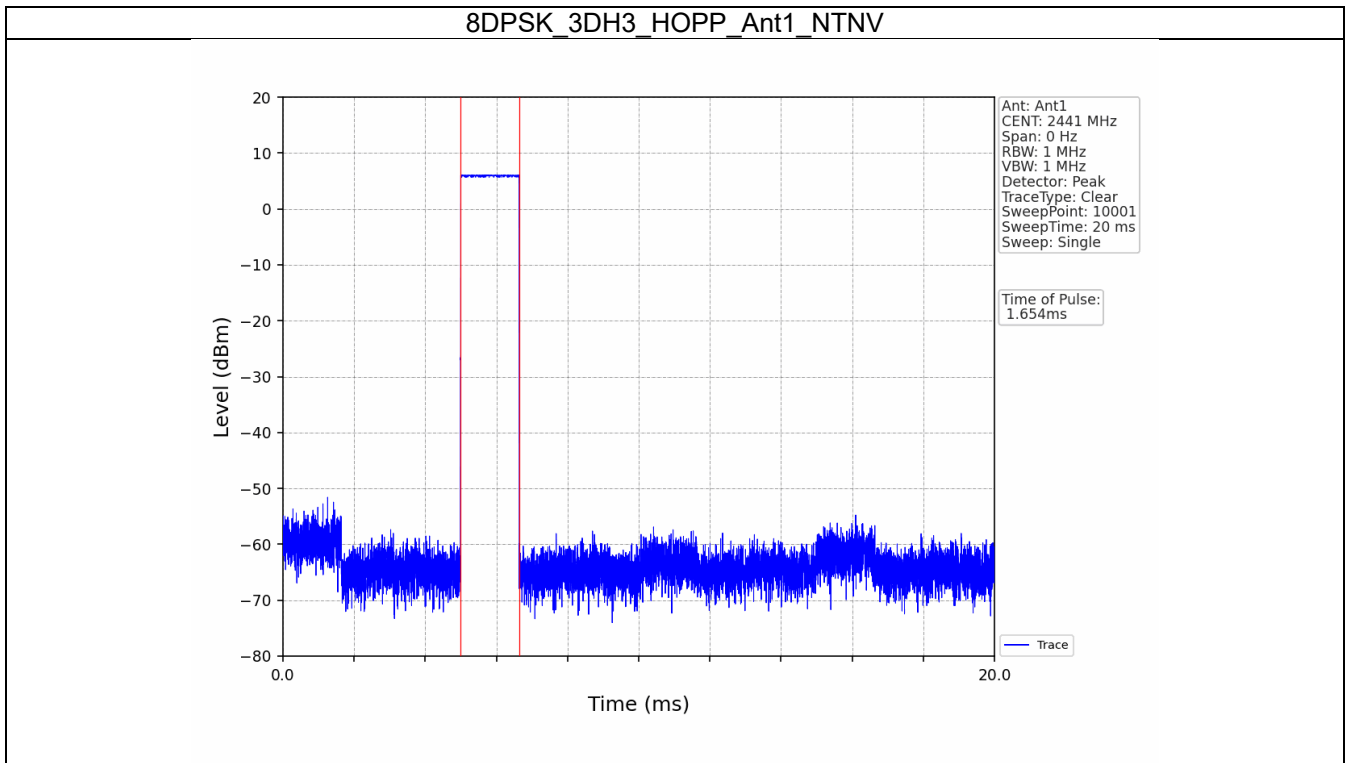


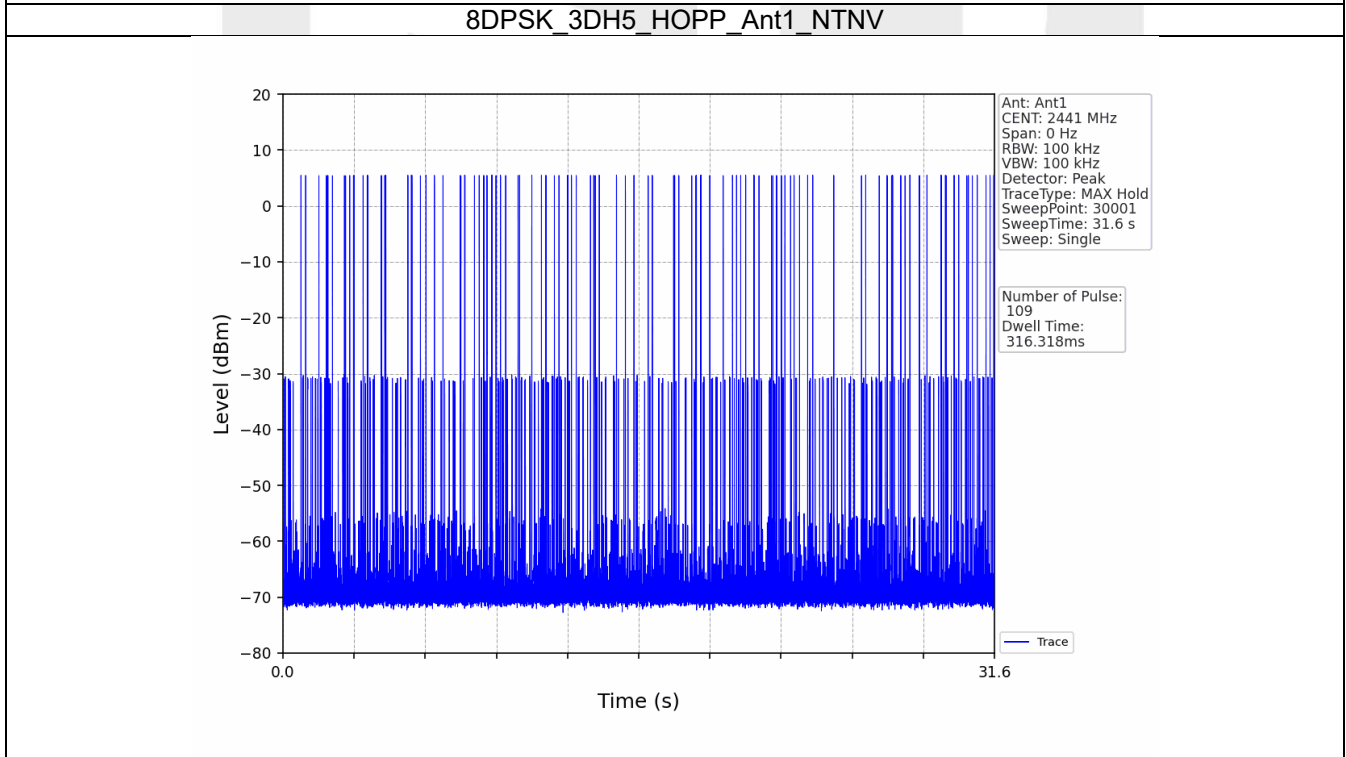
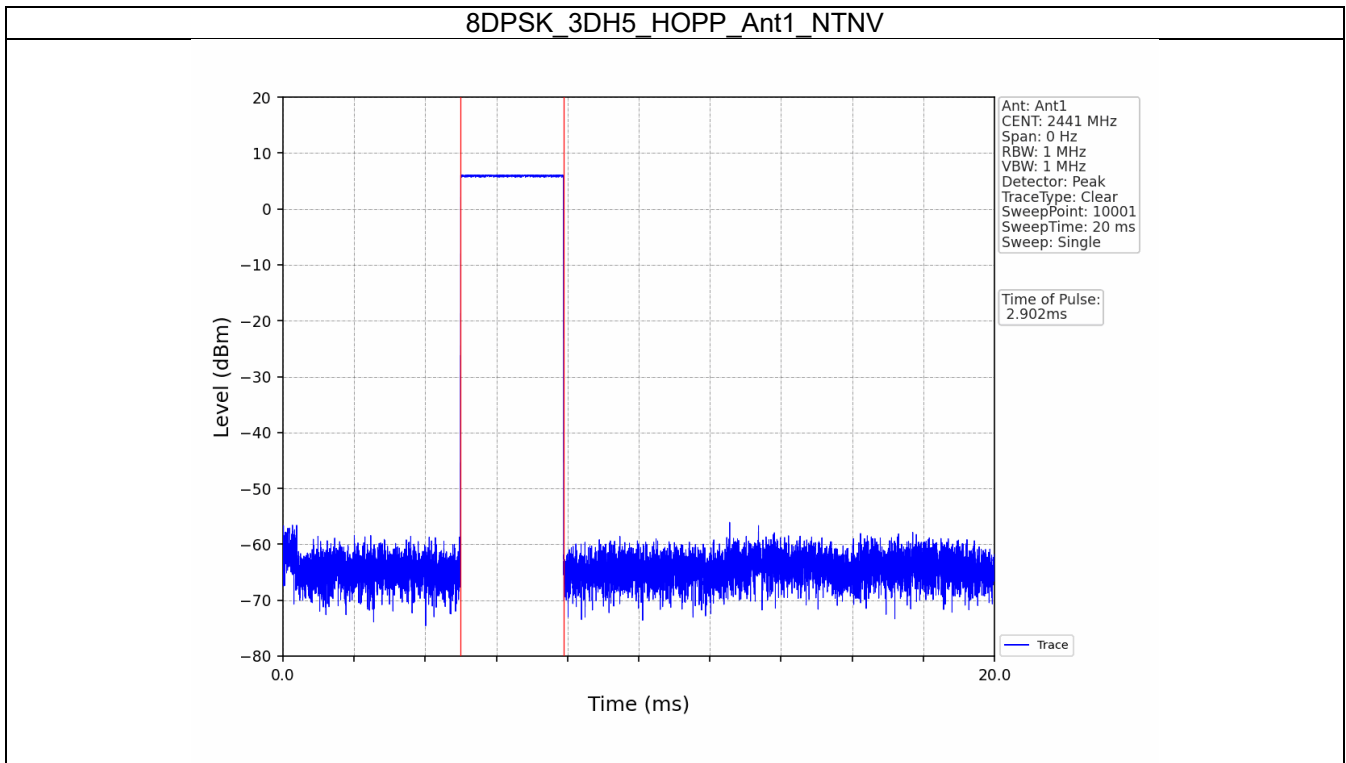












## 7. Unwanted Emissions In Non-restricted Frequency Bands

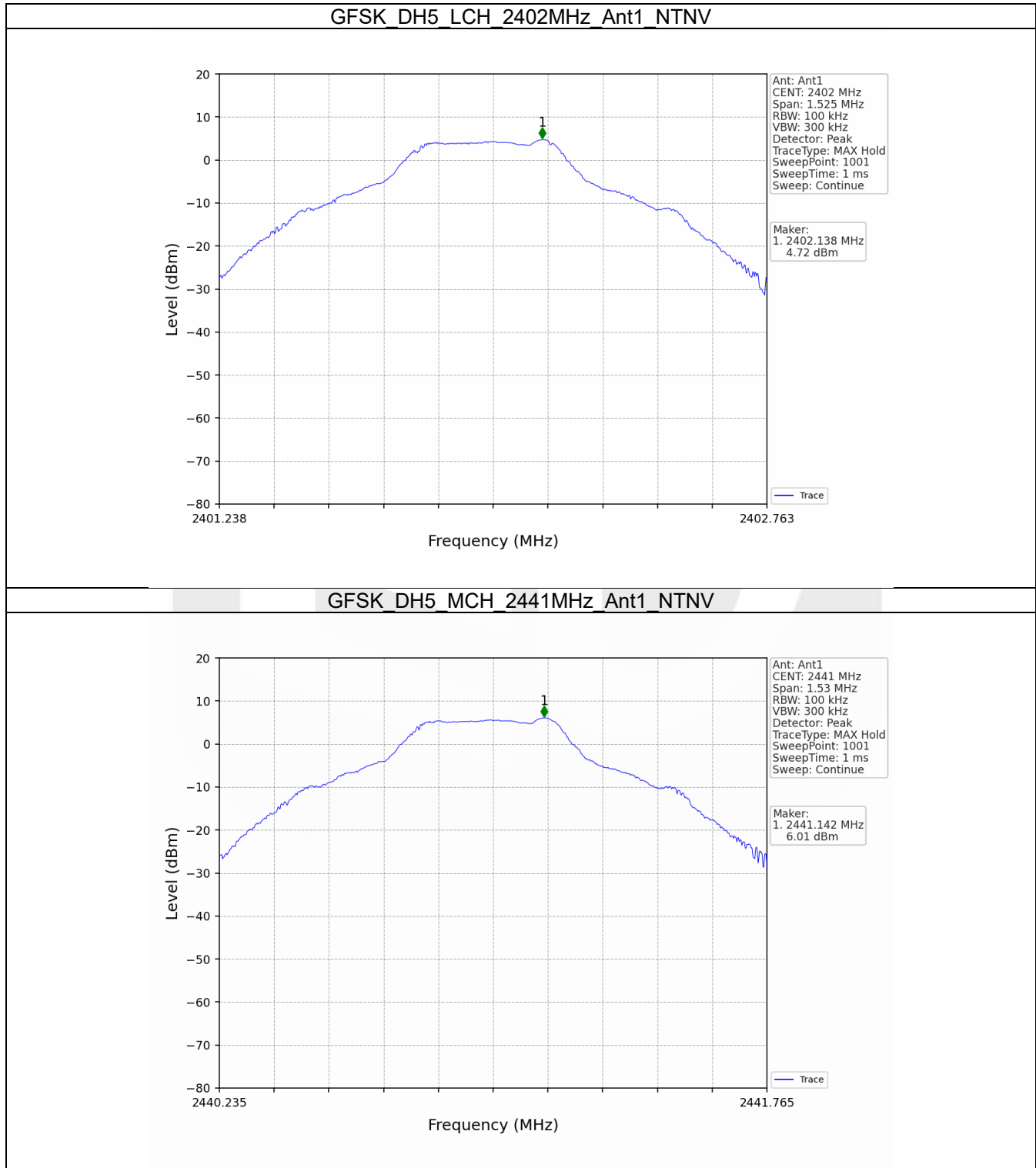
### 7.1 Ref

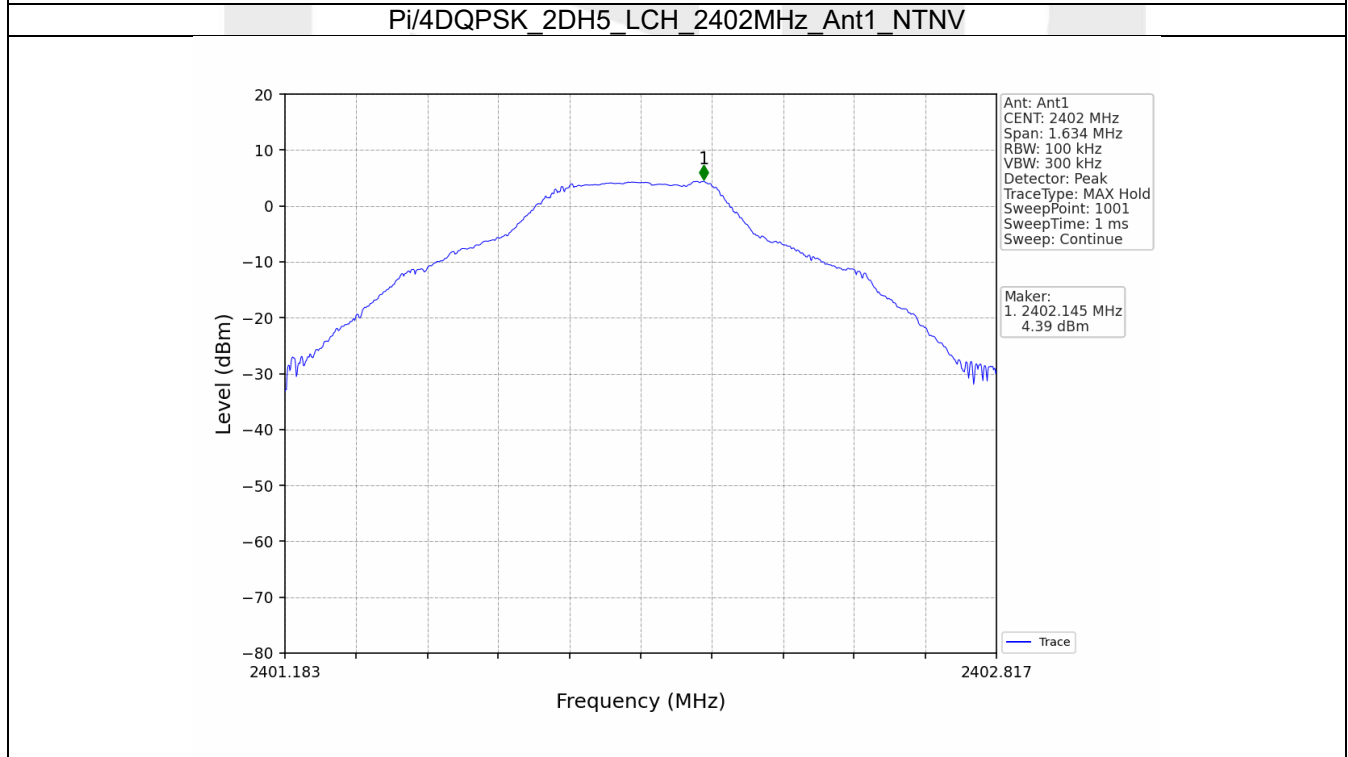
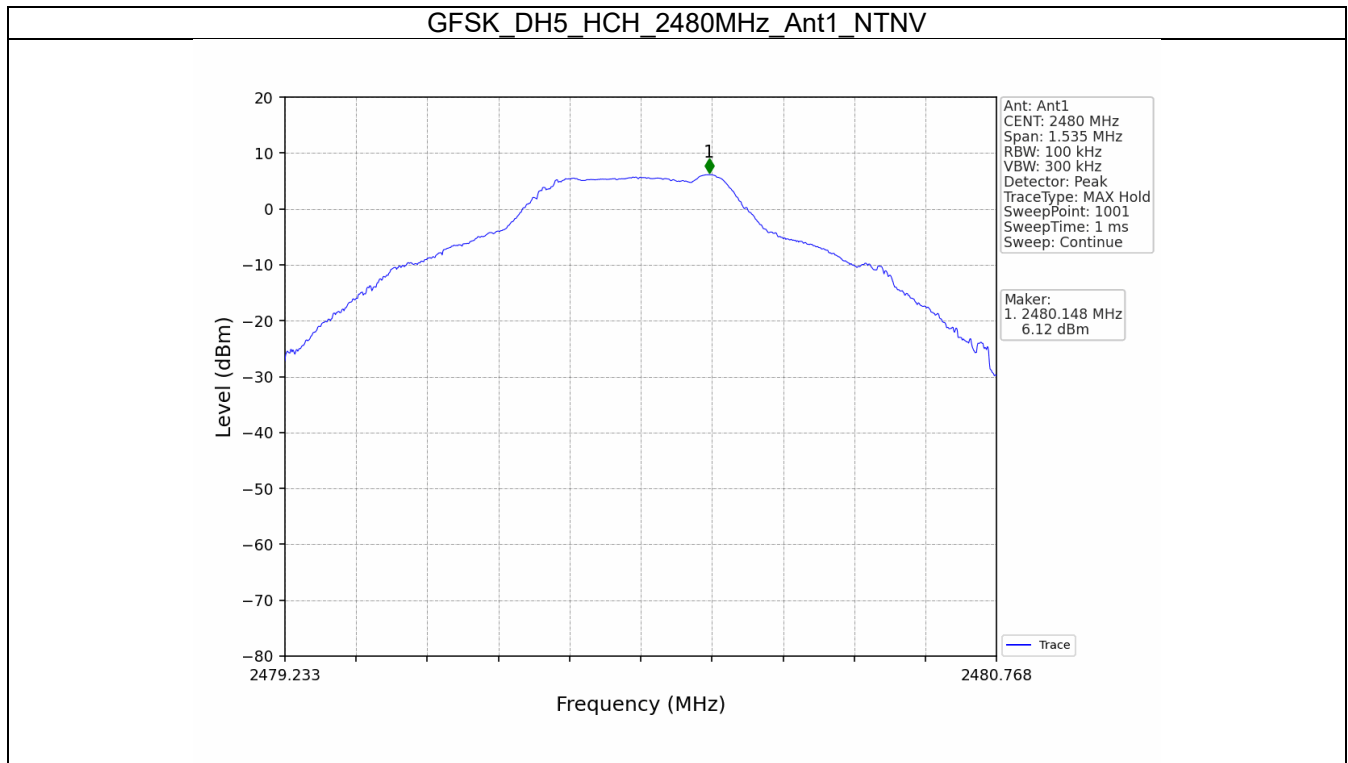
#### 7.1.1 Test Result

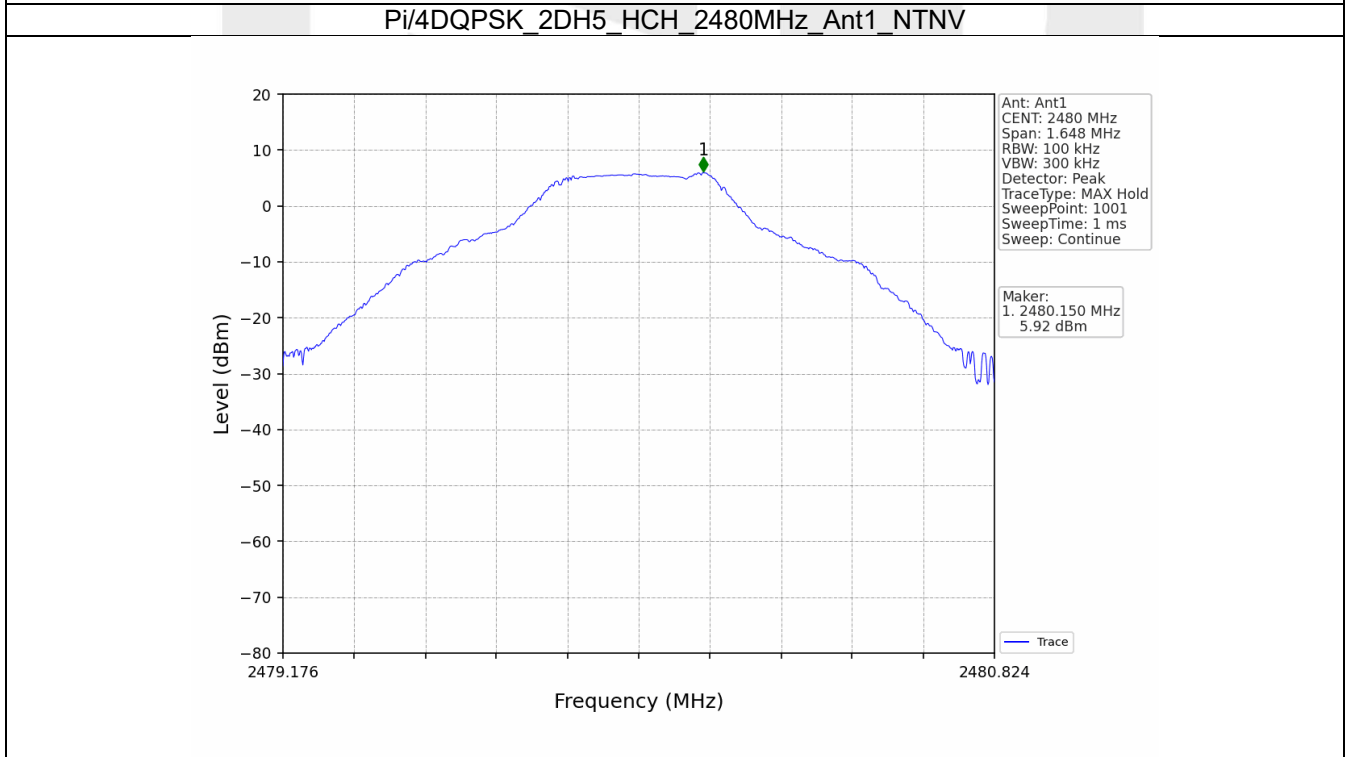
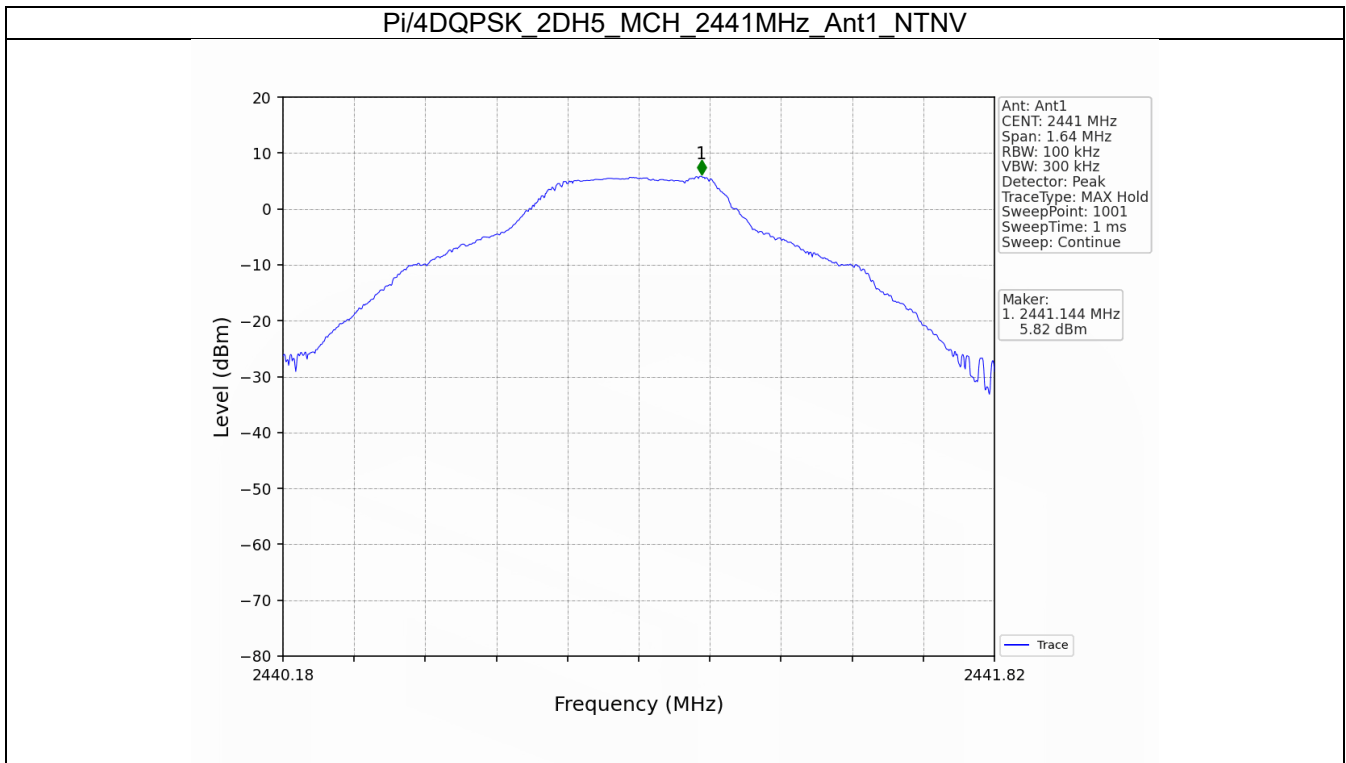
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	Level of Reference (dBm)
GFSK	SISO	2402	DH5	1	4.72
		2441	DH5	1	6.01
		2480	DH5	1	6.12
Pi/4DQPSK	SISO	2402	2DH5	1	4.39
		2441	2DH5	1	5.82
		2480	2DH5	1	5.92
8DPSK	SISO	2402	3DH5	1	4.64
		2441	3DH5	1	5.96
		2480	3DH5	1	6.06

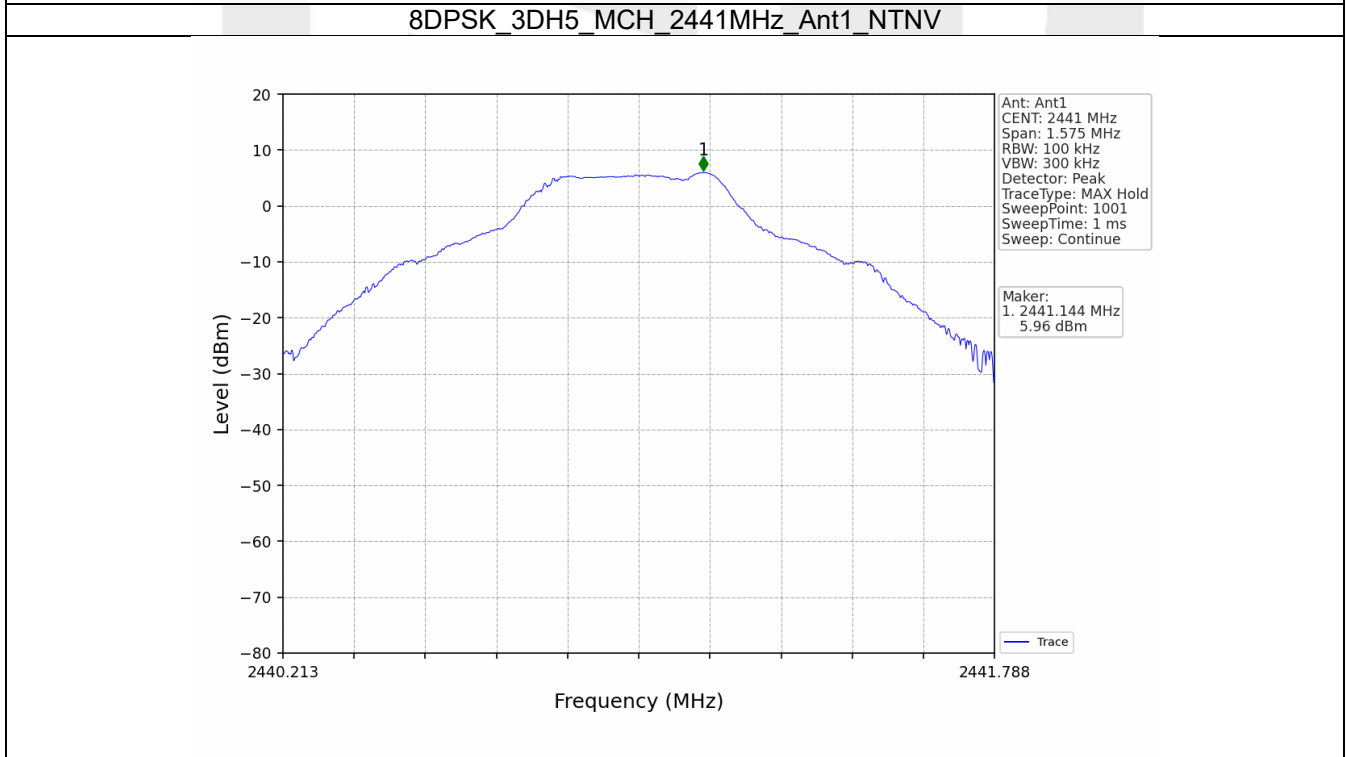
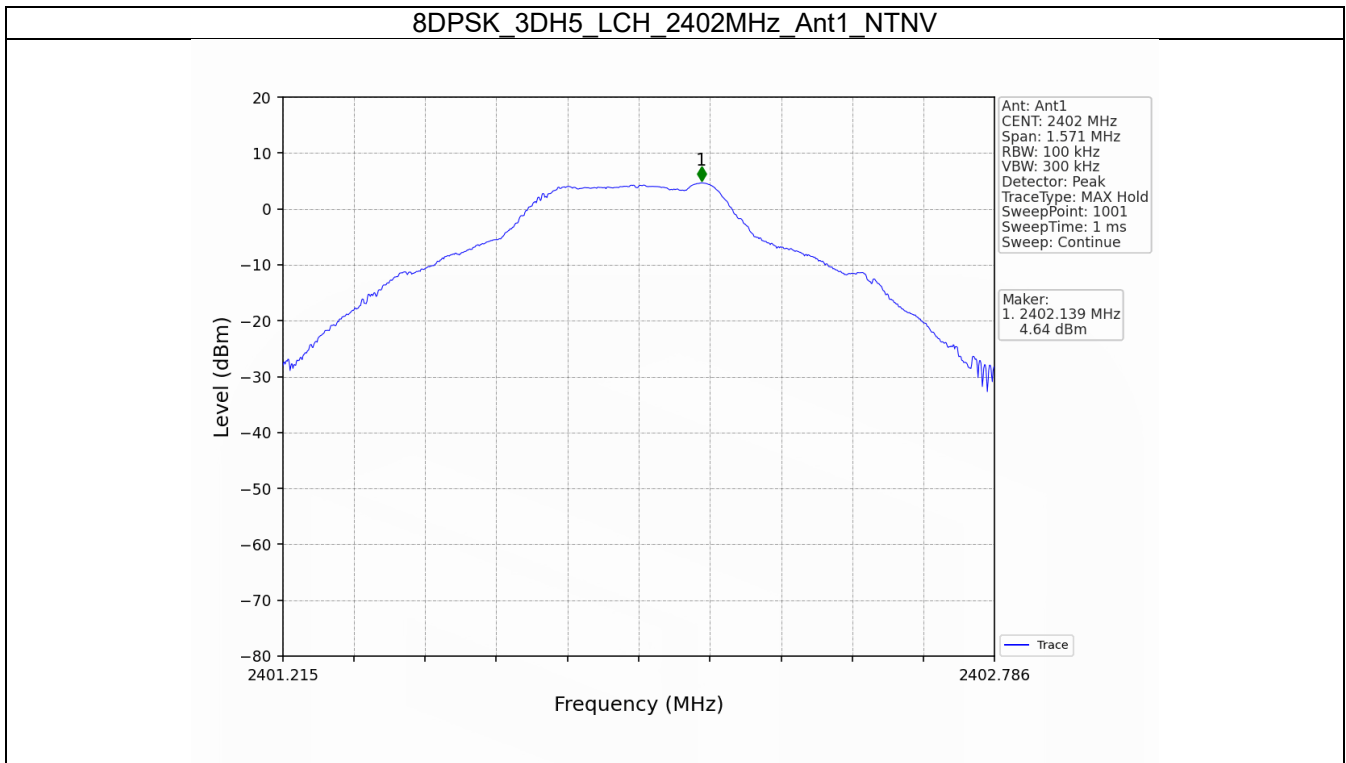
Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2020, the channel contains the maximum PSD level was used to establish the reference level.

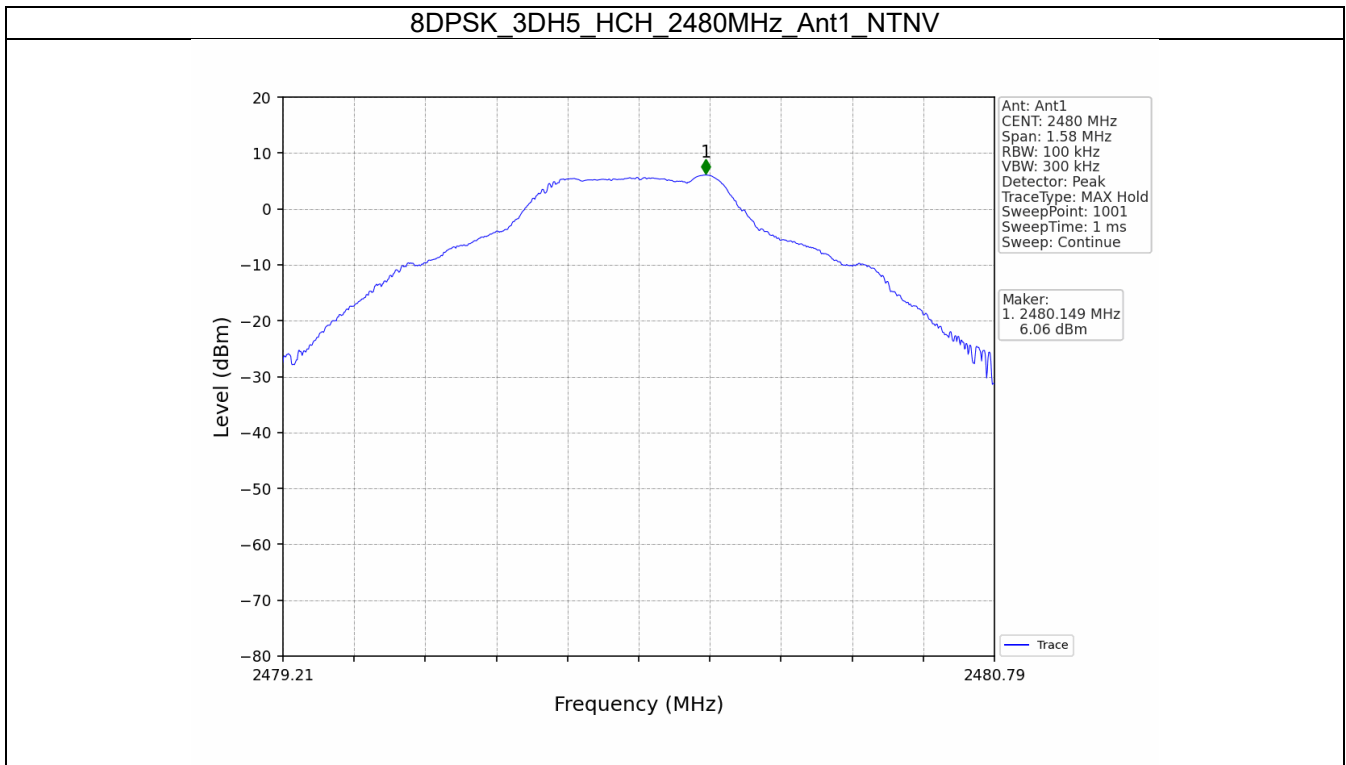
7.1.2 Test Graph











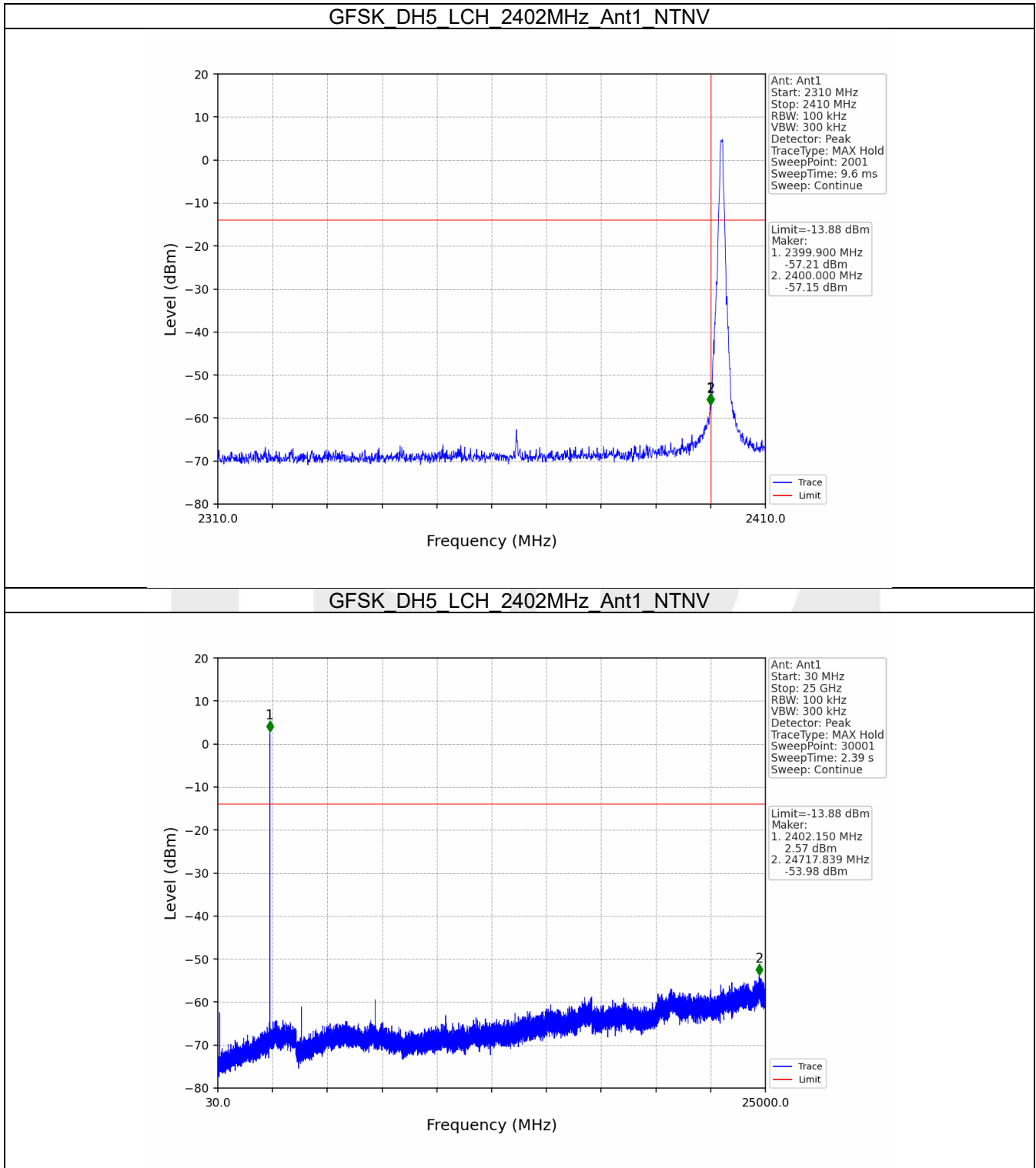


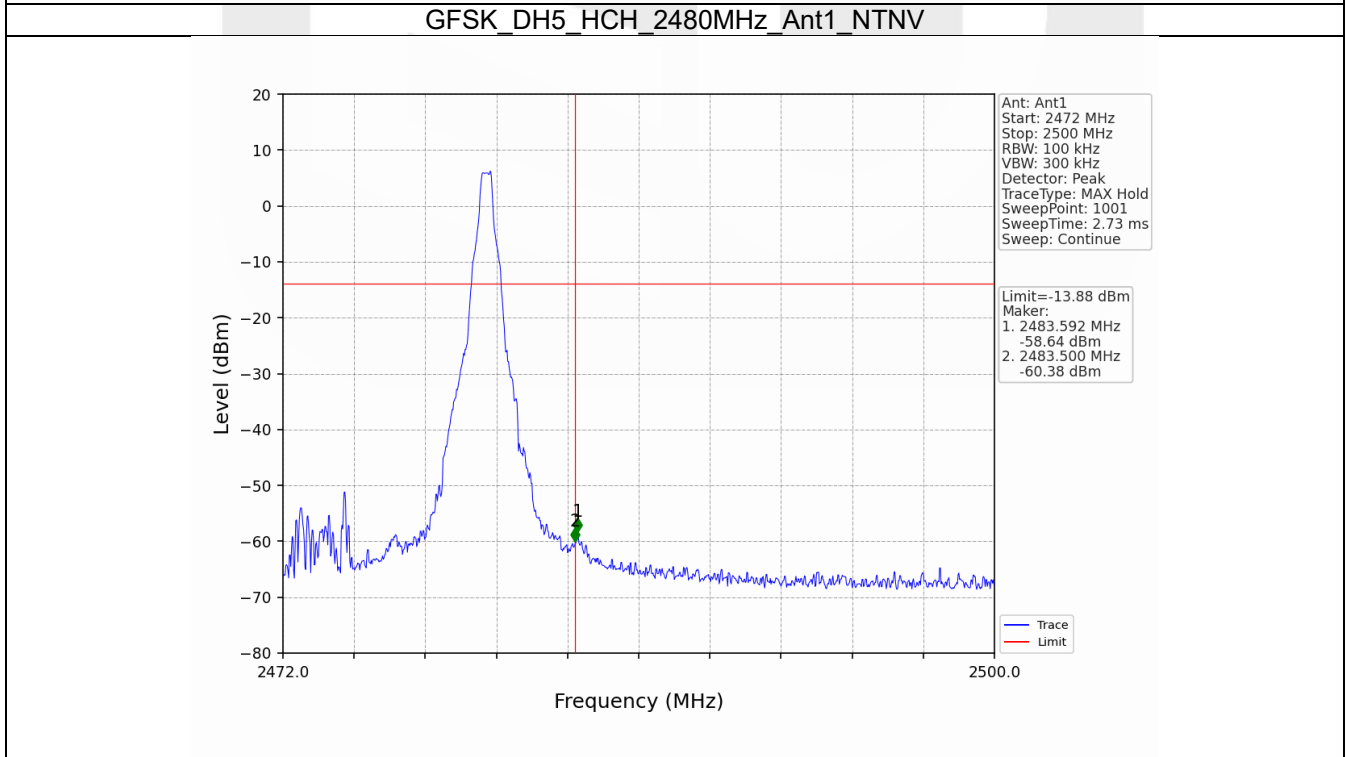
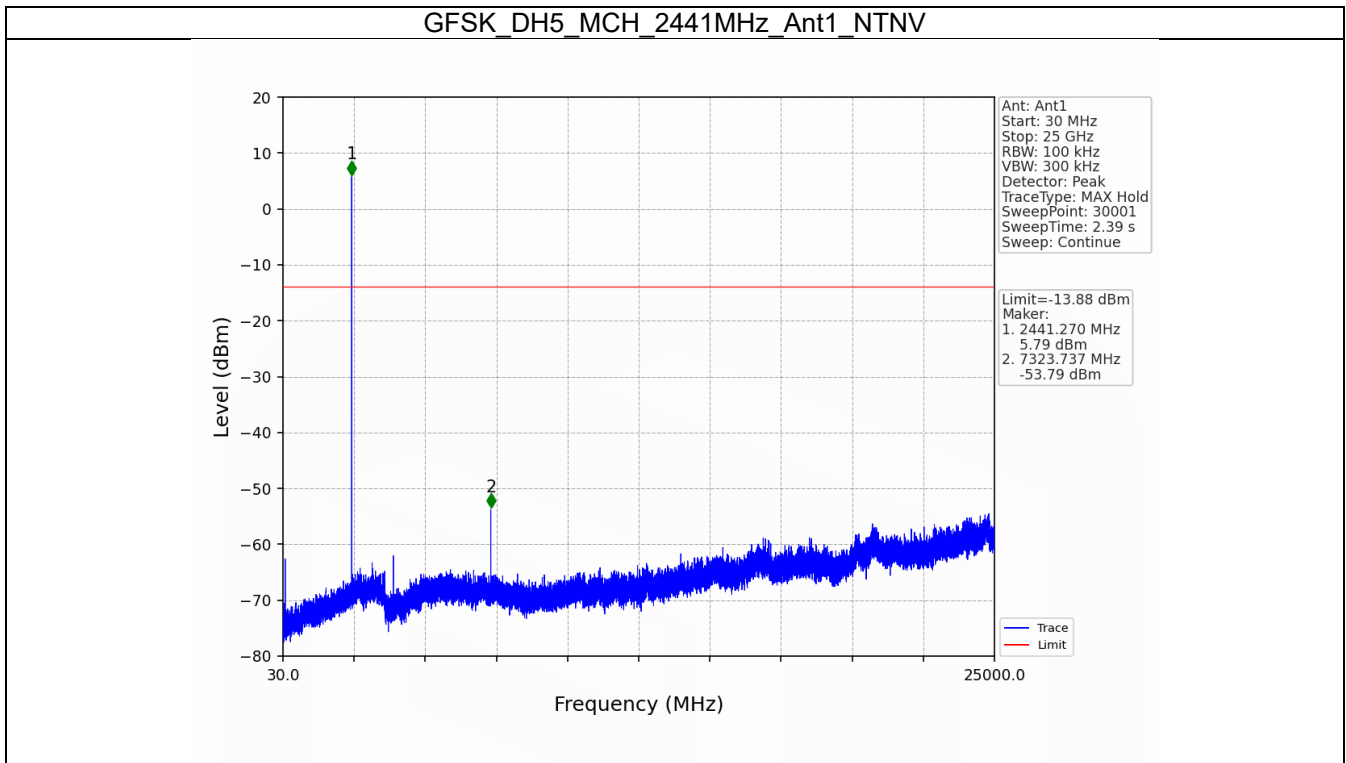
**7.2 CSE**
**7.2.1 Test Result**

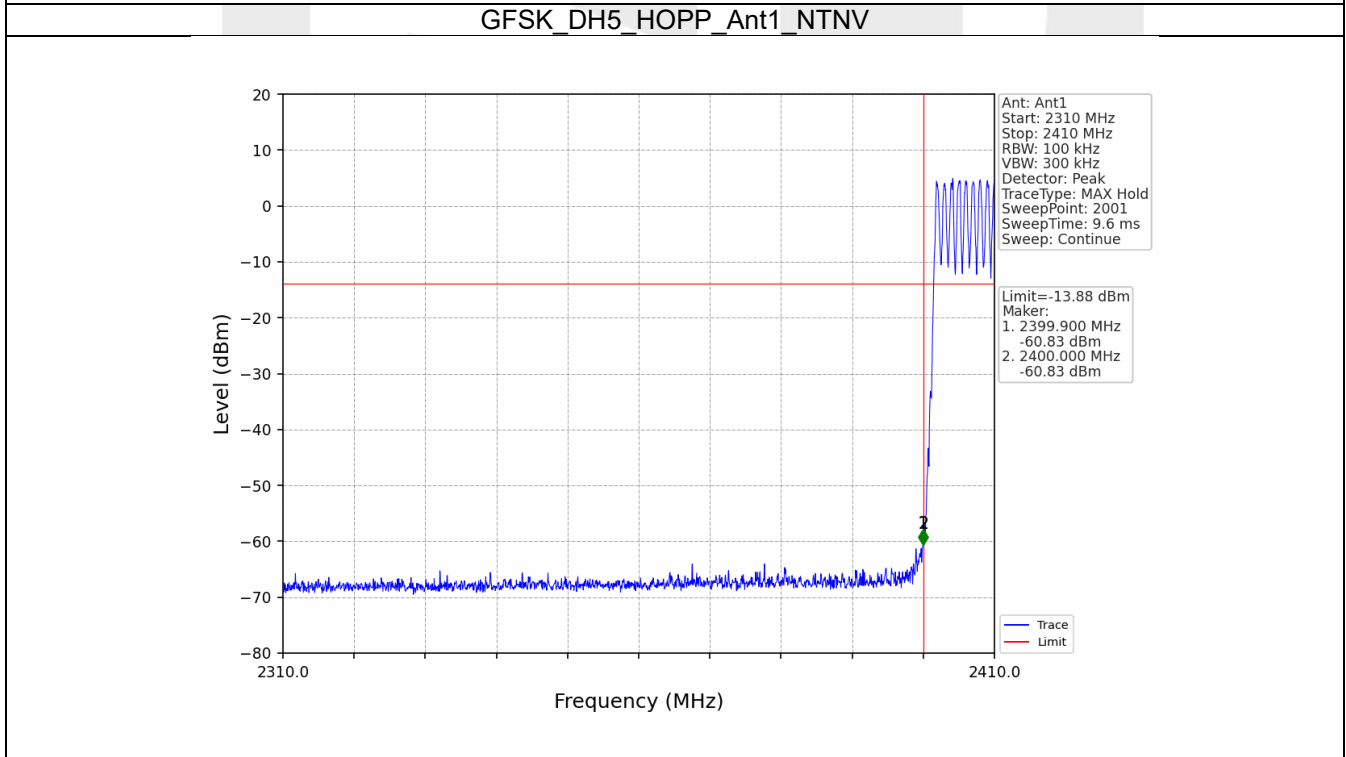
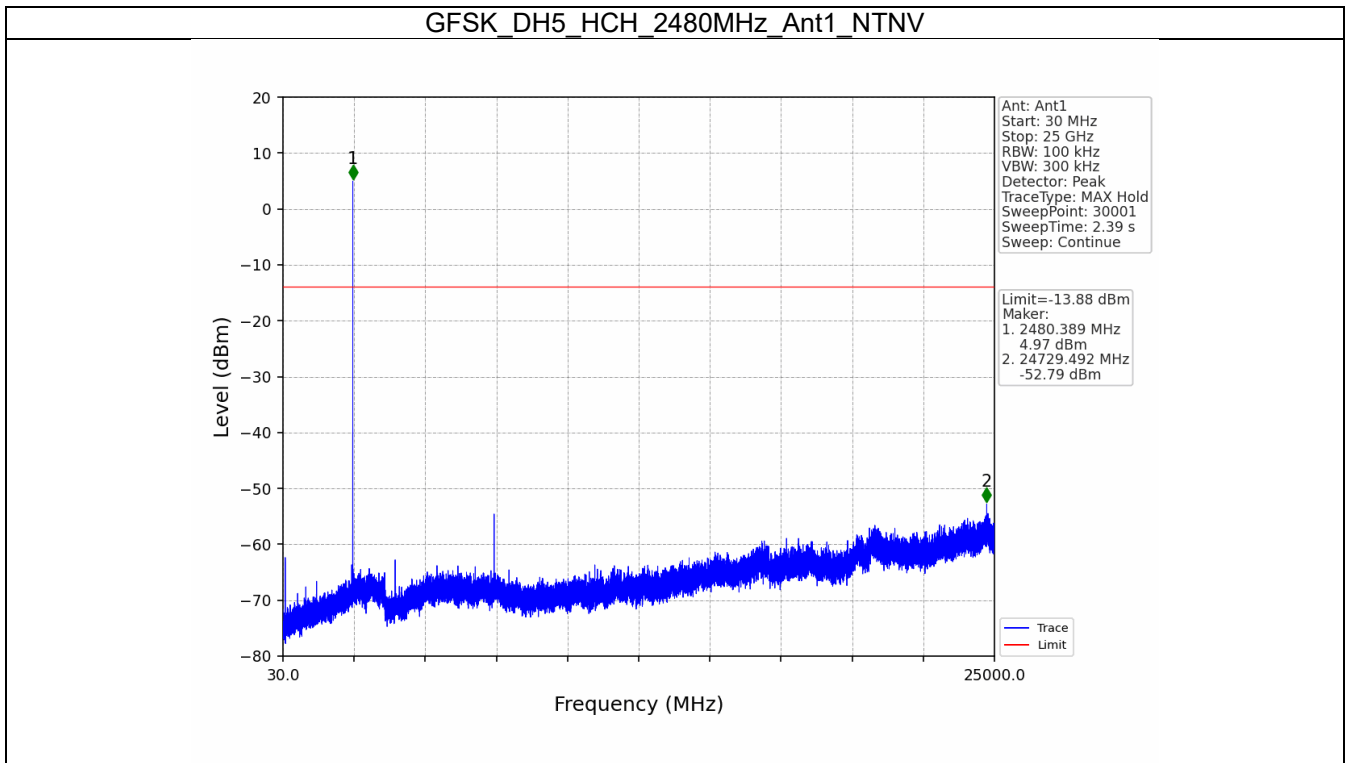
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	Level of Reference (dBm)	Limit (dBm)	Verdict
GFSK	SISO	2402	DH5	1	6.12	-13.88	Pass
		2441	DH5	1	6.12	-13.88	Pass
		2480	DH5	1	6.12	-13.88	Pass
		HOPP	DH5	1	6.12	-13.88	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	5.92	-14.08	Pass
		2441	2DH5	1	5.92	-14.08	Pass
		2480	2DH5	1	5.92	-14.08	Pass
		HOPP	2DH5	1	5.92	-14.08	Pass
8DPSK	SISO	2402	3DH5	1	6.06	-13.94	Pass
		2441	3DH5	1	6.06	-13.94	Pass
		2480	3DH5	1	6.06	-13.94	Pass
		HOPP	3DH5	1	6.06	-13.94	Pass

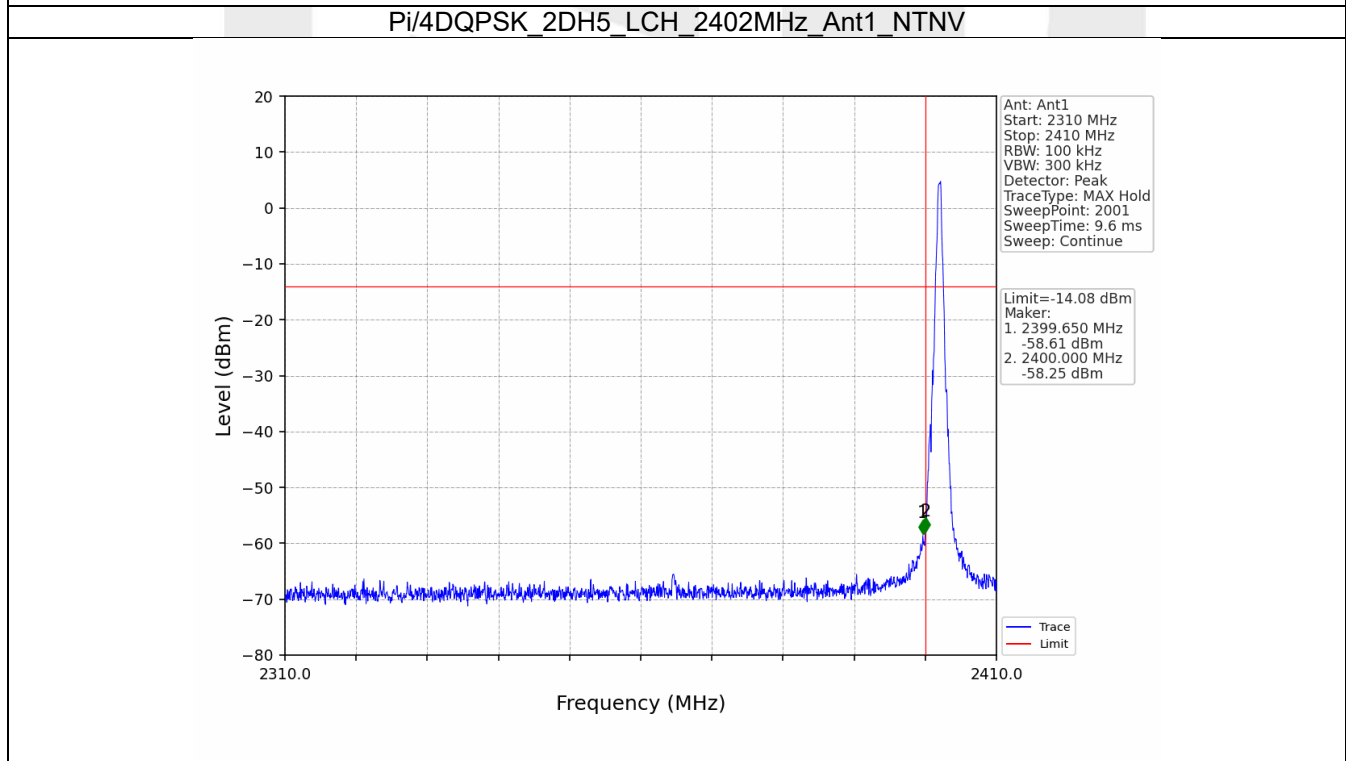
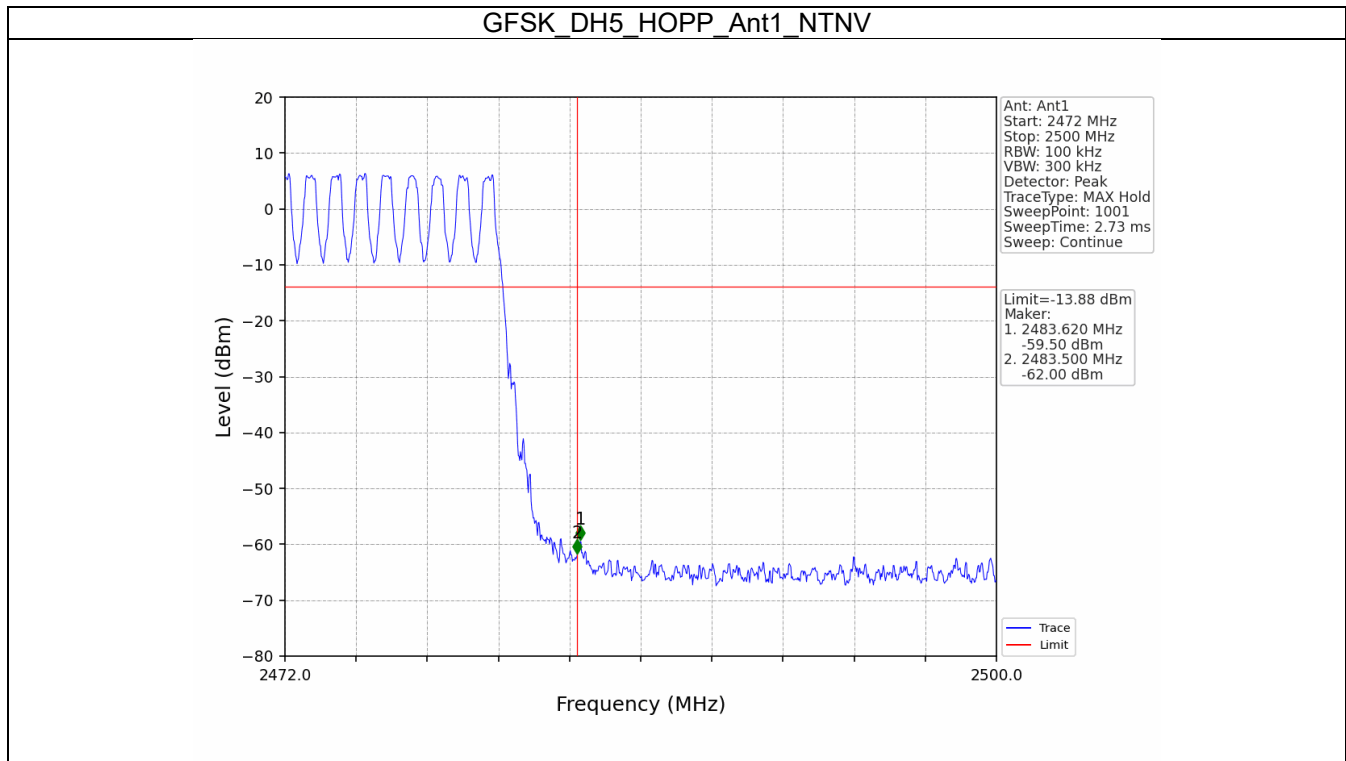
Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2020, the channel contains the maximum PSD level was used to establish the reference level.

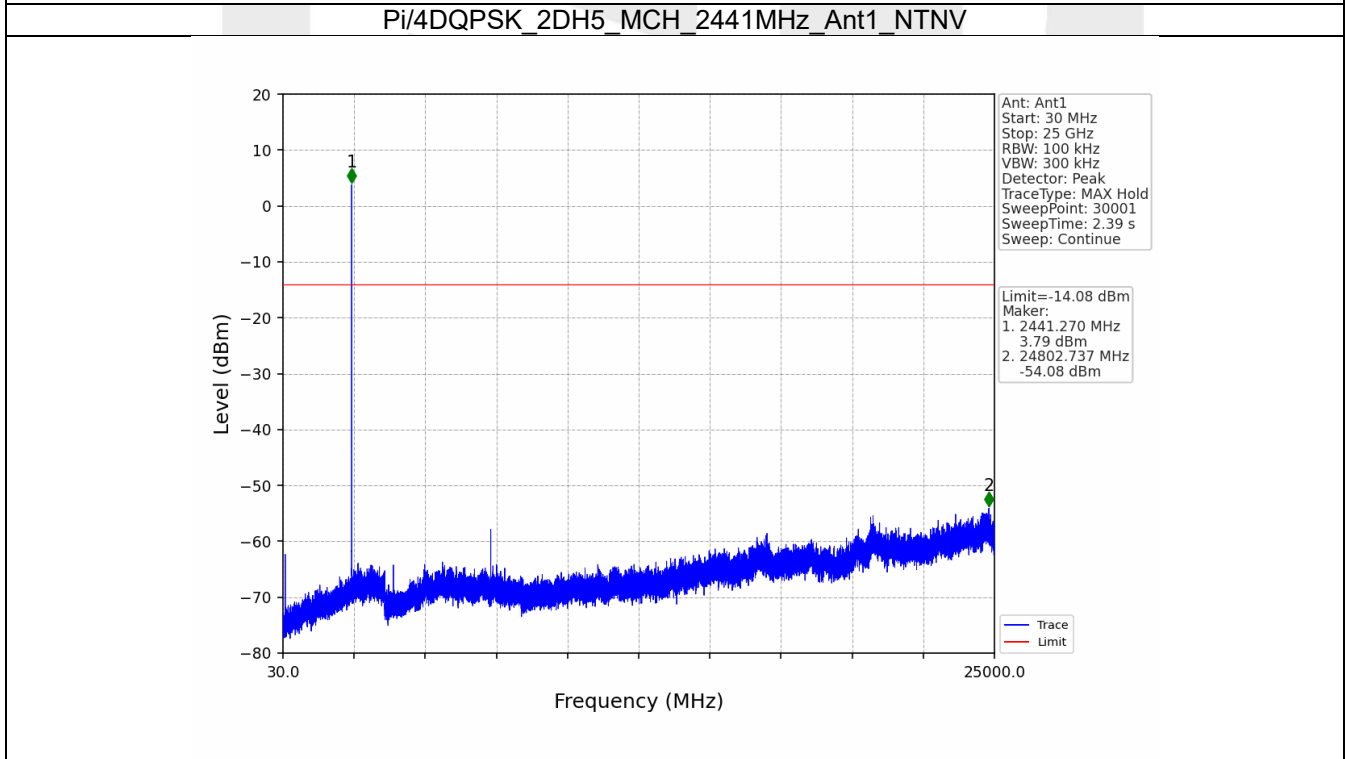
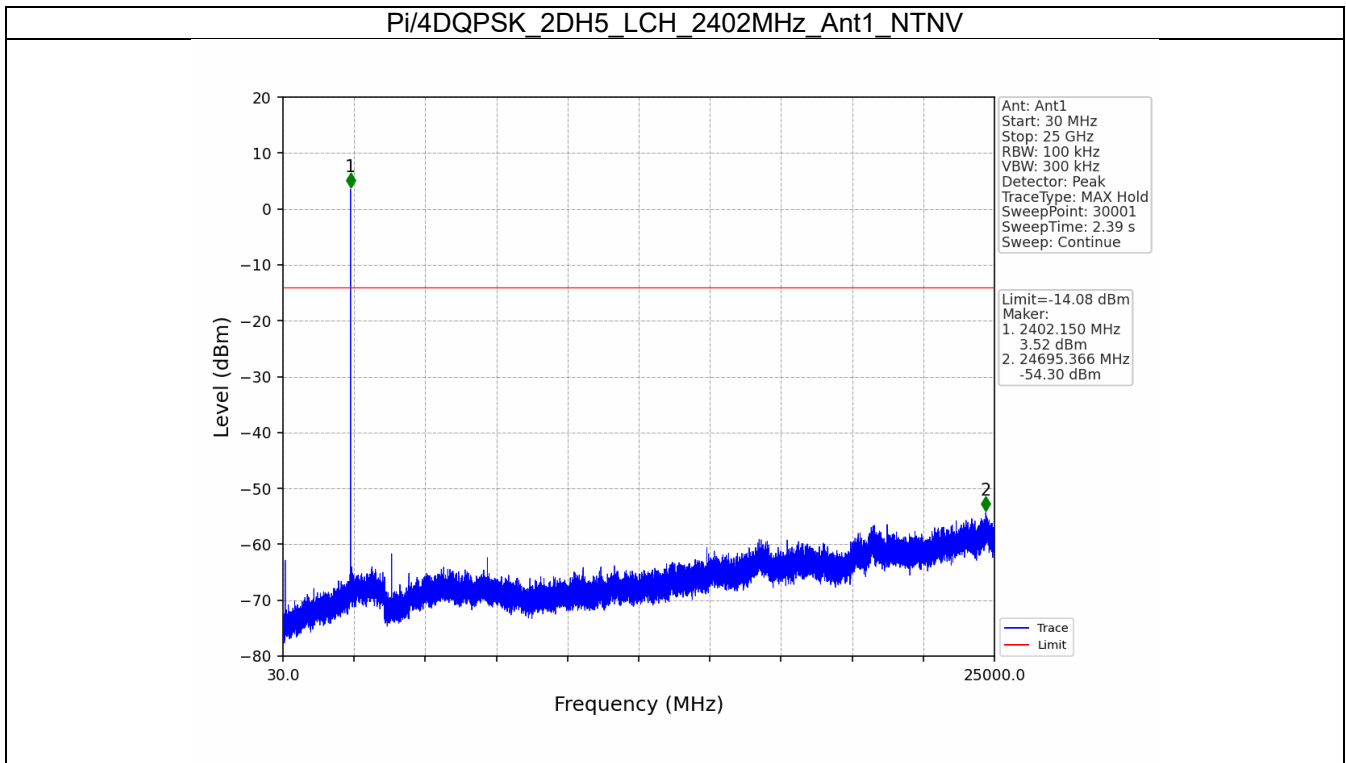
7.2.2 Test Graph

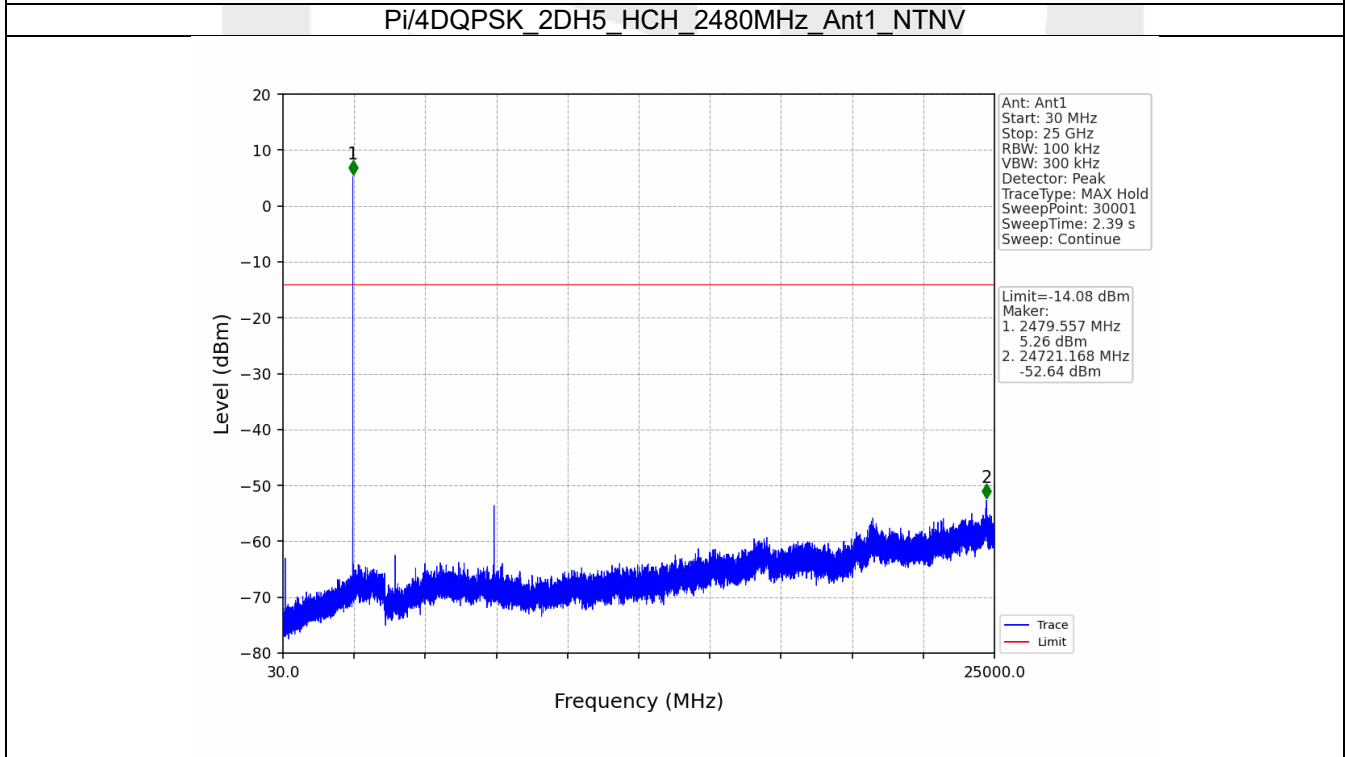
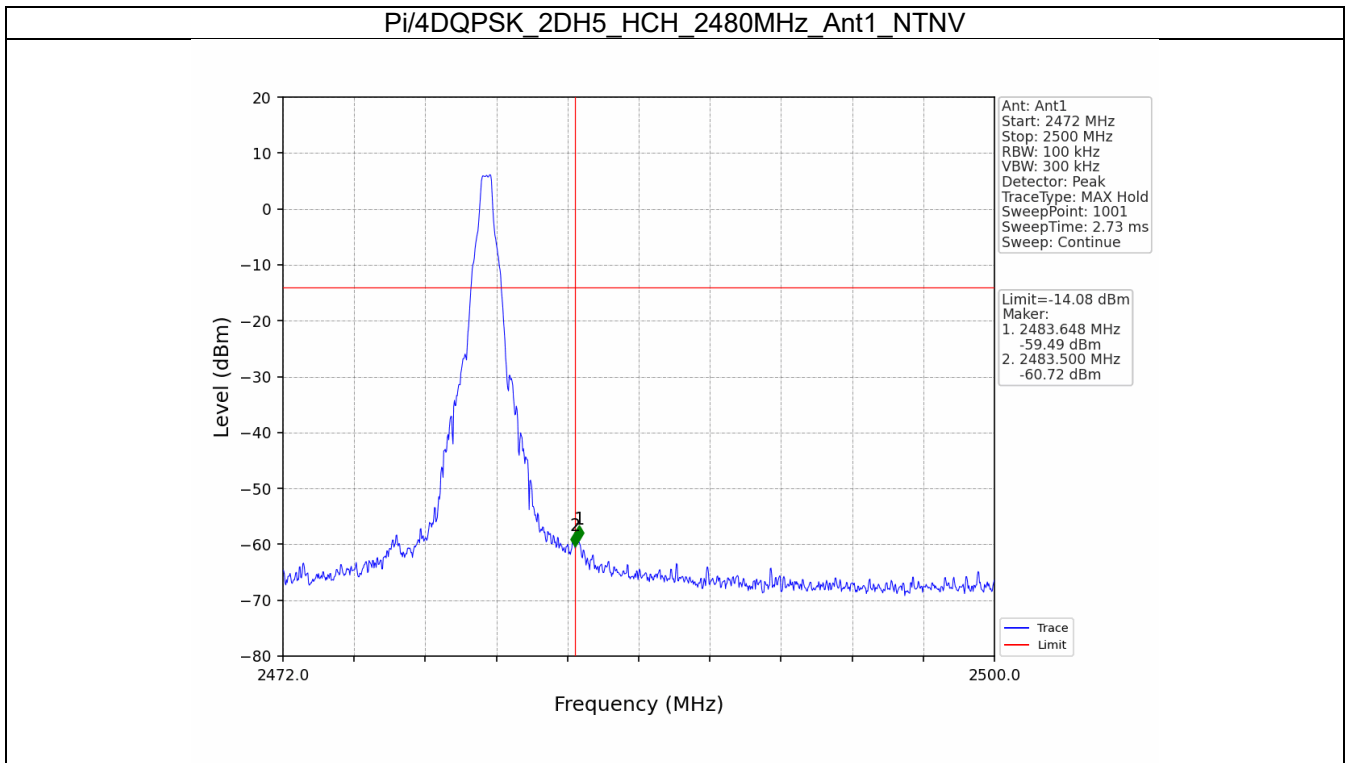


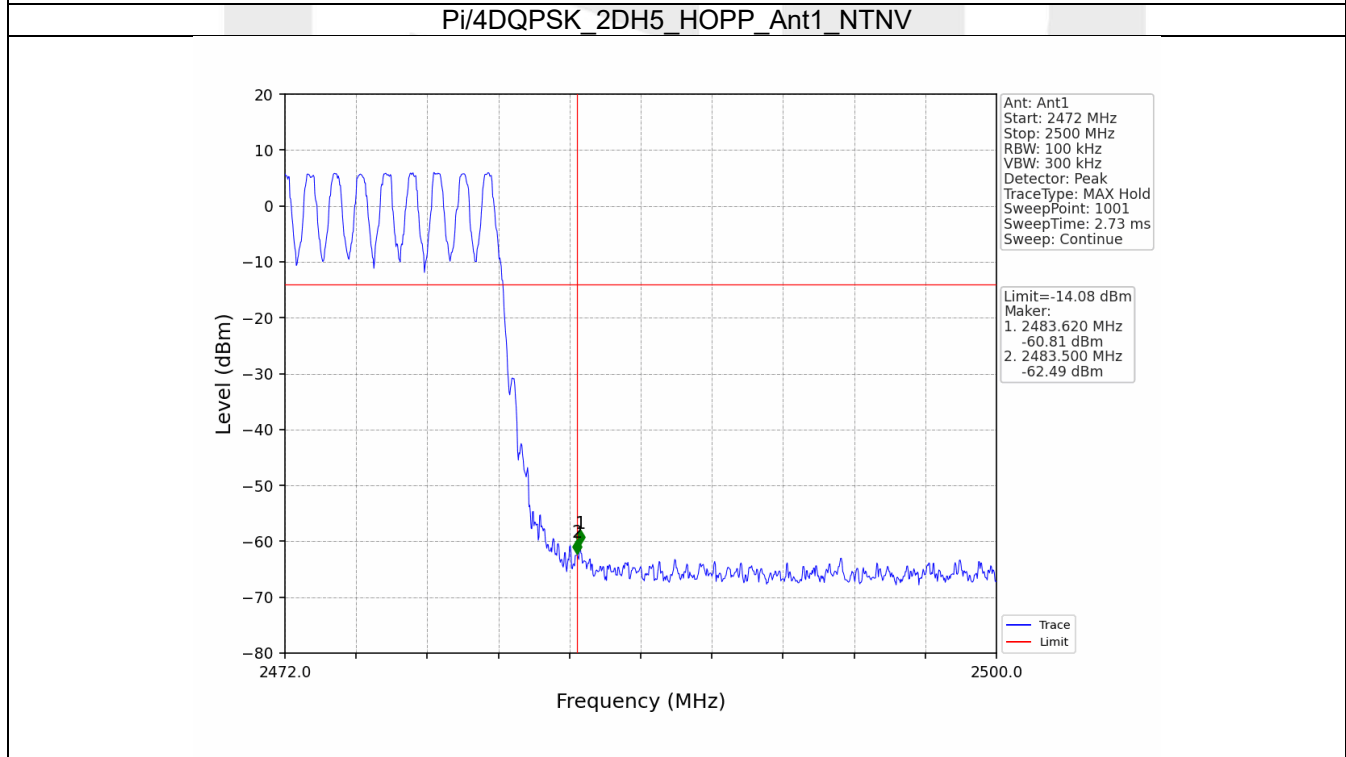
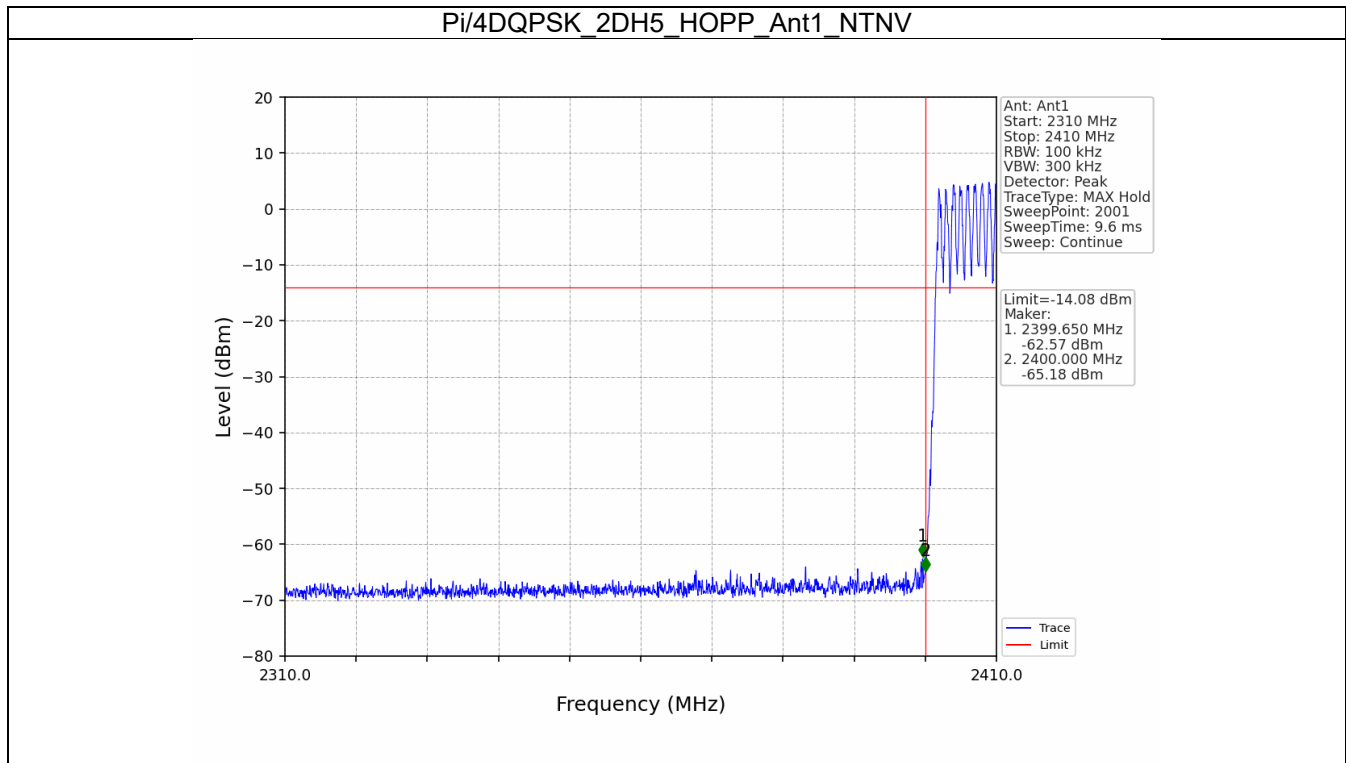




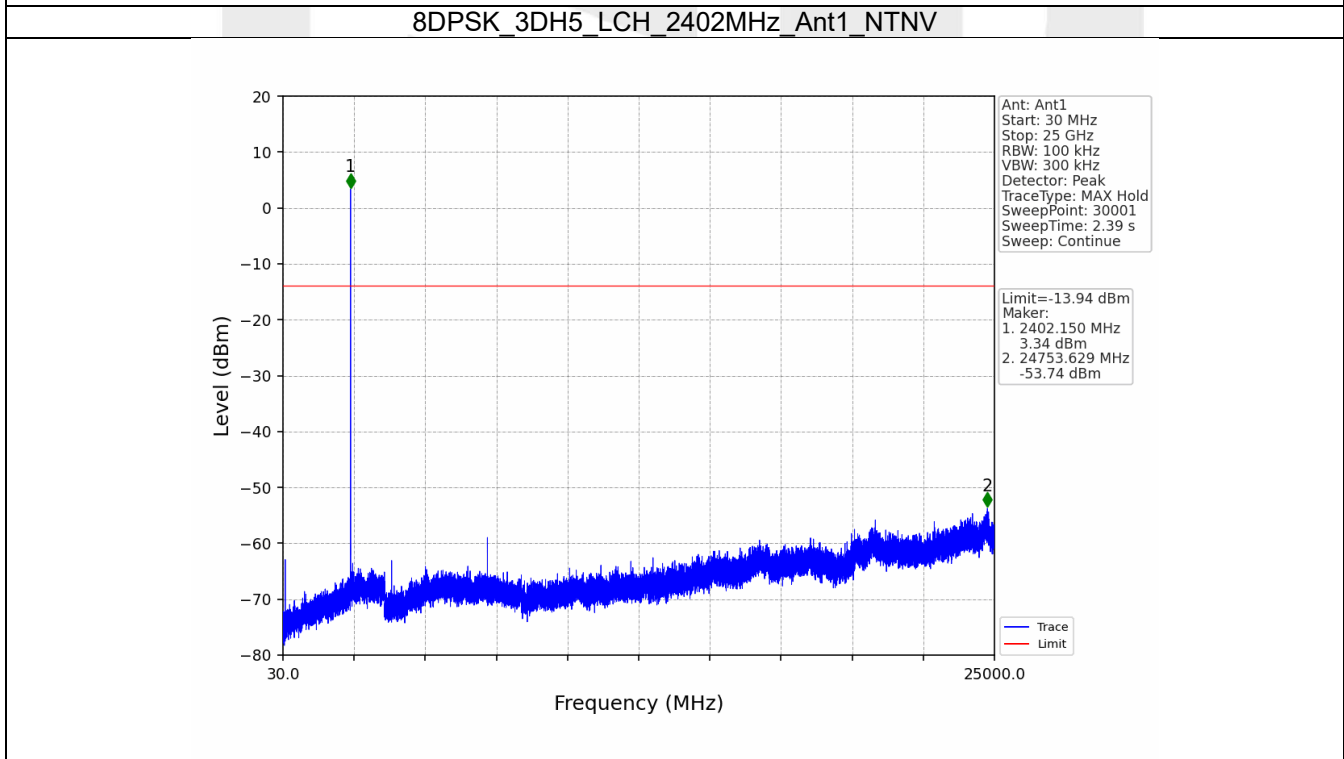
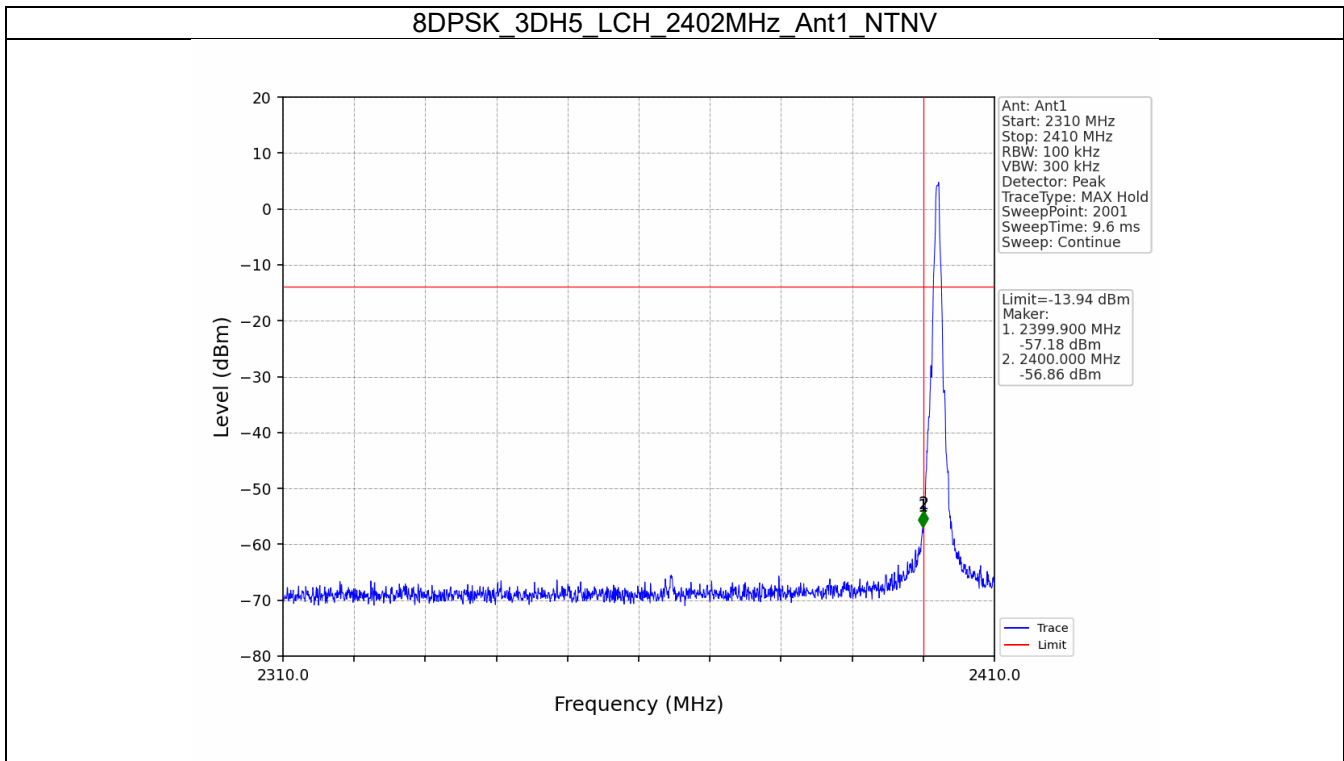


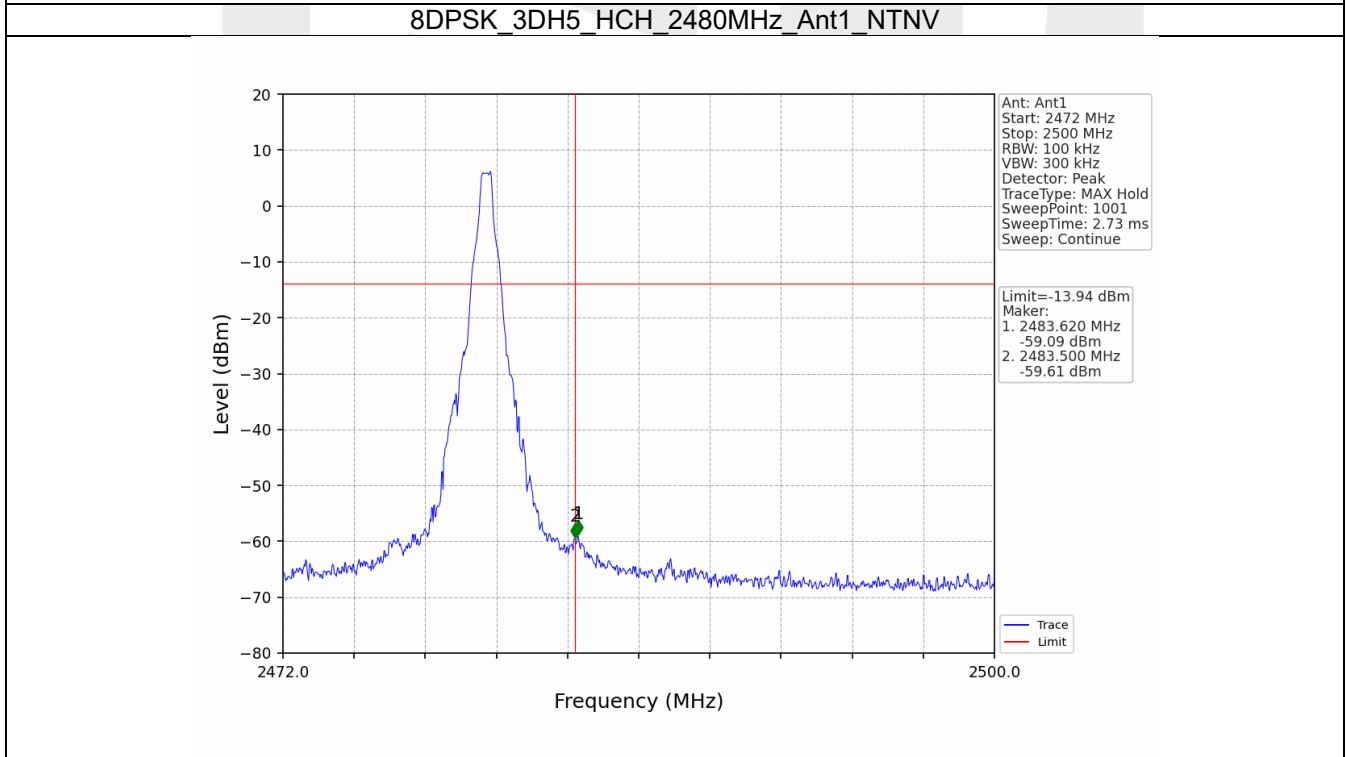
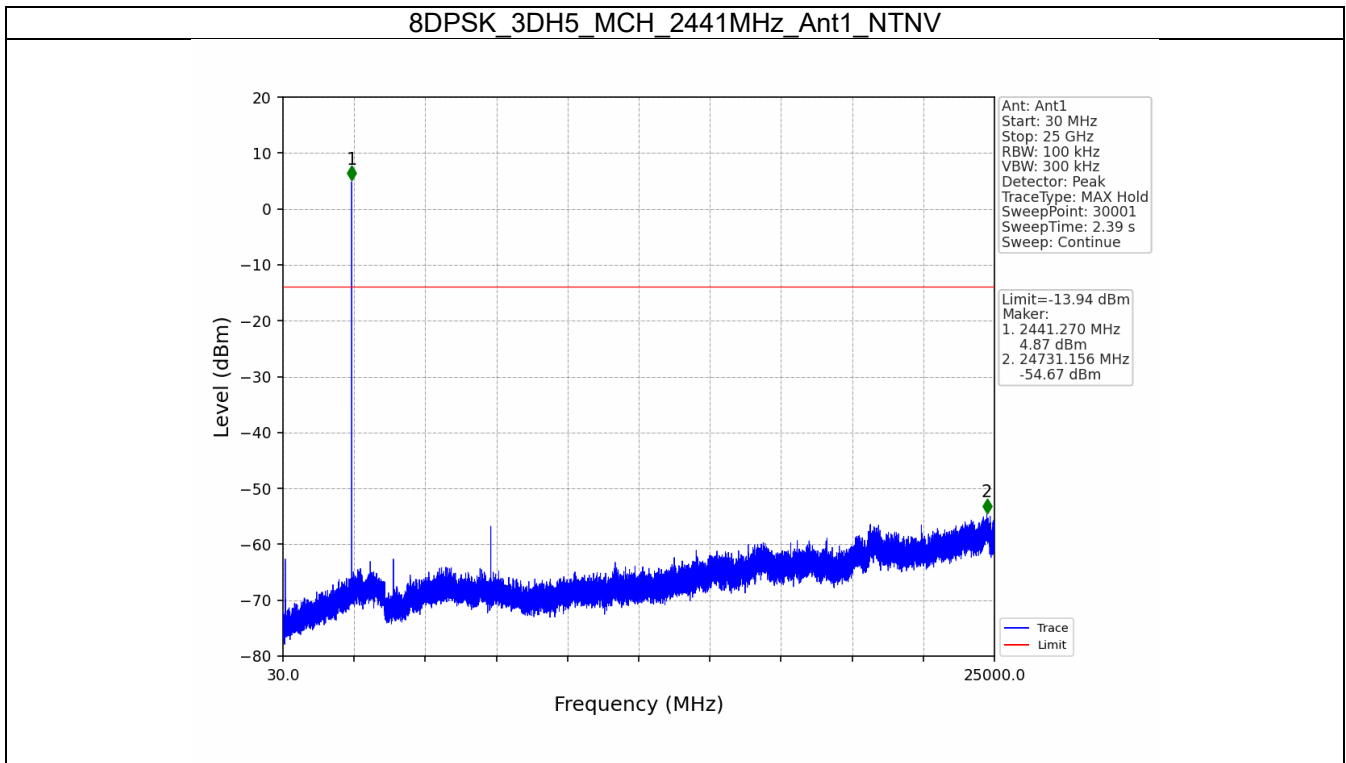


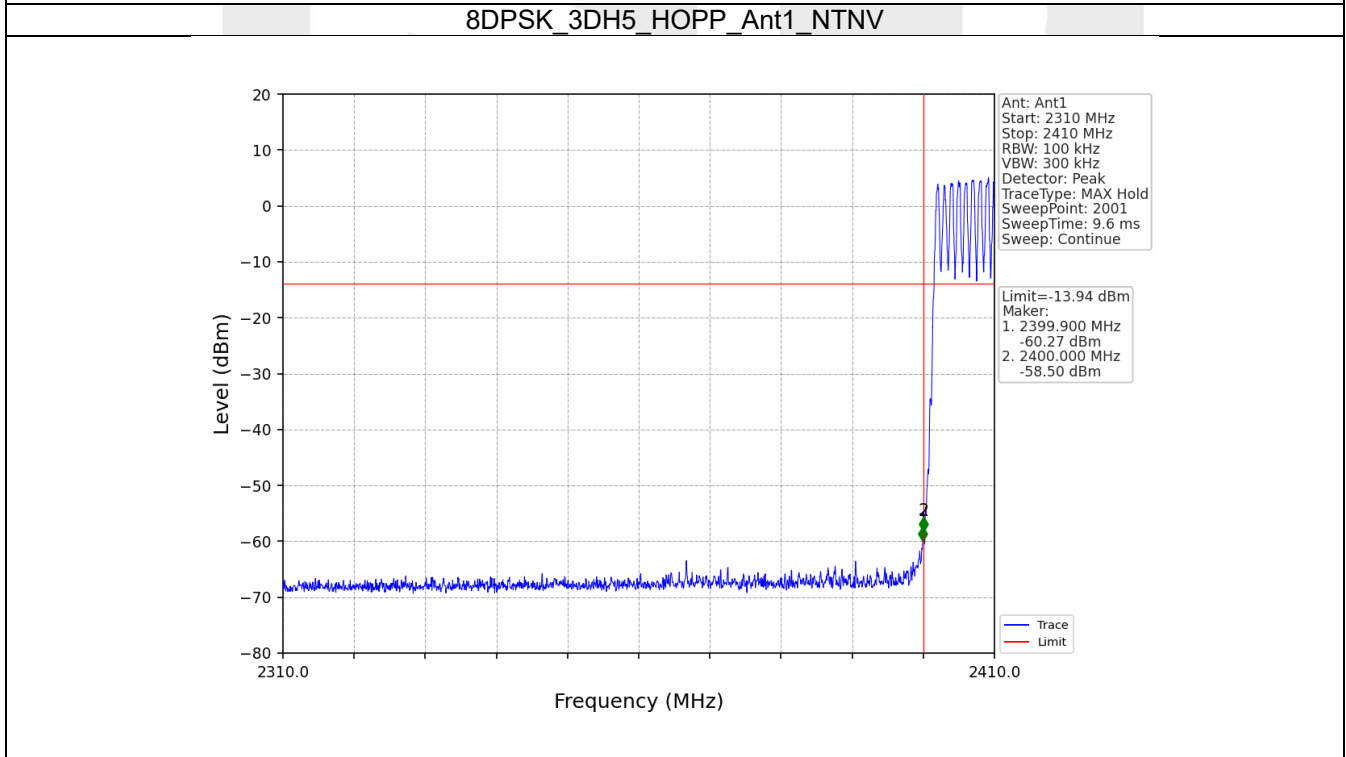
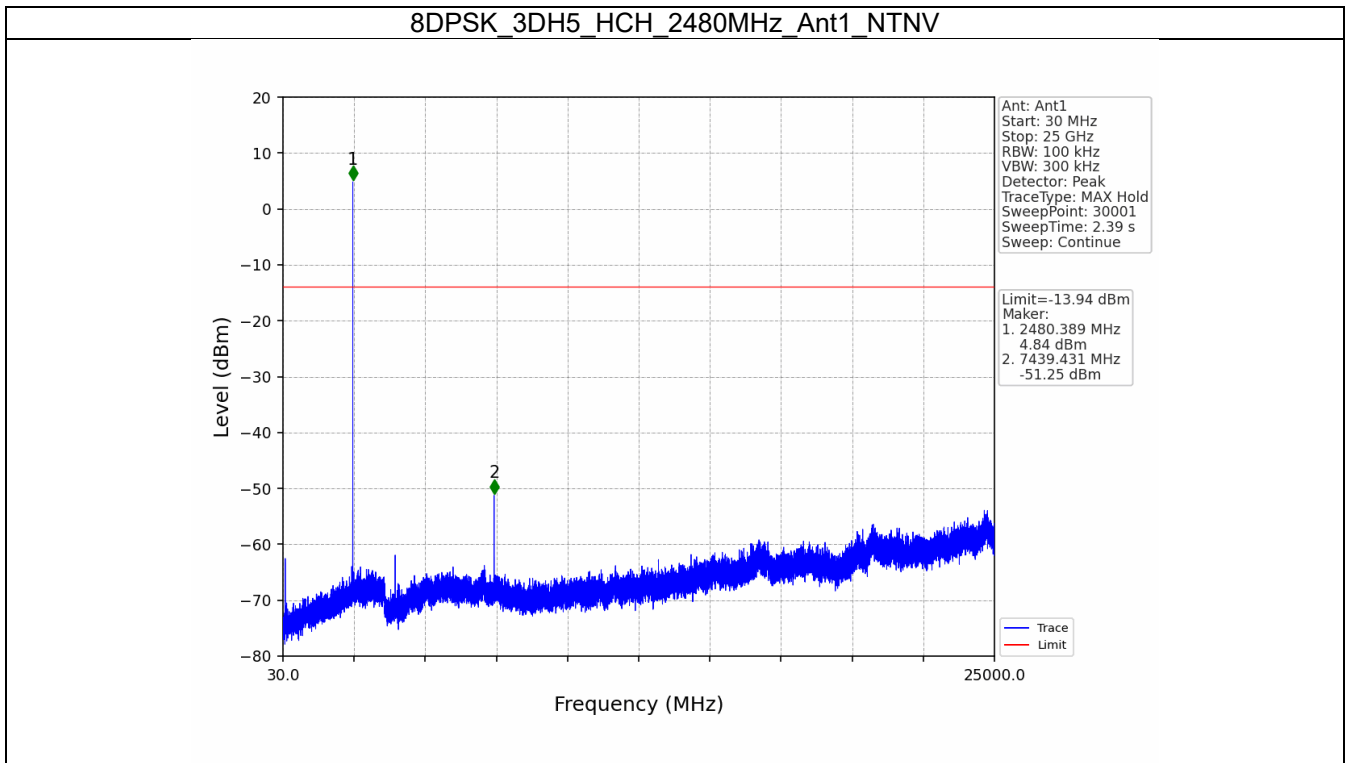


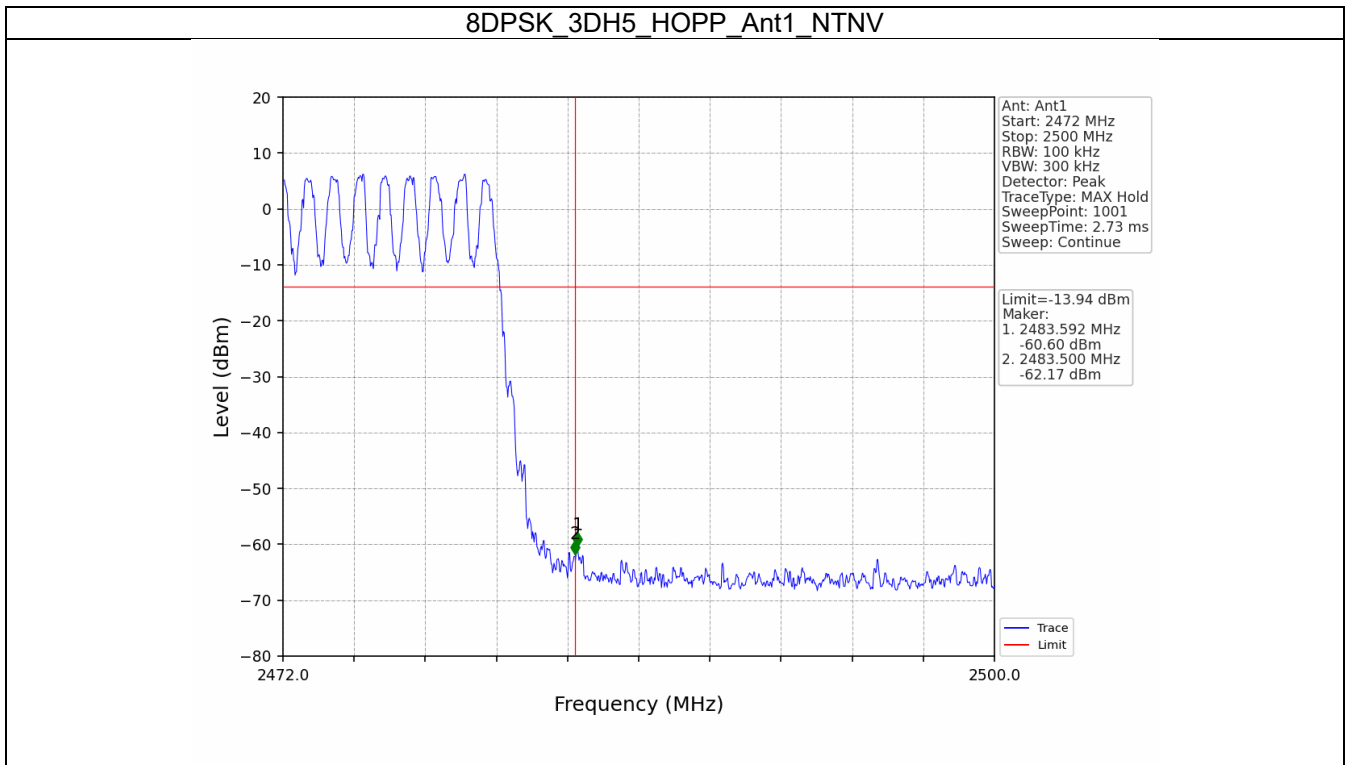












----- End of Report -----