11AC20SISO_Ant1_5825

11AC20SISO_Ant2_5825


11AC40SISO_Ant1_5190

11AC40SISO_Ant2_5190


11AC40SISO_Ant1_5230

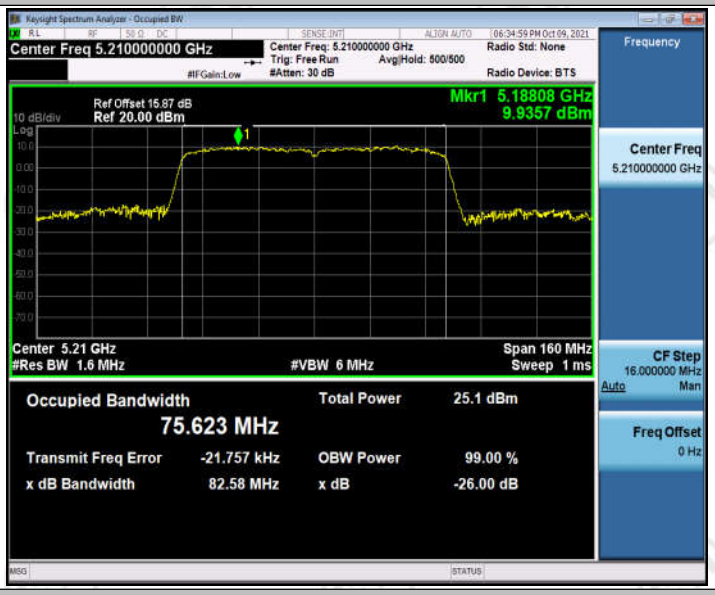
11AC40SISO_Ant2_5230


11AC40SISO_Ant1_5755

11AC40SISO_Ant2_5755


11AC40SISO_Ant1_5795

11AC40SISO_Ant2_5795


11AC80SISO_Ant1_5210

11AC80SISO_Ant2_5210


11AC80SISO_Ant1_5775

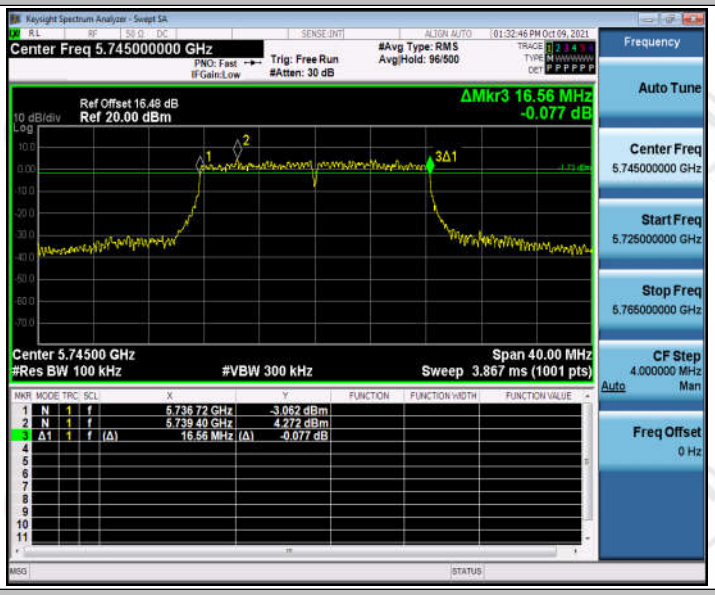
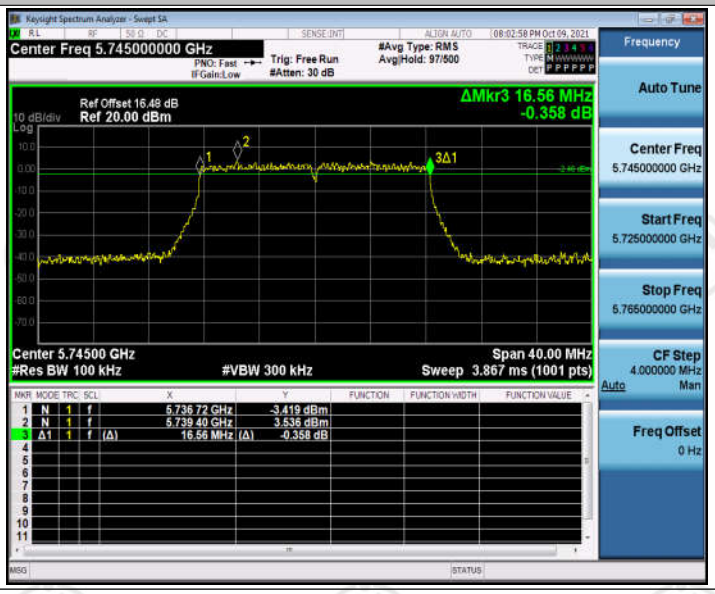
11AC80SISO_Ant2_5775

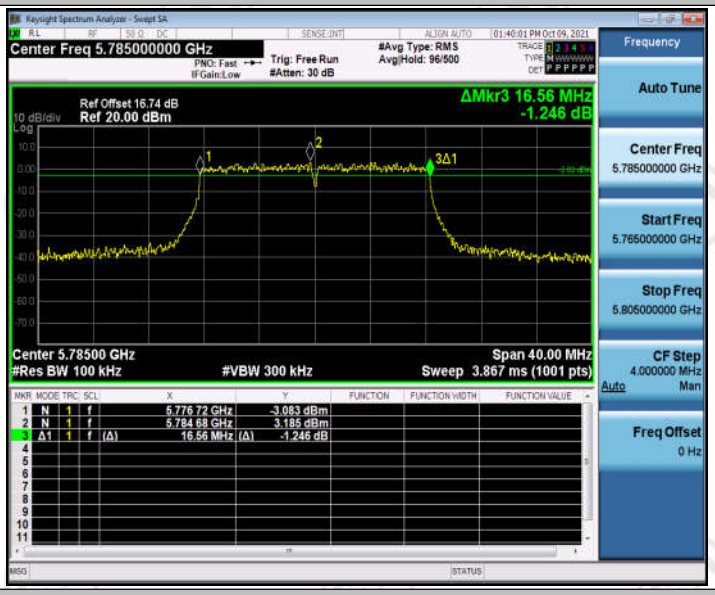
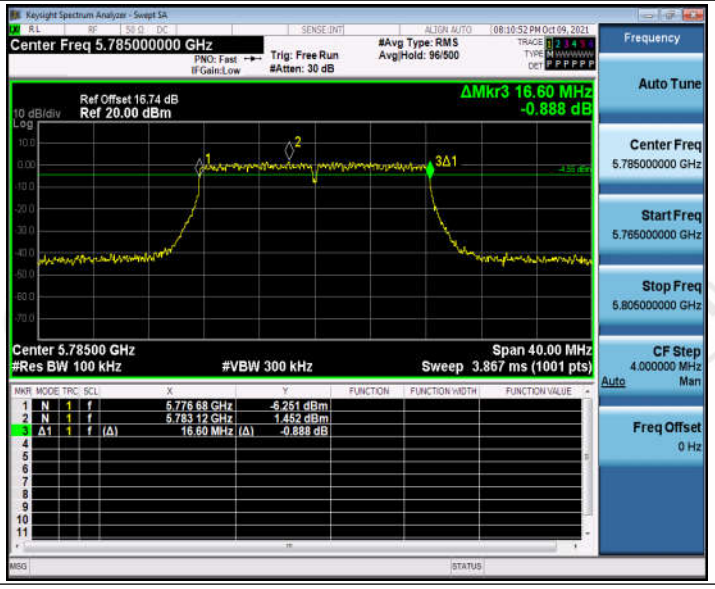

Appendix A3: Min emission bandwidth

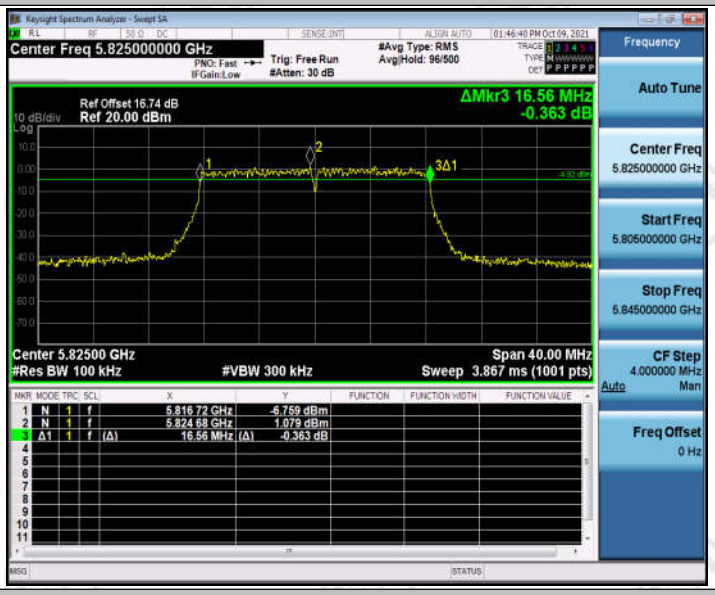
Test Result

TestMode	Antenna	Channel	6db EBW [MHz]	FL[MHz]	FH[MHz]	Limit[MHz]	Verdict
11A	Ant1	5745	16.560	5736.720	5753.280	0.5	PASS
	Ant2	5745	16.560	5736.720	5753.280	0.5	PASS
	Ant1	5785	16.560	5776.720	5793.280	0.5	PASS
	Ant2	5785	16.600	5776.680	5793.280	0.5	PASS
	Ant1	5825	16.560	5816.720	5833.280	0.5	PASS
	Ant2	5825	16.560	5816.720	5833.280	0.5	PASS
11N20SISO	Ant1	5745	17.840	5736.080	5753.920	0.5	PASS
	Ant2	5745	17.840	5736.080	5753.920	0.5	PASS
	Ant1	5785	17.760	5776.120	5793.880	0.5	PASS
	Ant2	5785	17.880	5776.040	5793.920	0.5	PASS
	Ant1	5825	17.840	5816.080	5833.920	0.5	PASS
	Ant2	5825	17.800	5816.080	5833.880	0.5	PASS
11N40SISO	Ant1	5755	36.480	5736.760	5773.240	0.5	PASS
	Ant2	5755	36.560	5736.760	5773.320	0.5	PASS
	Ant1	5795	36.480	5776.760	5813.240	0.5	PASS
	Ant2	5795	36.560	5776.760	5813.320	0.5	PASS
11AC20SISO	Ant1	5745	17.800	5736.080	5753.880	0.5	PASS
	Ant2	5745	17.760	5736.120	5753.880	0.5	PASS
	Ant1	5785	17.800	5776.080	5793.880	0.5	PASS
	Ant2	5785	17.800	5776.080	5793.880	0.5	PASS
	Ant1	5825	17.760	5816.120	5833.880	0.5	PASS
	Ant2	5825	17.760	5816.120	5833.880	0.5	PASS
11AC40SISO	Ant1	5755	36.560	5736.680	5773.240	0.5	PASS
	Ant2	5755	36.480	5736.760	5773.240	0.5	PASS
	Ant1	5795	36.480	5776.760	5813.240	0.5	PASS
	Ant2	5795	36.480	5776.760	5813.240	0.5	PASS
11AC80SISO	Ant1	5775	76.640	5736.600	5813.240	0.5	PASS
	Ant2	5775	76.800	5736.600	5813.400	0.5	PASS

Test Graphs

11A_Ant1_5745

11A_Ant2_5745


11A_Ant1_5785

11A_Ant2_5785


11A_Ant1_5825

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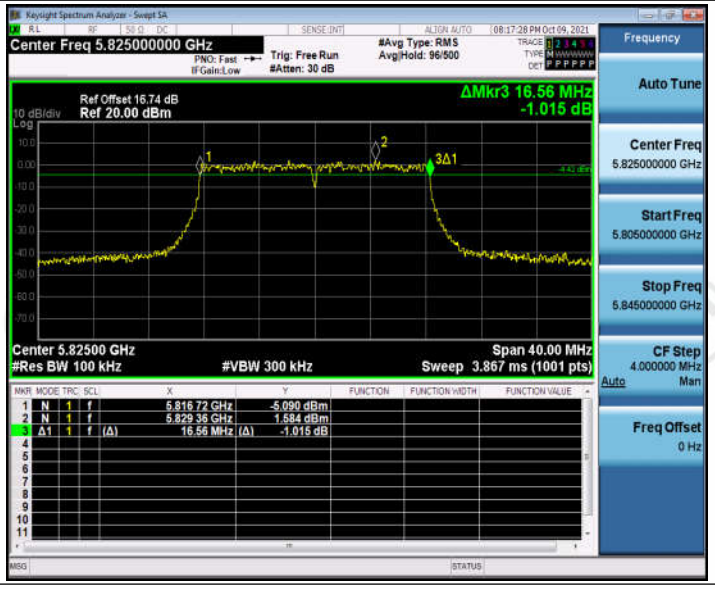
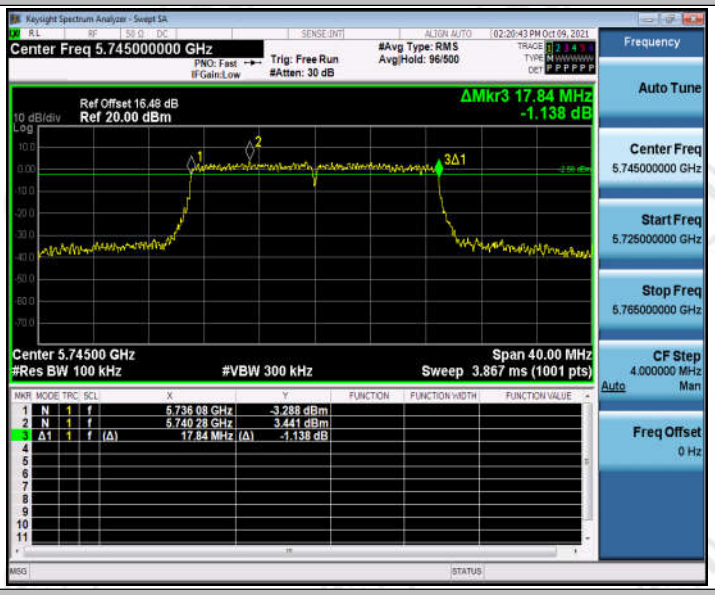


11N20SISO_Ant1_5745

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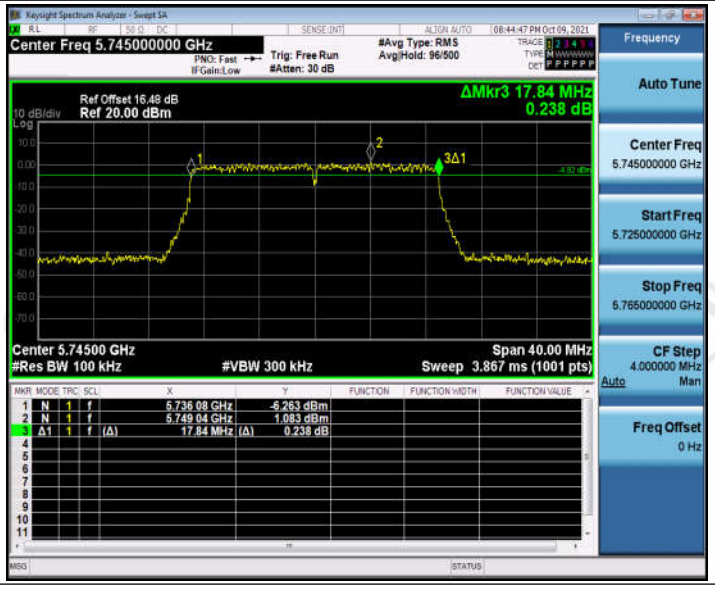
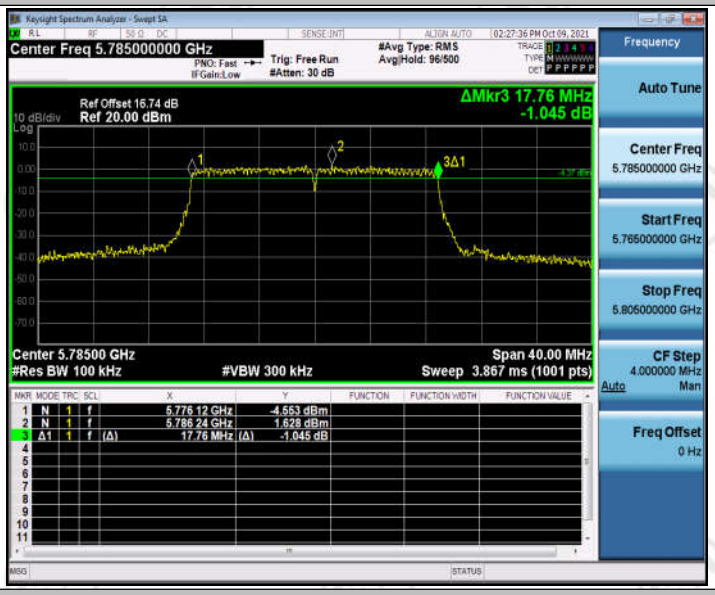


11N20SISO_Ant1_5785

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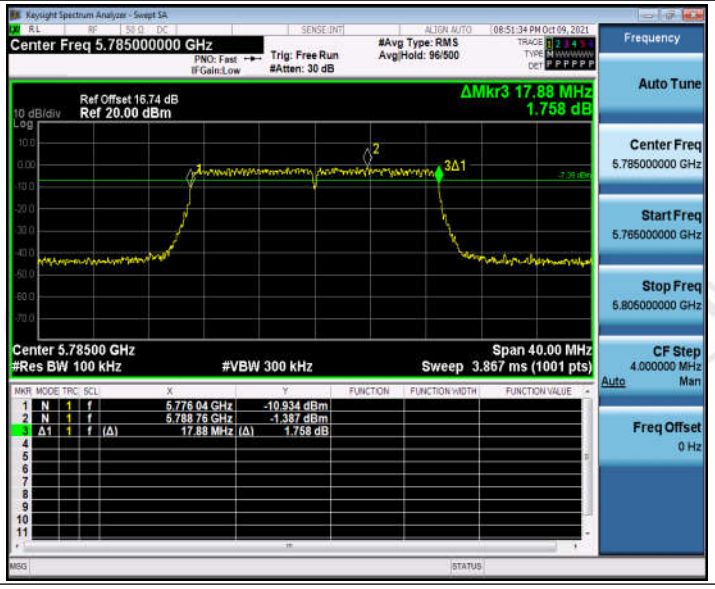
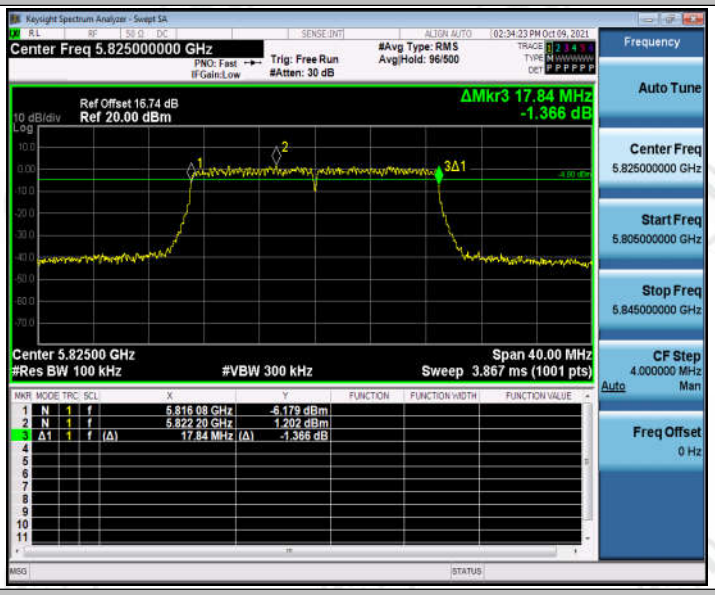
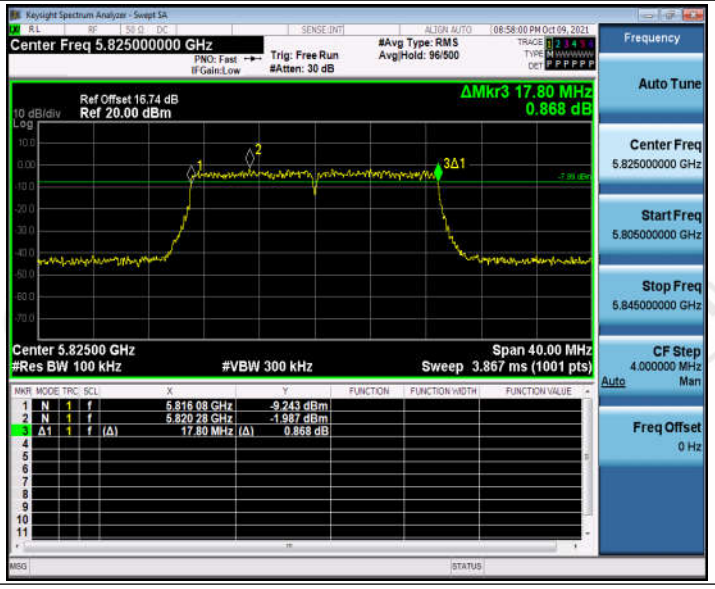
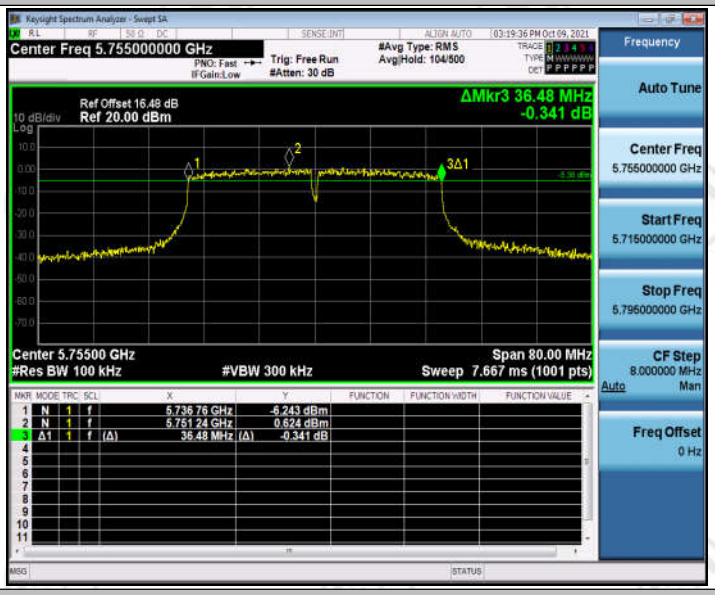
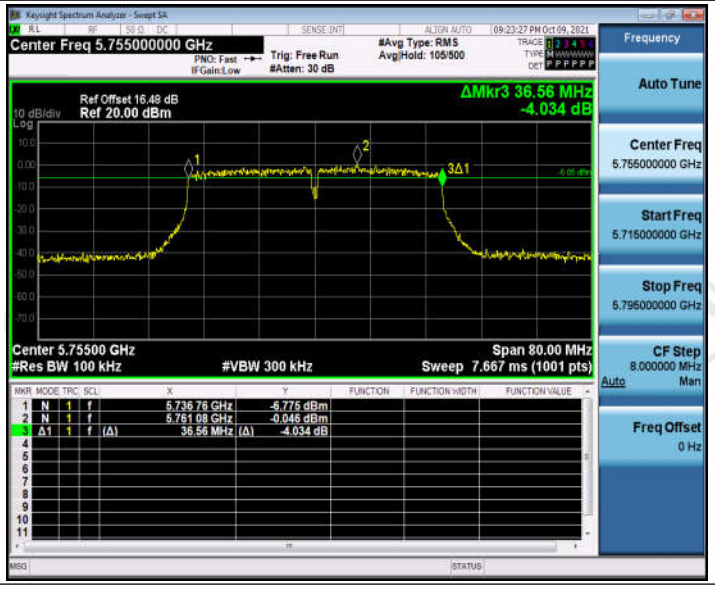
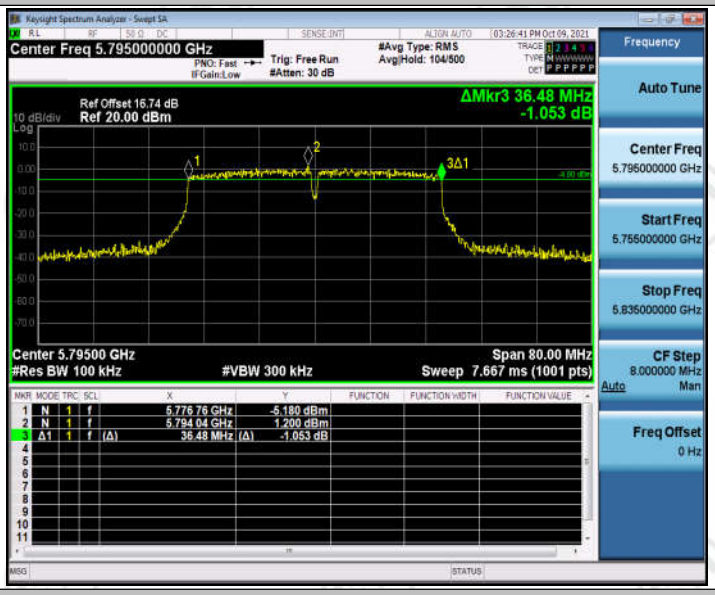
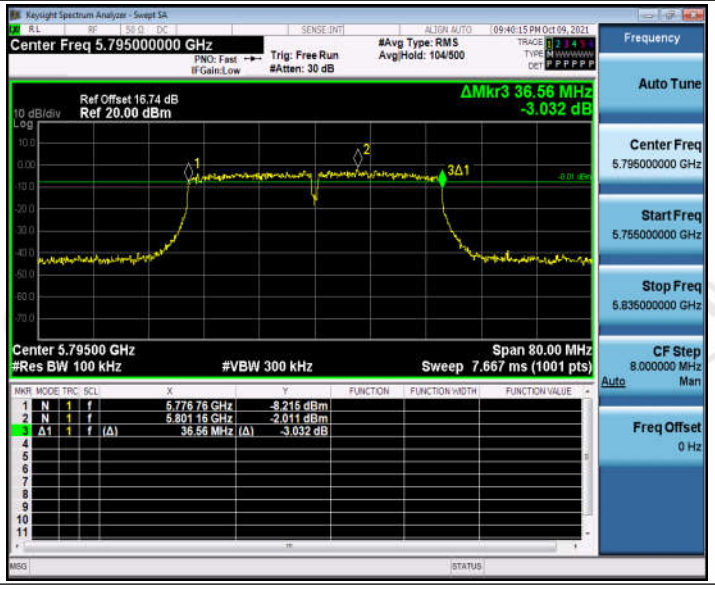
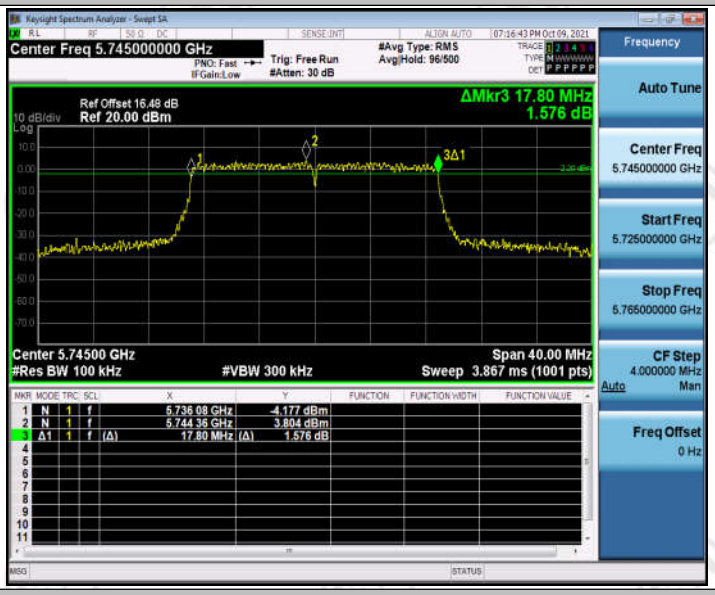


11N20SISO_Ant1_5825

11N20SISO_Ant2_5825


11N40SISO_Ant1_5755

11N40SISO_Ant2_5755


11N40SISO_Ant1_5795

11N40SISO_Ant2_5795


11AC20SISO_Ant1_5745

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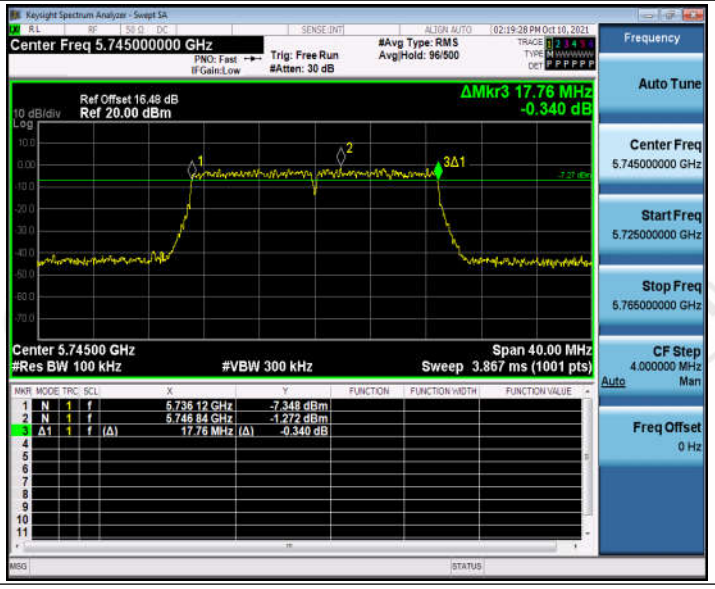
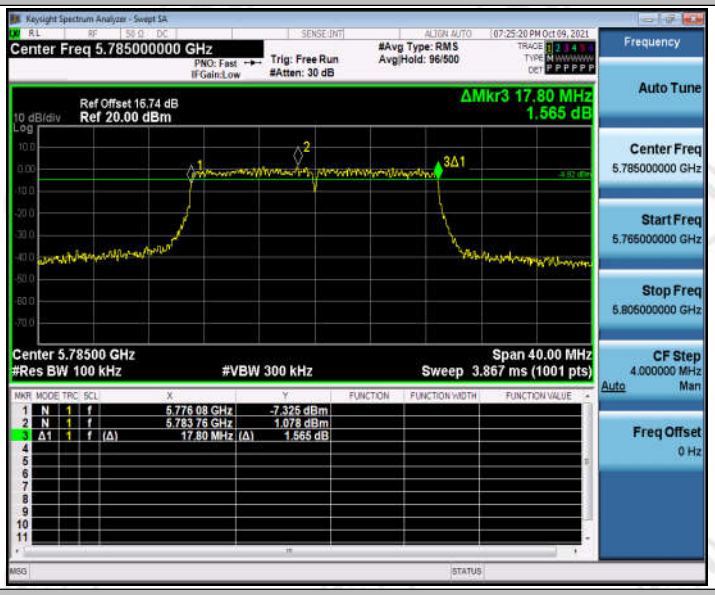
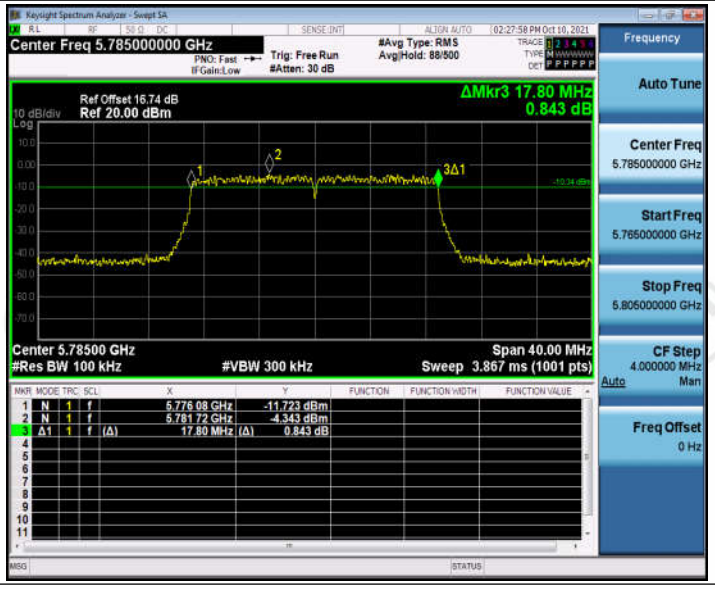
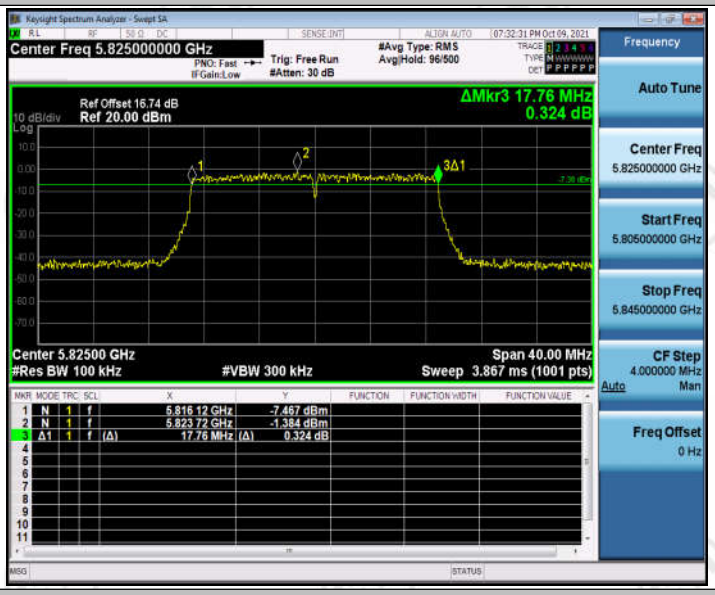
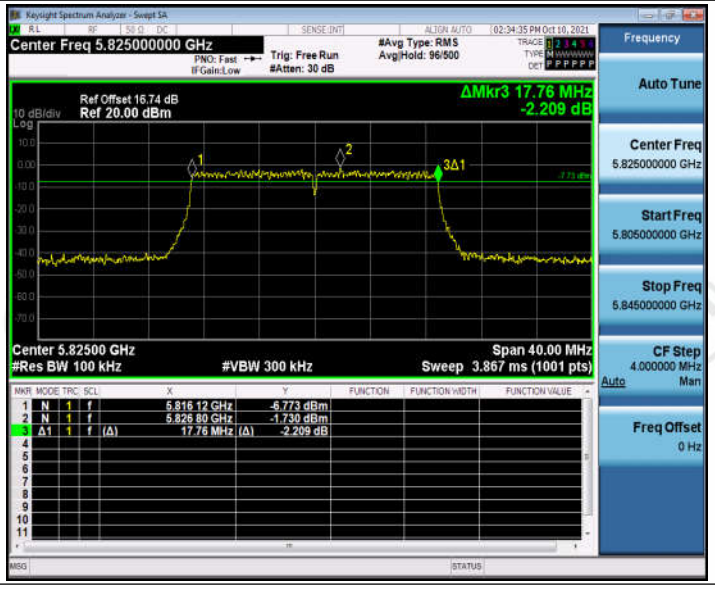
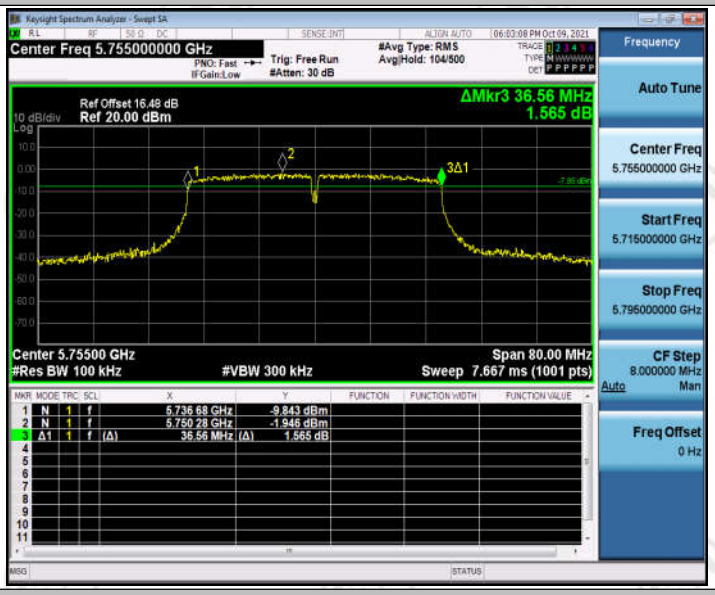
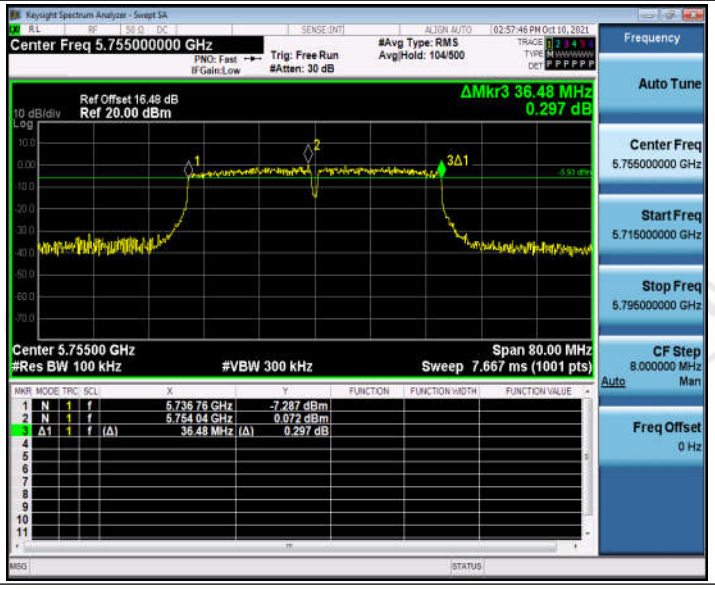
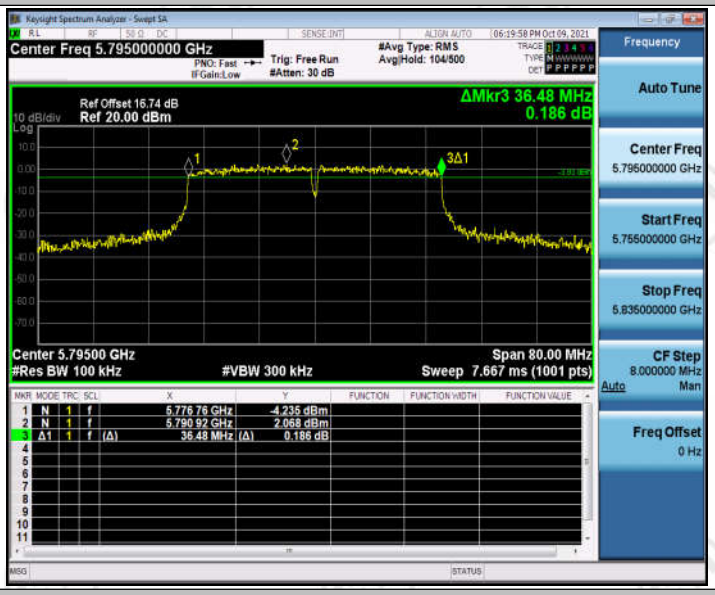
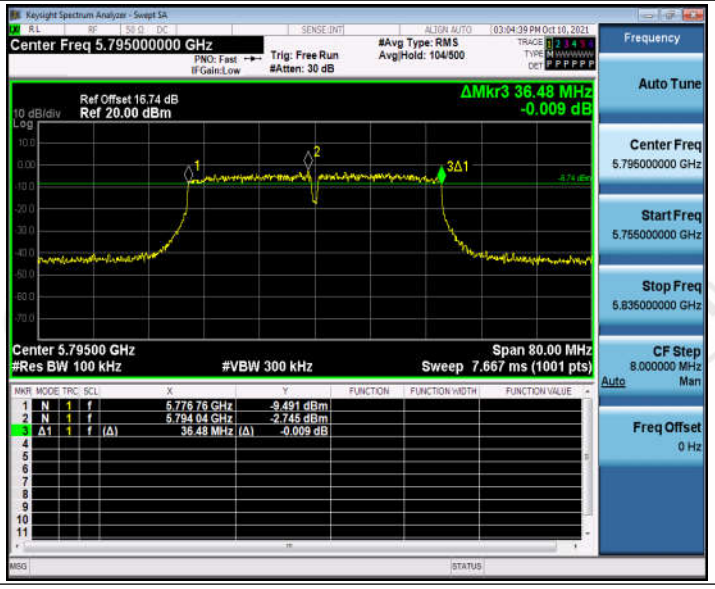
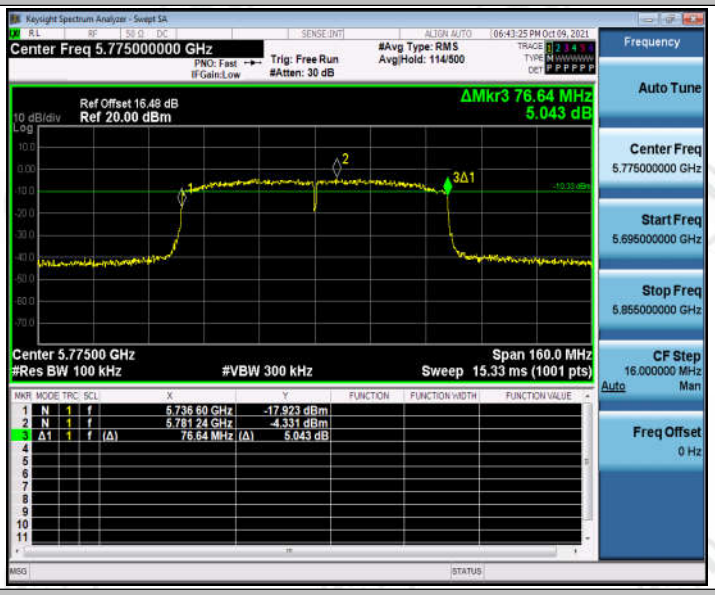


11AC20SISO_Ant1_5785

11AC20SISO_Ant2_5785


11AC20SISO_Ant1_5825

11AC20SISO_Ant2_5825


11AC40SISO_Ant1_5755

11AC40SISO_Ant2_5755


11AC40SISO_Ant1_5795

11AC40SISO_Ant2_5795


11AC80SISO_Ant1_5775

11AC80SISO_Ant2_5775


Appendix B: Maximum conducted output power

Test Result

TestMode	Antenna	Channel	Result[dBm]	Limit[dBm]	Verdict
11A	Ant1	5180	14.81	<=23.98	PASS
	Ant2	5180	14.02	<=23.98	PASS
	Ant1	5200	15.00	<=23.98	PASS
	Ant2	5200	13.85	<=23.98	PASS
	Ant1	5240	14.45	<=23.98	PASS
	Ant2	5240	14.10	<=23.98	PASS
	Ant1	5745	17.46	<=30	PASS
	Ant2	5745	16.32	<=30	PASS
	Ant1	5785	16.54	<=30	PASS
	Ant2	5785	16.04	<=30	PASS
	Ant1	5825	16.67	<=30	PASS
	Ant2	5825	15.88	<=30	PASS
11N20SISO	Ant1	5180	13.17	<=23.98	PASS
	Ant2	5180	13.42	<=23.98	PASS
	Ant1	5200	13.35	<=23.98	PASS
	Ant2	5200	13.38	<=23.98	PASS
	Ant1	5240	12.73	<=23.98	PASS
	Ant2	5240	12.98	<=23.98	PASS
	Ant1	5745	16.76	<=30	PASS
	Ant2	5745	15.80	<=30	PASS
	Ant1	5785	15.87	<=30	PASS
	Ant2	5785	15.60	<=30	PASS
	Ant1	5825	15.62	<=30	PASS
	Ant2	5825	14.50	<=30	PASS
11N40SISO	Ant1	5190	12.95	<=23.98	PASS
	Ant2	5190	13.21	<=23.98	PASS
	Ant1	5230	13.46	<=23.98	PASS
	Ant2	5230	13.67	<=23.98	PASS
	Ant1	5755	16.37	<=30	PASS
	Ant2	5755	15.76	<=30	PASS
	Ant1	5795	16.29	<=30	PASS
	Ant2	5795	16.19	<=30	PASS
11AC20SISO	Ant1	5180	14.19	<=23.98	PASS
	Ant2	5180	14.45	<=23.98	PASS
	Ant1	5200	14.35	<=23.98	PASS

	Ant2	5200	14.32	<=23.98	PASS
	Ant1	5240	13.69	<=23.98	PASS
	Ant2	5240	14.16	<=23.98	PASS
	Ant1	5745	16.60	<=30	PASS
	Ant2	5745	15.85	<=30	PASS
	Ant1	5785	16.77	<=30	PASS
	Ant2	5785	15.45	<=30	PASS
	Ant1	5825	16.63	<=30	PASS
	Ant2	5825	14.96	<=30	PASS
11AC40SISO	Ant1	5190	14.81	<=23.98	PASS
	Ant2	5190	14.84	<=23.98	PASS
	Ant1	5230	14.10	<=23.98	PASS
	Ant2	5230	13.91	<=23.98	PASS
	Ant1	5755	16.02	<=30	PASS
	Ant2	5755	16.41	<=30	PASS
	Ant1	5795	16.21	<=30	PASS
	Ant2	5795	16.86	<=30	PASS
11AC80SISO	Ant1	5210	13.13	<=23.98	PASS
	Ant2	5210	12.82	<=23.98	PASS
	Ant1	5775	14.71	<=30	PASS
	Ant2	5775	13.00	<=30	PASS
11N20MIMO	Ant1	5180	12.16	<=23.98	PASS
	Ant2	5180	11.68	<=23.98	PASS
	total	5180	14.9	<=23.98	PASS
	Ant1	5200	12.06	<=23.98	PASS
	Ant2	5200	11.45	<=23.98	PASS
	total	5200	14.8	<=23.98	PASS
	Ant1	5240	11.38	<=23.98	PASS
	Ant2	5240	10.82	<=23.98	PASS
	total	5240	14.1	<=23.98	PASS
	Ant1	5745	15.03	<=30	PASS
	Ant2	5745	12.60	<=30	PASS
	total	5745	17.0	<=30	PASS
	Ant1	5785	13.79	<=30	PASS
	Ant2	5785	10.23	<=30	PASS
	total	5785	15.4	<=30	PASS
	Ant1	5825	14.51	<=30	PASS
	Ant2	5825	10.57	<=30	PASS
	total	5825	16.0	<=30	PASS
11N40MIMO	Ant1	5190	11.68	<=23.98	PASS

	Ant2	5190	11.41	<=23.98	PASS
	total	5190	14.6	<=23.98	PASS
	Ant1	5230	10.92	<=23.98	PASS
	Ant2	5230	10.90	<=23.98	PASS
	total	5230	13.9	<=23.98	PASS
	Ant1	5755	14.70	<=30	PASS
	Ant2	5755	12.57	<=30	PASS
	total	5755	16.8	<=30	PASS
	Ant1	5795	14.11	<=30	PASS
	Ant2	5795	10.80	<=30	PASS
	total	5795	15.8	<=30	PASS
11AC20MIMO	Ant1	5180	12.82	<=23.98	PASS
	Ant2	5180	12.67	<=23.98	PASS
	total	5180	15.8	<=23.98	PASS
	Ant1	5200	11.45	<=23.98	PASS
	Ant2	5200	12.46	<=23.98	PASS
	total	5200	15.0	<=23.98	PASS
	Ant1	5240	12.30	<=23.98	PASS
	Ant2	5240	11.74	<=23.98	PASS
	total	5240	15.0	<=23.98	PASS
	Ant1	5745	14.79	<=30	PASS
	Ant2	5745	12.42	<=30	PASS
	total	5745	16.8	<=30	PASS
	Ant1	5785	14.46	<=30	PASS
	Ant2	5785	11.26	<=30	PASS
	total	5785	16.2	<=30	PASS
	Ant1	5825	14.20	<=30	PASS
	Ant2	5825	10.55	<=30	PASS
	total	5825	15.8	<=30	PASS
11AC40MIMO	Ant1	5190	11.60	<=23.98	PASS
	Ant2	5190	11.17	<=23.98	PASS
	total	5190	14.4	<=23.98	PASS
	Ant1	5230	10.67	<=23.98	PASS
	Ant2	5230	10.74	<=23.98	PASS
	total	5230	13.7	<=23.98	PASS
	Ant1	5755	14.73	<=30	PASS
	Ant2	5755	12.47	<=30	PASS
	total	5755	16.8	<=30	PASS
	Ant1	5795	13.32	<=30	PASS
	Ant2	5795	9.79	<=30	PASS

	total	5795	14.9	<=30	PASS
11AC80MIMO	Ant1	5210	11.88	<=23.98	PASS
	Ant2	5210	11.31	<=23.98	PASS
	total	5210	14.6	<=23.98	PASS
	Ant1	5775	14.65	<=30	PASS
	Ant2	5775	11.38	<=30	PASS
	total	5775	16.3	<=30	PASS

Note：The Duty Cycle Factor is compensated in the graph.

Appendix C: Maximum power spectral density

Test Result

TestMode	Antenna	Channel	Result [dBm/MHz]	Limit[dBm/MHz]	Verdict
11A	Ant1	5180	9.77	<=11	PASS
	Ant2	5180	8.74	<=11	PASS
	Ant1	5200	10.52	<=11	PASS
	Ant2	5200	8.3	<=11	PASS
	Ant1	5240	9.42	<=11	PASS
	Ant2	5240	9.25	<=11	PASS
	Ant1	5745	9.69	<=30	PASS
	Ant2	5745	7.98	<=30	PASS
	Ant1	5785	7.81	<=30	PASS
	Ant2	5785	8.32	<=30	PASS
	Ant1	5825	9.31	<=30	PASS
	Ant2	5825	7.26	<=30	PASS
11N20SISO	Ant1	5180	7.16	<=11	PASS
	Ant2	5180	8.16	<=11	PASS
	Ant1	5200	8.54	<=11	PASS
	Ant2	5200	7.71	<=11	PASS
	Ant1	5240	8.02	<=11	PASS
	Ant2	5240	8.11	<=11	PASS
	Ant1	5745	8.97	<=30	PASS
	Ant2	5745	7.73	<=30	PASS
	Ant1	5785	9.17	<=30	PASS
	Ant2	5785	6.88	<=30	PASS
	Ant1	5825	6.96	<=30	PASS
	Ant2	5825	6.67	<=30	PASS
11N40SISO	Ant1	5190	4.4	<=11	PASS
	Ant2	5190	4.64	<=11	PASS
	Ant1	5230	5.12	<=11	PASS
	Ant2	5230	4.91	<=11	PASS
	Ant1	5755	6.3	<=30	PASS
	Ant2	5755	4.26	<=30	PASS
	Ant1	5795	5.36	<=30	PASS
	Ant2	5795	5.42	<=30	PASS
11AC20SISO	Ant1	5180	8.77	<=11	PASS
	Ant2	5180	8.49	<=11	PASS
	Ant1	5200	8.84	<=11	PASS

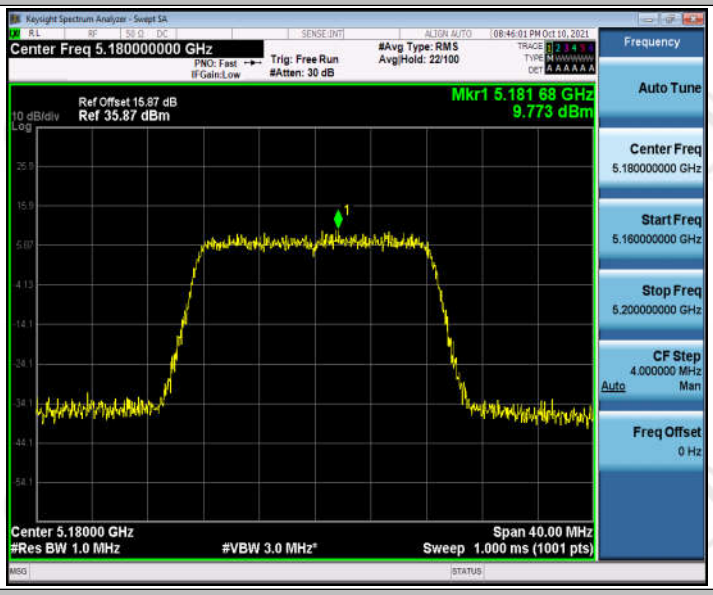
	Ant2	5200	8.62	<=11	PASS
	Ant1	5240	8.52	<=11	PASS
	Ant2	5240	8.35	<=11	PASS
	Ant1	5745	9.18	<=30	PASS
	Ant2	5745	8.22	<=30	PASS
	Ant1	5785	10.04	<=30	PASS
	Ant2	5785	6.66	<=30	PASS
	Ant1	5825	7.91	<=30	PASS
	Ant2	5825	6.59	<=30	PASS
11AC40SISO	Ant1	5190	7.26	<=11	PASS
	Ant2	5190	6.45	<=11	PASS
	Ant1	5230	6.53	<=11	PASS
	Ant2	5230	5.29	<=11	PASS
	Ant1	5755	4.97	<=30	PASS
	Ant2	5755	5.75	<=30	PASS
	Ant1	5795	6.24	<=30	PASS
	Ant2	5795	7.16	<=30	PASS
11AC80SISO	Ant1	5210	1.12	<=11	PASS
	Ant2	5210	1.18	<=11	PASS
	Ant1	5775	0.55	<=30	PASS
	Ant2	5775	-1.23	<=30	PASS
11N20MIMO	Ant1	5180	6.51	<=11	PASS
	Ant2	5180	5.32	<=11	PASS
	total	5180	8.97	<=11	PASS
	Ant1	5200	6.3	<=11	PASS
	Ant2	5200	4.87	<=11	PASS
	total	5200	8.65	<=11	PASS
	Ant1	5240	5.48	<=11	PASS
	Ant2	5240	5.7	<=11	PASS
	total	5240	8.60	<=11	PASS
	Ant1	5745	8.28	<=30	PASS
	Ant2	5745	3.9	<=30	PASS
	total	5745	9.63	<=30	PASS
	Ant1	5785	6.86	<=30	PASS
	Ant2	5785	2.09	<=30	PASS
	total	5785	8.11	<=30	PASS
	Ant1	5825	6.23	<=30	PASS
	Ant2	5825	1.95	<=30	PASS
	total	5825	7.61	<=30	PASS
11N40MIMO	Ant1	5190	3.33	<=11	PASS

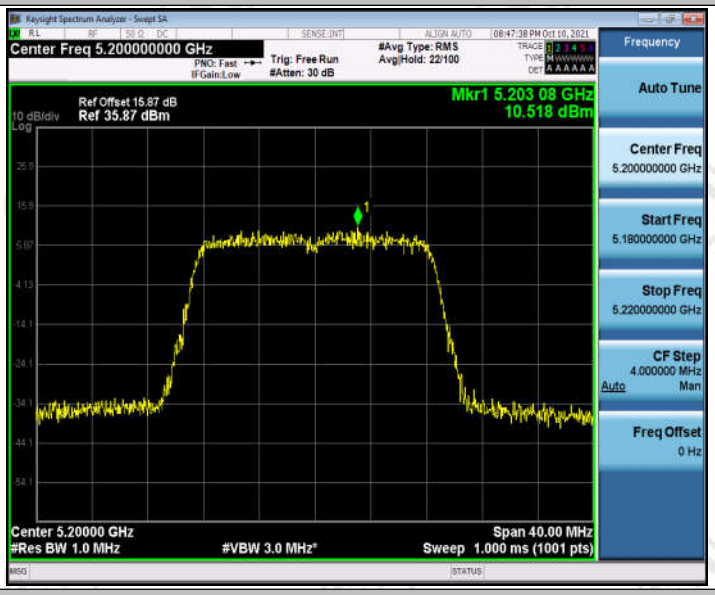
	Ant2	5190	3.47	<=11	PASS
	total	5190	6.41	<=11	PASS
	Ant1	5230	2.45	<=11	PASS
	Ant2	5230	2.13	<=11	PASS
	total	5230	5.30	<=11	PASS
	Ant1	5755	4.12	<=30	PASS
	Ant2	5755	2.45	<=30	PASS
	total	5755	6.38	<=30	PASS
	Ant1	5795	3.58	<=30	PASS
	Ant2	5795	-0.61	<=30	PASS
	total	5795	4.98	<=30	PASS
11AC20MIMO	Ant1	5180	7.38	<=11	PASS
	Ant2	5180	7.17	<=11	PASS
	total	5180	10.29	<=11	PASS
	Ant1	5200	5.72	<=11	PASS
	Ant2	5200	7	<=11	PASS
	total	5200	9.42	<=11	PASS
	Ant1	5240	6.96	<=11	PASS
	Ant2	5240	6.25	<=11	PASS
	total	5240	9.63	<=11	PASS
	Ant1	5745	7.18	<=30	PASS
	Ant2	5745	4.4	<=30	PASS
	total	5745	9.02	<=30	PASS
	Ant1	5785	7.42	<=30	PASS
	Ant2	5785	3.09	<=30	PASS
	total	5785	8.78	<=30	PASS
	Ant1	5825	5.28	<=30	PASS
	Ant2	5825	2.57	<=30	PASS
	total	5825	7.14	<=30	PASS
11AC40MIMO	Ant1	5190	3.16	<=11	PASS
	Ant2	5190	2.21	<=11	PASS
	total	5190	5.72	<=11	PASS
	Ant1	5230	2.04	<=11	PASS
	Ant2	5230	2.04	<=11	PASS
	total	5230	5.05	<=11	PASS
	Ant1	5755	4.63	<=30	PASS
	Ant2	5755	1.03	<=30	PASS
	total	5755	6.20	<=30	PASS
	Ant1	5795	2.15	<=30	PASS
	Ant2	5795	-0.66	<=30	PASS

	total	5795	3.98	<=30	PASS
11AC80MIMO	Ant1	5210	-0.12	<=11	PASS
	Ant2	5210	-0.56	<=11	PASS
	total	5210	2.68	<=11	PASS
	Ant1	5775	0.91	<=30	PASS
	Ant2	5775	-2.61	<=30	PASS
	total	5775	2.51	<=30	PASS

Note：1.The Result and Limit Unit is dBm/510 kHz in the band 5.725–5.85 GHz.
2.The Duty Cycle Factor and RBW Factor is compensated in the graph.

Test Graphs

11A_Ant1_5180

11A_Ant2_5180


11A_Ant1_5200

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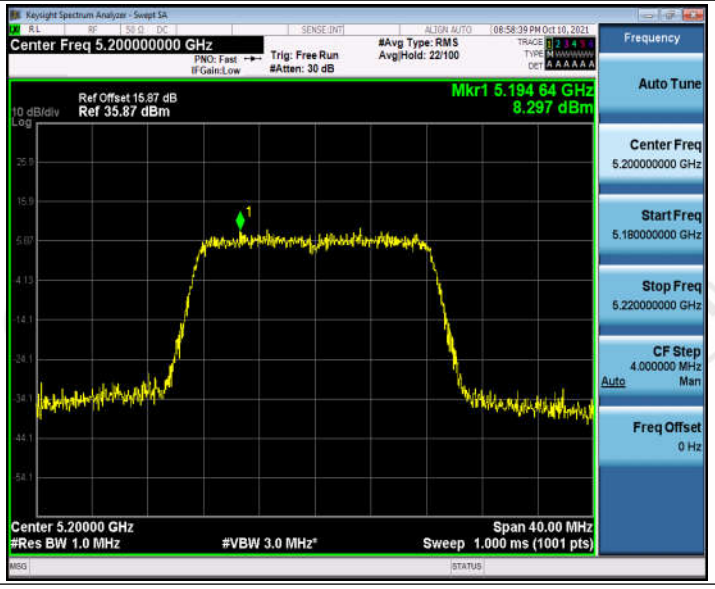

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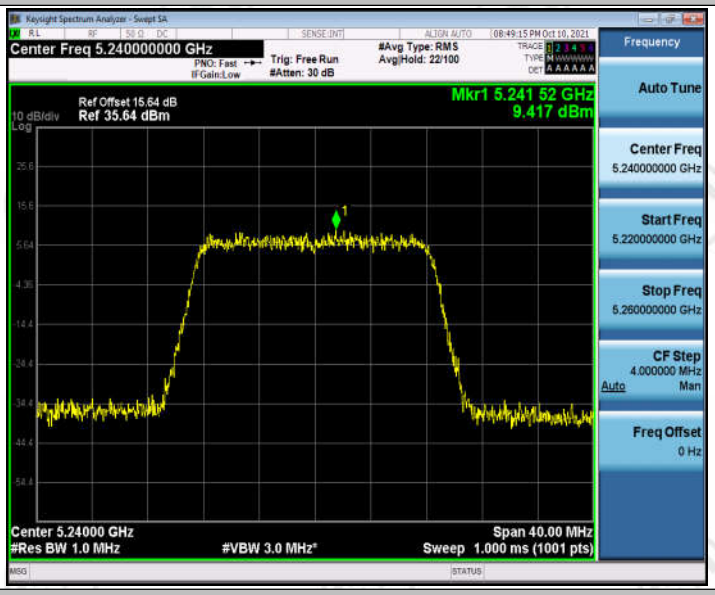
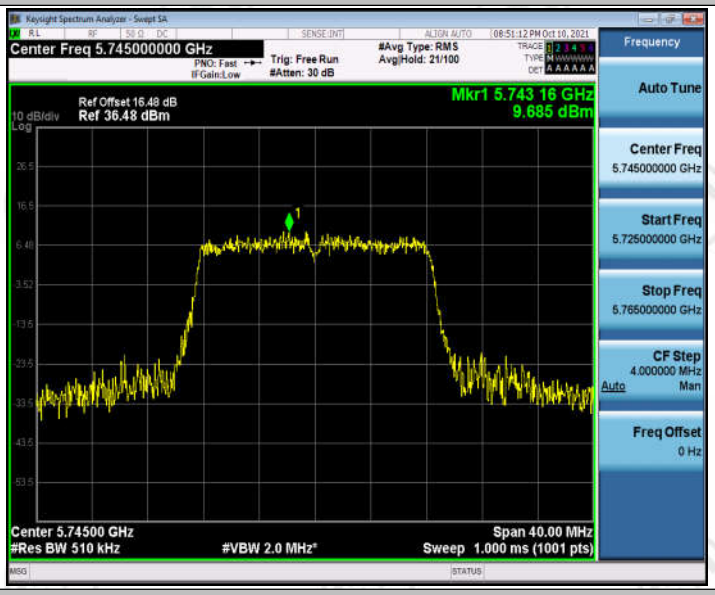

11A_Ant2_5240


11A_Ant1_5745

11A_Ant2_5745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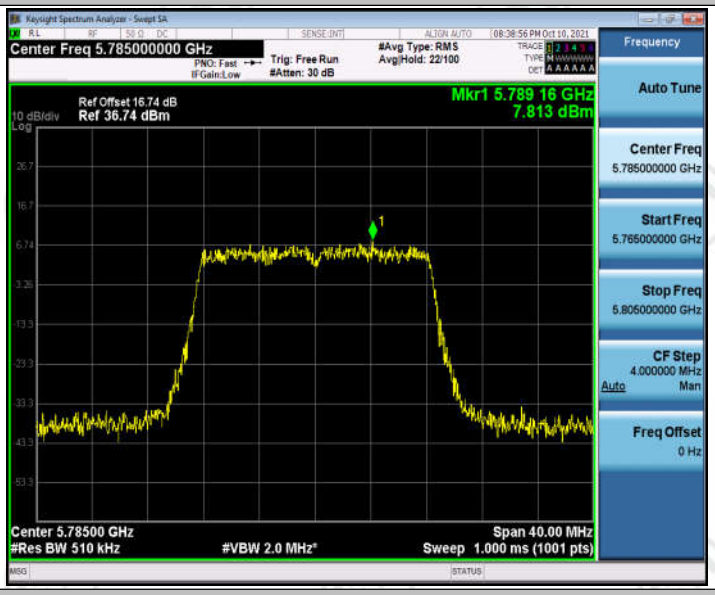
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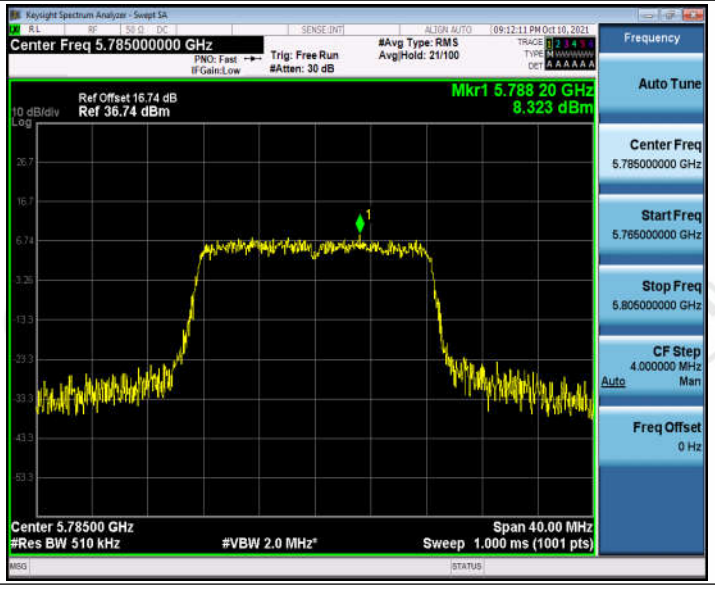

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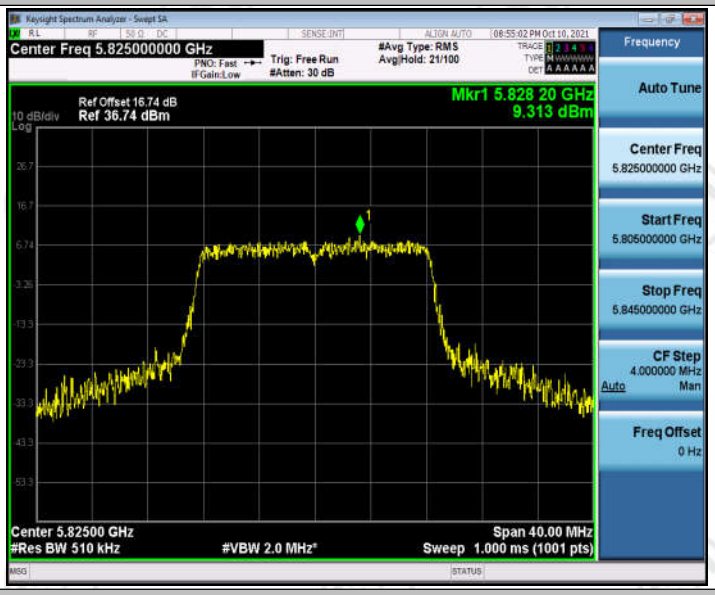
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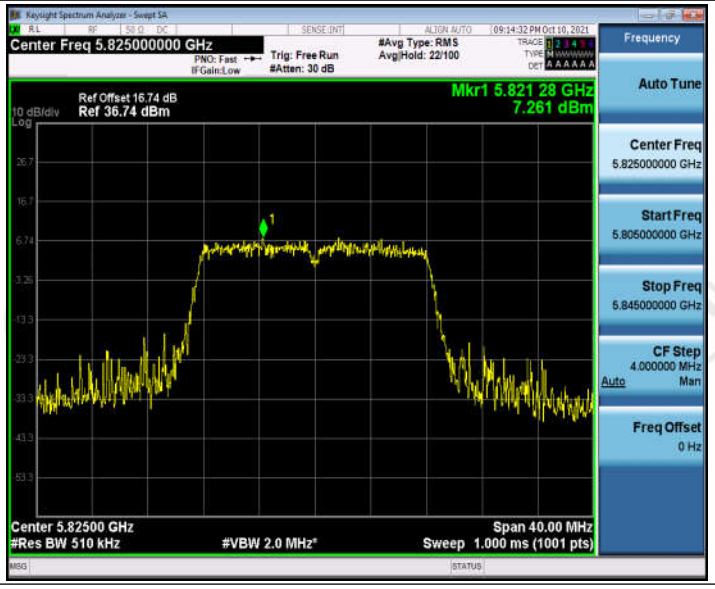

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11N20SISO_Ant2_5180


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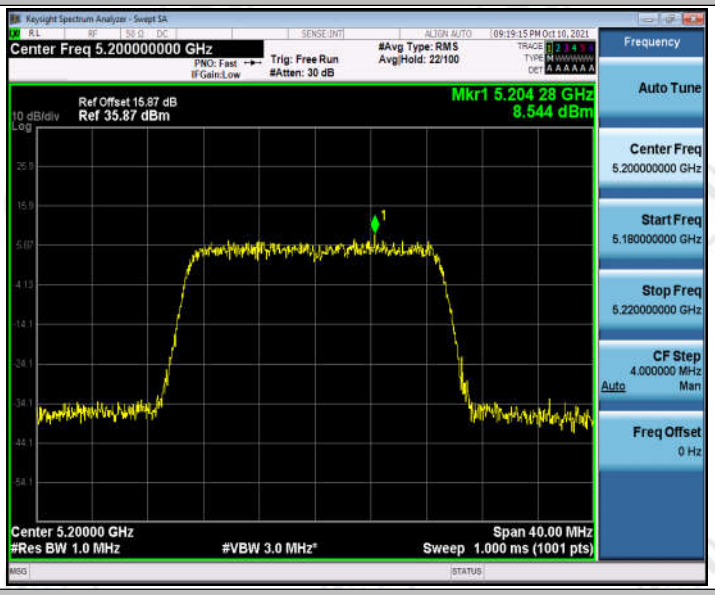
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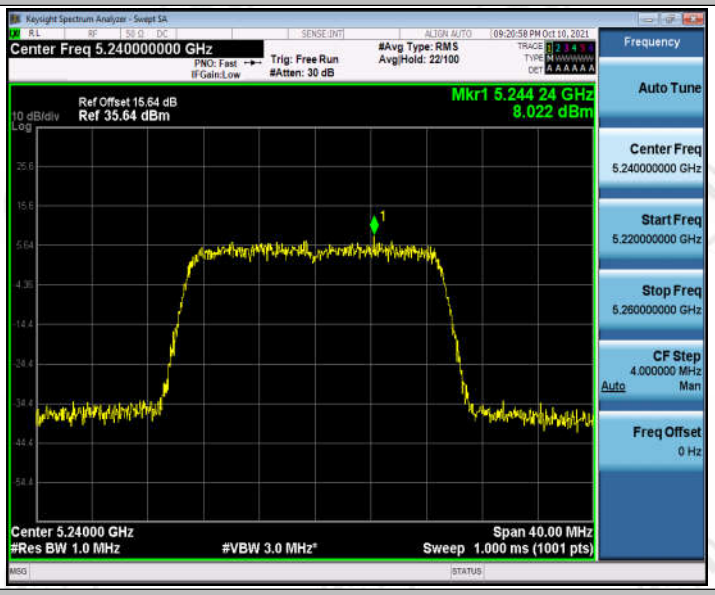
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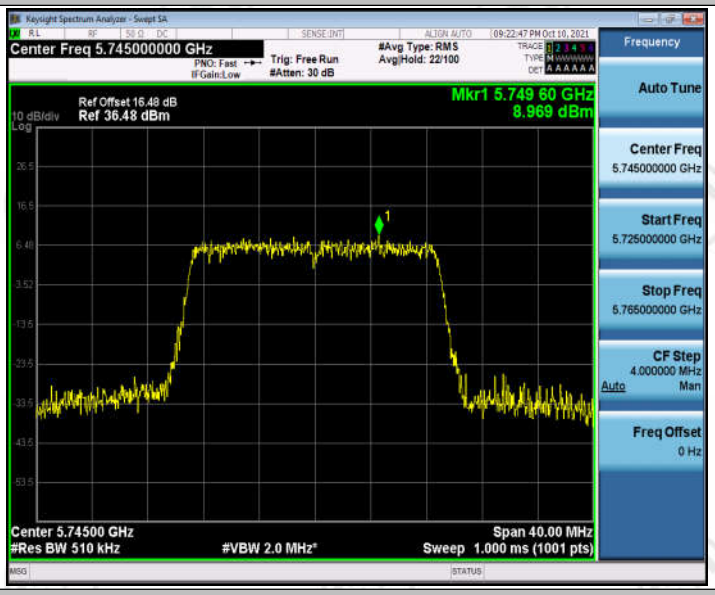
11N20SISO_Ant1_5745

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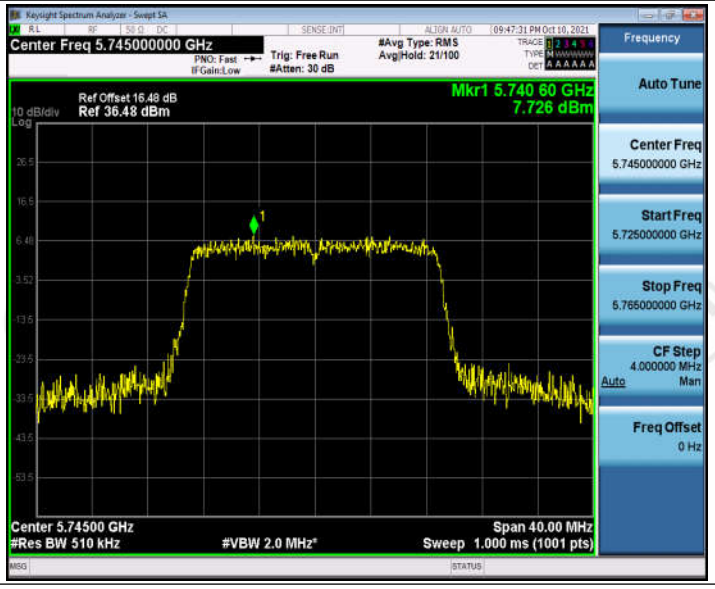

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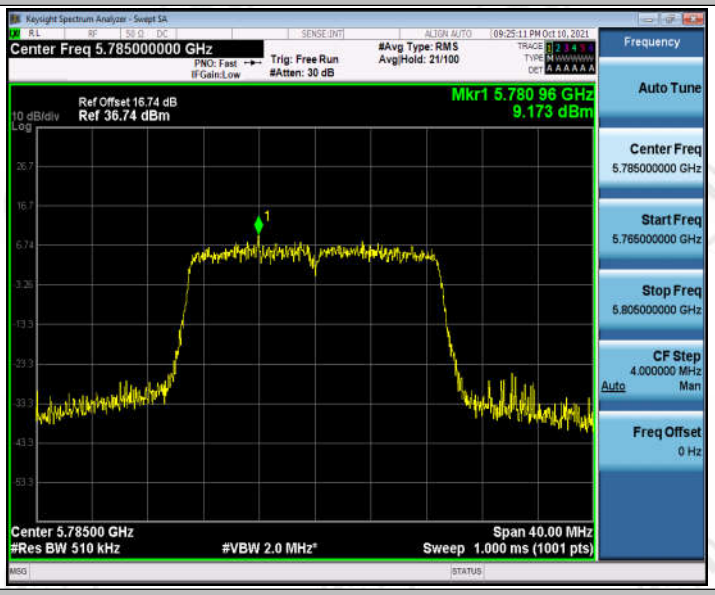
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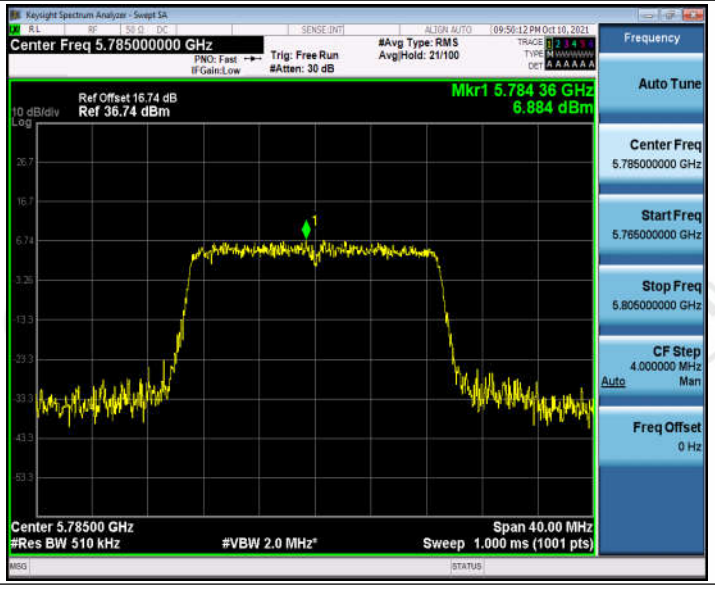

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11N20SISO_Ant2_5825


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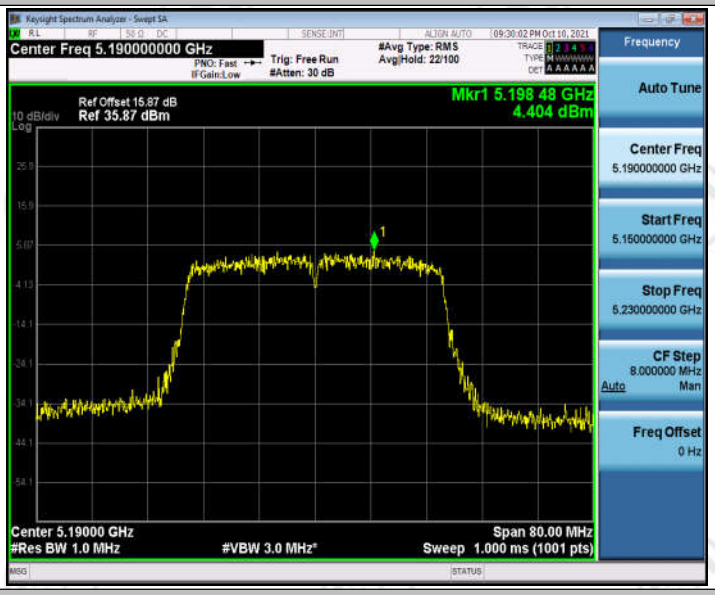
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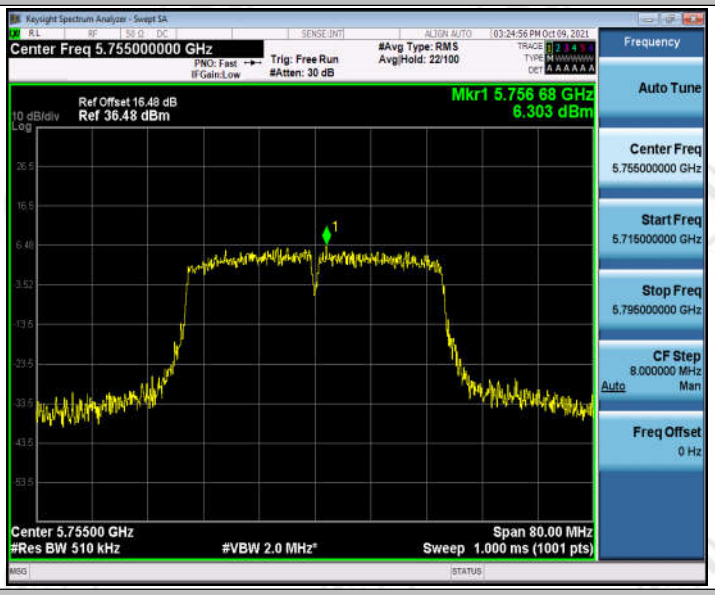
11N40SISO_Ant1_5230

11N40SISO_Ant2_5230


11N40SISO_Ant1_5755

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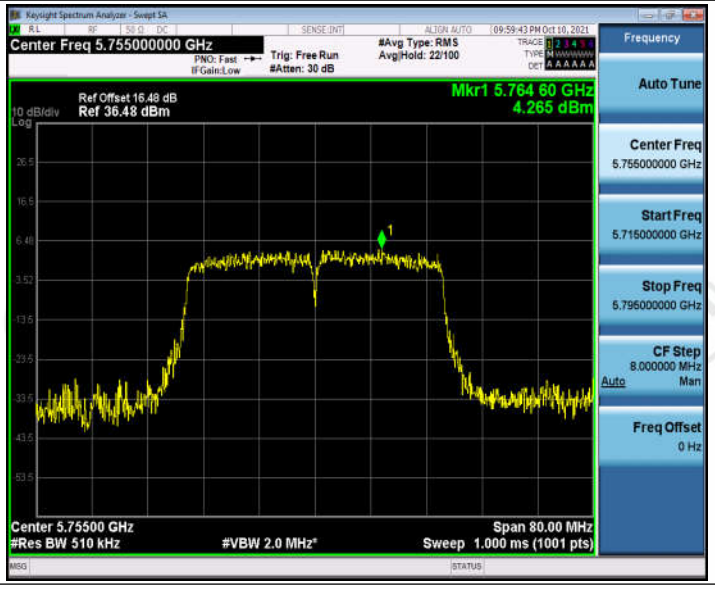

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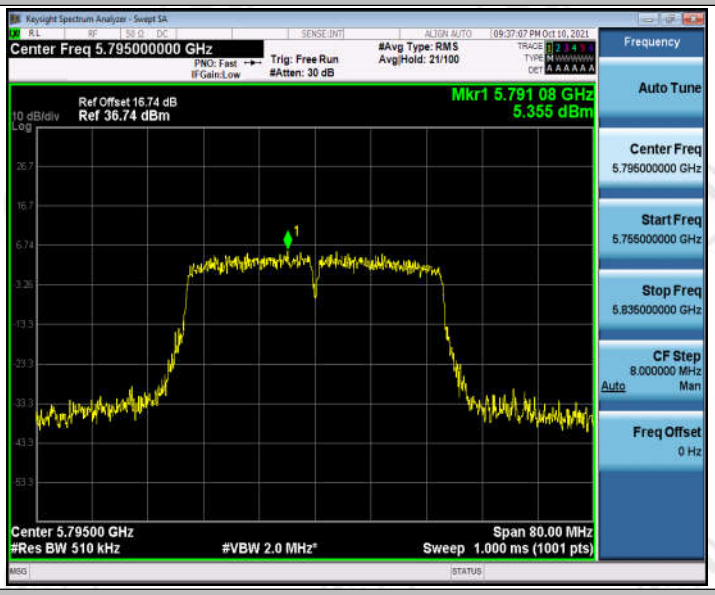
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11AC20SISO_Ant1_5180

11AC20SISO_Ant2_5180


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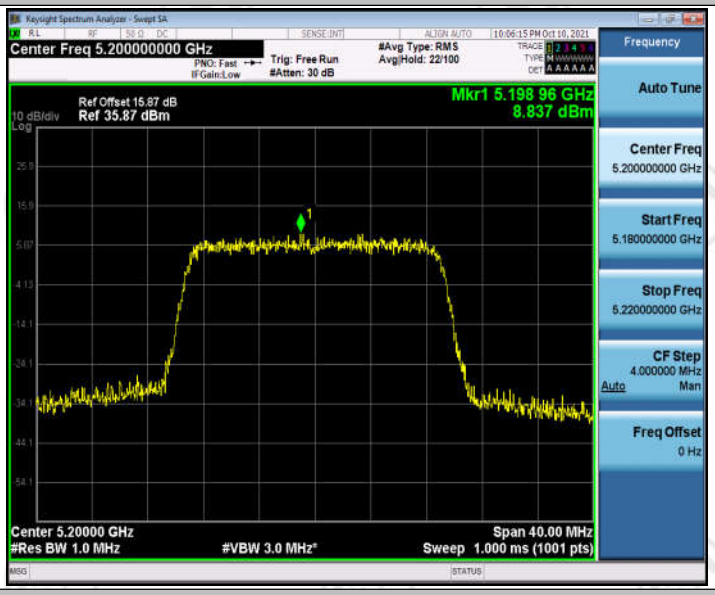
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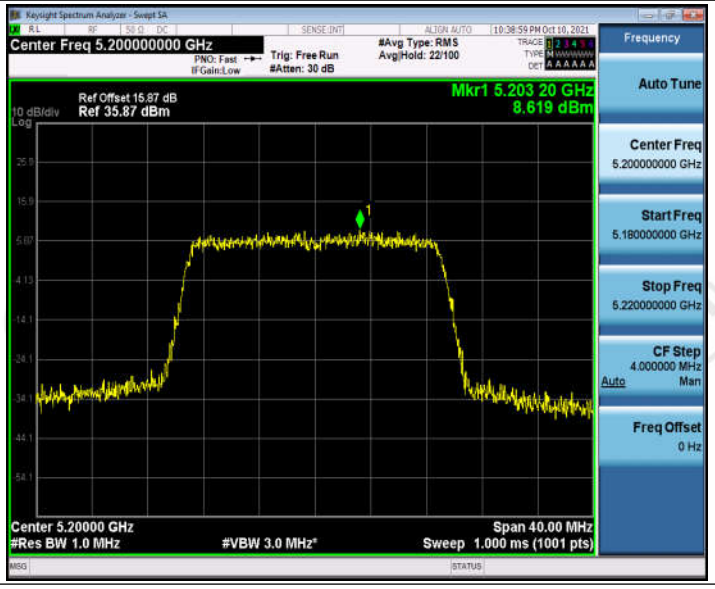

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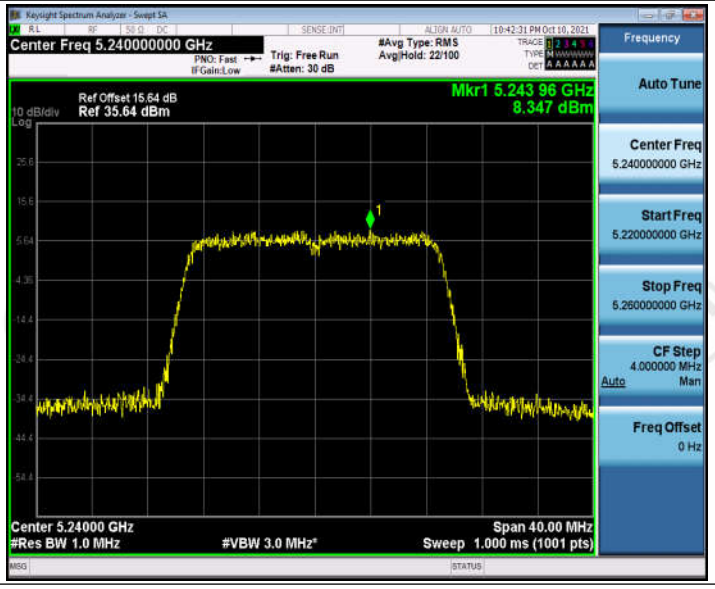

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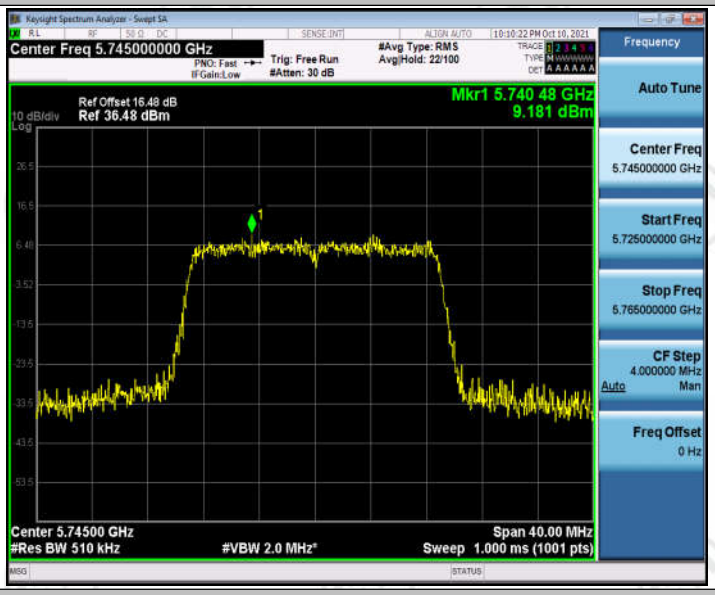
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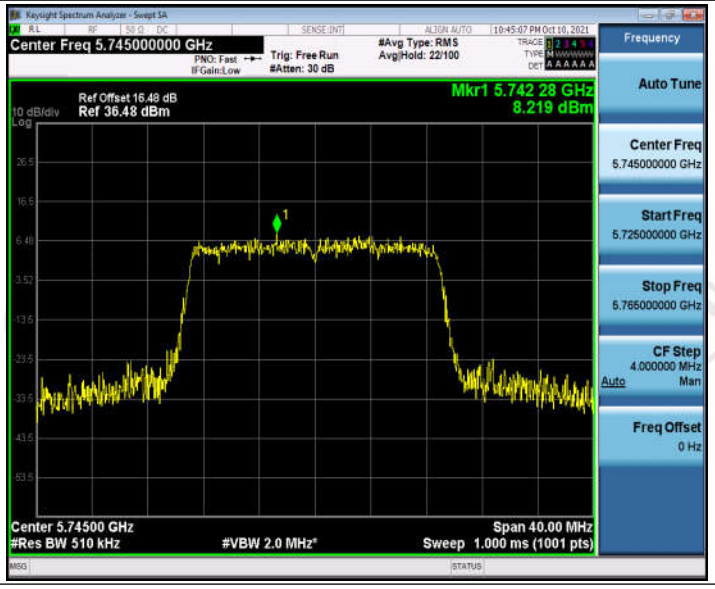

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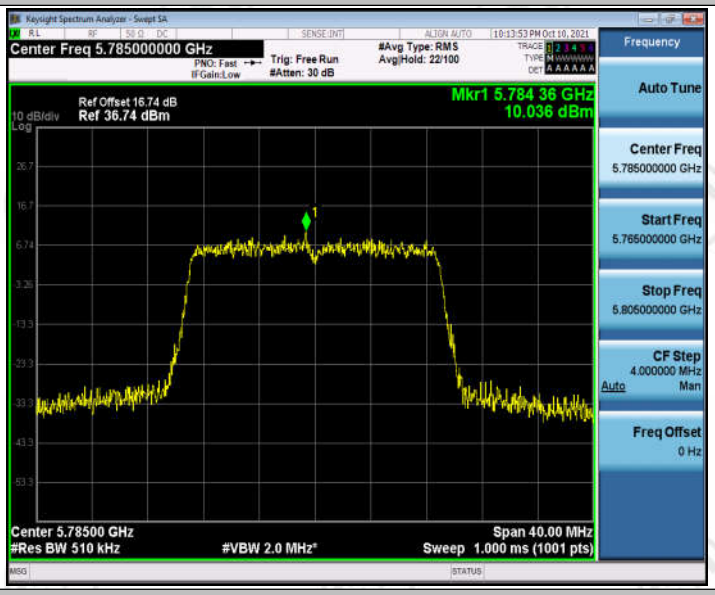
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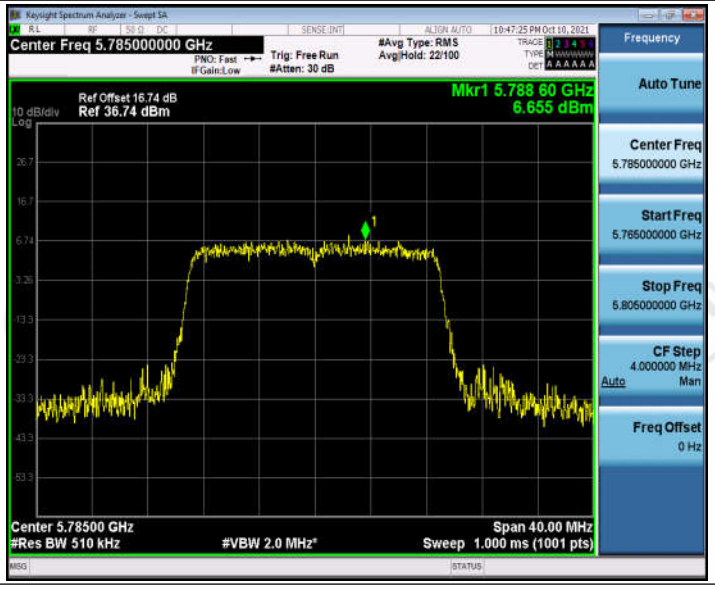

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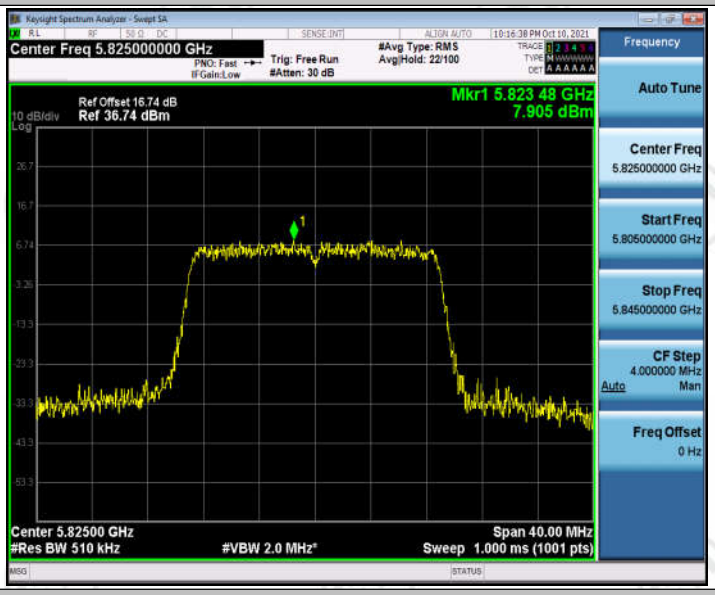
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11AC40SISO_Ant1_5190

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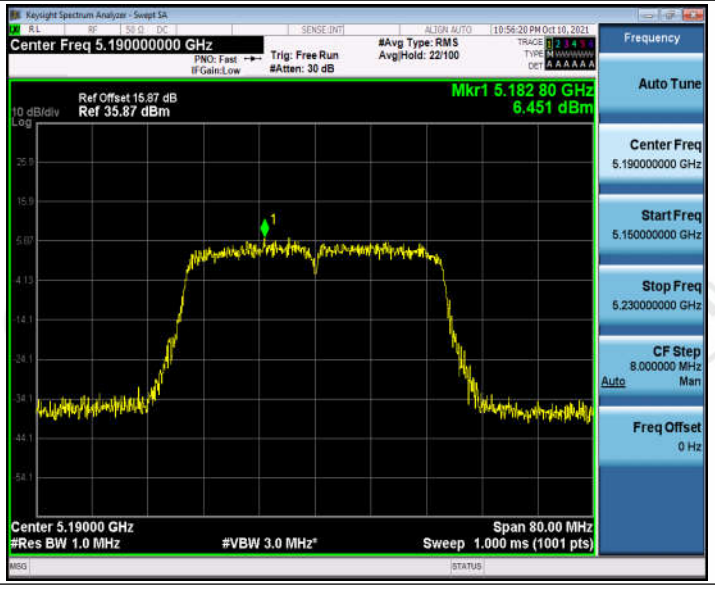

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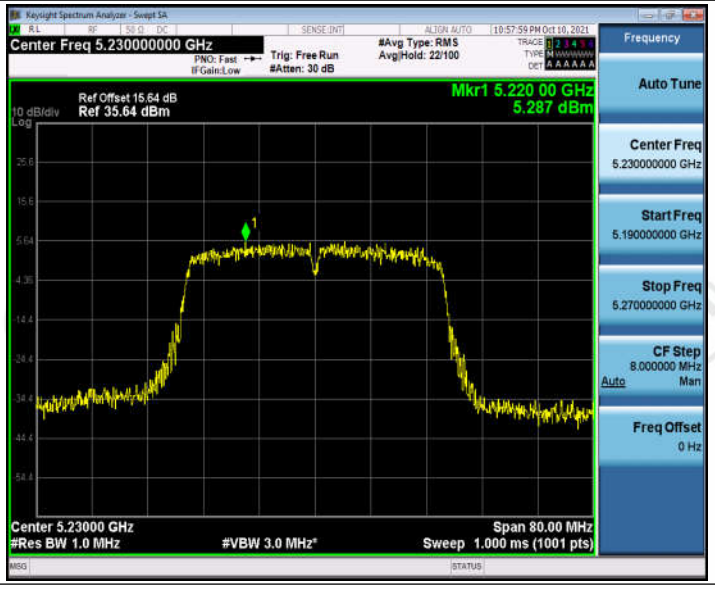

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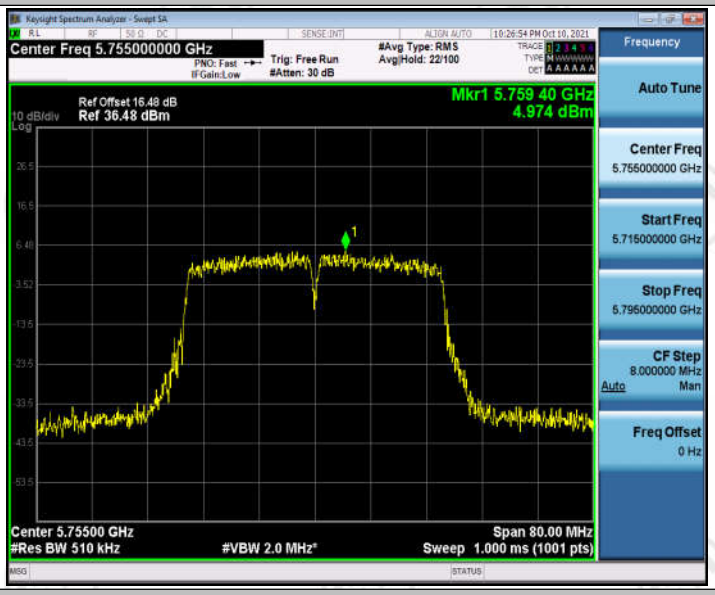
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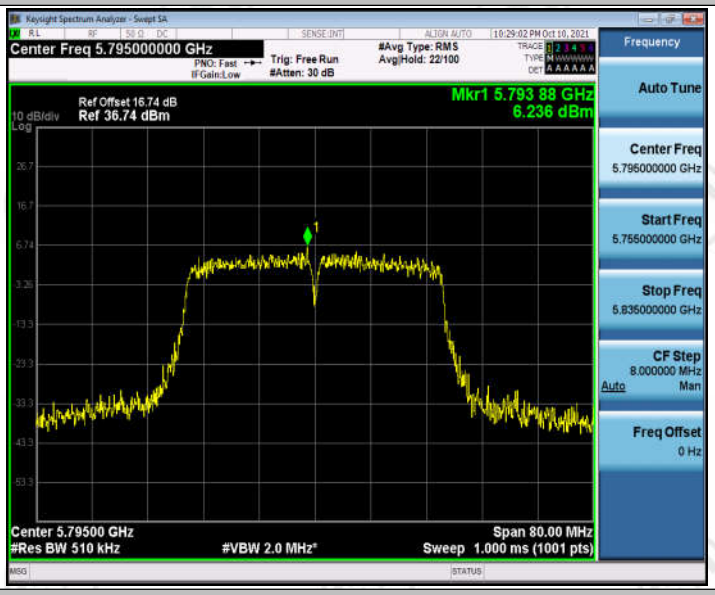
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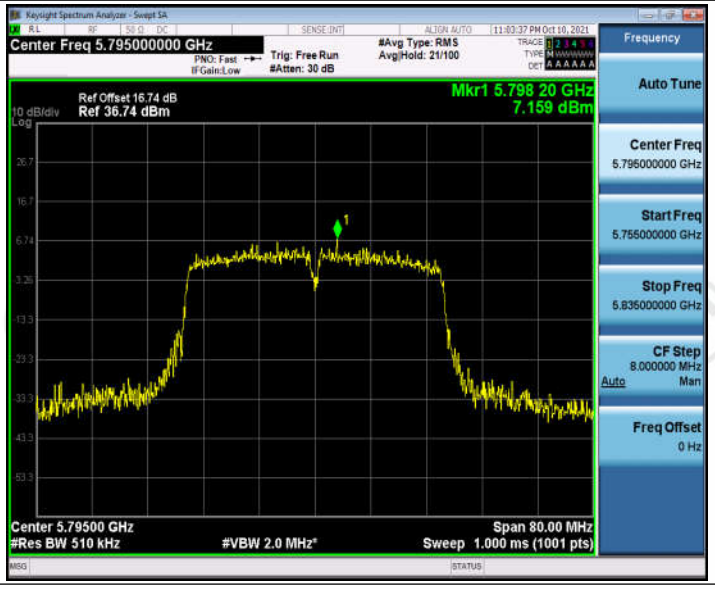

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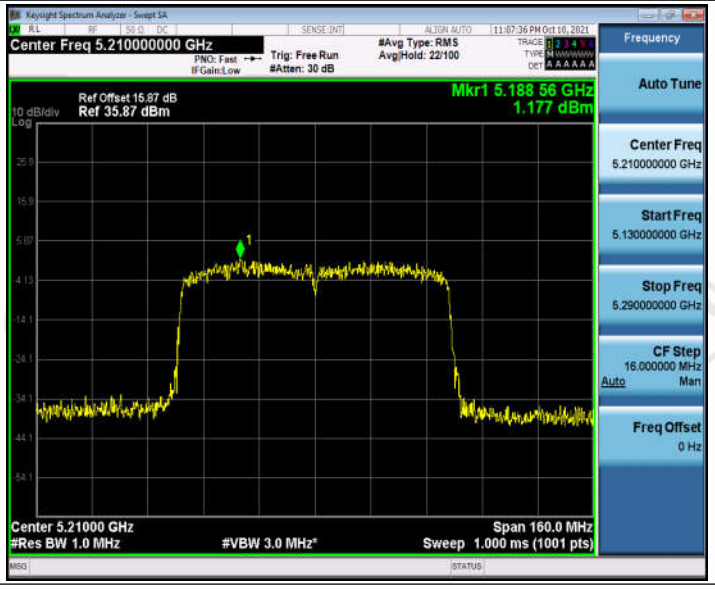

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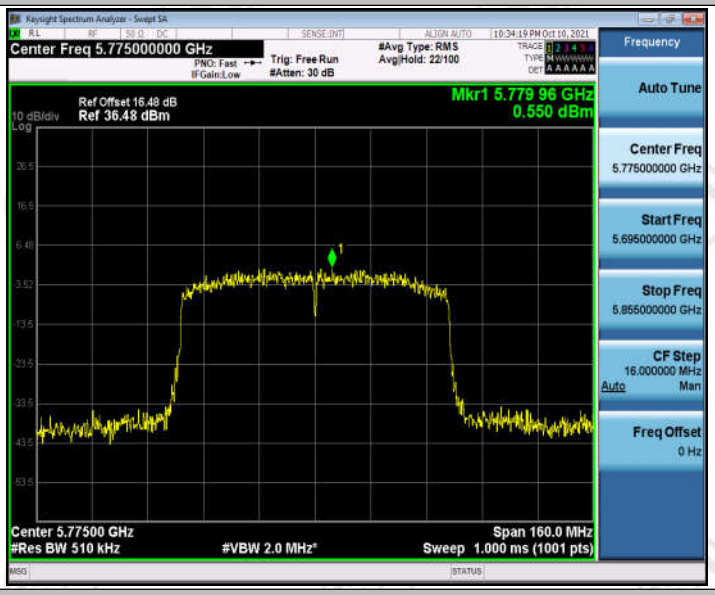
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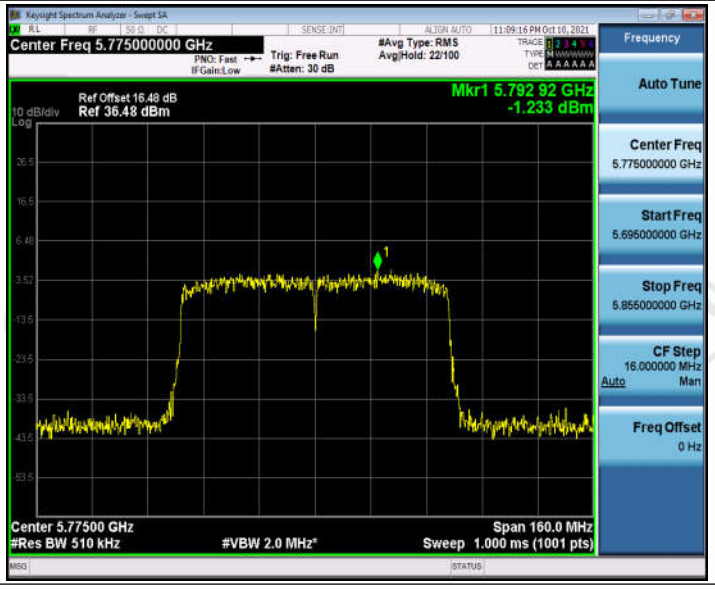

11N20MIMO_Ant1_5180

11N20MIMO_Ant2_5180


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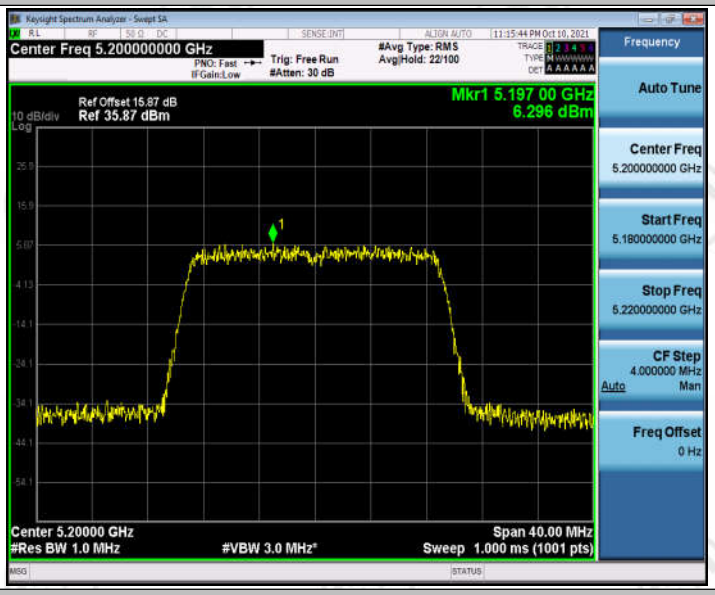
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11N20MIMO_Ant1_5240

11N20MIMO_Ant2_5240