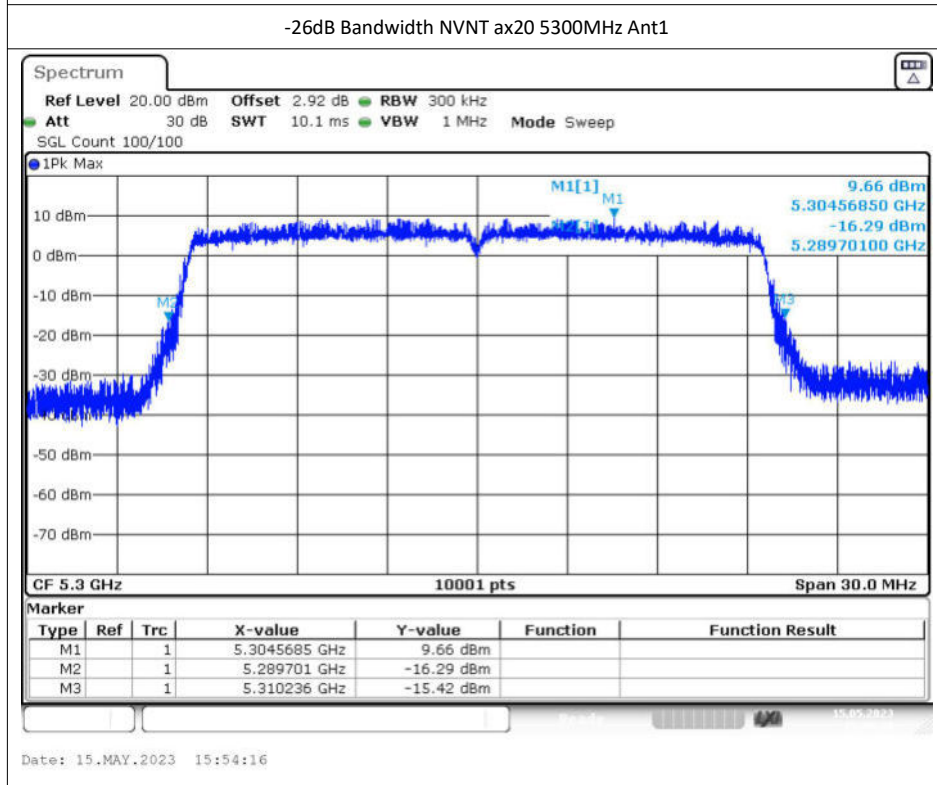
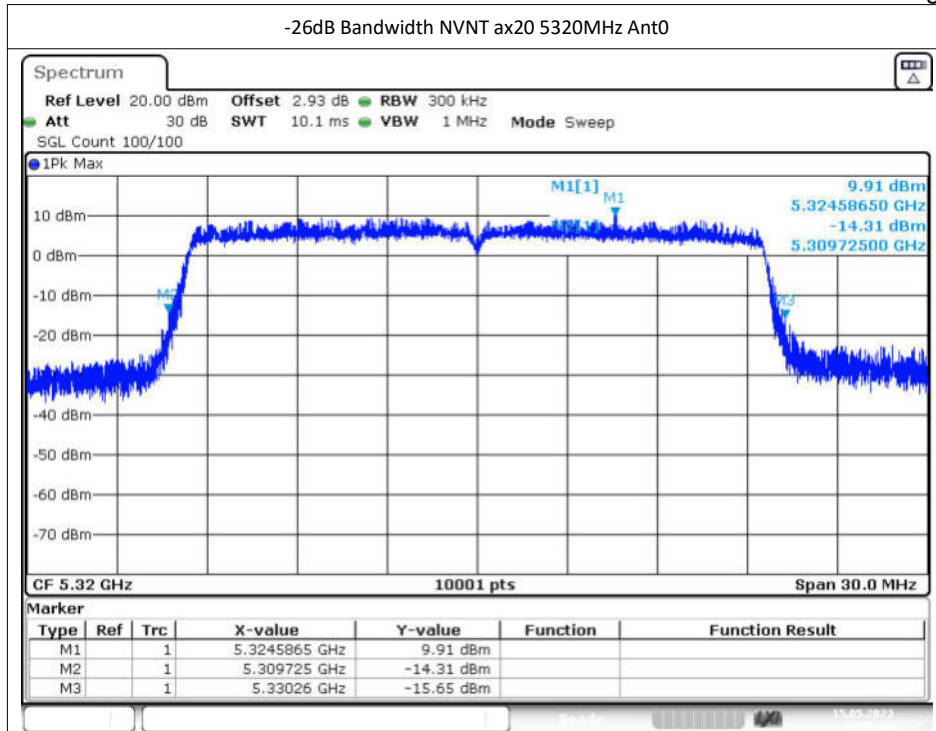


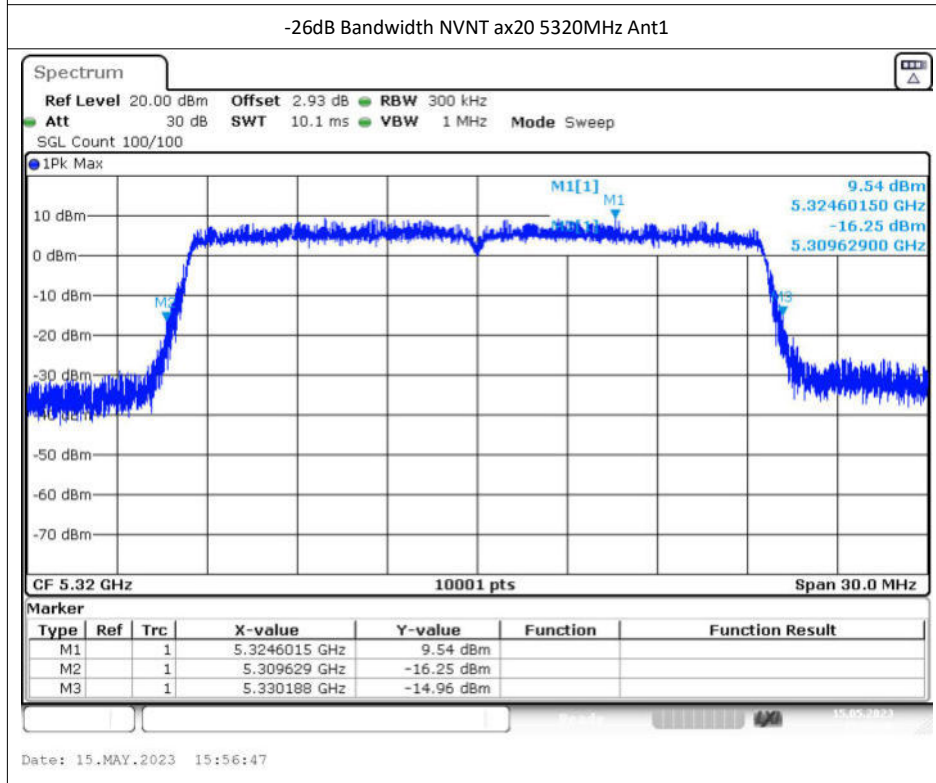
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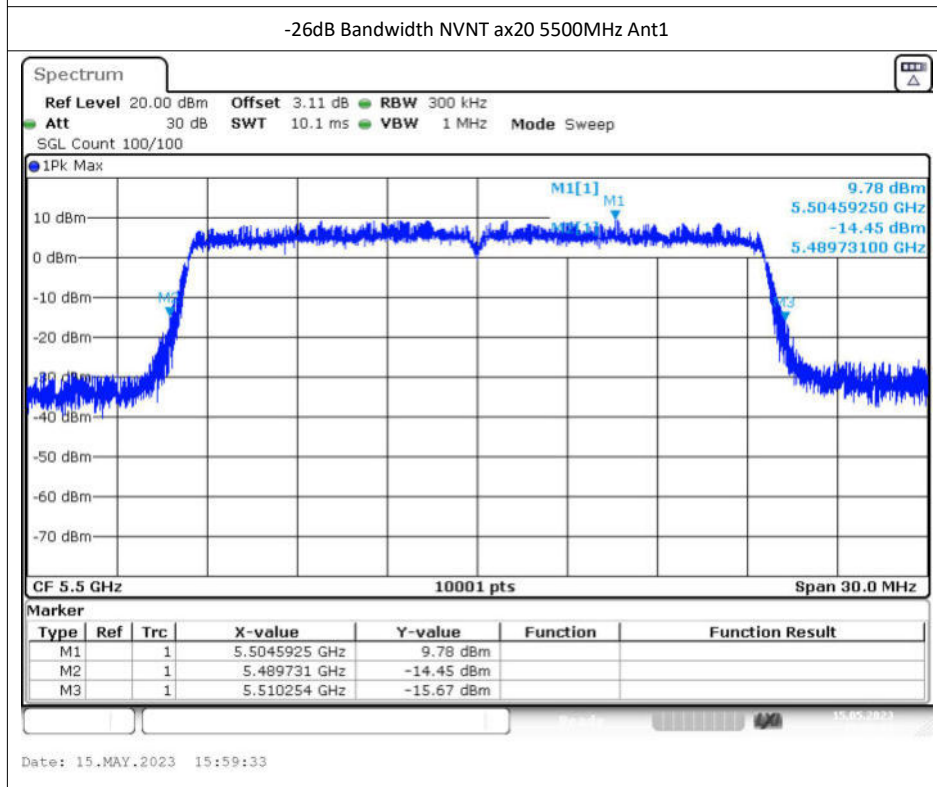
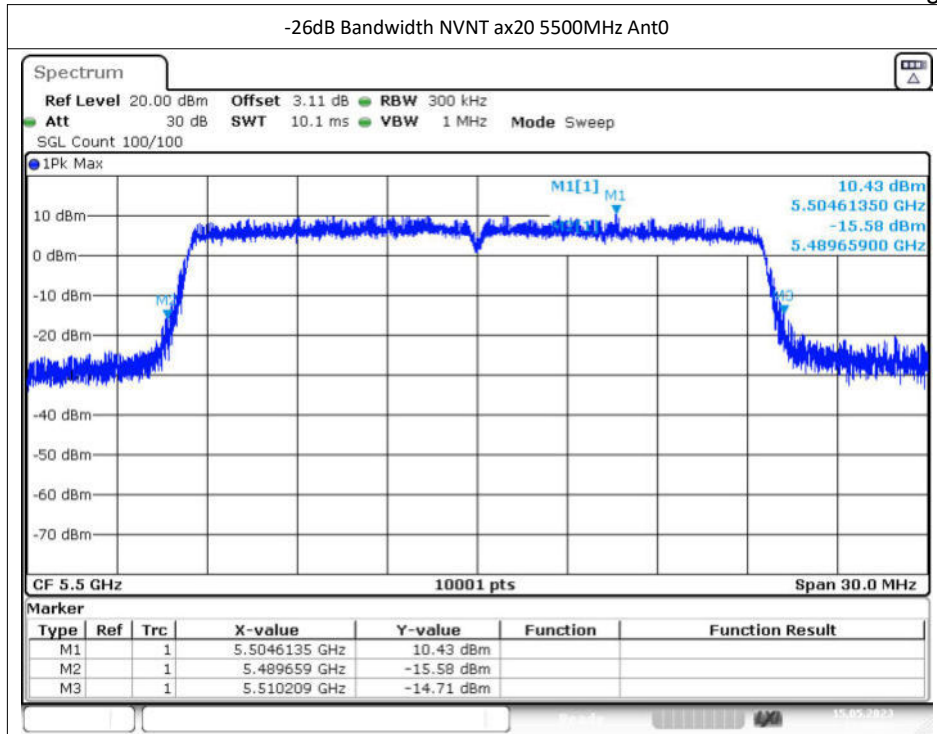
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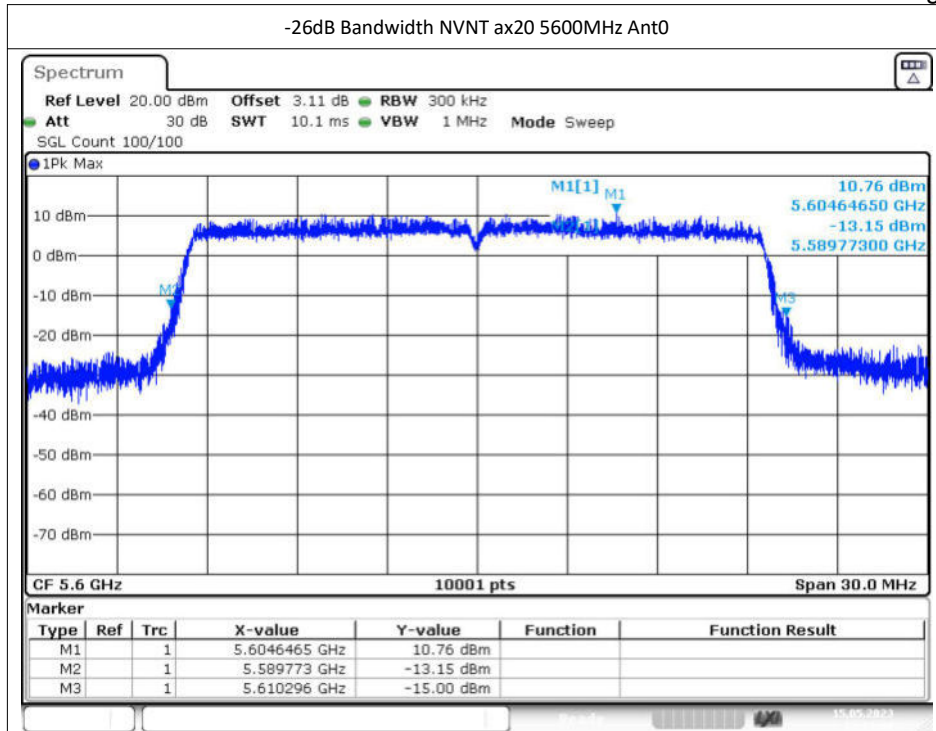


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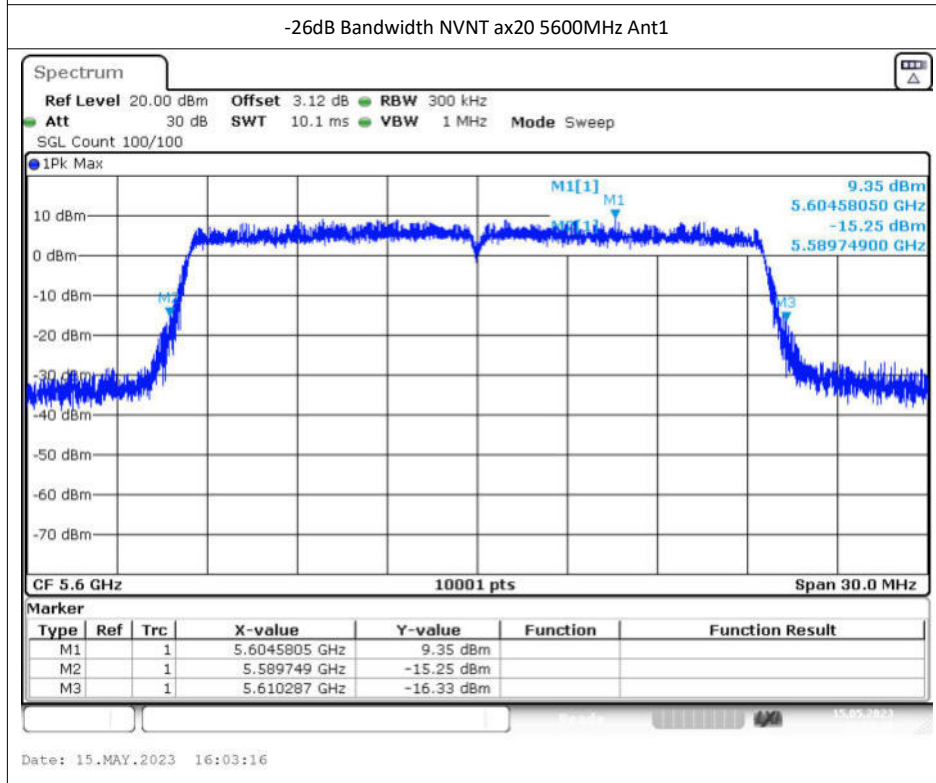


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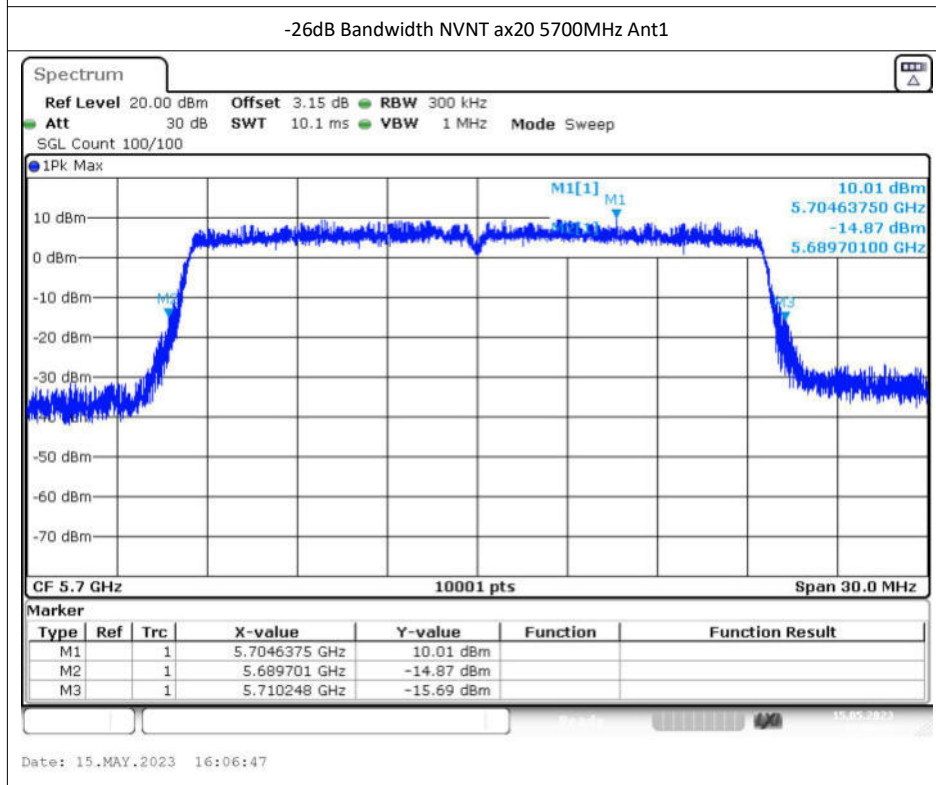
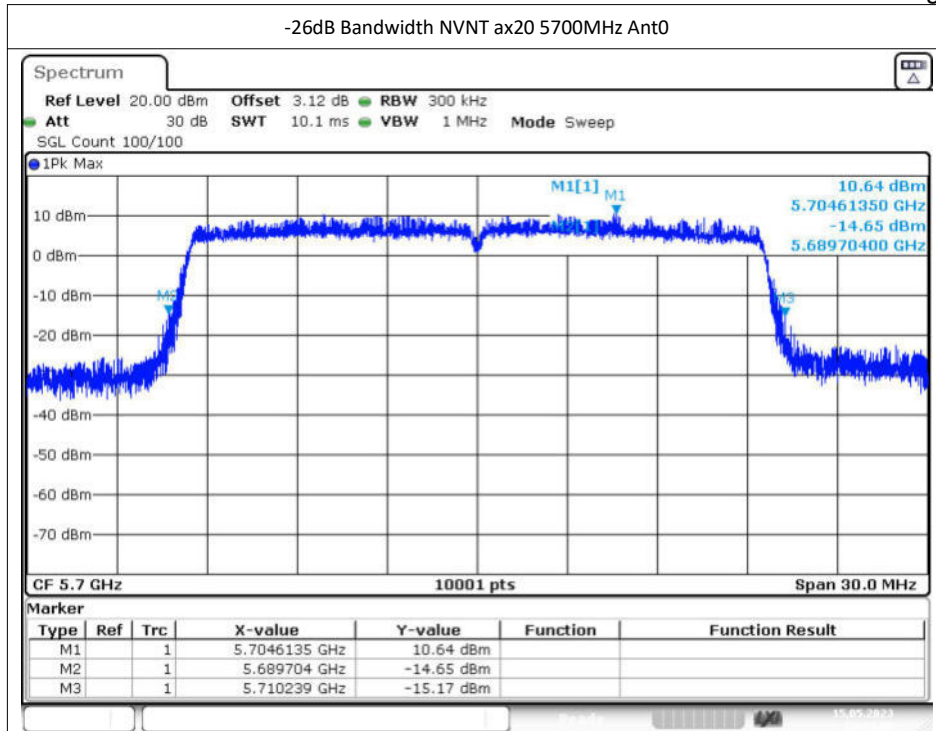


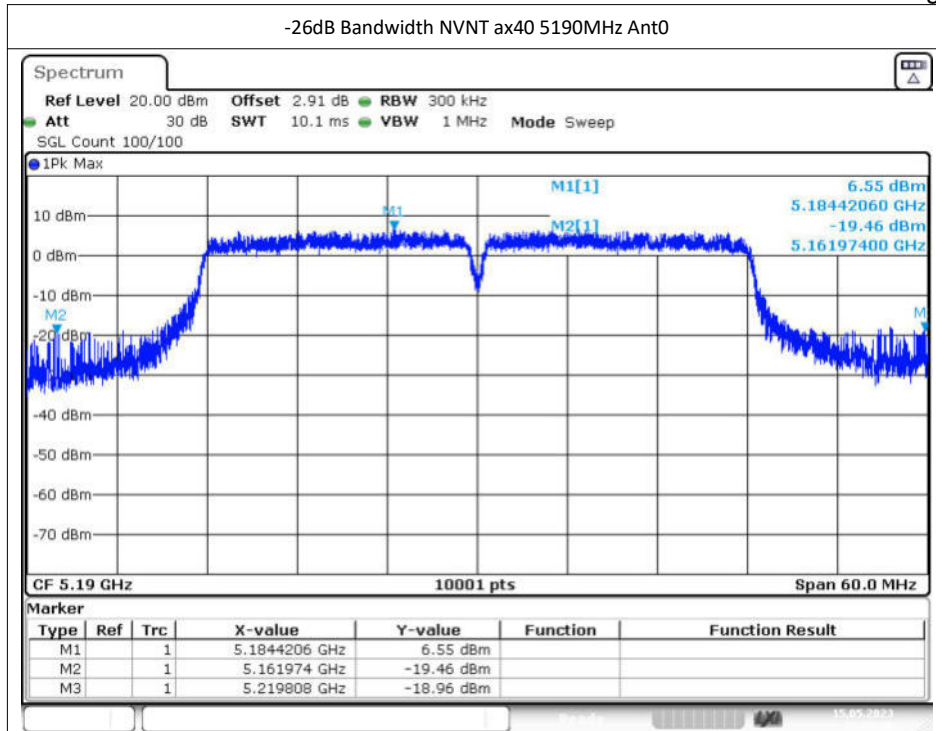


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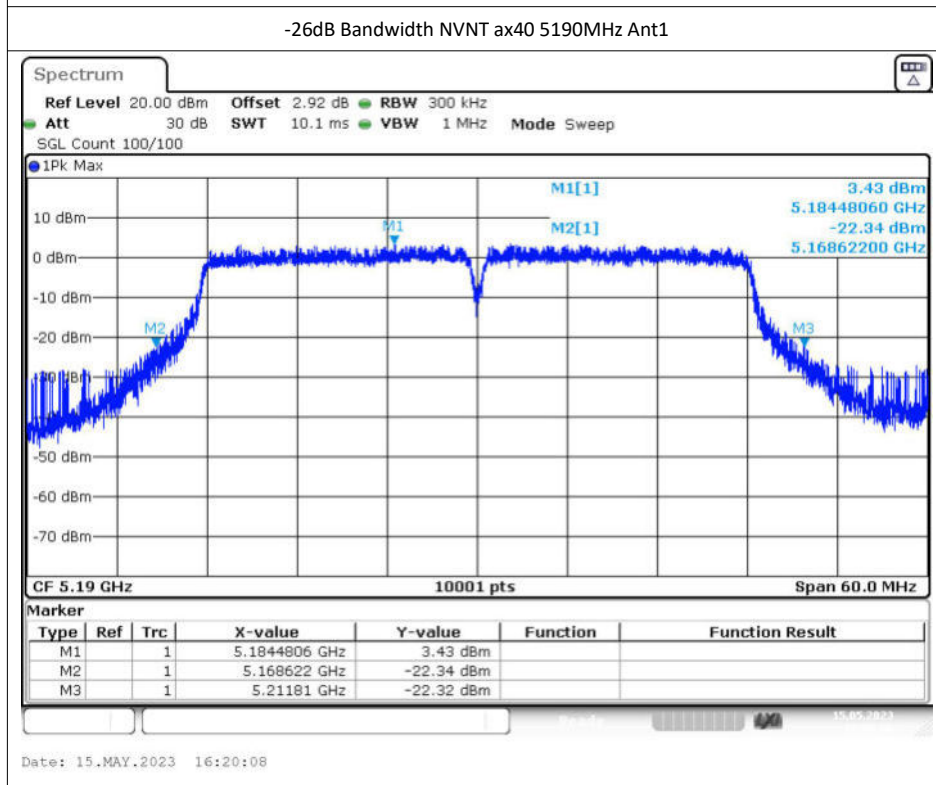


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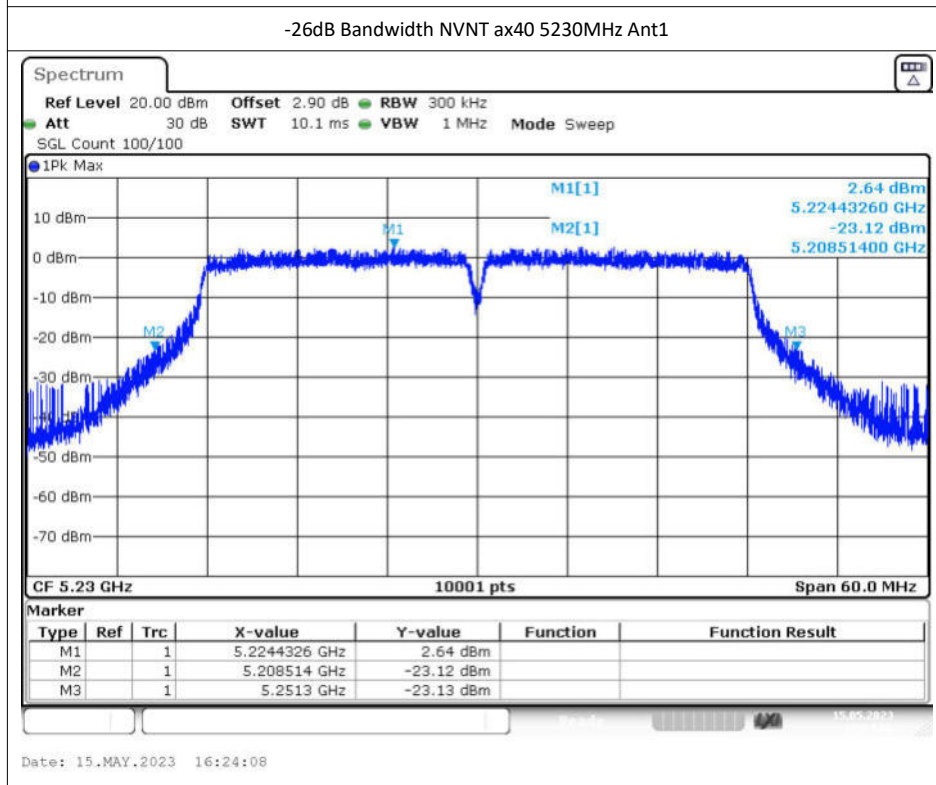
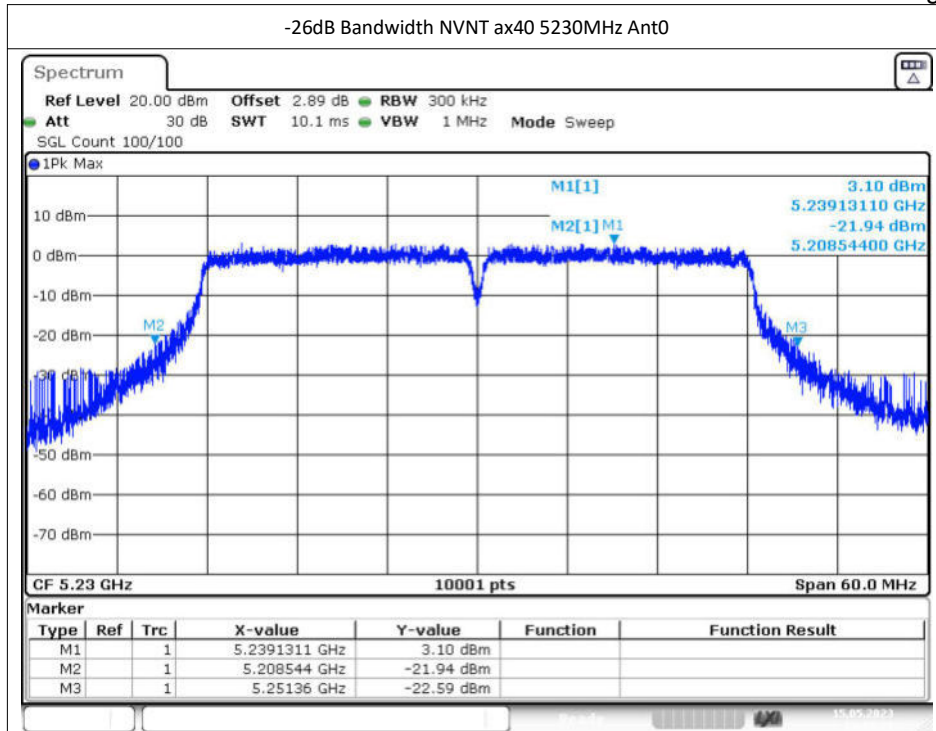


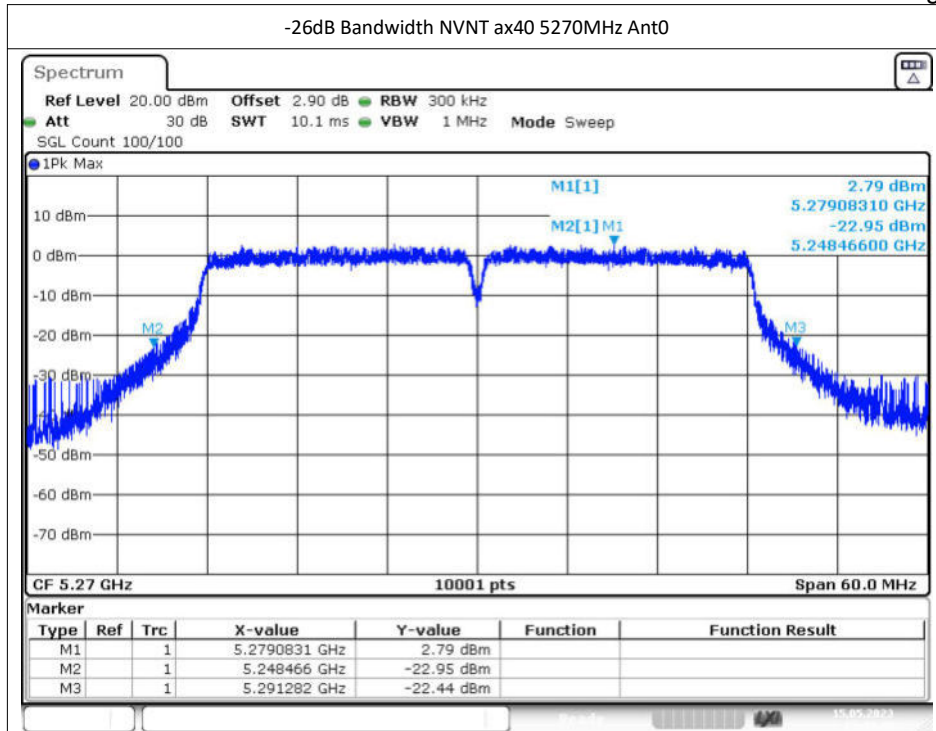


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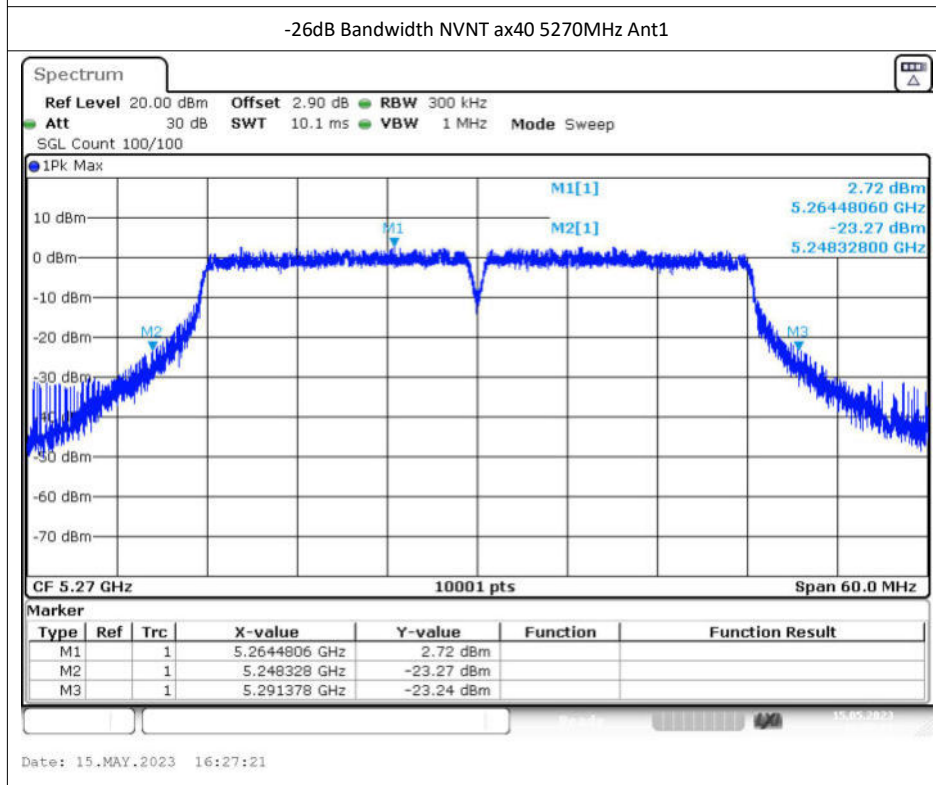


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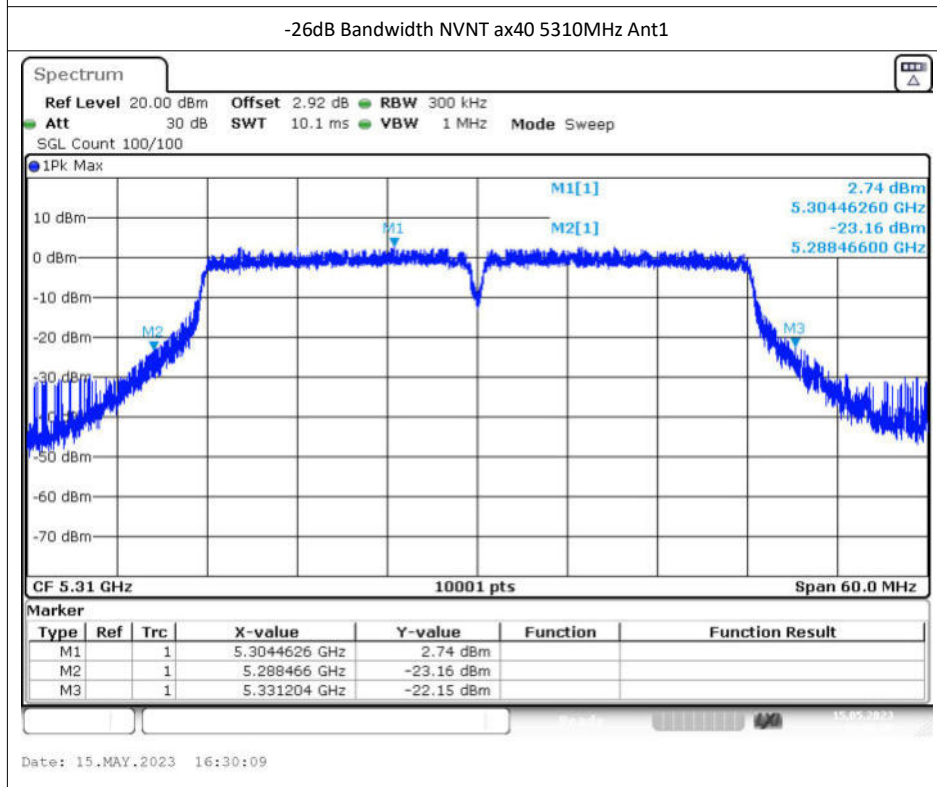
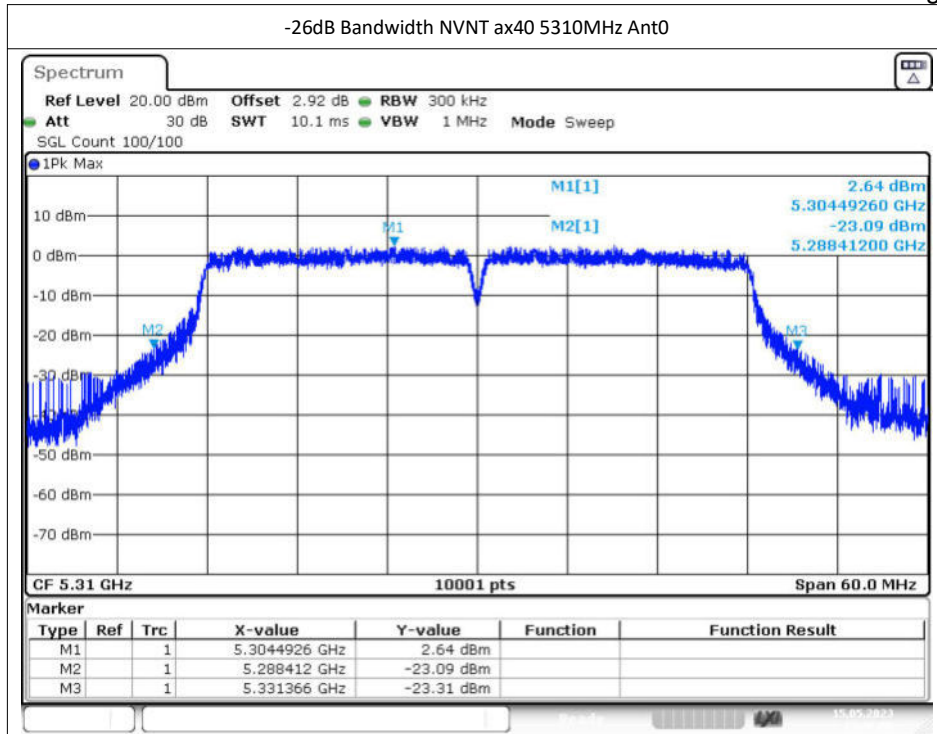


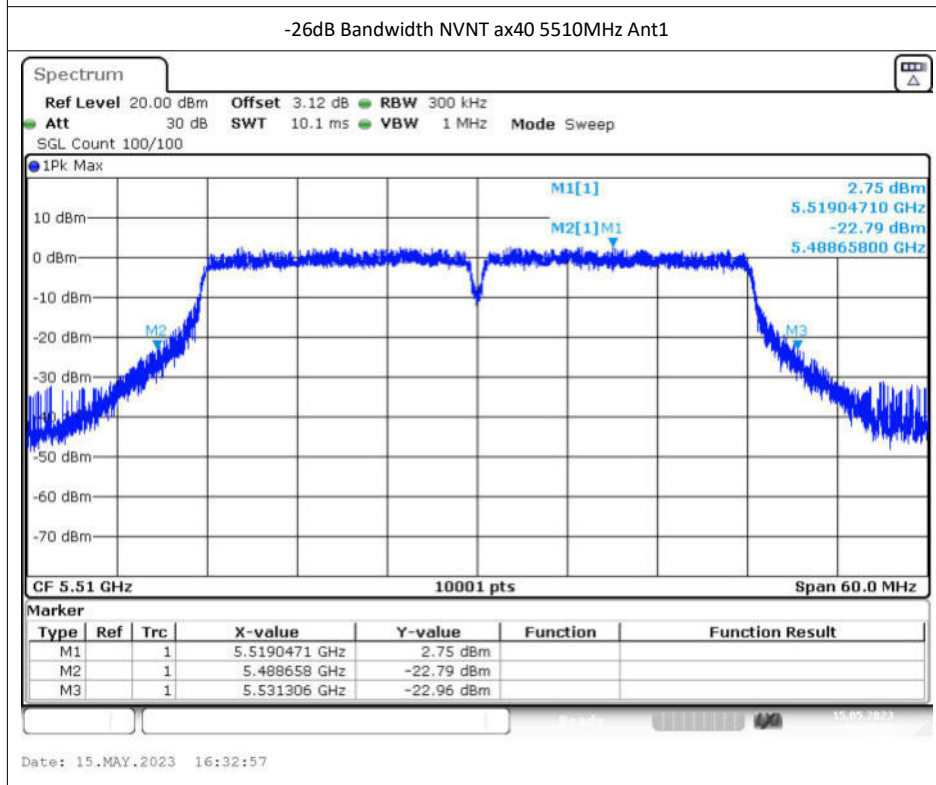
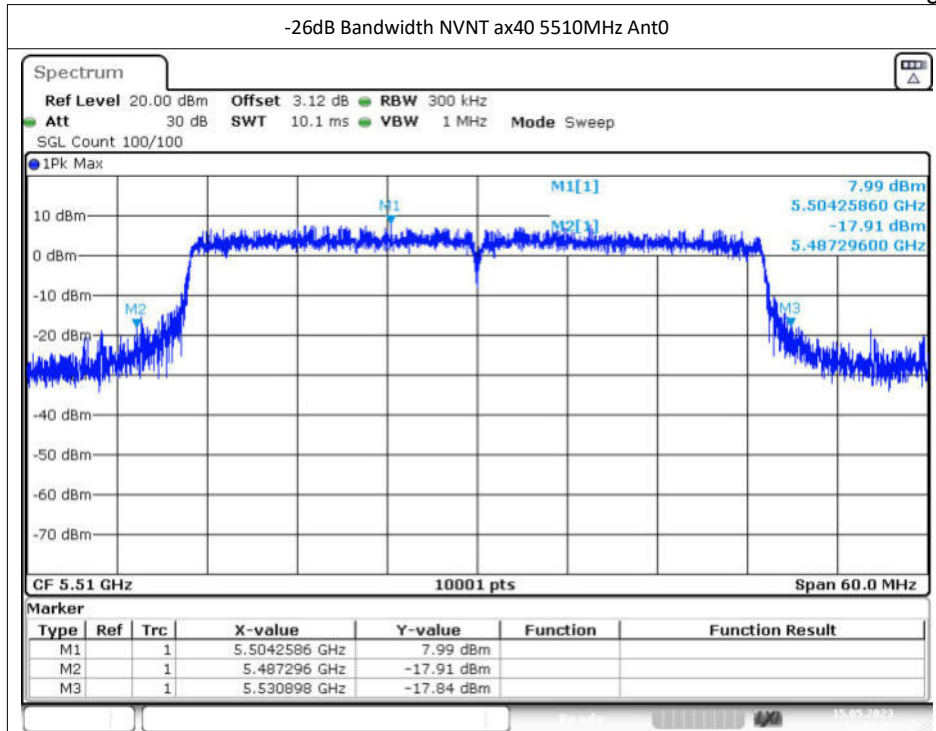


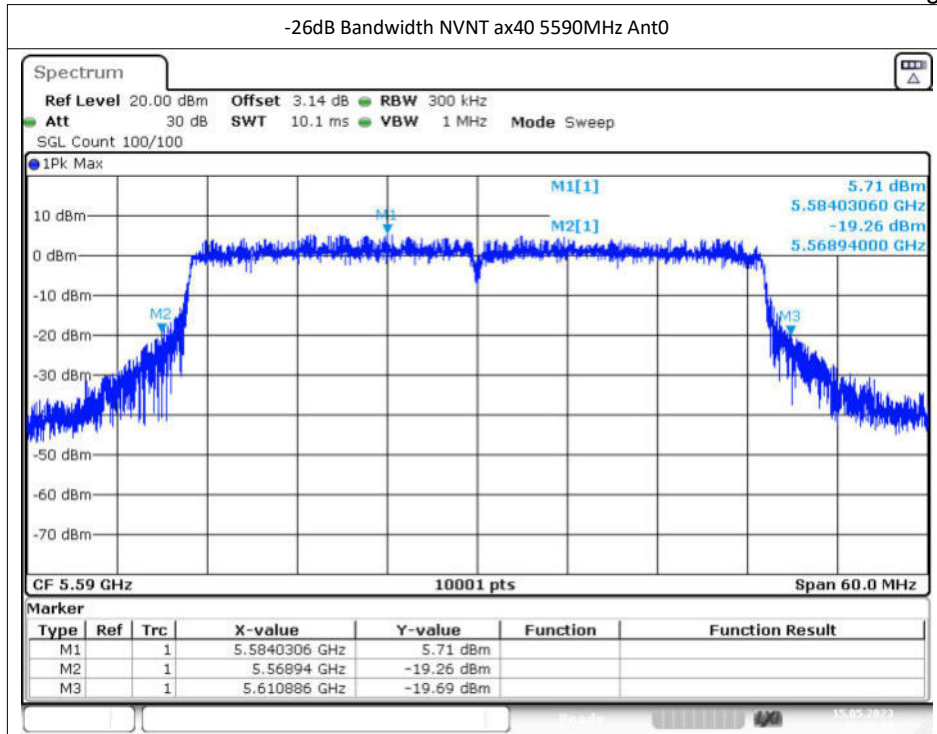
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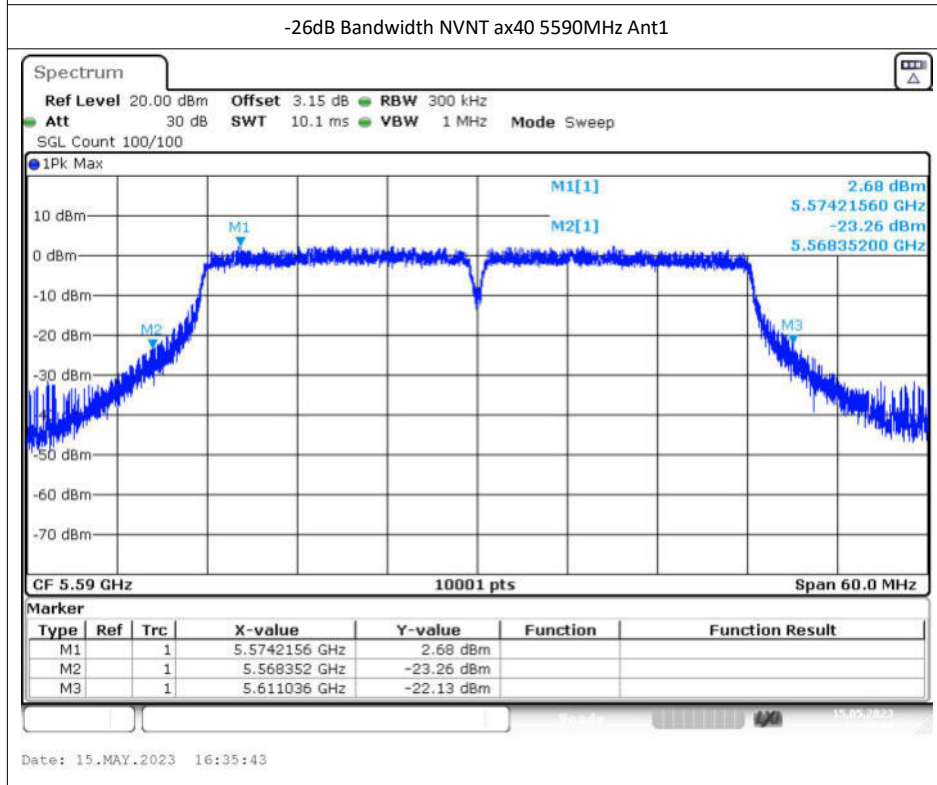
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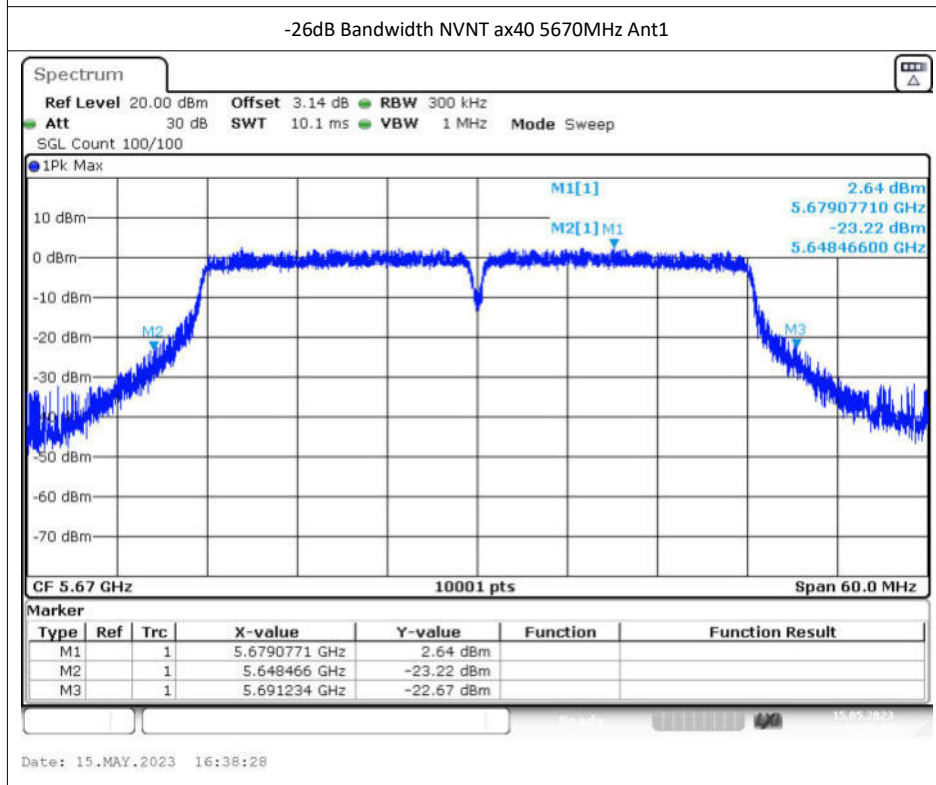
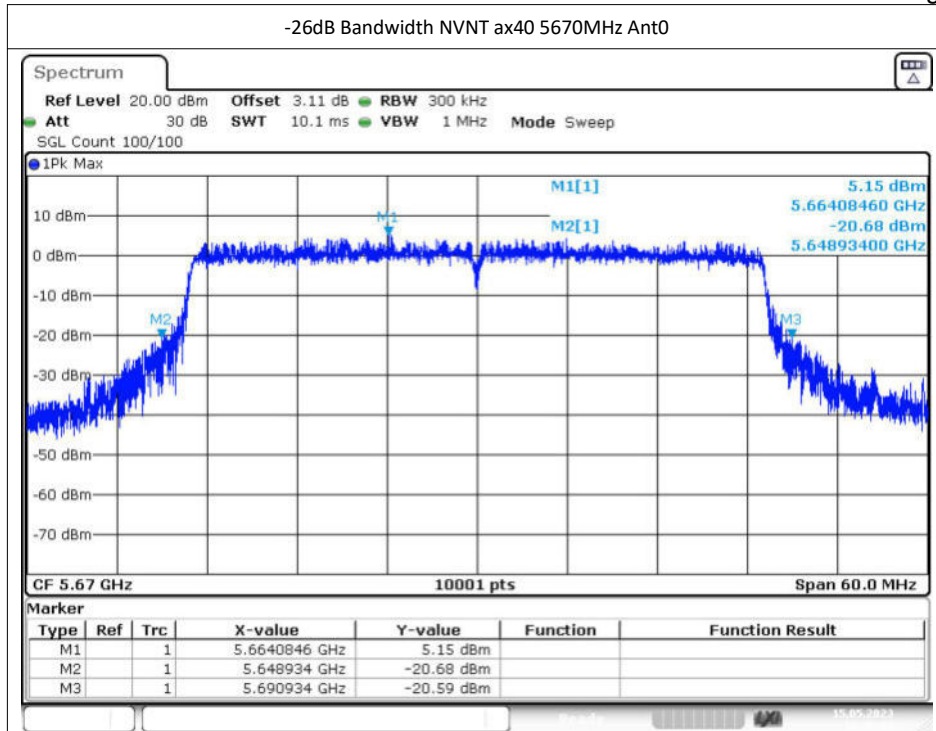


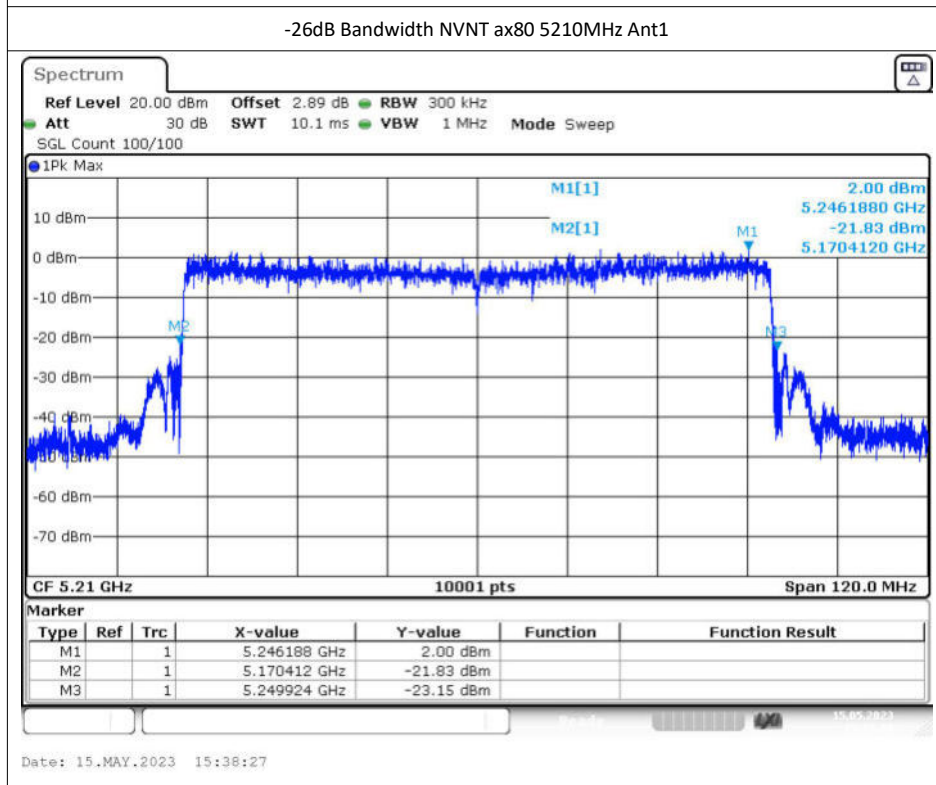
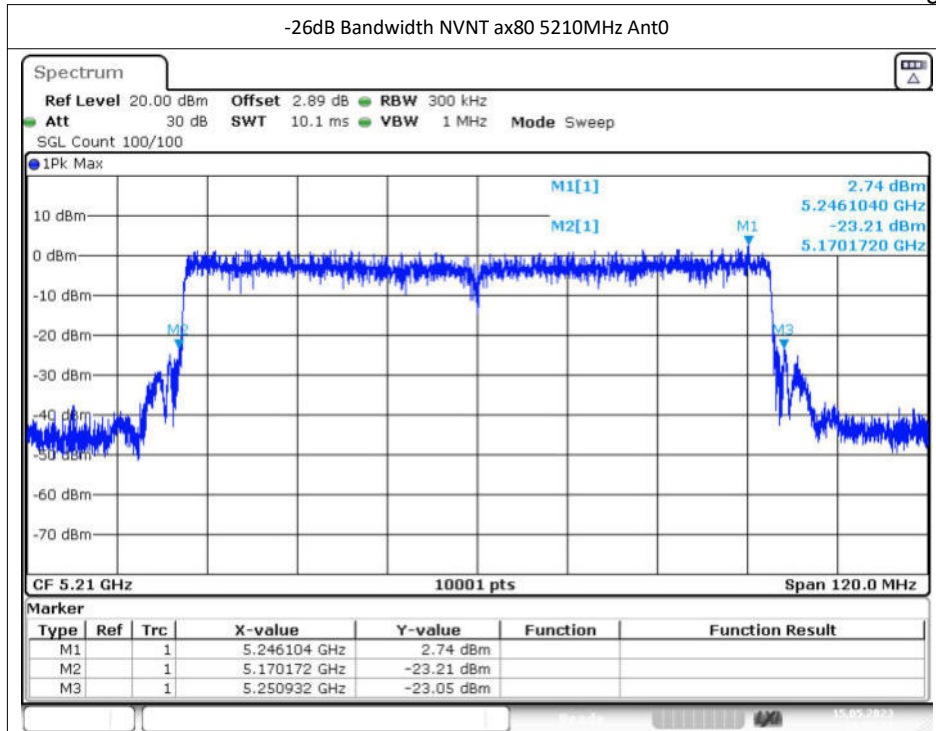


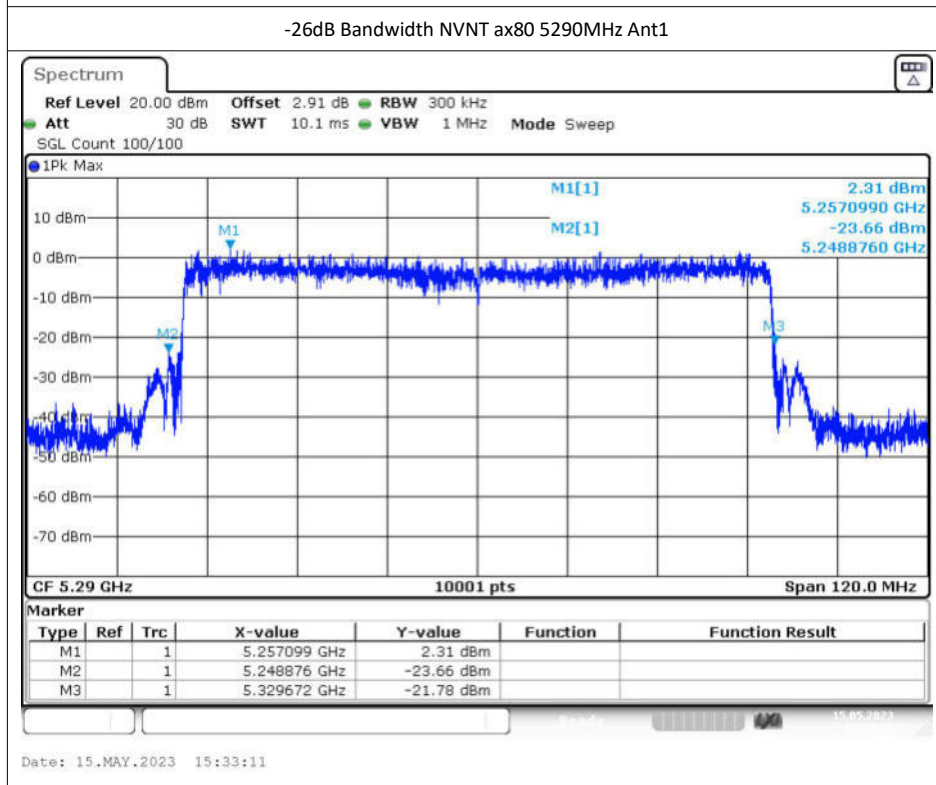
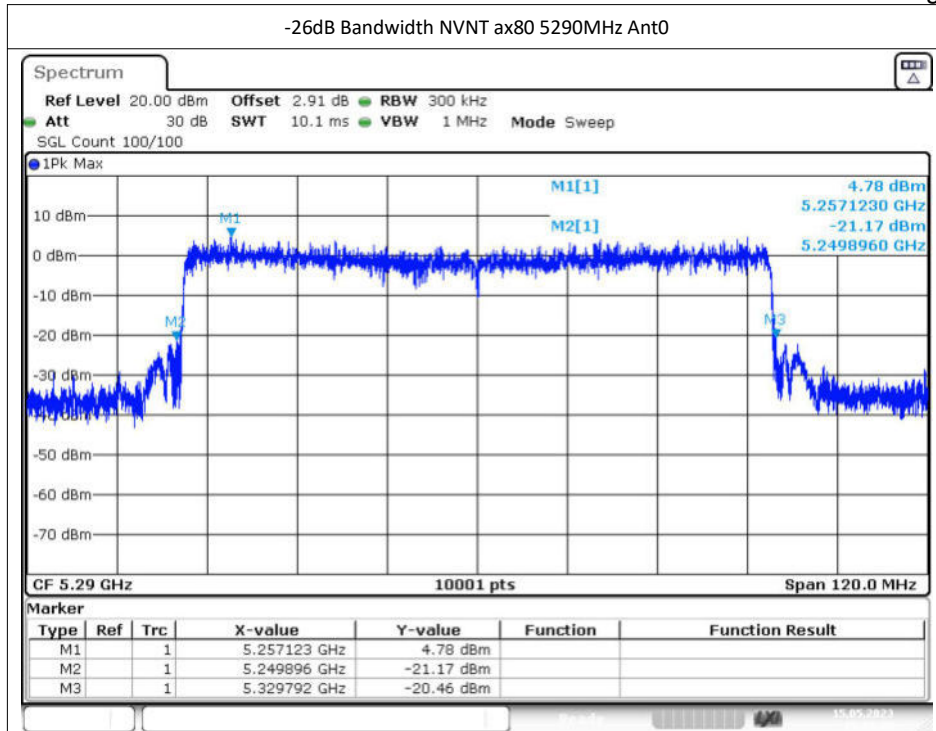
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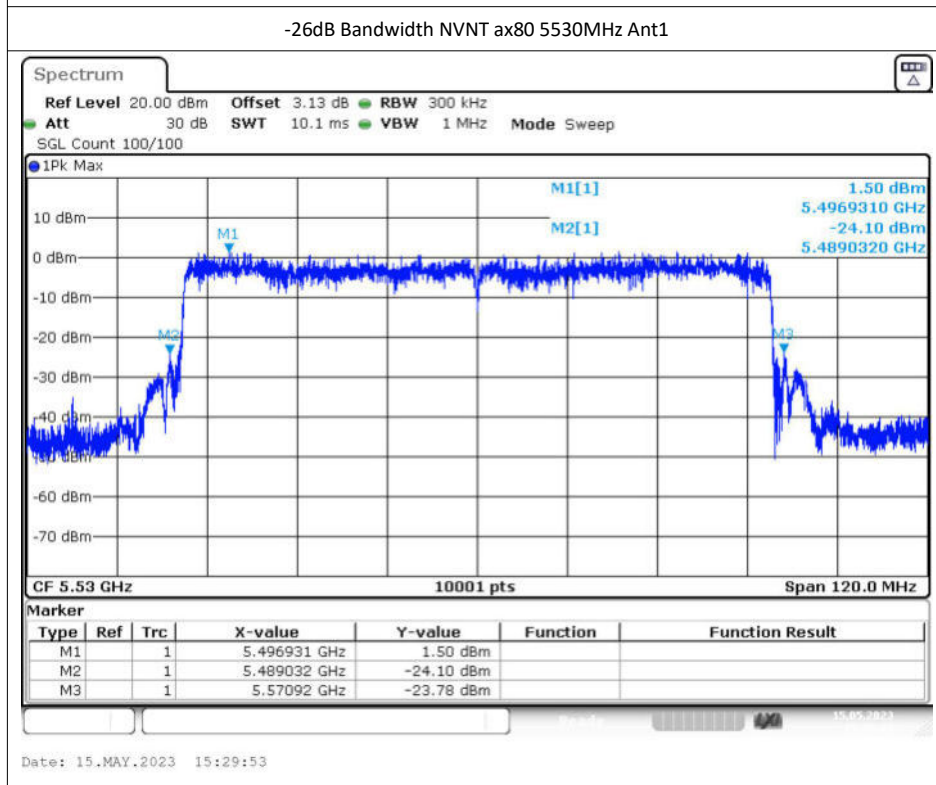
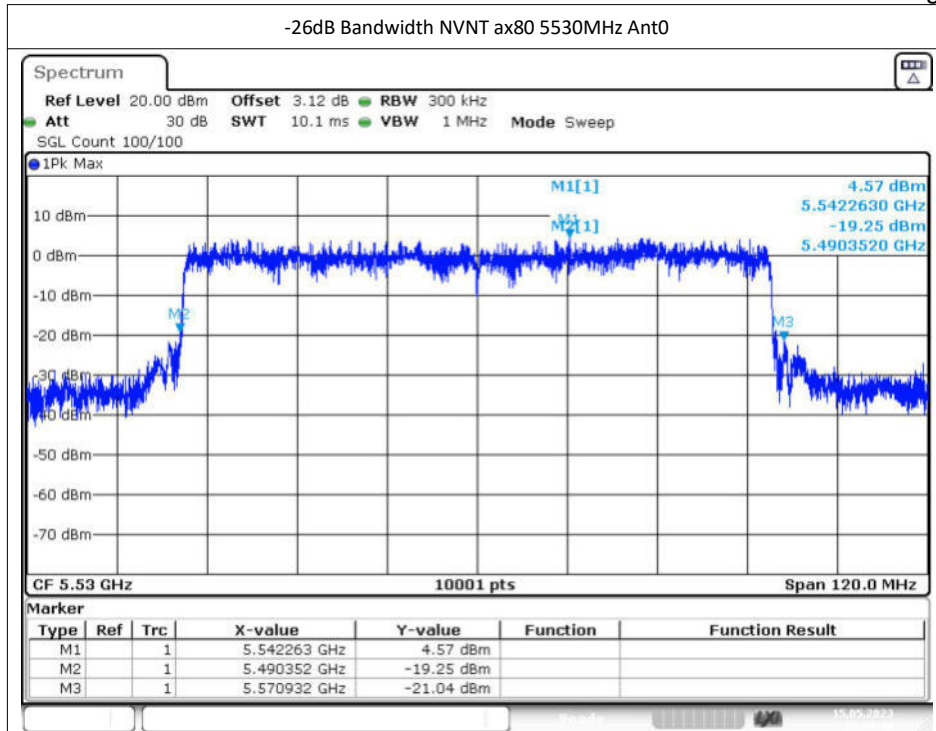


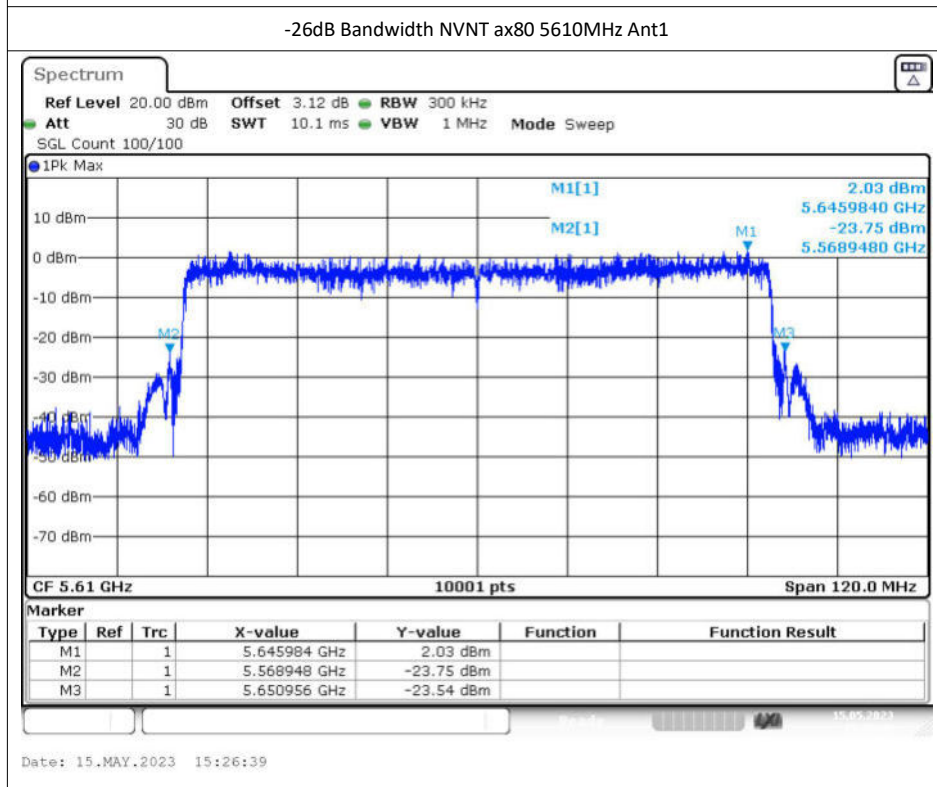
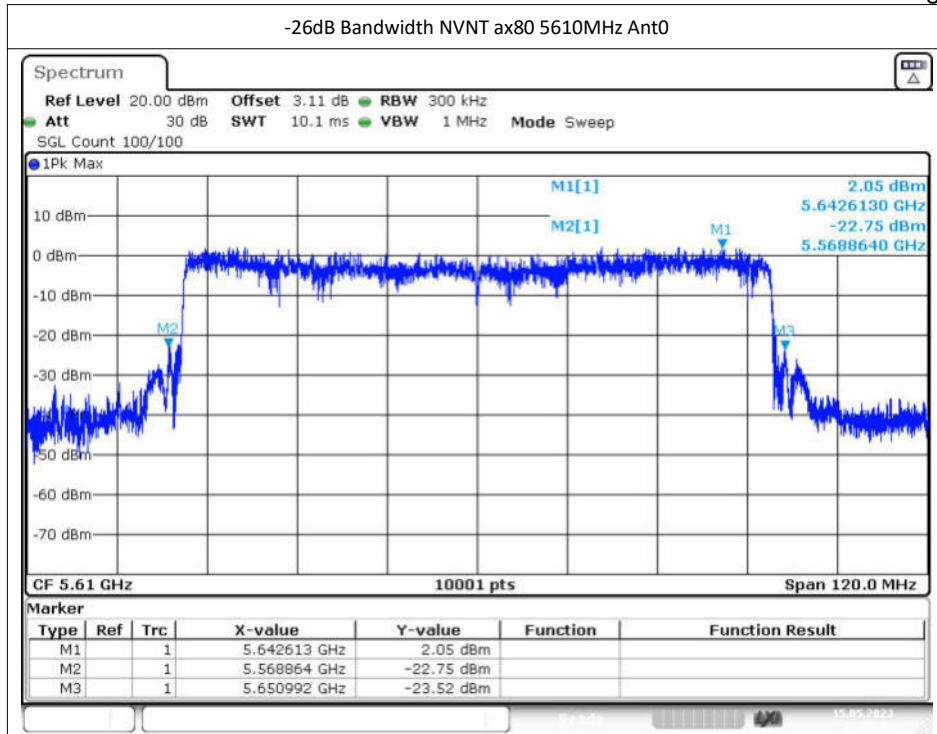
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Occupied Channel Bandwidth

Condition	Mode	Frequency (MHz)	Antenna	99% OBW (MHz)
NVNT	a	5180	Ant0	16.363
NVNT	a	5180	Ant1	16.423
NVNT	a	5200	Ant0	16.375
NVNT	a	5200	Ant1	16.351
NVNT	a	5240	Ant0	16.381
NVNT	a	5240	Ant1	16.357
NVNT	a	5260	Ant0	16.336
NVNT	a	5260	Ant1	16.393
NVNT	a	5300	Ant0	16.357
NVNT	a	5300	Ant1	16.351
NVNT	a	5320	Ant0	16.366
NVNT	a	5320	Ant1	16.396
NVNT	a	5500	Ant0	16.384
NVNT	a	5500	Ant1	16.36
NVNT	a	5600	Ant0	16.345
NVNT	a	5600	Ant1	16.357
NVNT	a	5700	Ant0	16.339
NVNT	a	5700	Ant1	16.345
NVNT	a	5745	Ant0	16.372
NVNT	a	5745	Ant1	16.375
NVNT	a	5785	Ant0	16.384
NVNT	a	5785	Ant1	16.372
NVNT	a	5825	Ant0	16.375
NVNT	a	5825	Ant1	16.357
NVNT	n20	5180	Ant0	17.572
NVNT	n20	5180	Ant1	17.545
NVNT	n20	5200	Ant0	17.539
NVNT	n20	5200	Ant1	17.56
NVNT	n20	5240	Ant0	17.518
NVNT	n20	5240	Ant1	17.554
NVNT	n20	5260	Ant0	17.557
NVNT	n20	5260	Ant1	17.575
NVNT	n20	5300	Ant0	17.539
NVNT	n20	5300	Ant1	17.566
NVNT	n20	5320	Ant0	17.548
NVNT	n20	5320	Ant1	17.569
NVNT	n20	5500	Ant0	17.533
NVNT	n20	5500	Ant1	17.575



NVNT	n20	5600	Ant0	17.542
NVNT	n20	5600	Ant1	17.554
NVNT	n20	5700	Ant0	17.521
NVNT	n20	5700	Ant1	17.536
NVNT	n20	5745	Ant0	17.602
NVNT	n20	5745	Ant1	17.566
NVNT	n20	5785	Ant0	17.62
NVNT	n20	5785	Ant1	17.575
NVNT	n20	5825	Ant0	17.575
NVNT	n20	5825	Ant1	17.557
NVNT	n40	5190	Ant0	36.434
NVNT	n40	5190	Ant1	36.338
NVNT	n40	5230	Ant0	36.392
NVNT	n40	5230	Ant1	36.416
NVNT	n40	5270	Ant0	36.44
NVNT	n40	5270	Ant1	36.362
NVNT	n40	5310	Ant0	36.428
NVNT	n40	5310	Ant1	36.302
NVNT	n40	5510	Ant0	36.38
NVNT	n40	5510	Ant1	36.278
NVNT	n40	5590	Ant0	36.41
NVNT	n40	5590	Ant1	36.422
NVNT	n40	5670	Ant0	36.422
NVNT	n40	5670	Ant1	36.272
NVNT	n40	5755	Ant0	36.35
NVNT	n40	5755	Ant1	36.344
NVNT	n40	5795	Ant0	36.416
NVNT	n40	5795	Ant1	36.476
NVNT	ac20	5180	Ant0	17.593
NVNT	ac20	5180	Ant1	17.56
NVNT	ac20	5200	Ant0	17.554
NVNT	ac20	5200	Ant1	17.533
NVNT	ac20	5240	Ant0	17.554
NVNT	ac20	5240	Ant1	17.533
NVNT	ac20	5260	Ant0	17.545
NVNT	ac20	5260	Ant1	17.563
NVNT	ac20	5300	Ant0	17.539
NVNT	ac20	5300	Ant1	17.584
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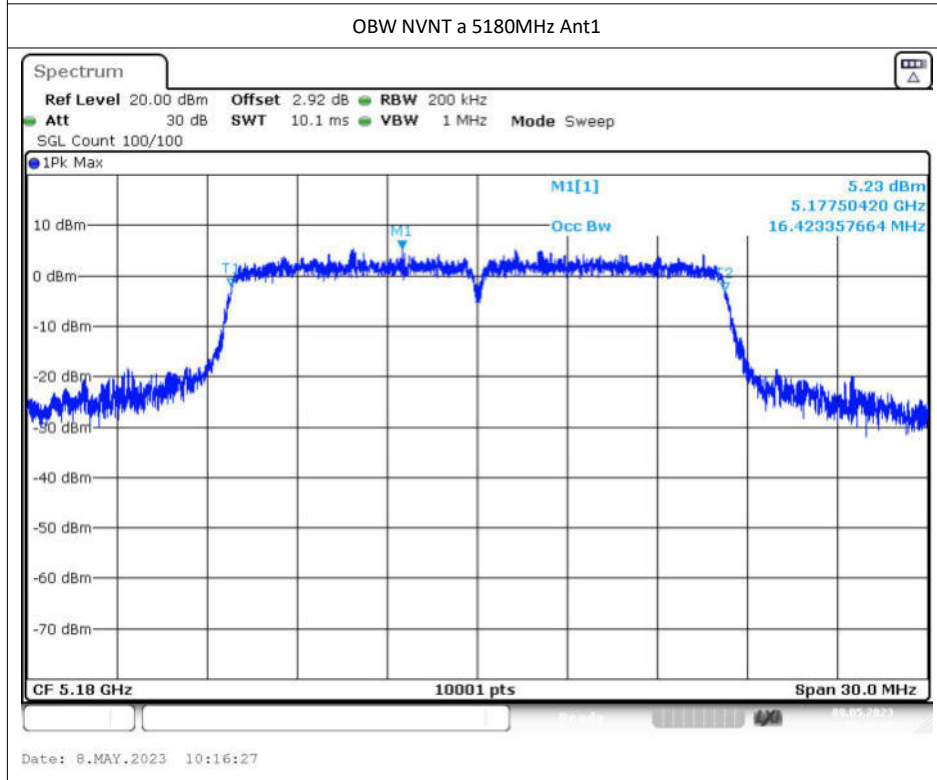
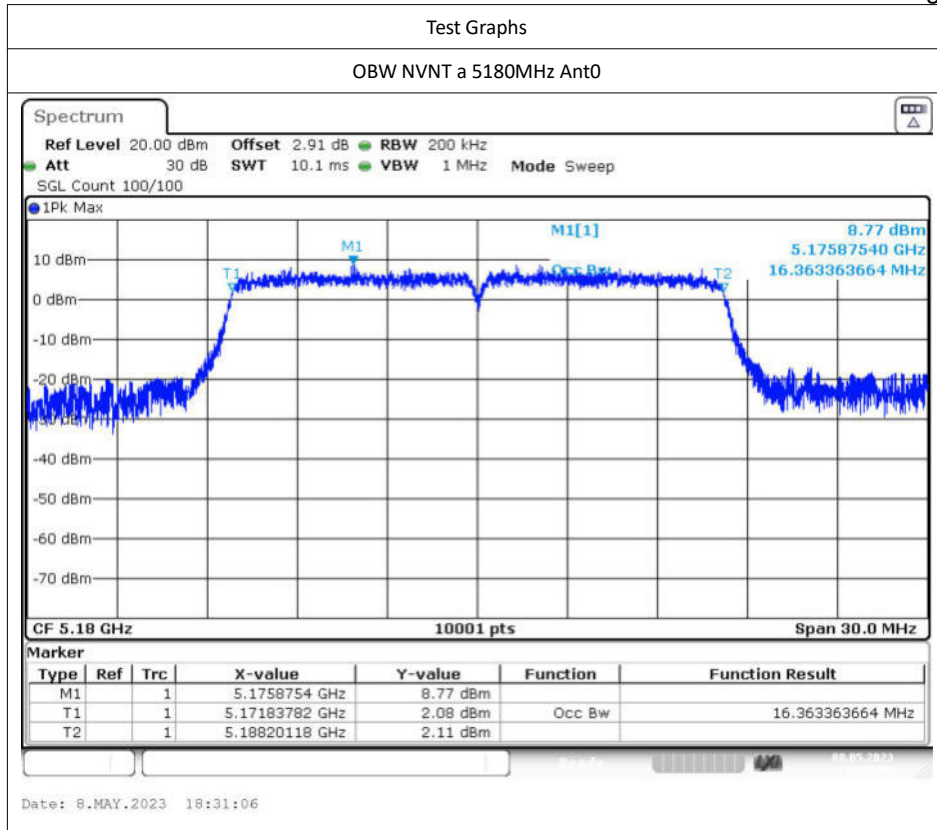
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NVNT	ac20	5700	Ant1	17.533
NVNT	ac20	5745	Ant0	17.53
NVNT	ac20	5745	Ant1	17.56
NVNT	ac20	5785	Ant0	17.578
NVNT	ac20	5785	Ant1	17.581
NVNT	ac20	5825	Ant0	17.569
NVNT	ac20	5825	Ant1	17.557
NVNT	ac40	5190	Ant0	36.356
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NVNT	ac40	5230	Ant0	36.398
NVNT	ac40	5230	Ant1	36.272
NVNT	ac40	5270	Ant0	36.368
NVNT	ac40	5270	Ant1	36.398
NVNT	ac40	5310	Ant0	36.35
NVNT	ac40	5310	Ant1	36.326
NVNT	ac40	5510	Ant0	36.416
NVNT	ac40	5510	Ant1	36.422
NVNT	ac40	5590	Ant0	36.422
NVNT	ac40	5590	Ant1	36.38
NVNT	ac40	5670	Ant0	36.374
NVNT	ac40	5670	Ant1	36.284
NVNT	ac40	5755	Ant0	36.32
NVNT	ac40	5755	Ant1	36.398
NVNT	ac40	5795	Ant0	36.428
NVNT	ac40	5795	Ant1	36.362
NVNT	ac80	5210	Ant0	76.276
NVNT	ac80	5210	Ant1	76.252
NVNT	ac80	5290	Ant0	76.348
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NVNT	ac80	5530	Ant0	75.868
NVNT	ac80	5530	Ant1	76.264
NVNT	ac80	5610	Ant0	76.252
NVNT	ac80	5610	Ant1	76.084
NVNT	ac80	5775	Ant0	76.06
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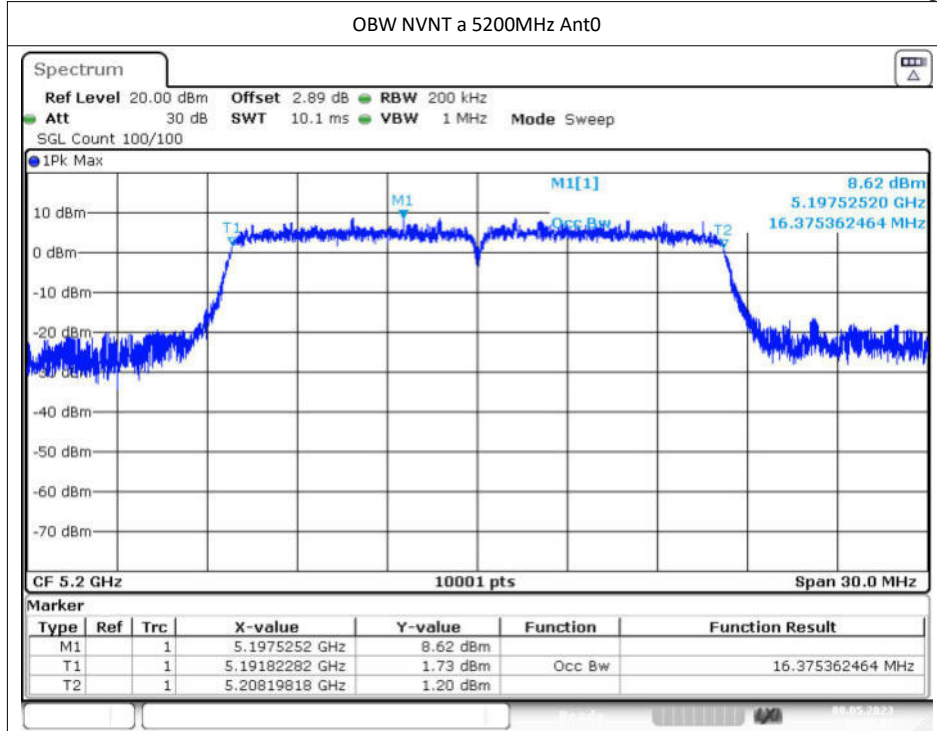


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NVNT	ax20	5260	Ant0	18.892
NVNT	ax20	5260	Ant1	18.88
NVNT	ax20	5300	Ant0	18.904
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NVNT	ax20	5500	Ant0	18.862
NVNT	ax20	5500	Ant1	18.88
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NVNT	ax20	5745	Ant0	18.904
NVNT	ax20	5745	Ant1	18.889
NVNT	ax20	5785	Ant0	18.922
NVNT	ax20	5785	Ant1	18.91
NVNT	ax20	5825	Ant0	18.886
NVNT	ax20	5825	Ant1	18.871
NVNT	ax40	5190	Ant0	36.326
NVNT	ax40	5190	Ant1	36.284
NVNT	ax40	5230	Ant0	36.29
NVNT	ax40	5230	Ant1	36.308
NVNT	ax40	5270	Ant0	36.266
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NVNT	ax40	5590	Ant0	37.76
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NVNT	ax40	5670	Ant1	36.302
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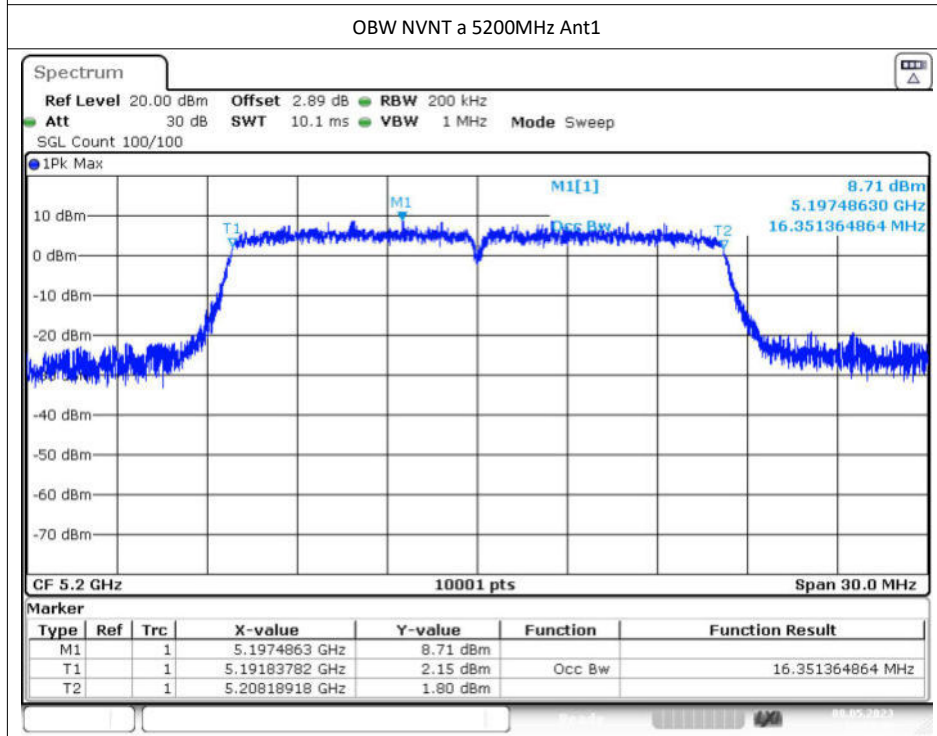


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NVNT	ax80	5530	Ant1	76.996
NVNT	ax80	5610	Ant0	77.224
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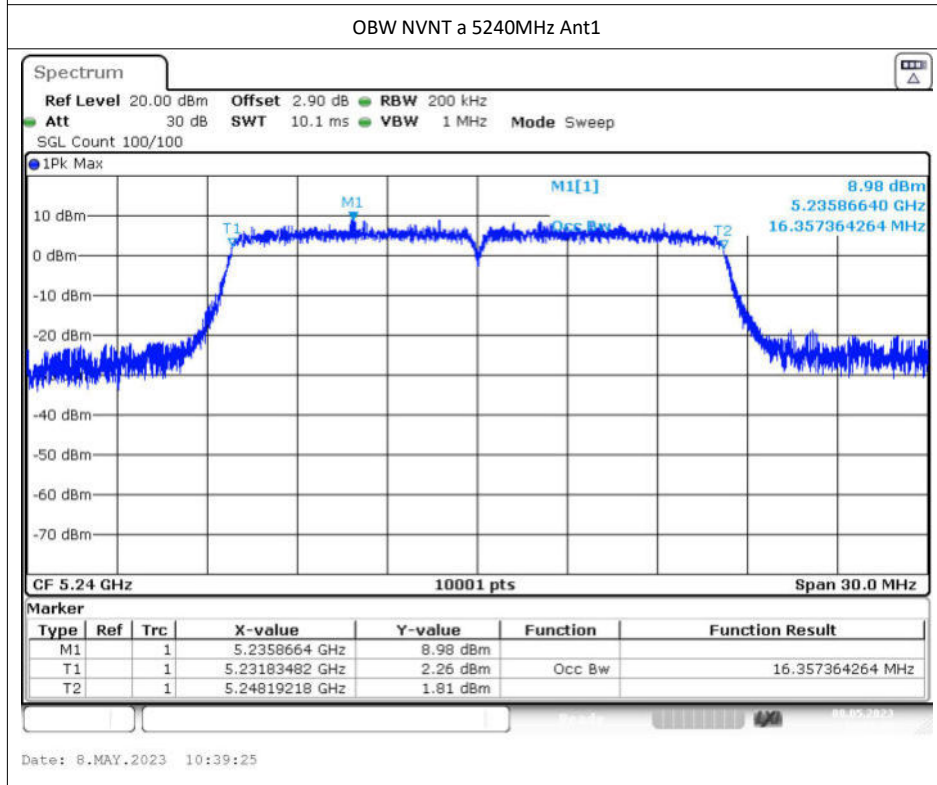
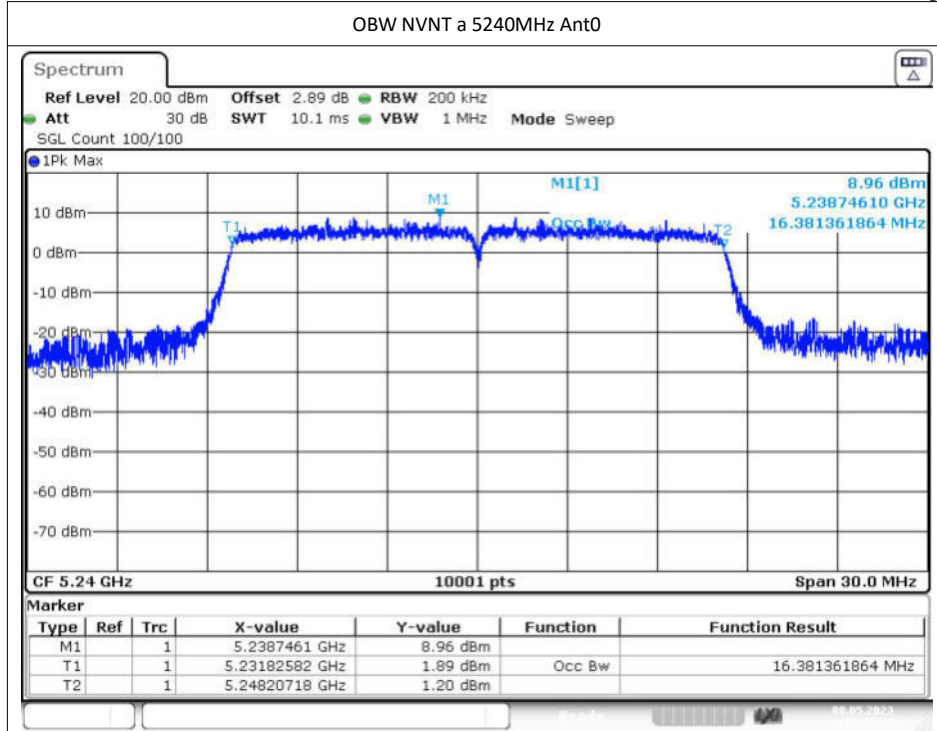


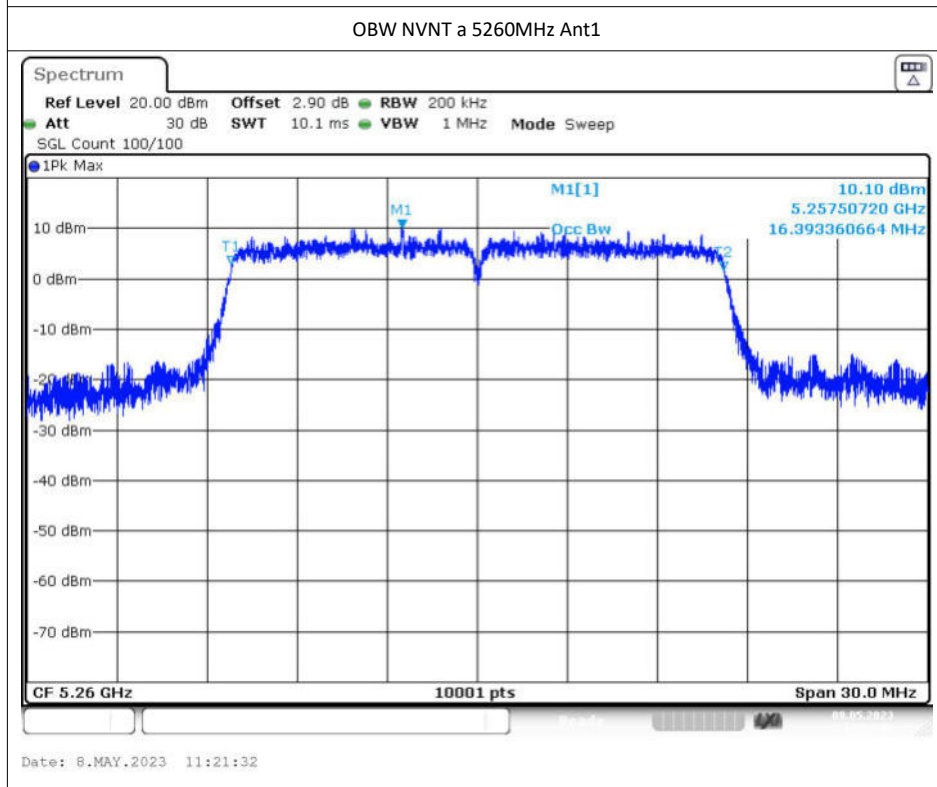
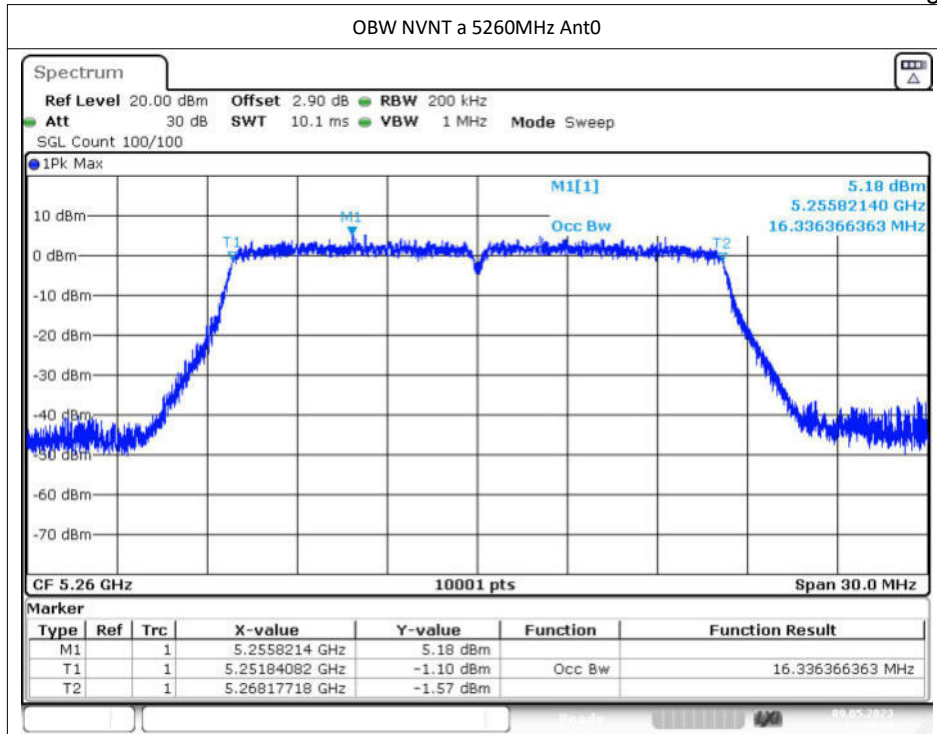


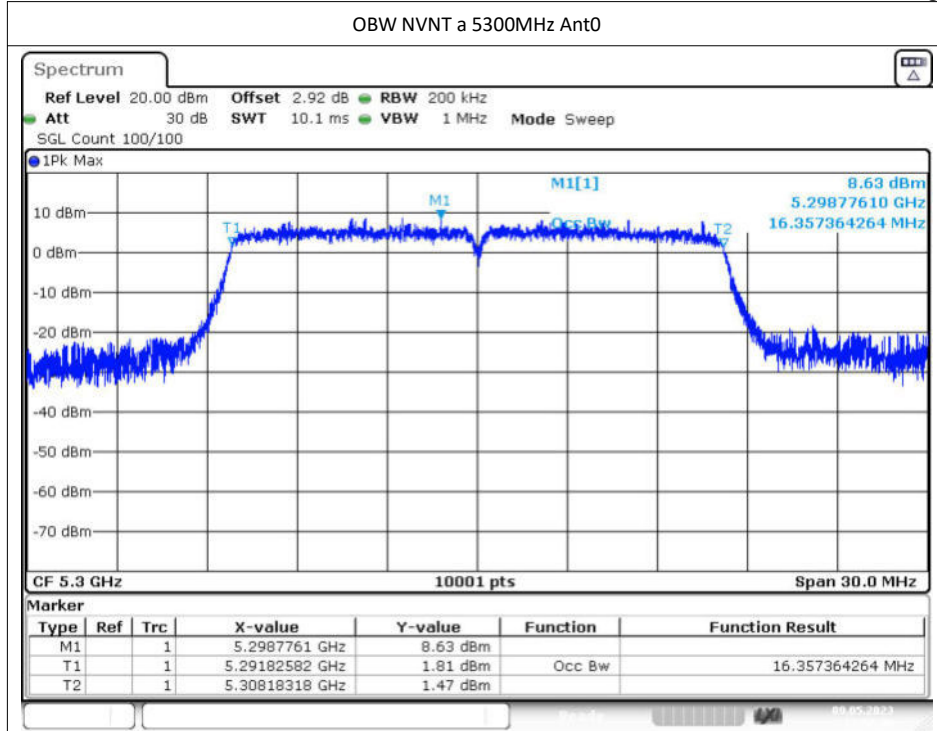
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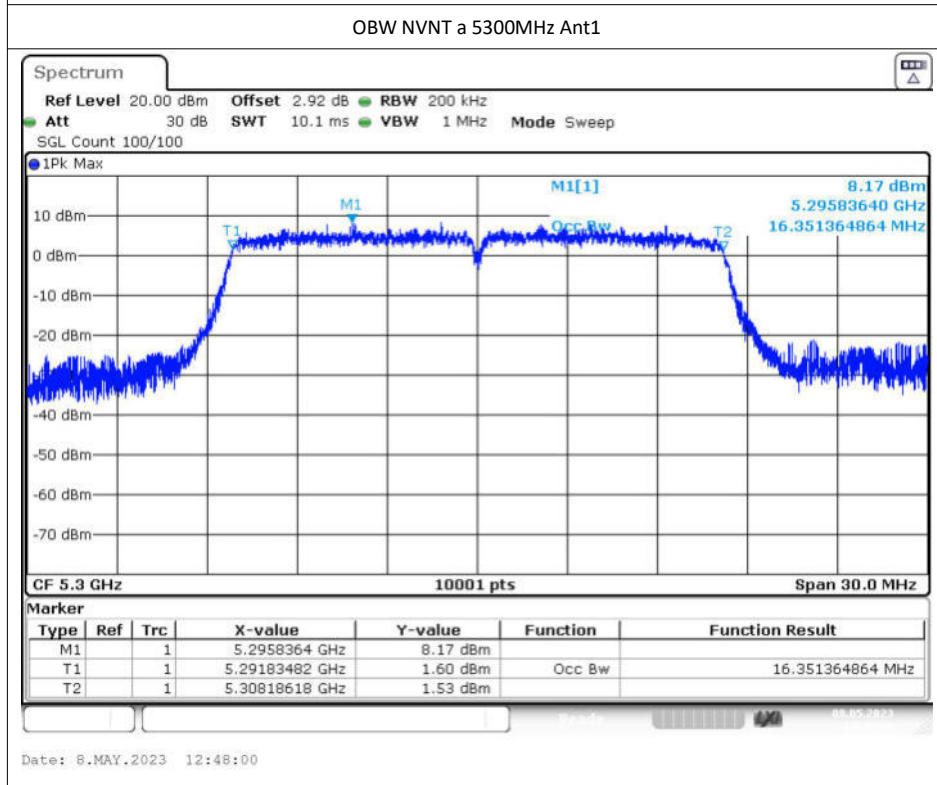
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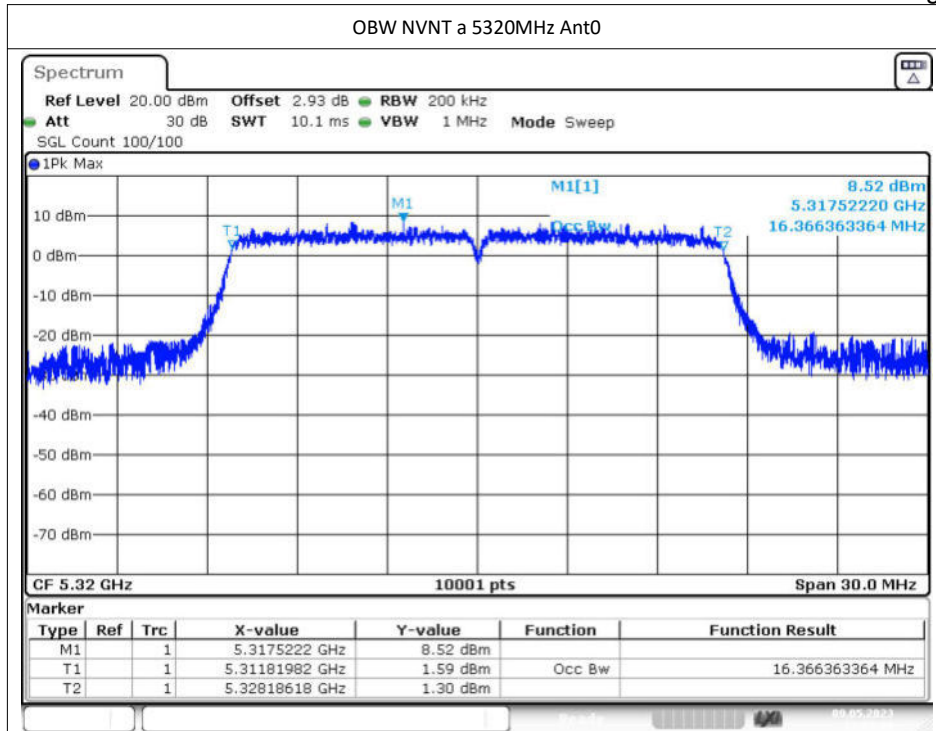




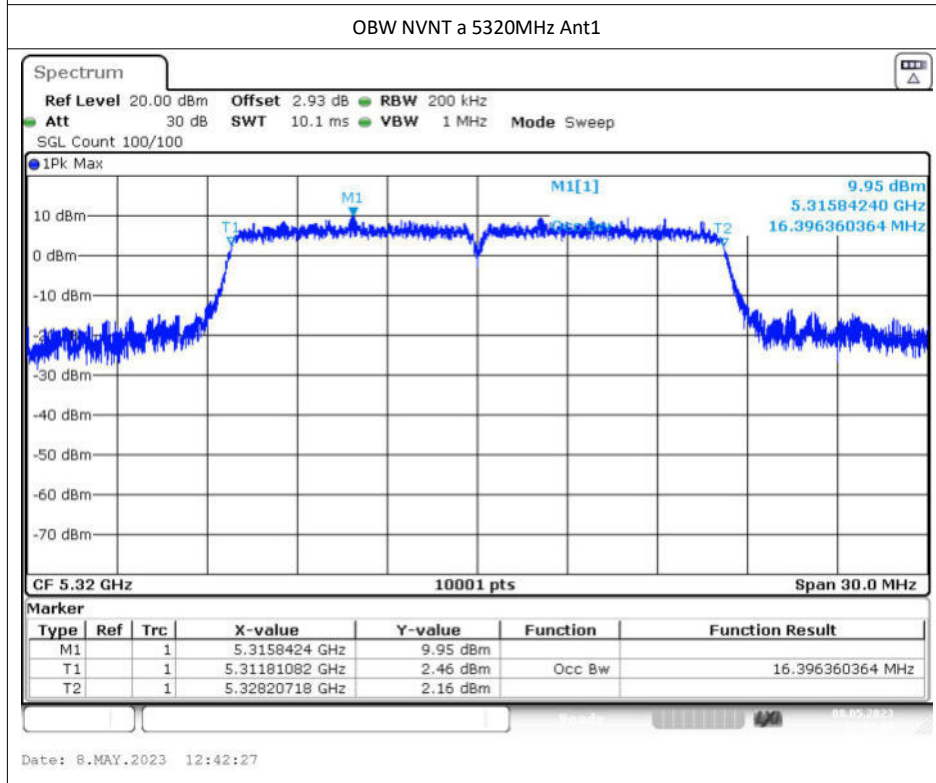
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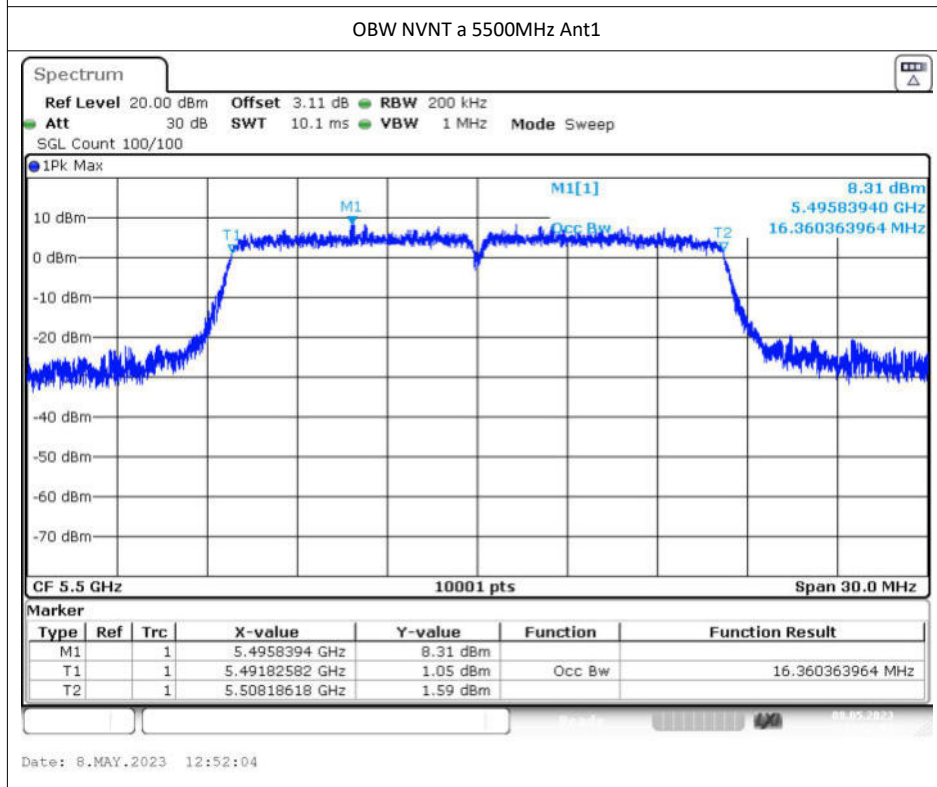
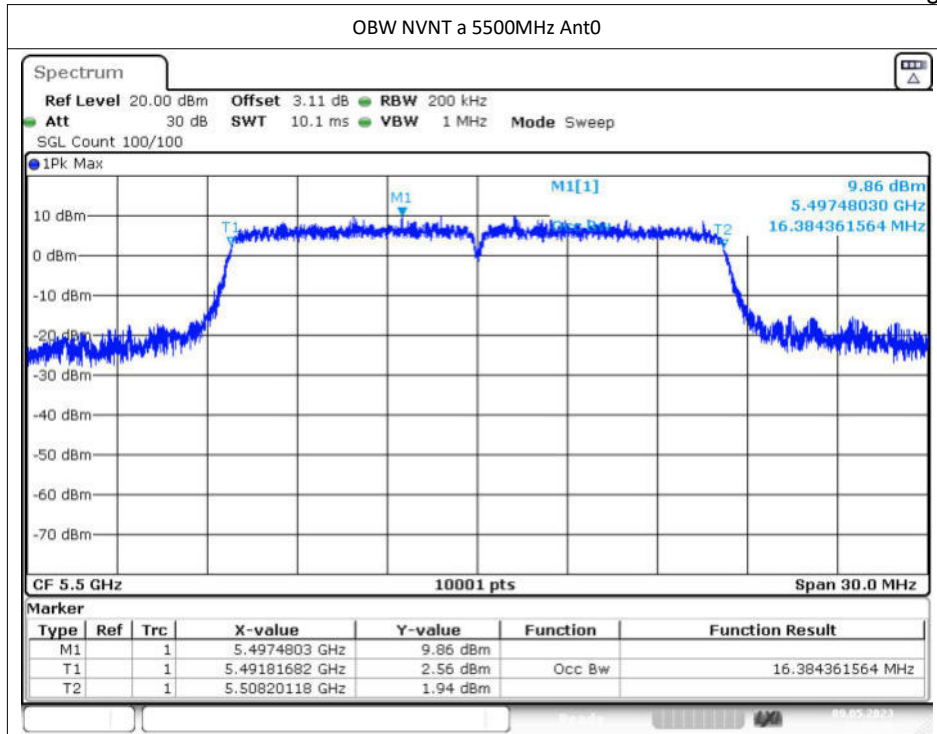
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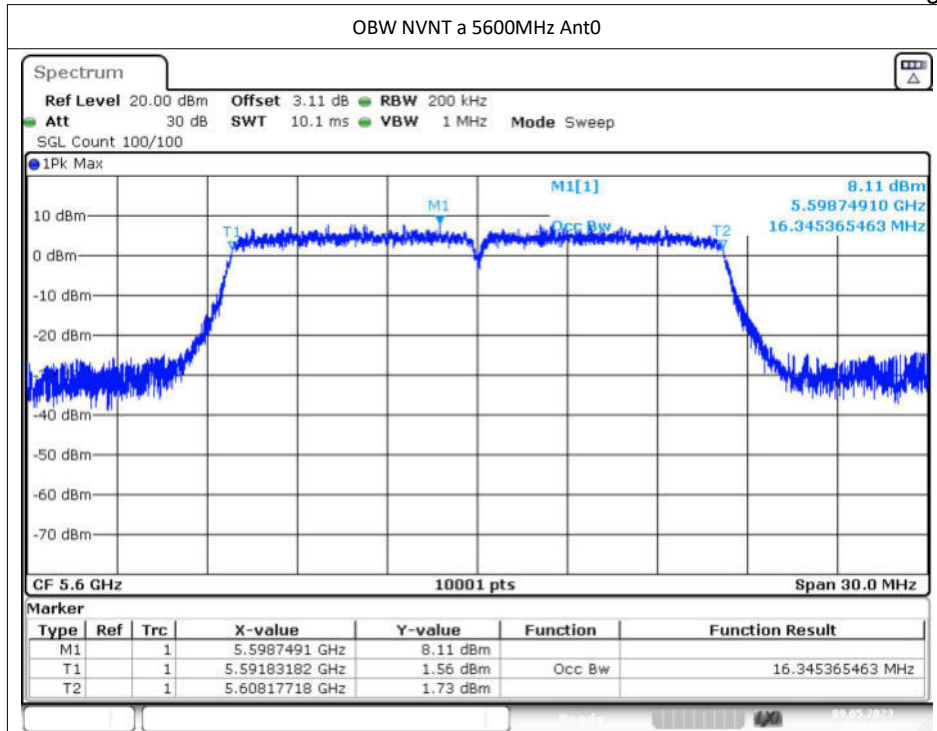


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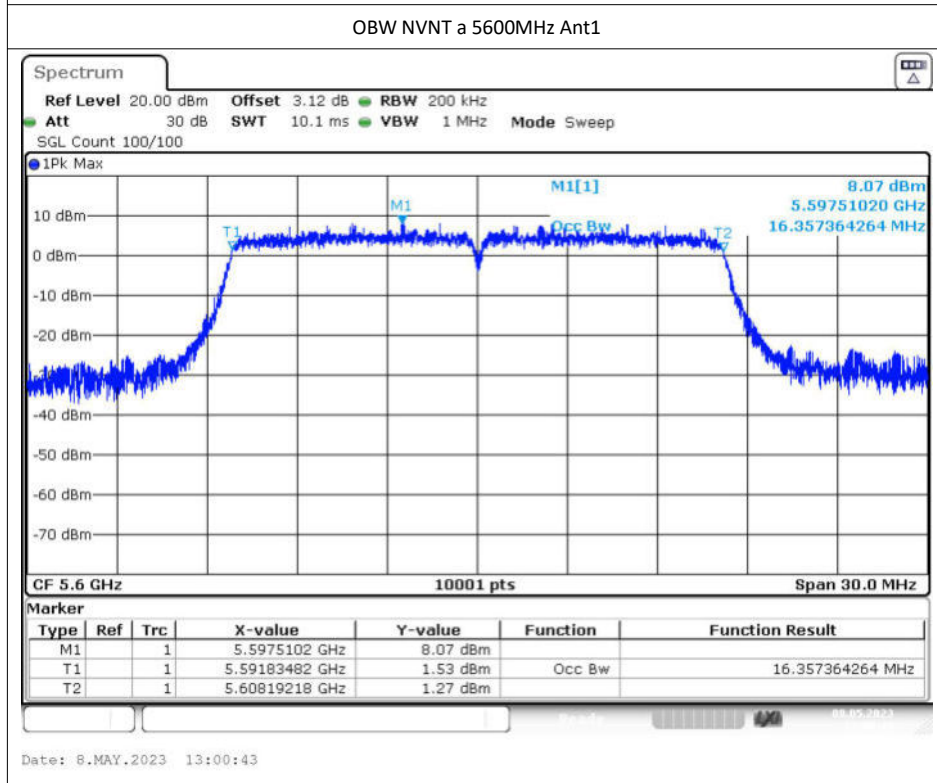


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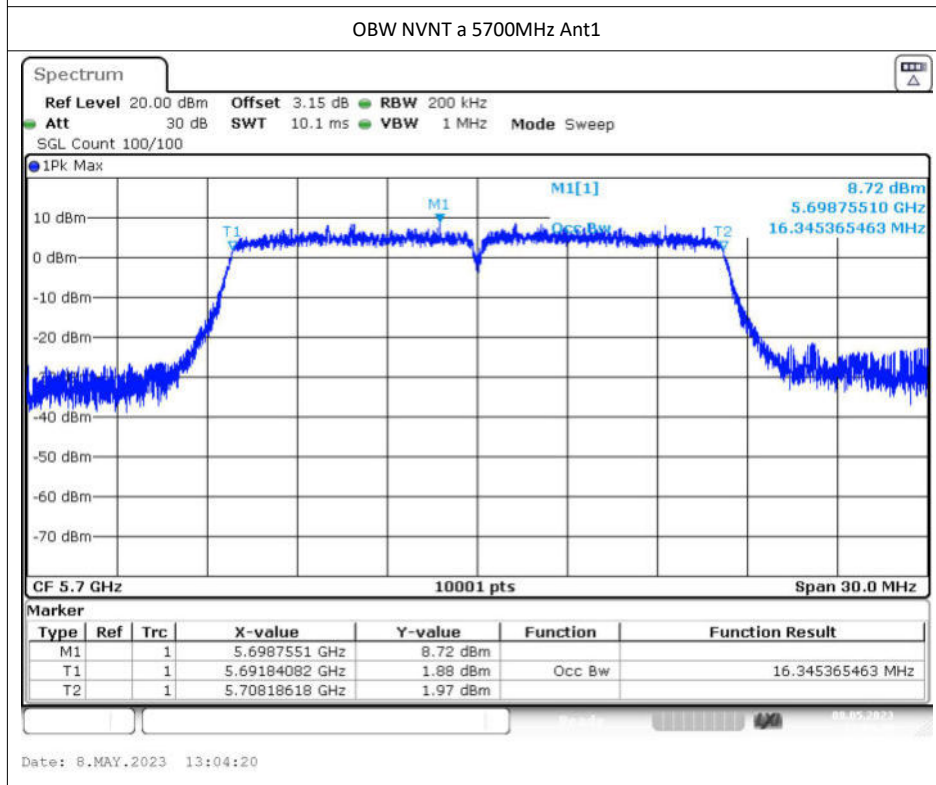
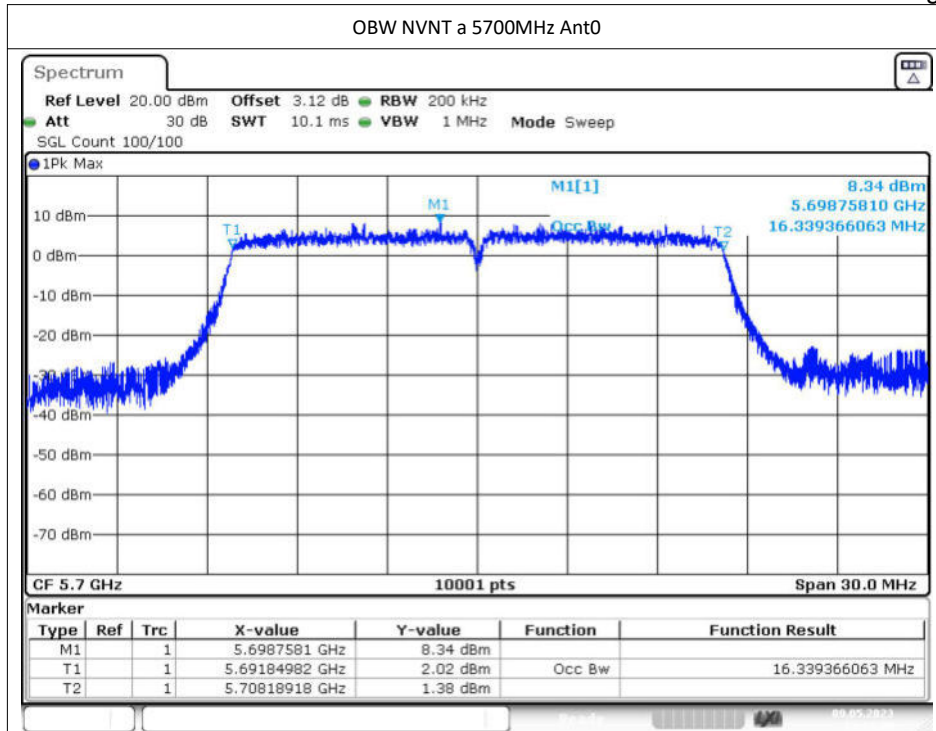


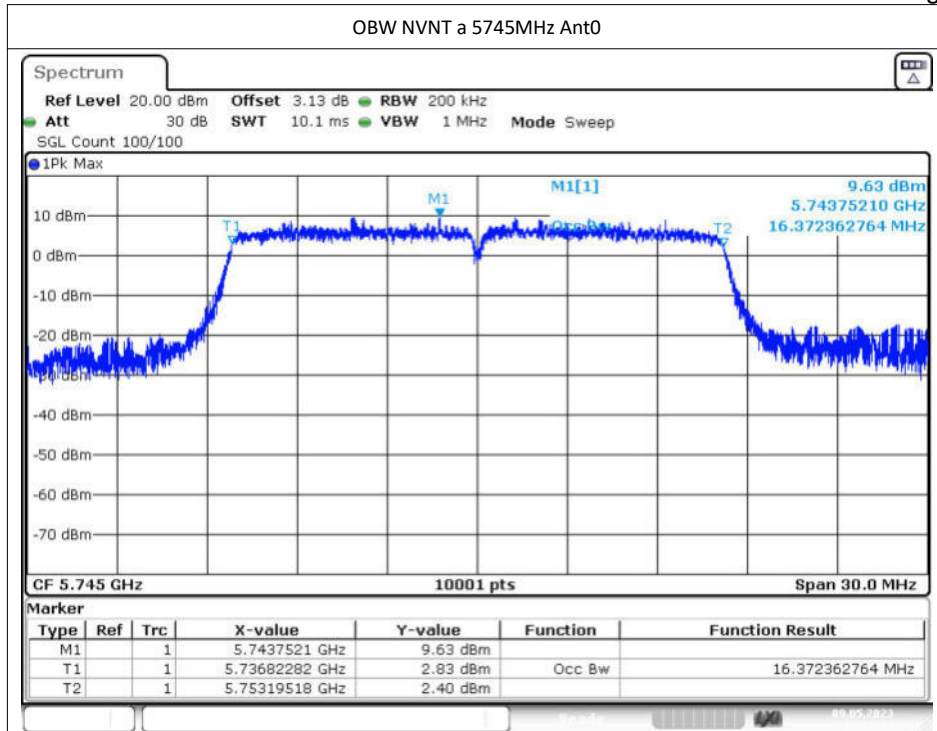


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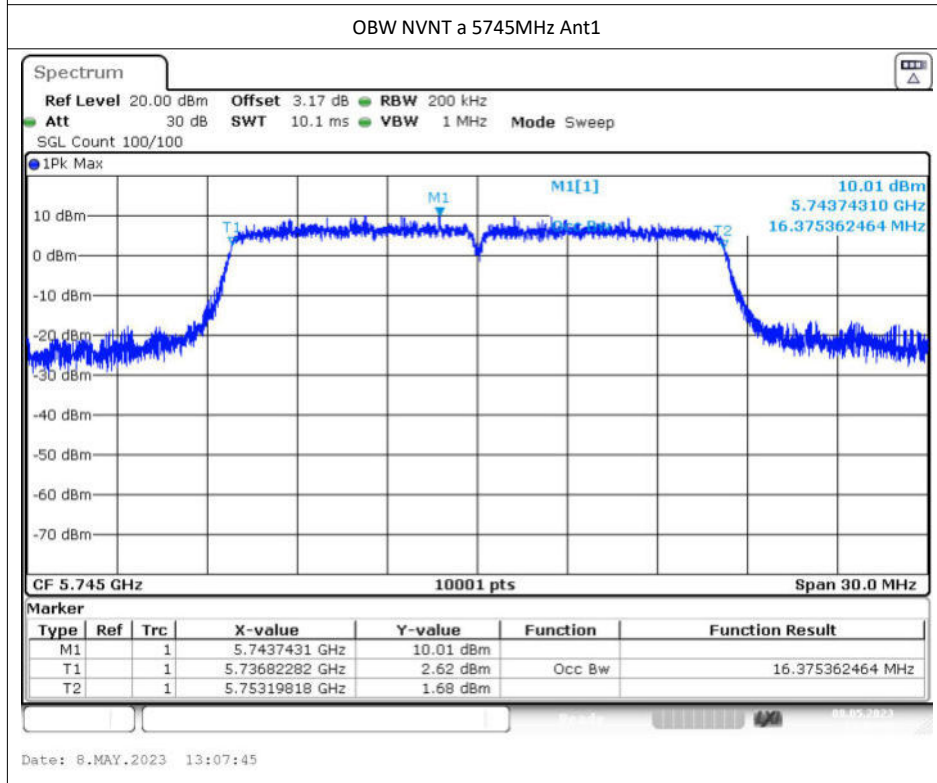


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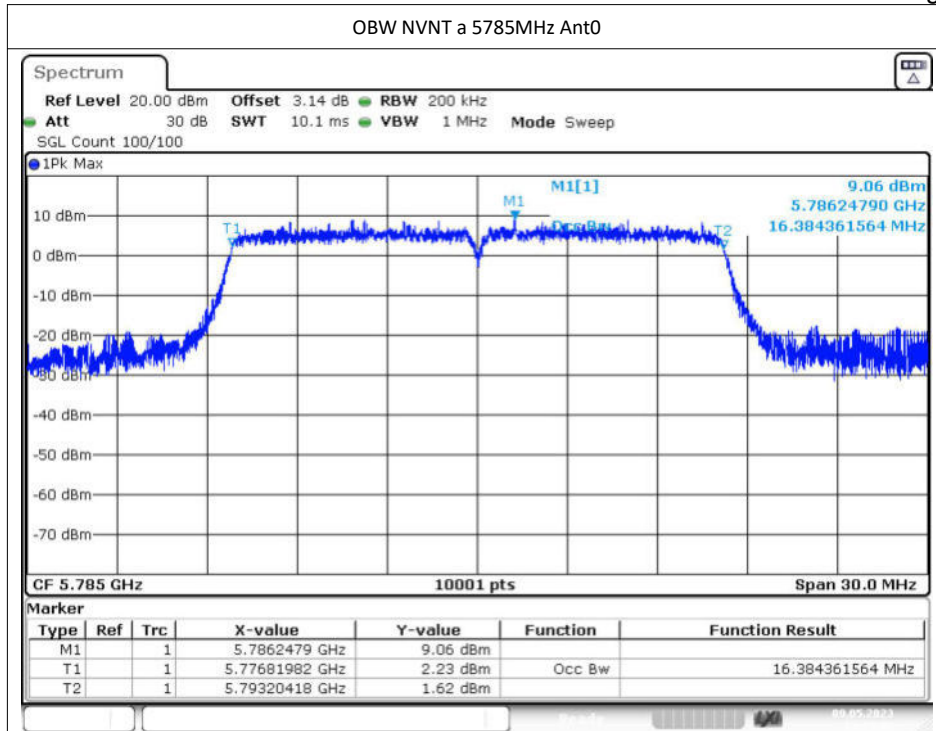




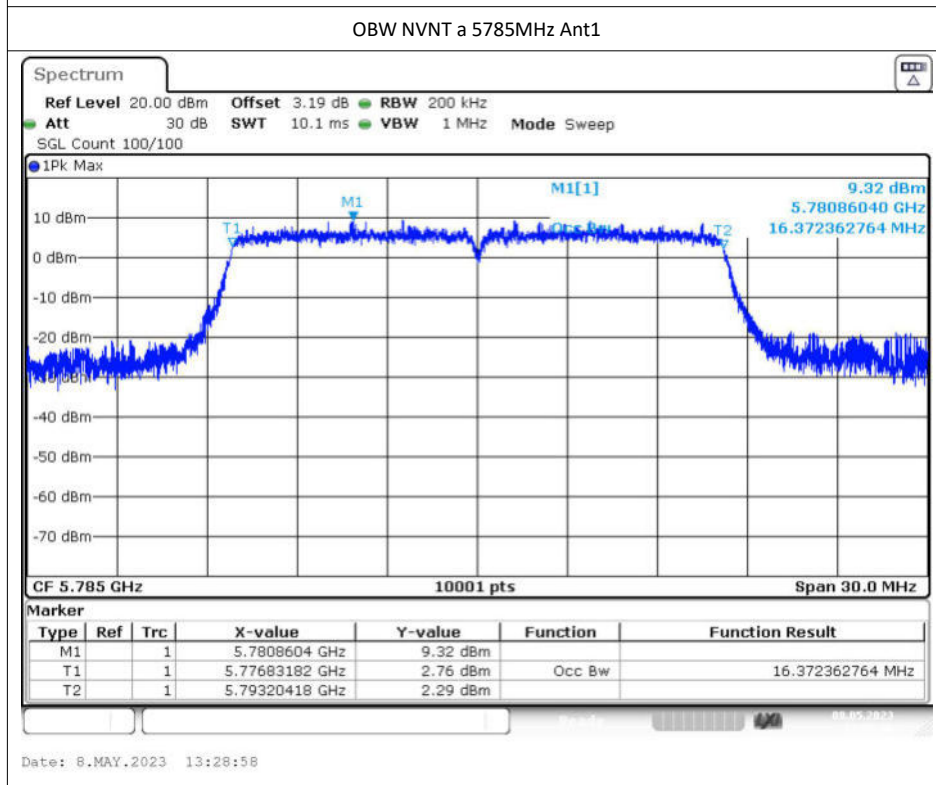
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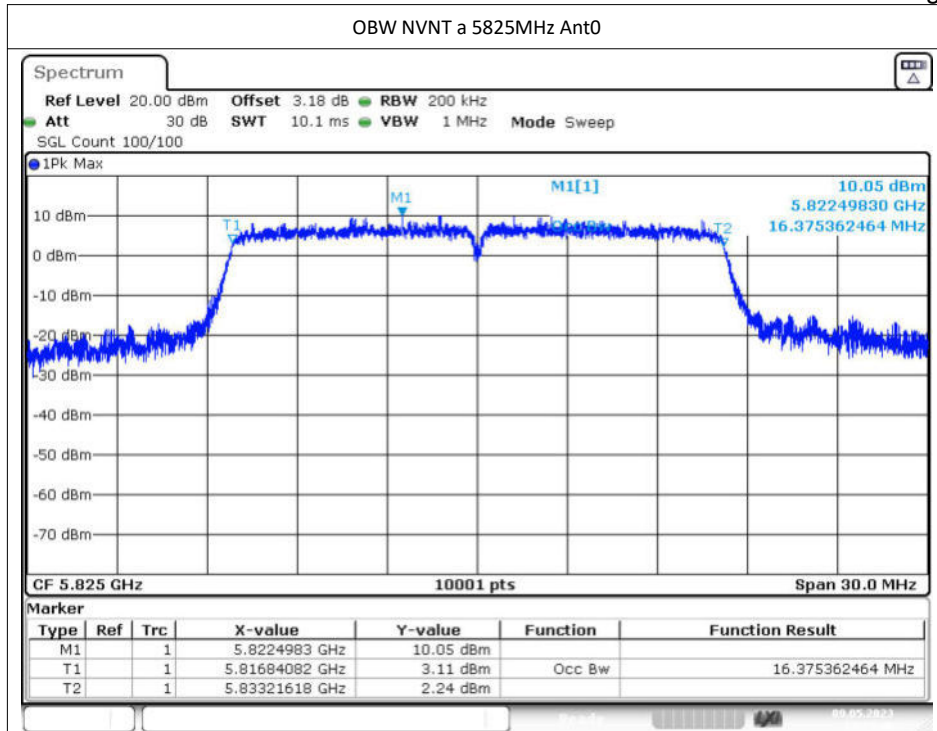
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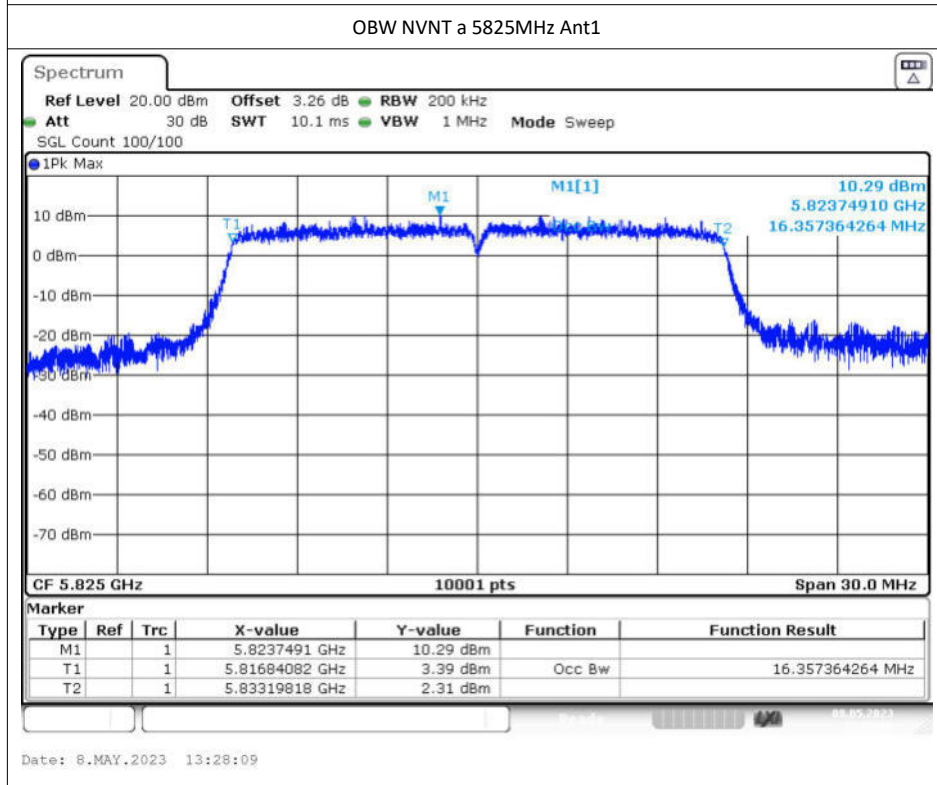
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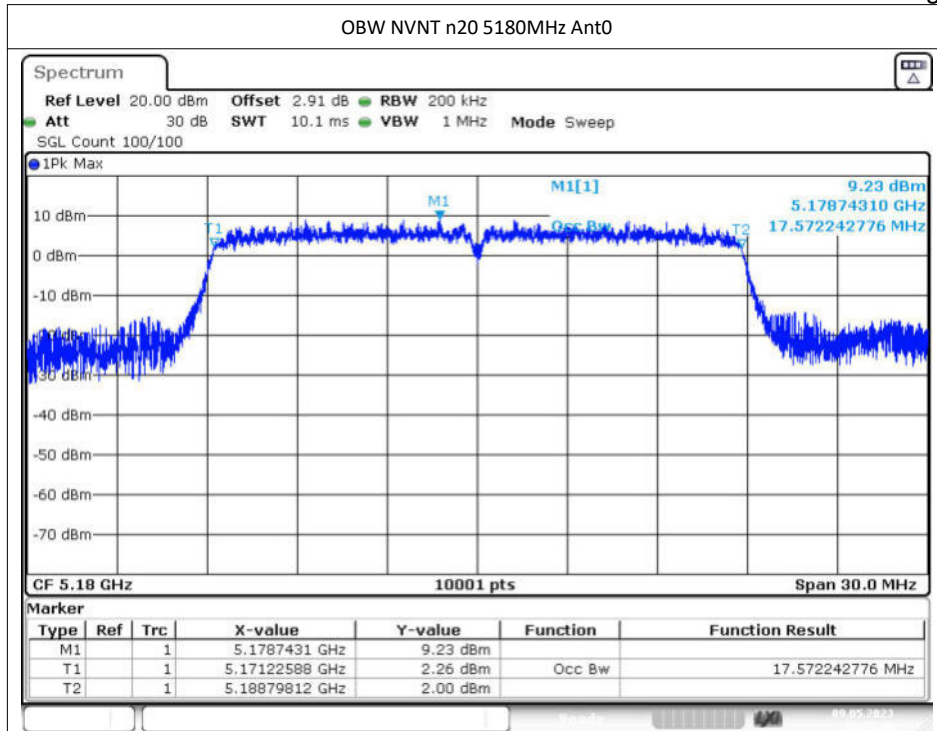
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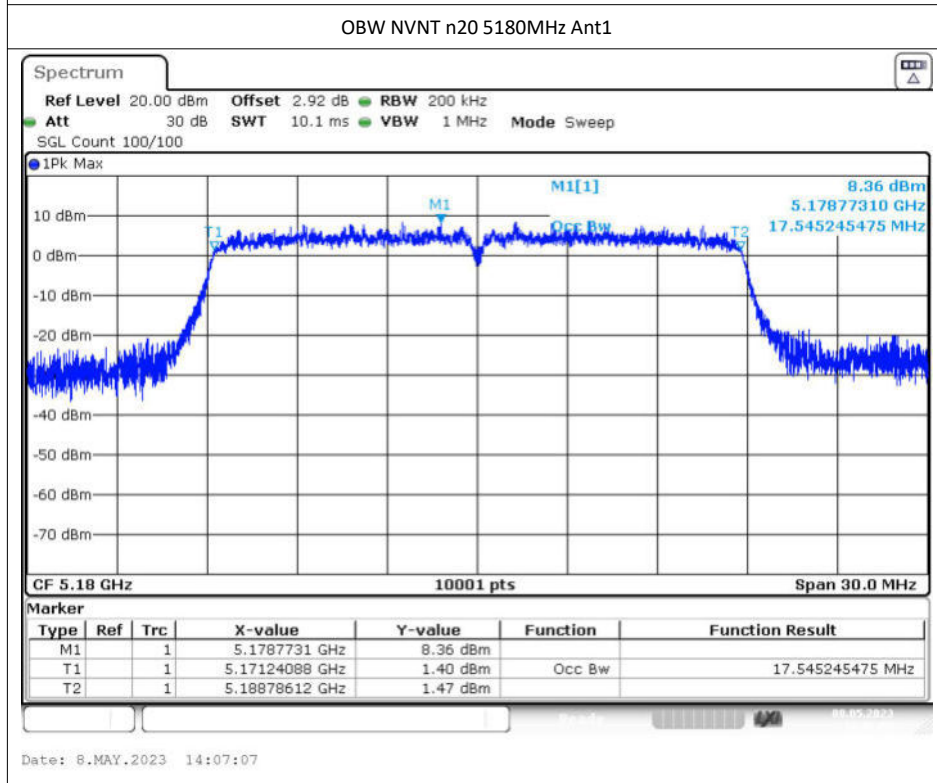
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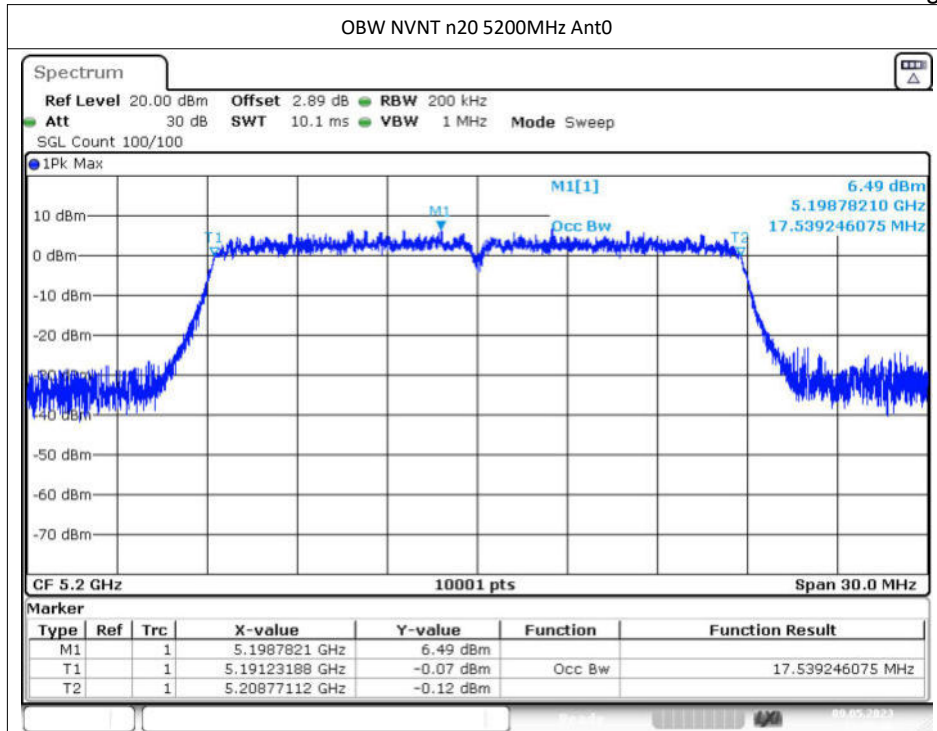
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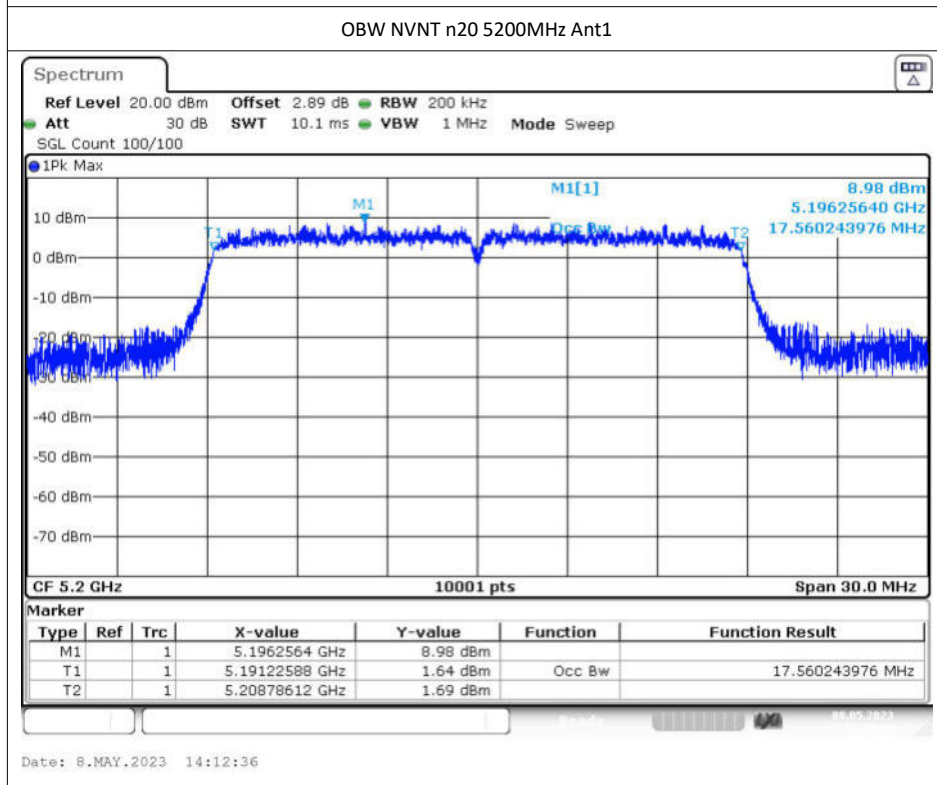
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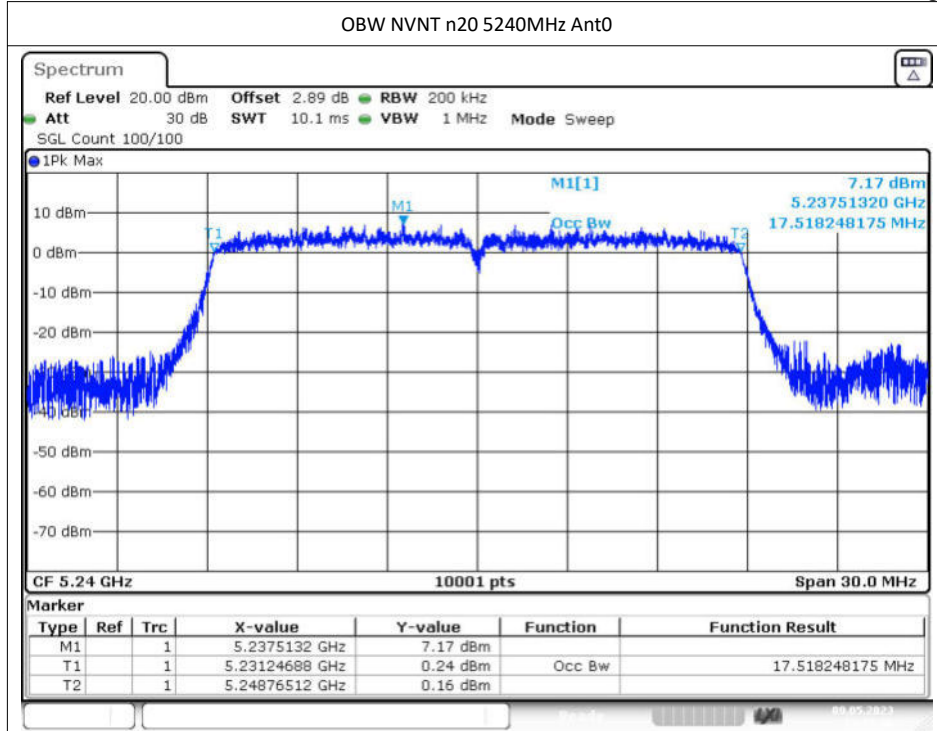
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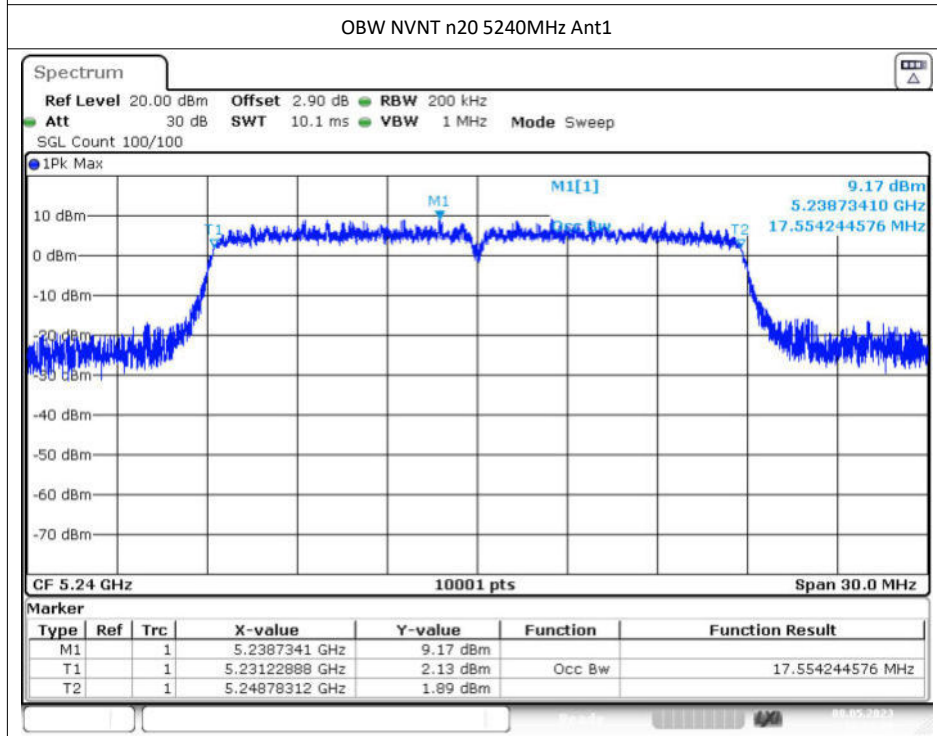
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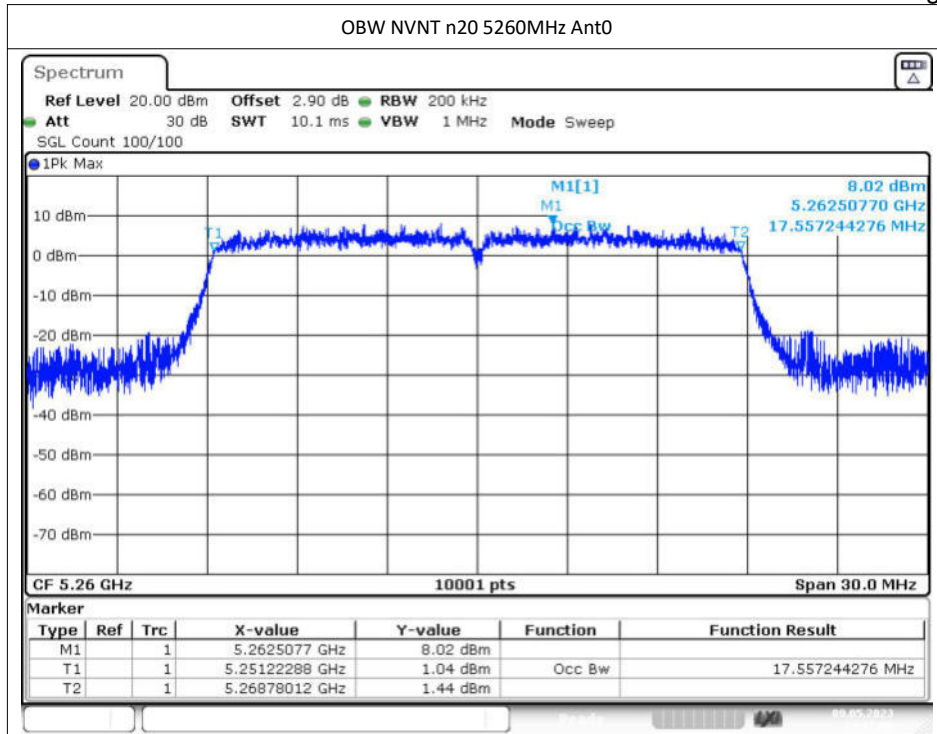
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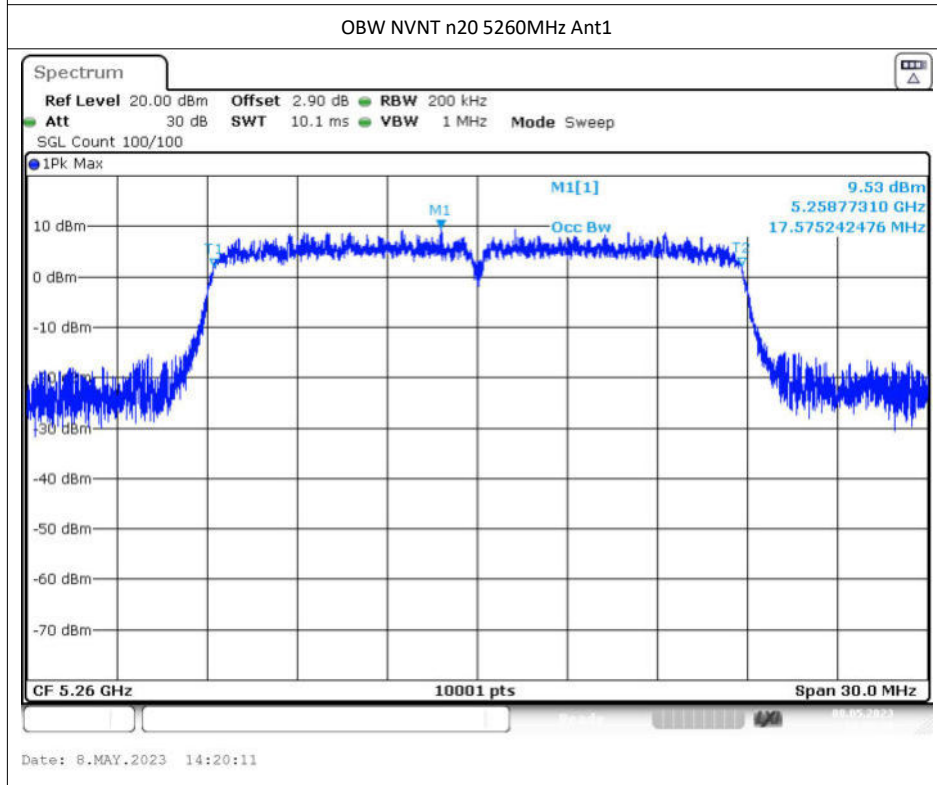
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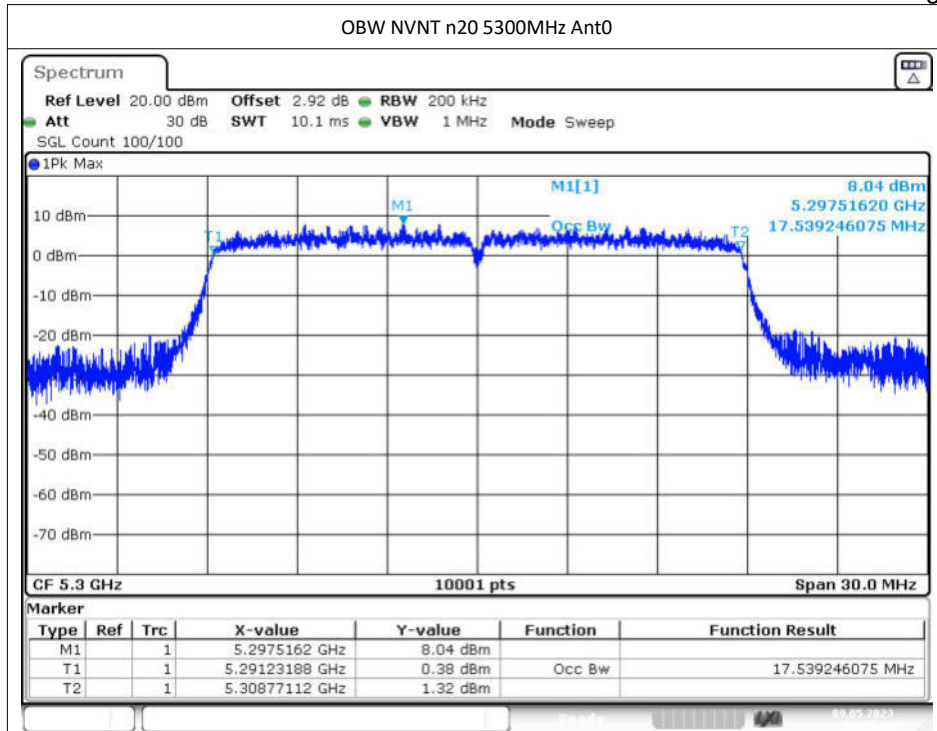
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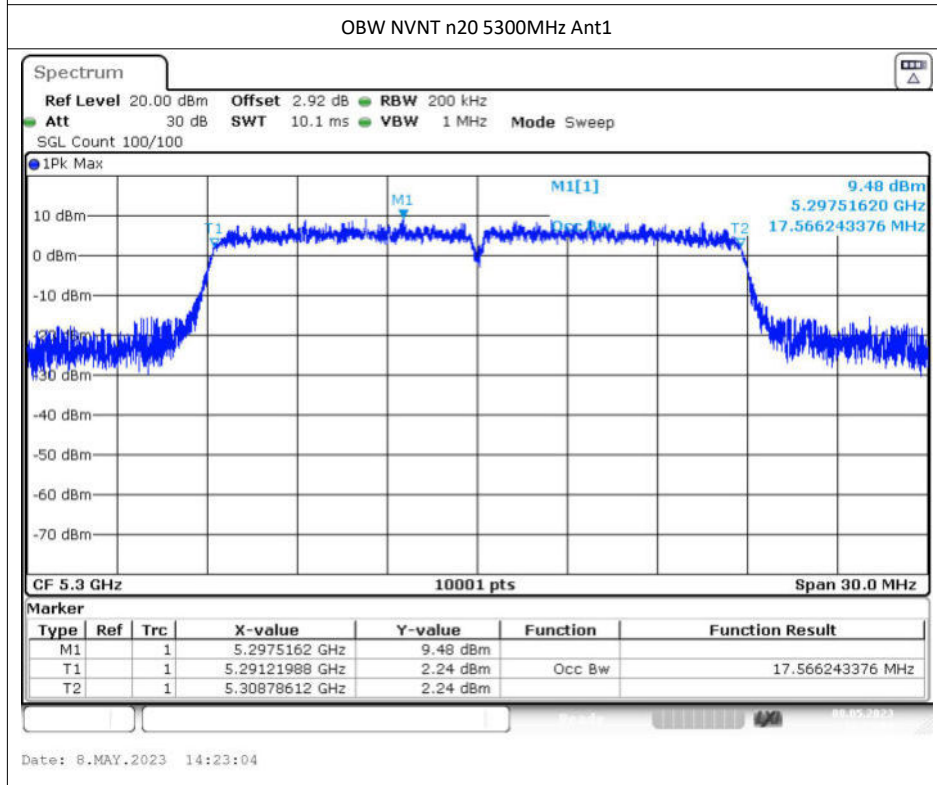
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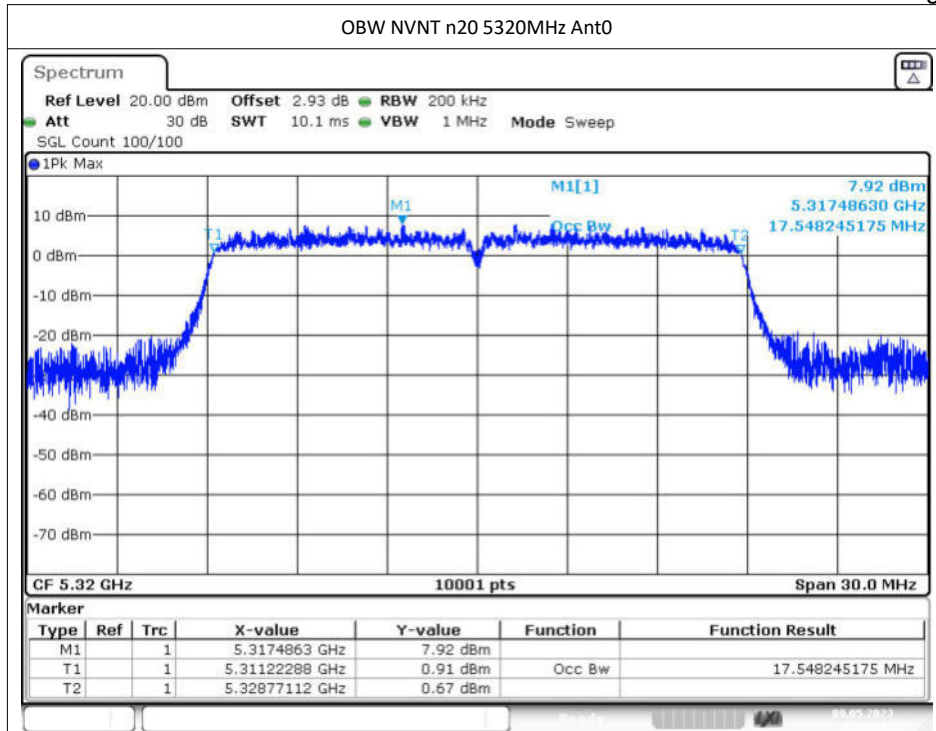
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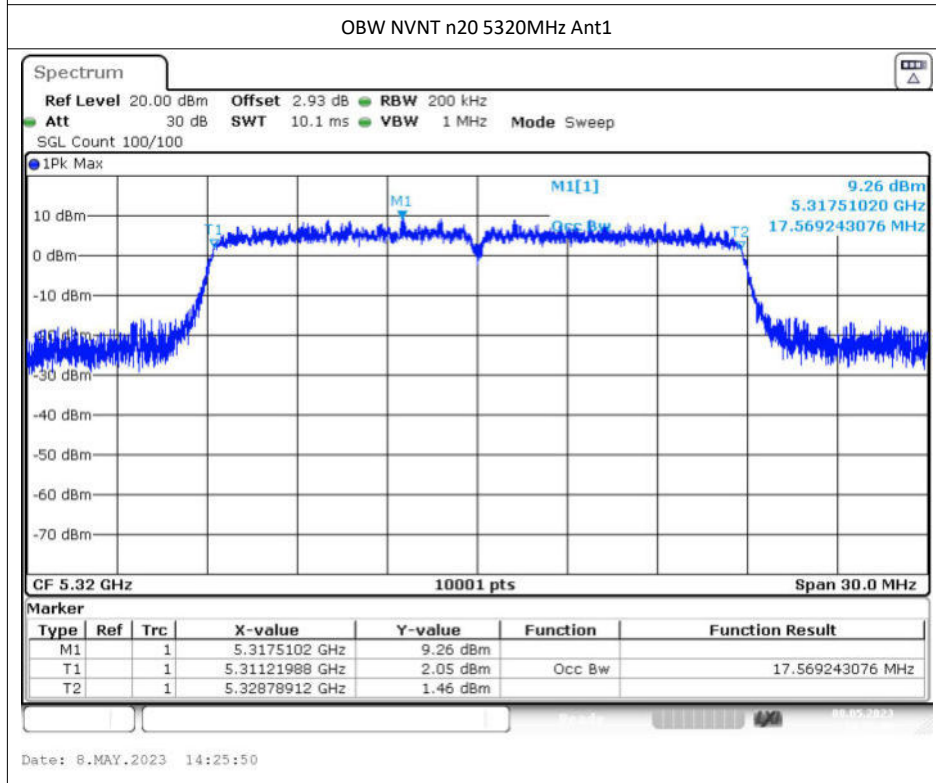
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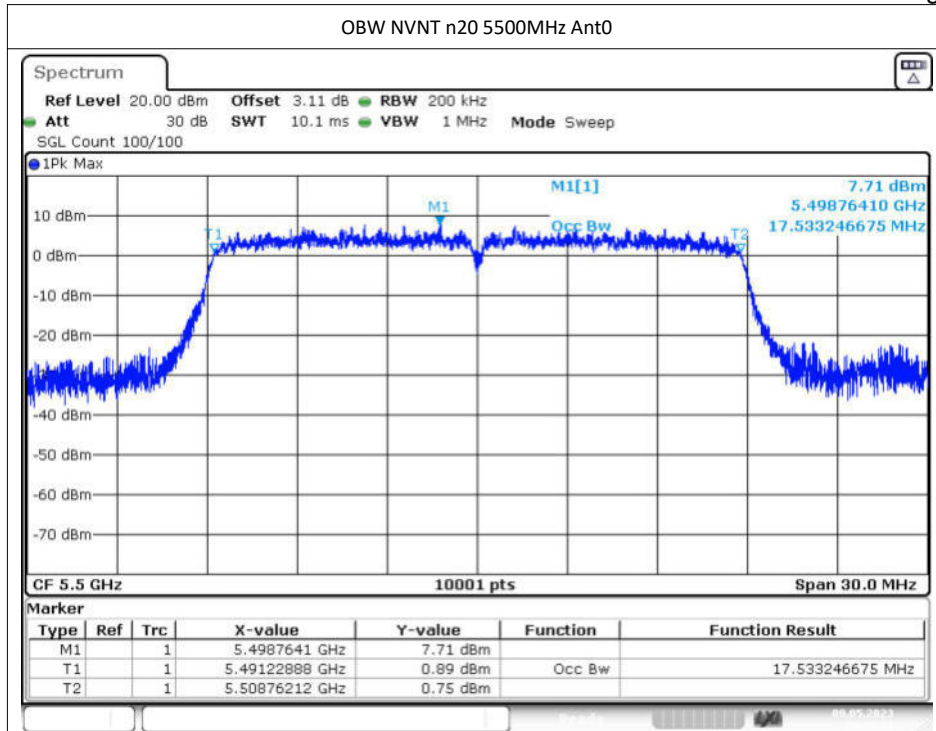
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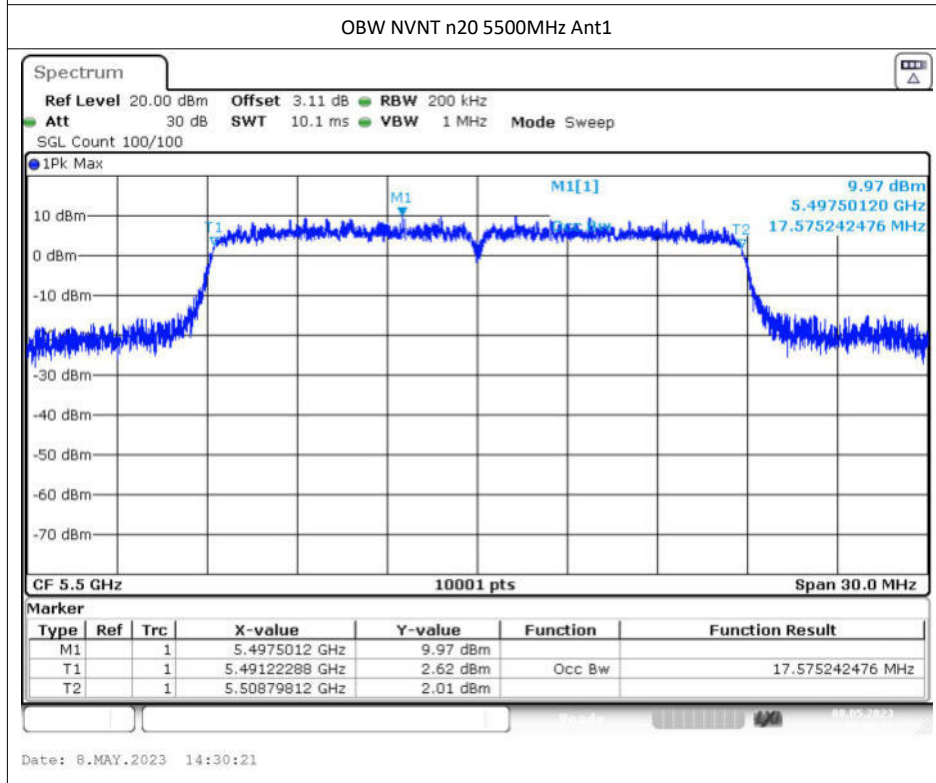
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