

FCC Request for clarification - regarding FCC-ID 2AAGMVZ120Q (LTE Module; Sequans Communications);
2nd TCB answer (04-15-2016), forwarding the grantee's 1st response of 04-08-2016:

Correspondence Reference Number: 229123
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From: Jeremy Gosteau [<mailto:jgosteau@sequans.com>]
Sent: Friday, April 08, 2016 8:03 AM
To: Wahl, Nikolaus [CETECOM]
Cc: US-PCB
Subject: RE: FCC Request for clarification - regarding FCC-ID 2AAGMVZ120Q (LTE Module; Sequans Communications);

Hi Niko,

Thanks for your prompt response and help – and I'm very much apologetic for not having responded before.

For clarification sake, who's supposed to reply to the FCC? Do you continue being the interface, with our support for technical information?

As you're pointing out, there can be confusion (and I agree on the ambiguity of the documentation and related photos) on the three items

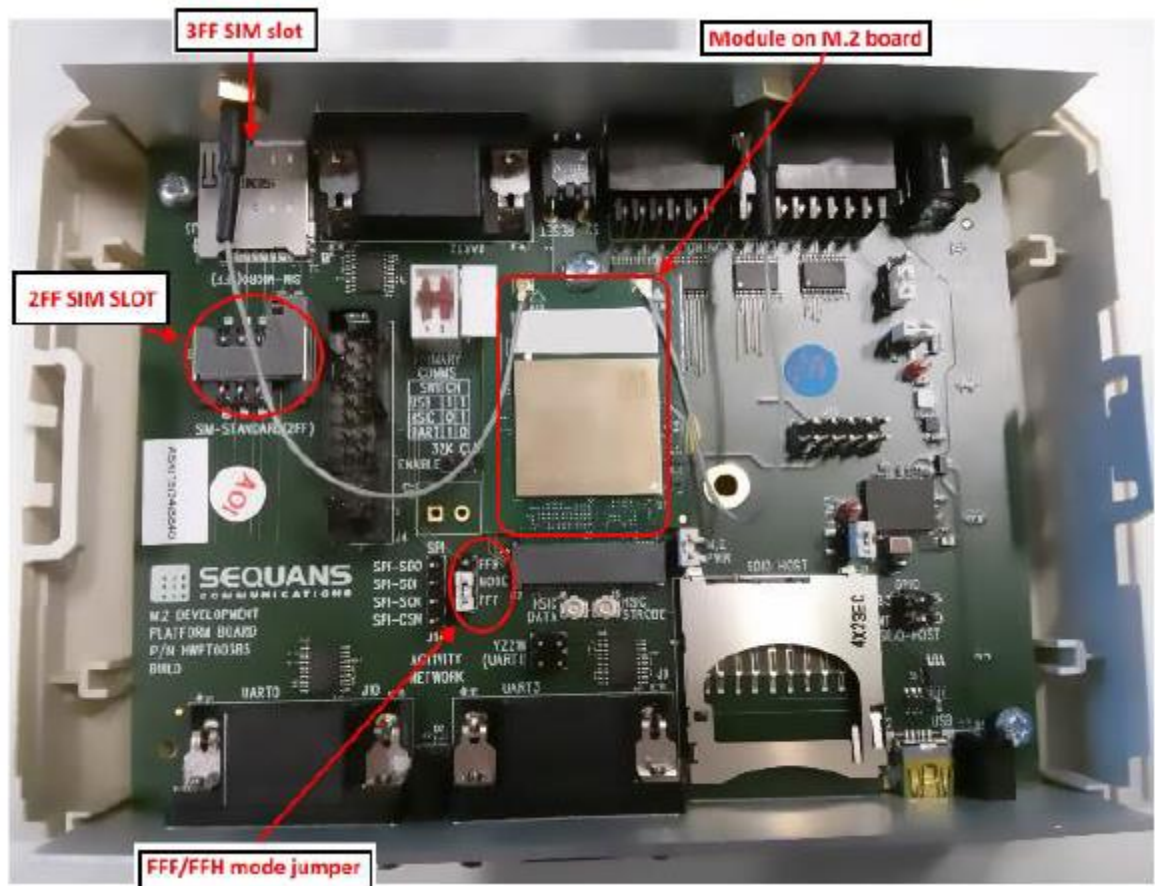
- The shielded part [called VZ120Q module]



- The M.2 adaptation board (a.k.a. NGF) on which the VZ120Q module is soldered



- The evaluation kit in which the M.2 adaptation board is plugged

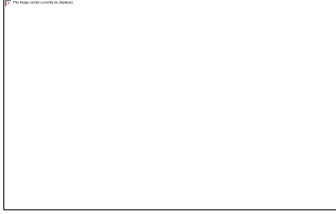


The module which is the subject of the certification is the shielded part (the VZ120Q module). The M.2 adaptation board and the evaluation kit are the vehicles used for testing the module. The M.2 adaptation board has the small antenna connectors, and a set of standardized interfaces (as per the M.2 standard). The evaluation board allows plugging external antennas through SMA connectors, and adding a SIM card, for testing sake.

These different layers can indeed add ambiguity in the documentation provided. I like your approach when you're asking the FCC team if they can point out to the ambiguous parts, so that we can provide detailed answers to those, and correct the related documentation.

For the documentation provided, here is some answer

- part list: relates to the VZ120Q module (nothing related to the M.2 adaptation board)
- block diagram: relates to the VZ120Q module (nothing related to the M.2 adaptation board)
- schematics: relates to the VZ120Q module pinout on the M.2 adaptation board (this is why the bottom right label reads as follows



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Based on this, do you believe any corrective documentation/information is required?

Thanks and best regards,
Jeremy