





SPI Flash



Table 1: Pin assignment and booting sequence from external devices

Pin	Step	SPI MASTER	SPI MASTER	UART	SPI SLAVE	Step	I2C		
P0_0	1	SCK	SCK	TX	SCK	8	SCL		
P0_1			CS	RX			SDA		
P0_2			MISO	TX			9	SCL	
P0_3			CS	MOSI			CS	SDA	
P0_4				5			RX	10	SCL
P0_5			MOSI				RX	MISO	SDA
P0_6			MISO				TX	MOSI	11
P0_7			RX			SDA			