





SPI Flash



Table 1: Pin assignment and booting sequence from external devices

Pin	SPI MASTER	SPI MASTER	UART	SPI SLAVE	I2C
P0_0	1	SCK	TX	7	SCL
P0_1		CS	RX		SDA
P0_2		MISO	TX	9	SCL
P0_3		CS			SDA
P0_4			TX	10	SCL
P0_5		MOSI	RX		SDA
P0_6		MISO	TX	11	SCL
P0_7			RX		SDA