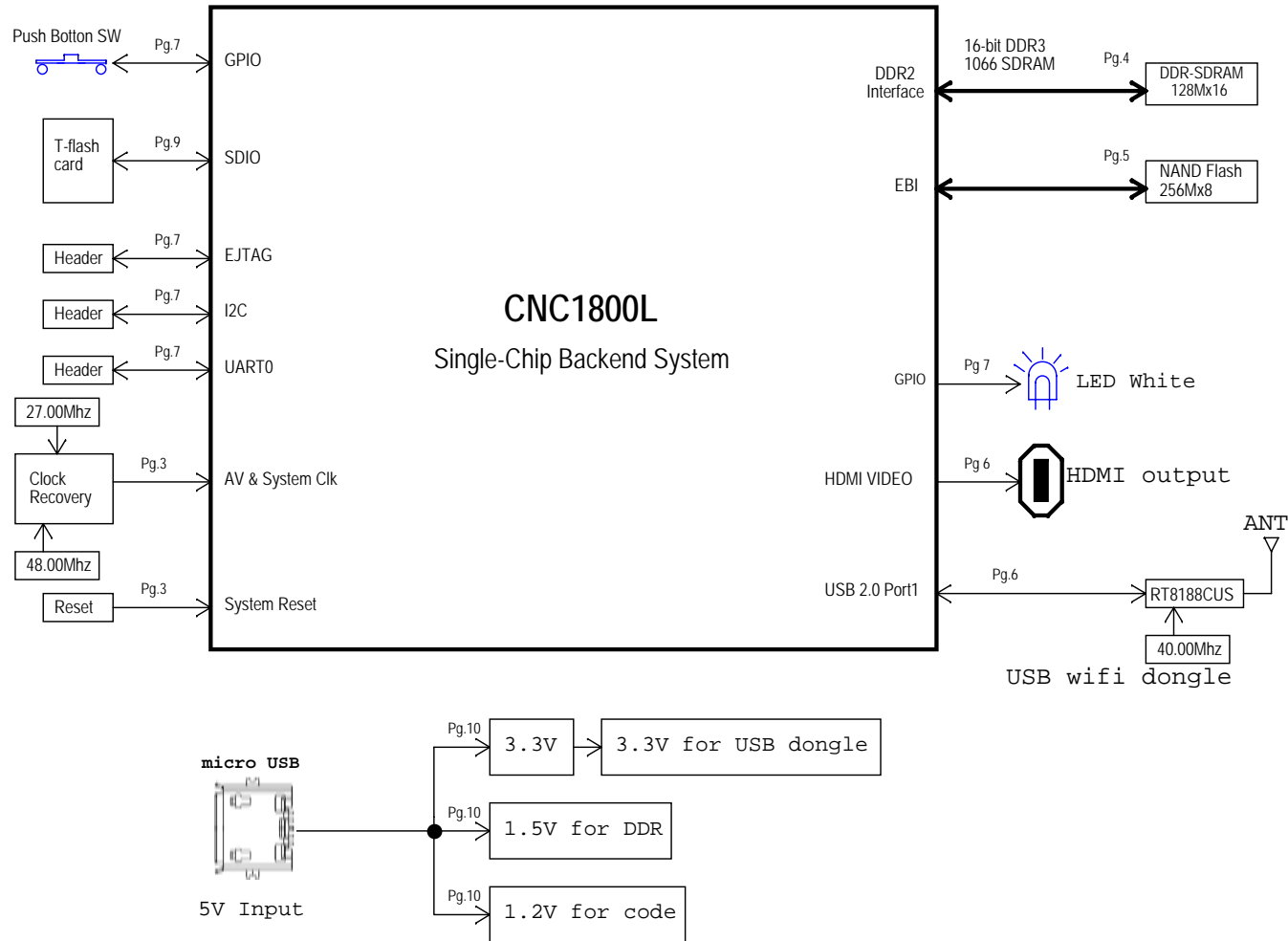


## FreeOTT Block Diagram



### Revision History:

Rev	Description	Date	Approved
1	Preliminary Release	2012/03/14	

### Layout Notes:

1. Discrete (especially resistors) should be placed close to Large components which are on the same schematic page

NOTES, UNLESS OTHERWISE SPECIFIED:

- RESISTANCE VALUES IN OHMS.
- CAPACITANCE VALUES IN MICROFARADS.
- REFERENCE DESIGNATORS USED.
- ALL 0.1 uF CAPACITORS ARE DECOUPLING CAPS UNLESS OTHERWISE NOTED. THEY ARE SHOWN ON THE PAGE WITH THE INTEGRATED CIRCUITS THEY SHOULD BE PLACED NEAR.
- BOARD PROPERTIES:
  - FR-4 RoHS COMPLIANT MATERIAL PER IPC-4101/129 OR BETTER
  - ALL SIGNAL LAYERS - 0.5 OZ CU
  - PLANE LAYERS - 0.5 OZ CU
  - IMPEDENCE CONTROL:
    - 50 OHMS +/- 10% SINGLE ENDED TRACES
    - 60 OHMS +/- 10% SINGLE ENDED TRACES
    - 90 OHMS +/- 10% SINGLE ENDED TRACES
    - 100 OHMS +/- 10% DIFFERENTIAL TRACES
  - LAYER STACKUP:

Syabas Technology Inc.



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