EnzyTek
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7F, No. 35, Hsueh Fu Rd., Hsinchu 300,
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TEL：886-3-573-6708 FAX： 886-3-573-8749

BTA-C1000-2

Preliminary Datasheet

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EnzyTek Bluetooth® Low Energy Module With on Board Antenna

BTA-C1000-2

OVERVIEW

- Highly integration BT 4.0 Low Energy Class II module, CSR CS1000 + Memory + Filter + X'Tal + Chip Antenna.
- Wireless communications module conforming to Bluetooth Version 4.0.
- UART, SPI interfaces available to various applications.
- 5 GPIO ports available for user's application.
- 3 Analog IO ports available for user's application.

BT Chipset	: CSR CSR1000
Standards	: Bluetooth 4.0 Low Energy.
Frequency	: 2402 ~ 2480 MHz
TX Output Power	: 4 dBm (max)
RX Sensitivity	: -88 dBm (min)
Range	: > 10 m (line-of-sight at open space)
Memory	: EEPROM (512K bits)
Operation Voltage	: 1.8V ~ 3.6V
Dimension	: 18 x 13 x 2.2 $_{(max)}$ mm^3 (L×W×H)
Environmental Range	: Operation Temperature : 0~+70°C, Relative humidity : 0~95%

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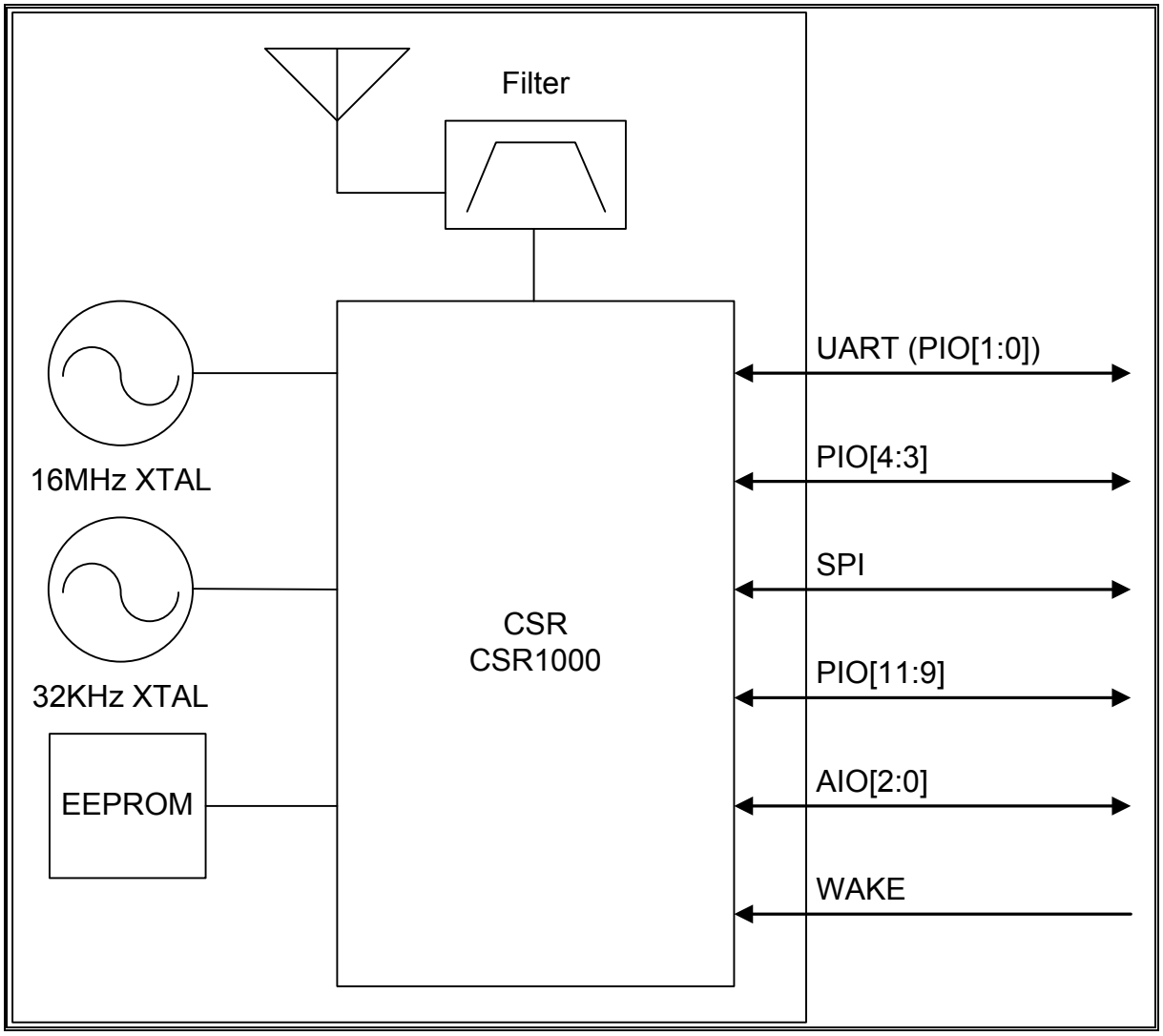
System Block Diagram



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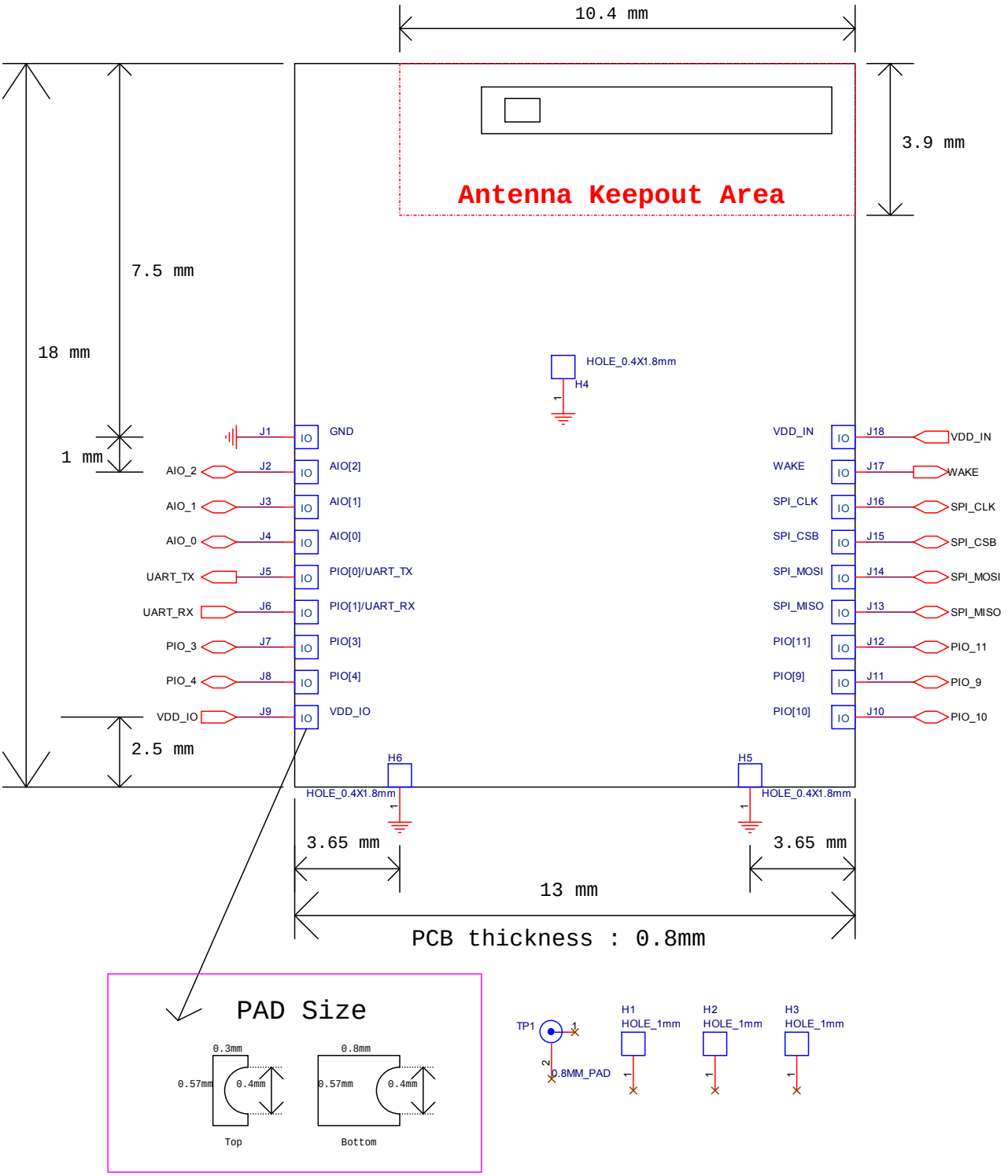
Pinout Diagram / Dimension

Unit : mm

Note: Please contact EnzyTek to get the detail footprint of the module to do the PCB design.



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I/O PIN LISTING

Pin No.	Pin Name	Type	Description
J1	GND	Power	Ground
J2	AIO_2	Analog bi-directional	Programmable input/output line
J3	AIO_1	Analog bi-directional	Programmable input/output line
J4	AIO_0	Analog bi-directional	Programmable input/output line
J5	UART_TX (PIO_0)	CMOS output, tri-state, with weak internal pull-up	UART data output t, optional PIO0 which is defined by FW.
J6	UART_RX (PIO_1)	CMOS input with weak internal pull-down	UART data input, optional PIO1 which is defined by FW.
J7	PIO_3	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
J8	PIO_4	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
J9	VDD_IO	Power	IO VDD, NC.
J10	PIO_10	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
J11	PIO_9	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
J12	PIO_11	Bi-directional with programmable strength internal pull-up/down	Programmable input/output line
J13	SPI_MISO	CMOS output, tri-state, with weak internal pull-down	Serial Peripheral Interface data output
J14	SPI_MOSI	CMOS input with weak internal pull-down	Serial Peripheral Interface data input
J15	SPI_CSB	CMOS input with weak internal pull-up	Chip select for Synchronous Serial Interface active low
J16	SPI_CLK	CMOS input with weak internal pull-down	Serial Peripheral Interface clock
J17	WAKE	Input has no internal pull-up or pull-down, use external pull-down.	Input to wake the module from hibernate or dormant.
J18	VDD_IN	Power	3.3V input

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Electrical Characteristics

Absolute Maximum Ratings :

	Min.	Typ.	Max.	Unit
Supply Voltage	-	-	3.6	V
Storage Temperature	-40	-	85	°C

Recommend Operation Conditions :

	Min.	Typ.	Max.	Unit
Supply Voltage	1.8	-	3.6	V
Storage Temperature	0	-	70	°C

Input/Output Terminal Characteristics :

	Min.	Typ.	Max.	Unit
Digital (UART, PIO)				
V_{IL} Input Voltage Low	-0.4	-	+0.4	V
V_{IH} Input Voltage High	0.7xVDD	-	VDD+0.4	V
V_{OL} Output Voltage Low, (I_O is 4mA)	-	-	0.4	V
V_{OH} Output Voltage High, (I_O is -4mA)	0.75xVDD	-	-	V

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Radio Characteristics

VCC = 3.3V

	Min	Typ	Max	Limits(BLE SPEC)	Unit
Output Power					
Max Power	4			<10	dBm
Min Power	-20			>-20	dBm
Peak to Average		0		<3	dBm
Carrier drift					
Fn	-150		150	<=150	kHz
Drift rate	-20		20	<20	kHz/50us
Max Power	-50		50	<50	kHz
Modulation Characteristic					
F1avg','F1max'	225		275	225<= <=275	kHz
F2avg','F2max'	185			>=185	kHz
F1/F2 Ratio		0.8		>=0.8	
Sensitivity (-88dBm)					
Frame Error Rate	0		30.8	<=30.8(-70dBm)	%
PER Integrity					
Frame Error Rate	50		65.4	50<= <=65.4	%
Max Input Power					
Frame Error Rate		0		<=30.8(-40dBm)	%

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Current Consumption

HW	BTM-C1000-2	
FW version	F-Serial_Port-v003	
FW configuration	Role	Gatt Server, device side
	Service	SPS Service
	Baud Rate	2400
	Default Power	Scale 0
BT BLE Host	iPhone 4S (ios5)	
Current Meter	Fluke 189	

	Min.	**Avg.**	**Max.**
Power On No connection	5.93 uA	6.79 uA	39.90uA
Power On advertising	202 uA	365 uA	567 uA
Connected No Data Transfer	15 uA	69 uA	143 uA
Connected TX Data/sec (from module to host)	17 uA	184 uA	1210 uA
Connected TX Data/500ms (from module to host)	17 uA	275 uA	1213 uA

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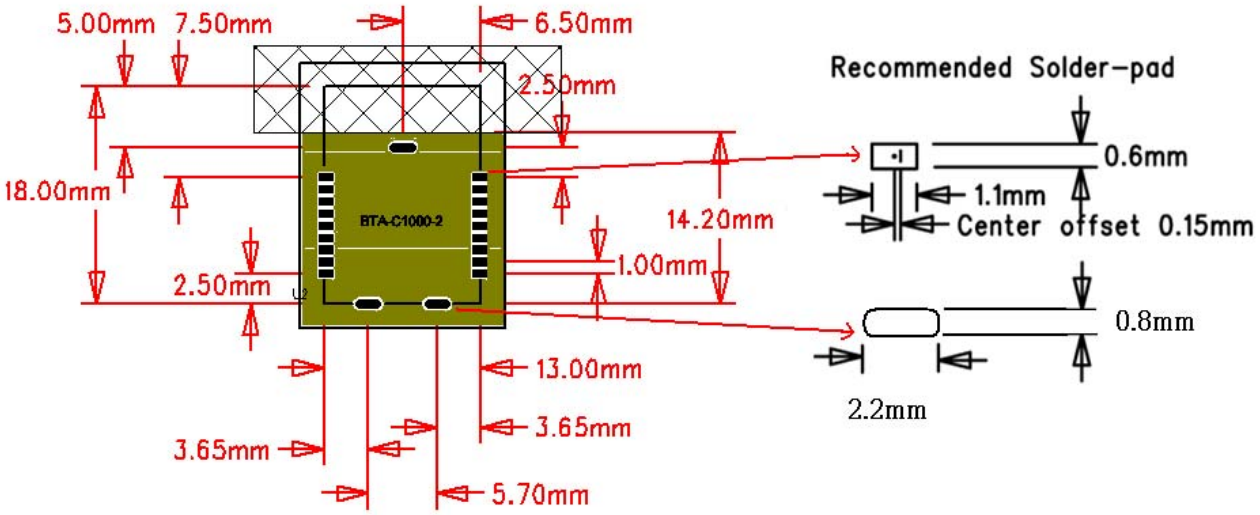
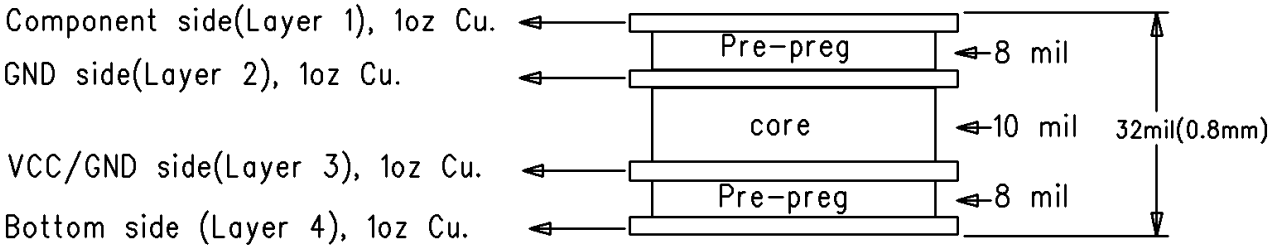
PCB Layout Guide



The 4-Layer Stackup



Material : FR4
DR=4.2+/-10%@1GHz and,DF=0.014+/-10%@1GHz
CPWG - 50-ohm Transmission Line: Gap=10mil, W=14mil

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Application Schematic