

11A_Ant 0_5320



11A_Ant 0_5500



11A_Ant 0_5580



11A_Ant 0_5700



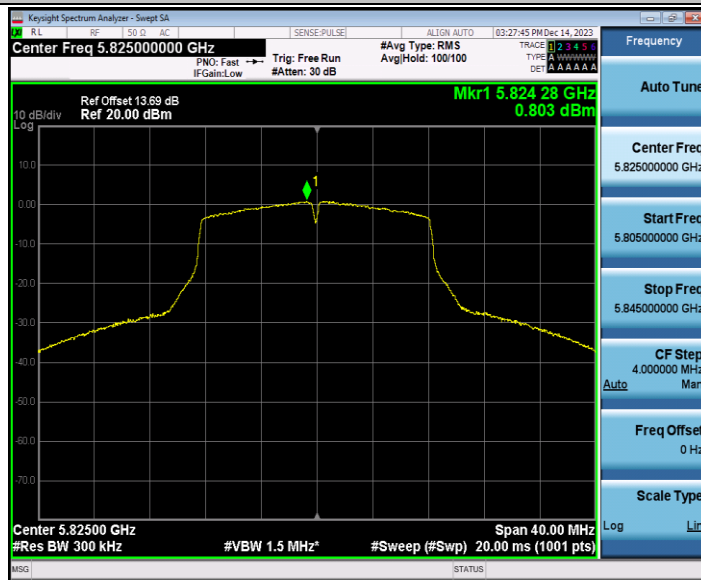
11A_Ant 0_5745



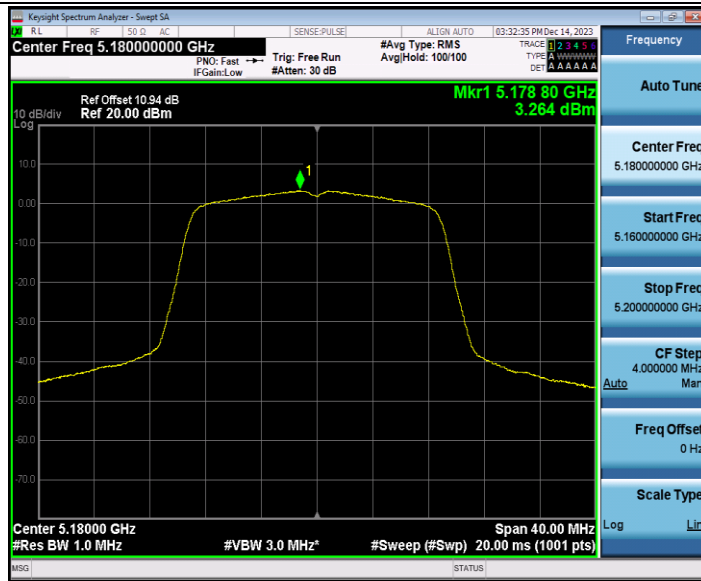
11A_Ant 0_5785



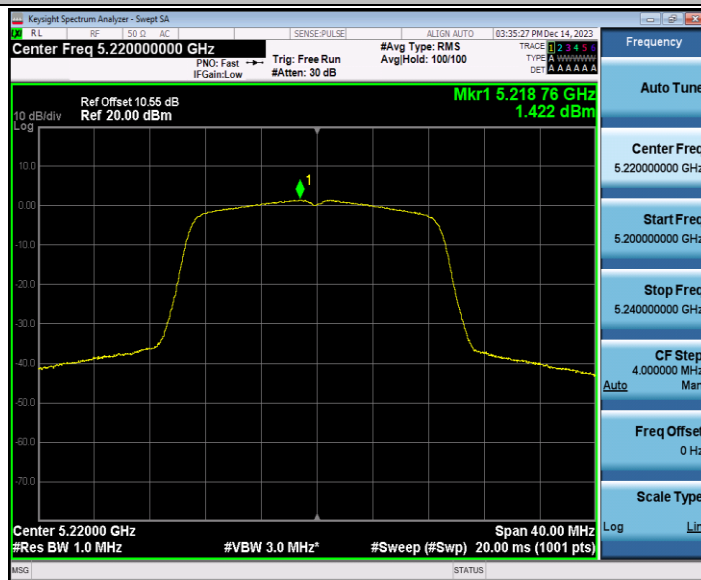
11A_Ant 0_5825



11N20SISO_Ant 0_5180



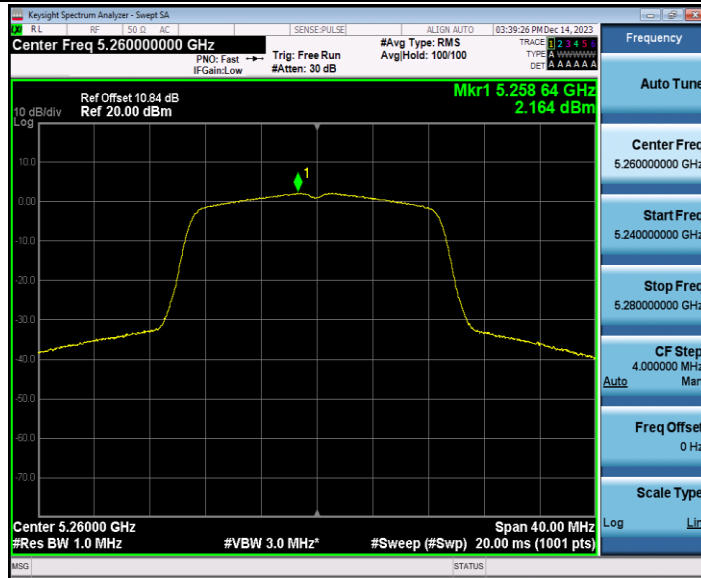
11N20SISO_Ant 0_5220



11N20SISO_Ant 0_5240



11N20SISO_Ant 0_5260



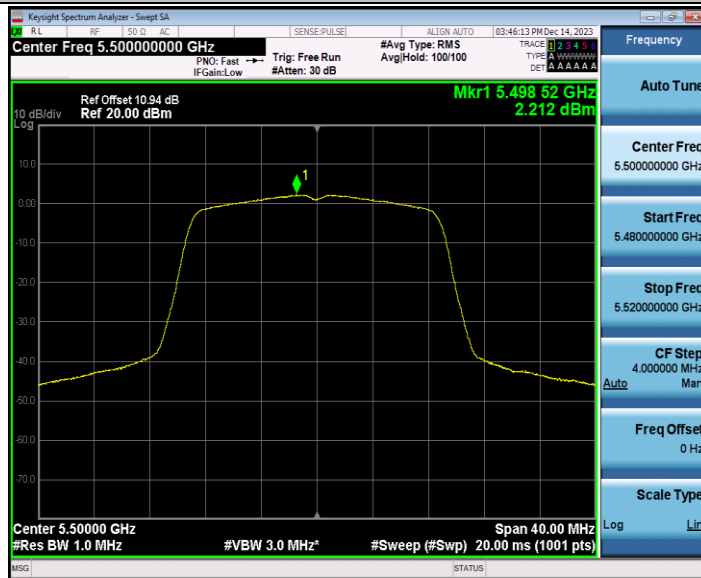
11N20SISO_Ant 0_5300



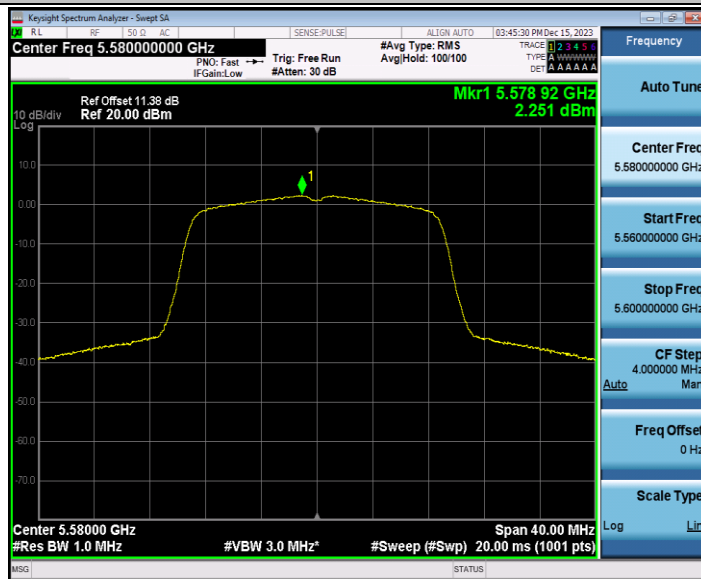
11N20SISO_Ant 0_5320



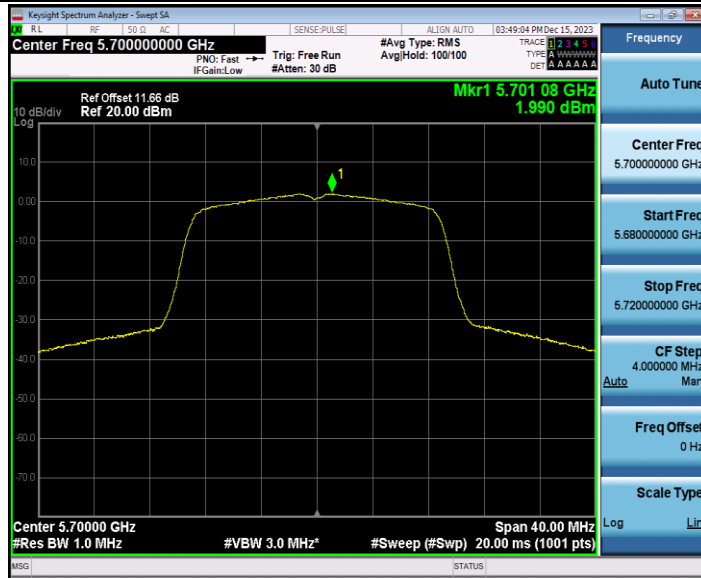
11N20SISO_Ant 0_5500



11N20SISO_Ant 0_5580



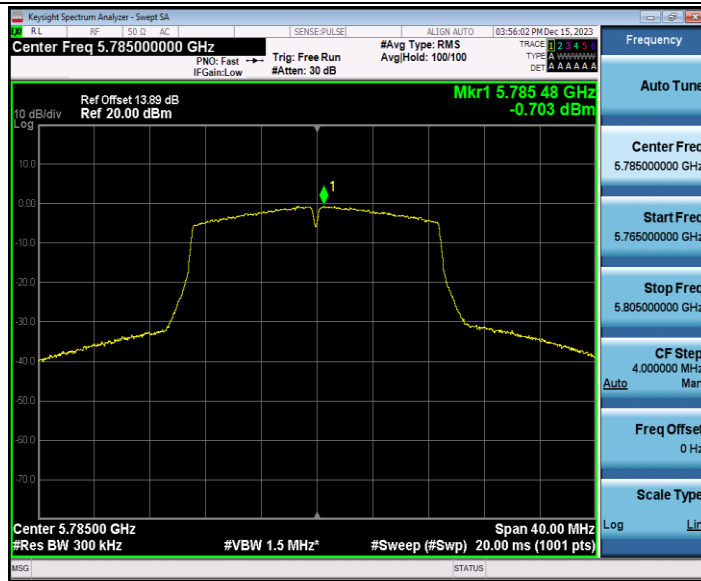
11N20SISO_Ant 0_5700



11N20SISO_Ant 0_5745



11N20SISO_Ant 0_5785



11N20SISO_Ant 0_5825



11N40SISO_Ant 0_5190



11N40SISO_Ant 0_5230



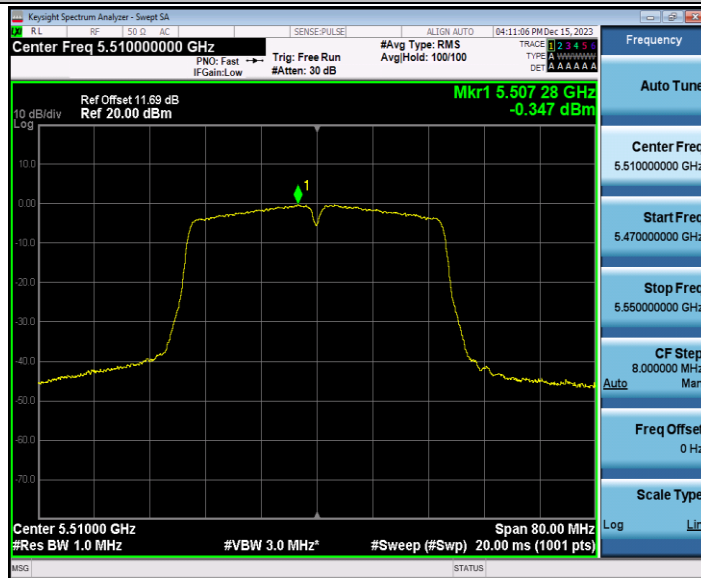
11N40SISO_Ant 0_5270



11N40SISO_Ant 0_5310



11N40SISO_Ant 0_5510



11N40SISO_Ant 0_5550



11N40SISO_Ant 0_5670



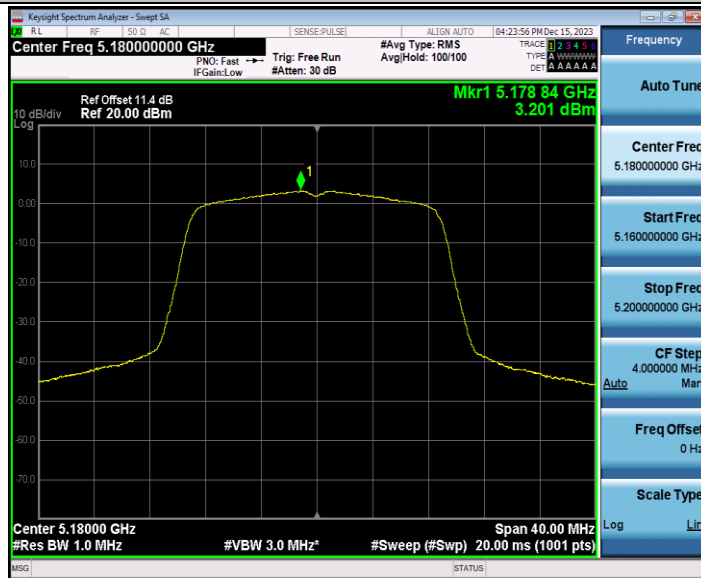
11N40SISO_Ant 0_5755



11N40SISO_Ant 0_5795



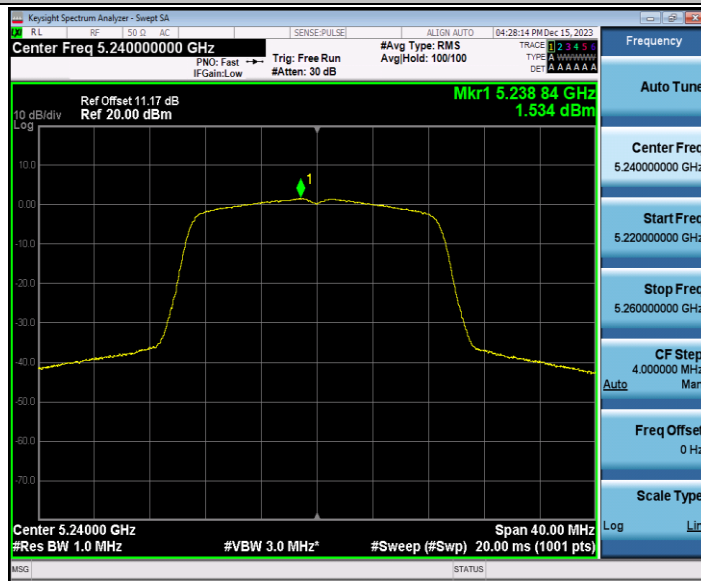
11AC20SISO_Ant 0_5180



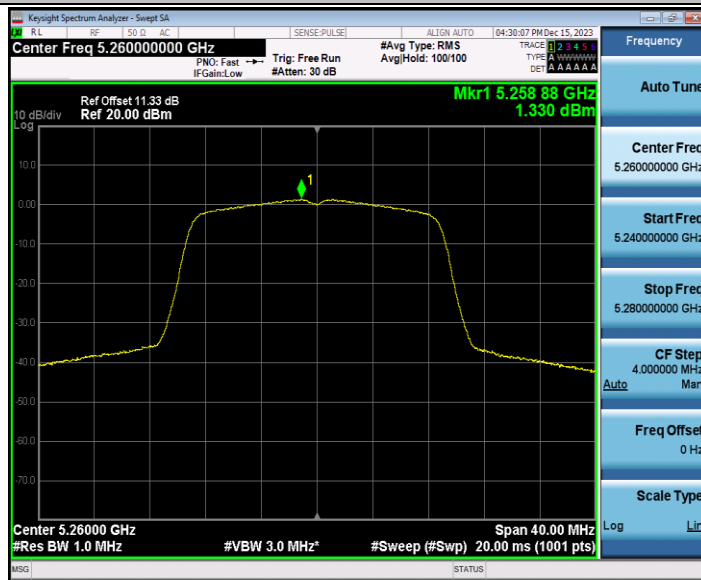
11AC20SISO_Ant 0_5220



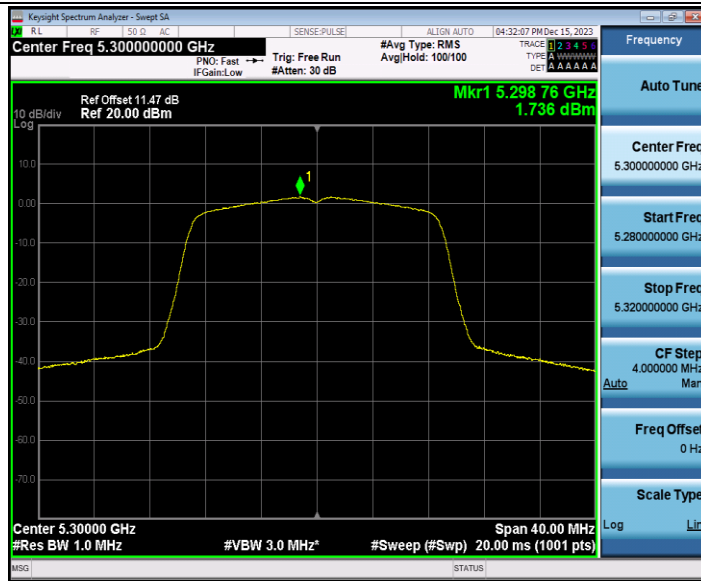
11AC20SISO_Ant 0_5240



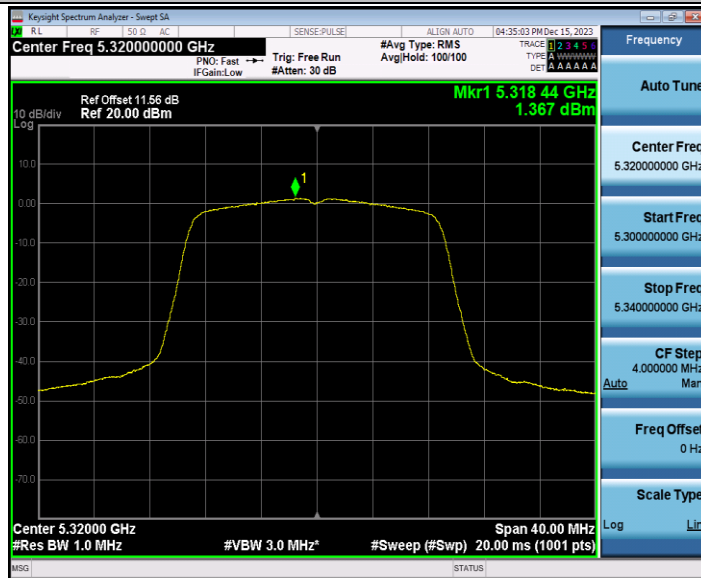
11AC20SISO_Ant 0_5260



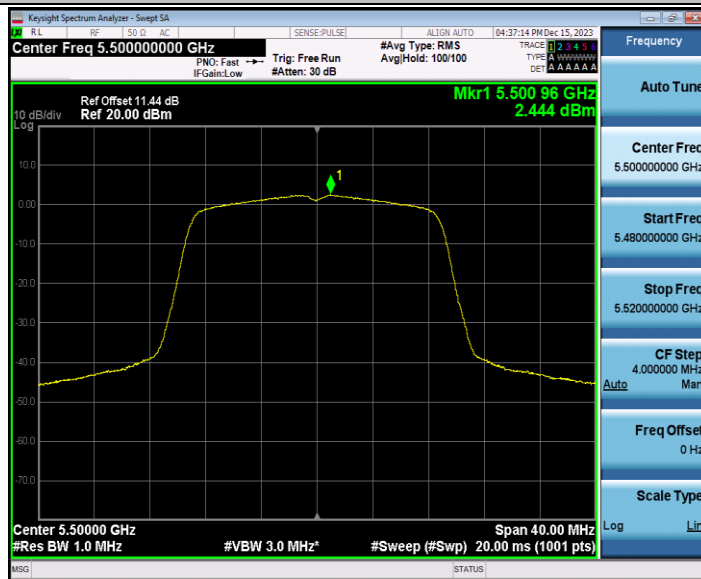
11AC20SISO_Ant 0_5300



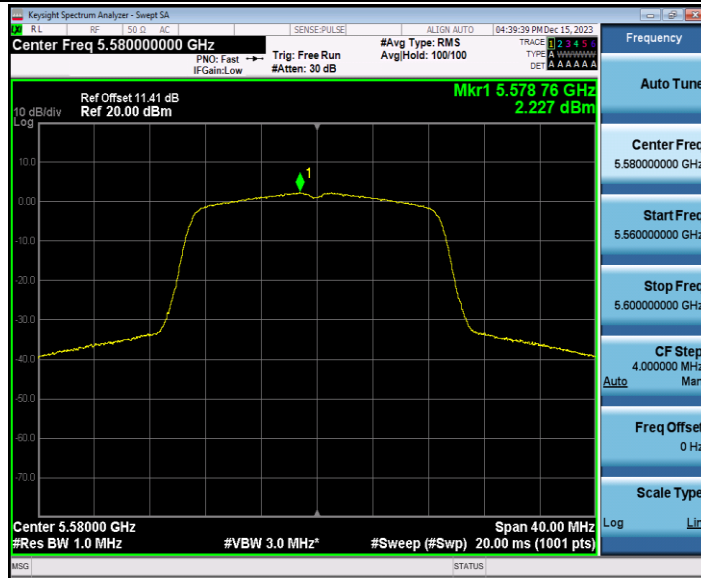
11AC20SISO_Ant 0_5320



11AC20SISO_Ant 0_5500



11AC20SISO_Ant 0_5580



11AC20SISO_Ant 0_5700



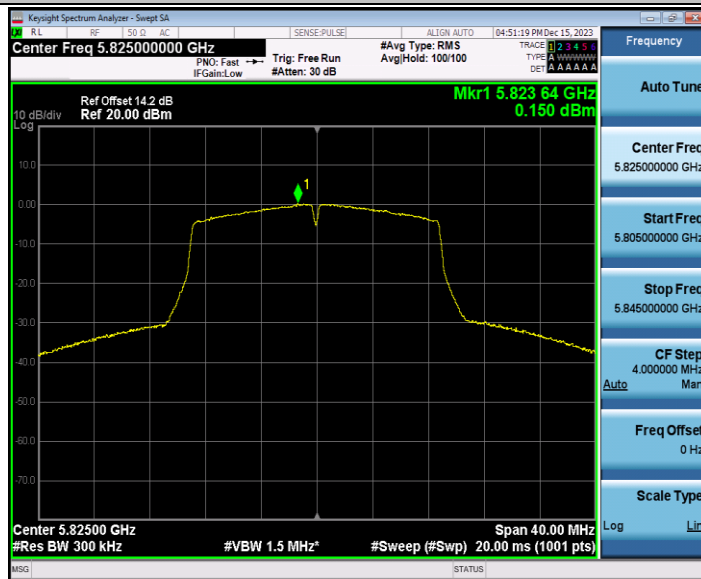
11AC20SISO_Ant 0_5745



11AC20SISO_Ant 0_5785



11AC20SISO_Ant 0_5825



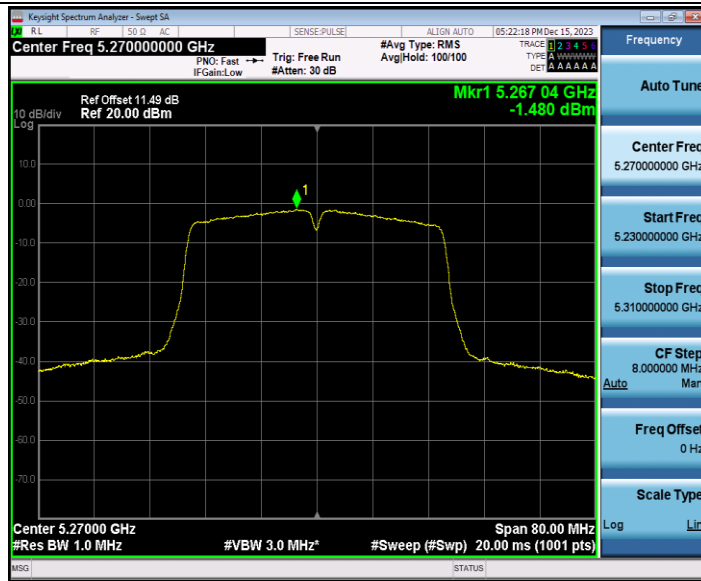
11AC40SISO_Ant 0_5190



11AC40SISO_Ant 0_5230



11AC40SISO_Ant 0_5270



11AC40SISO_Ant 0_5310



11AC40SISO_Ant 0_5510



11AC40SISO_Ant 0_5550



11AC40SISO_Ant 0_5670



11AC40SISO_Ant 0_5755



11AC40SISO_Ant 0_5795



11AC80SISO_Ant 0_5210



11AC80SISO_Ant 0_5290



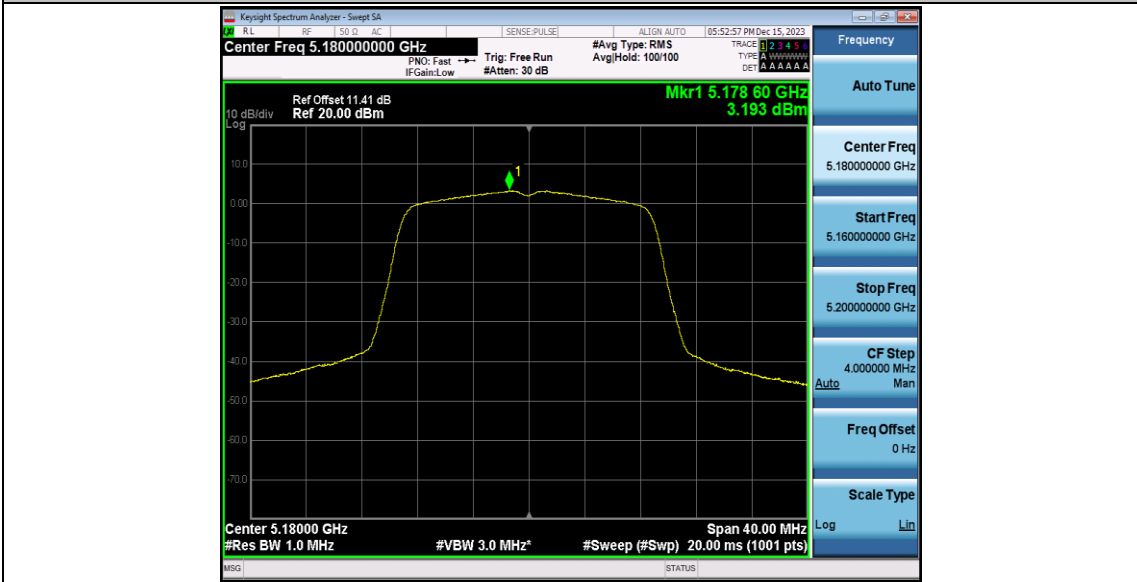
11AC80SISO_Ant 0_5530



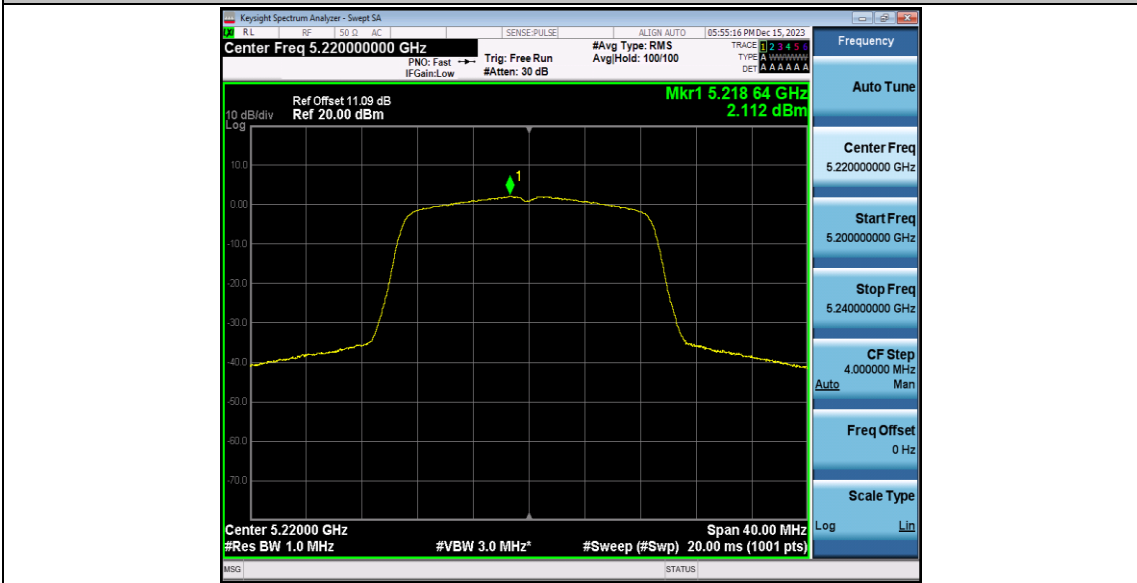
11AC80SISO_Ant 0_5775



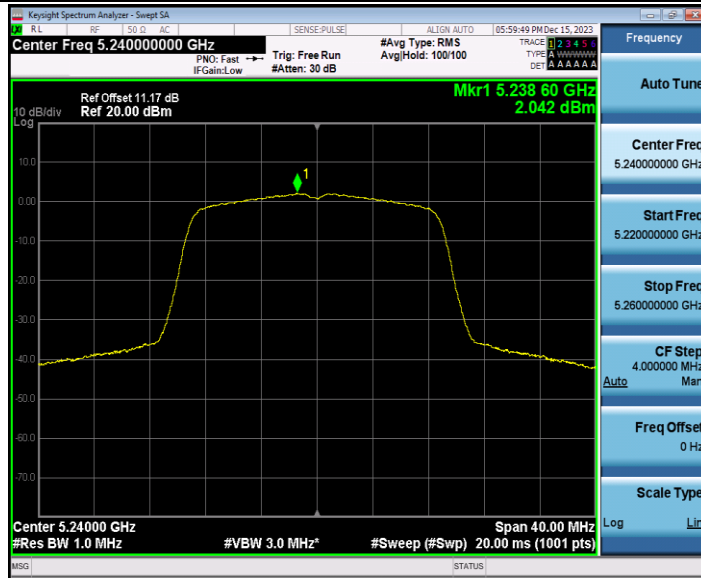
11AX20SISO_Ant 0_5180



11AX20SISO_Ant 0_5220



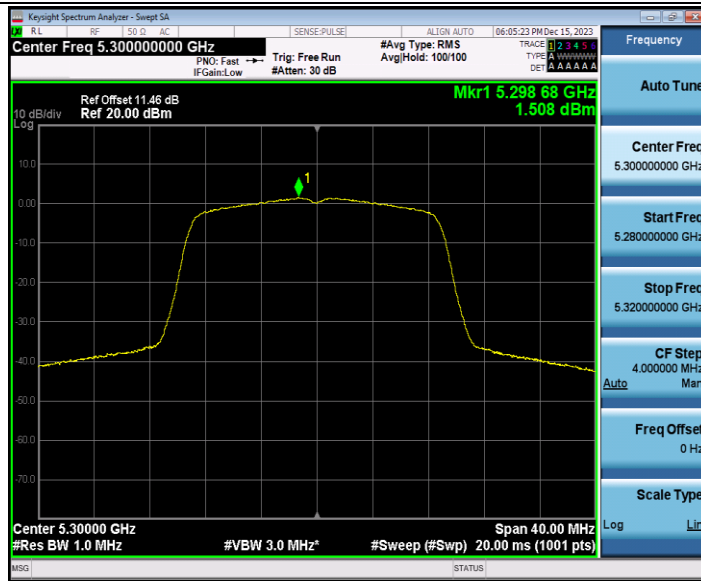
11AX20SISO_Ant 0_5240



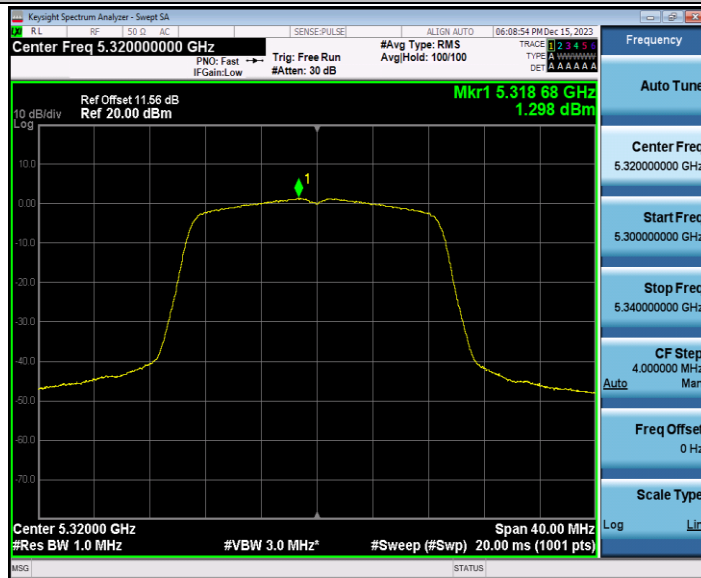
11AX20SISO_Ant 0_5260



11AX20SISO_Ant 0_5300



11AX20SISO_Ant 0_5320



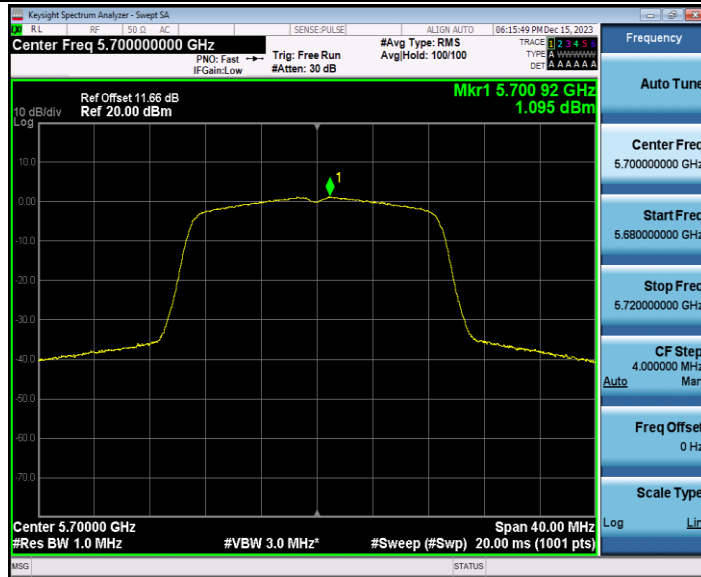
11AX20SISO_Ant 0_5500



11AX20SISO_Ant 0_5580



11AX20SISO_Ant 0_5700



11AX20SISO_Ant 0_5745