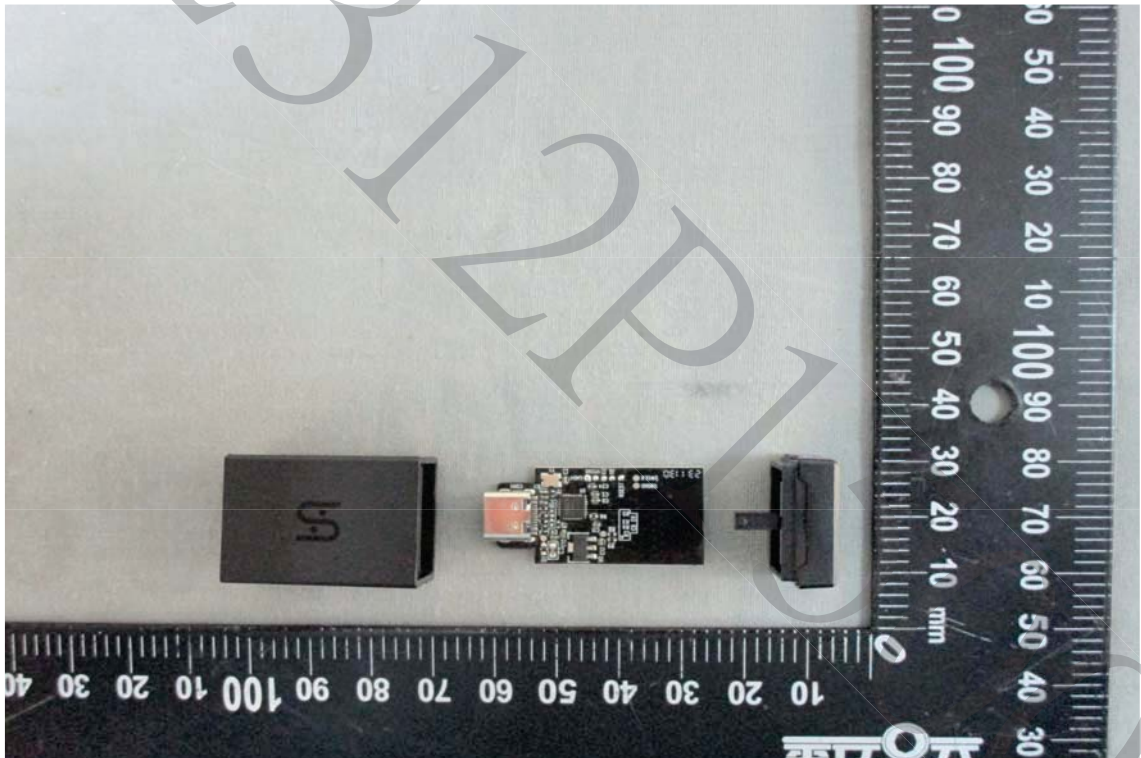


Appendix II - Photograph of EUT 2

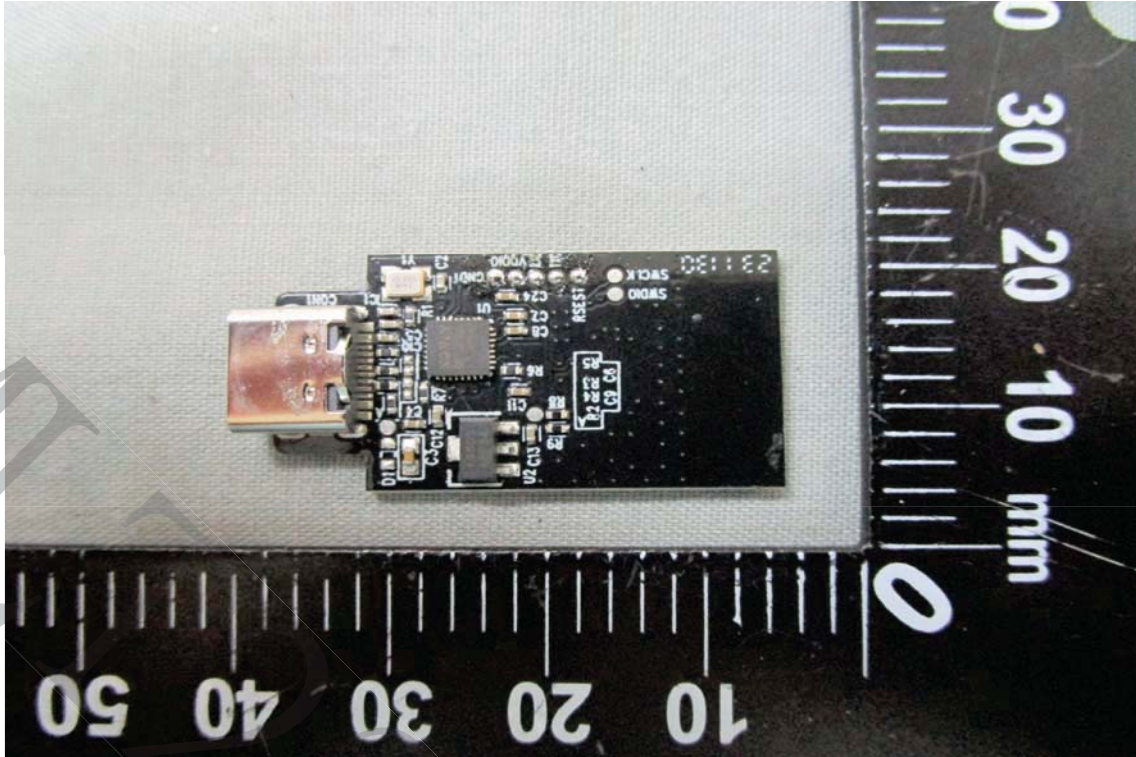
< I/O Port of EUT >



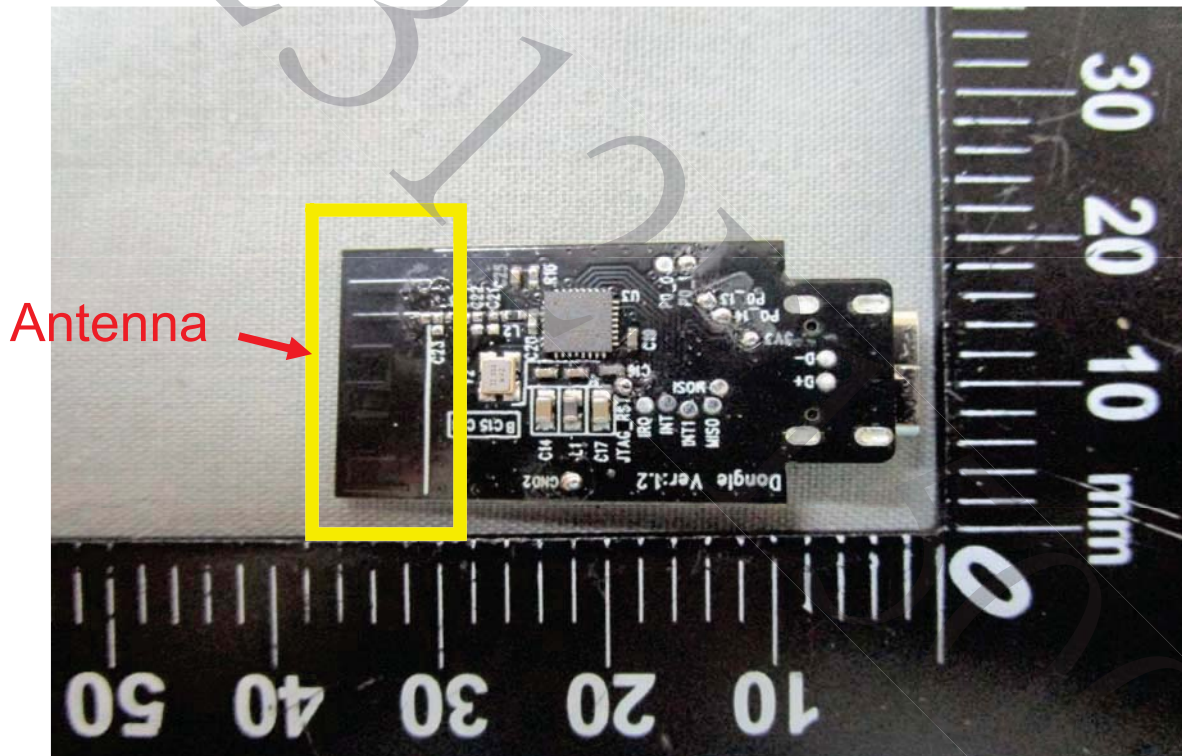
< Inside View of EUT >



< PCB-F >



< PCB-R >



< Module Close in >

