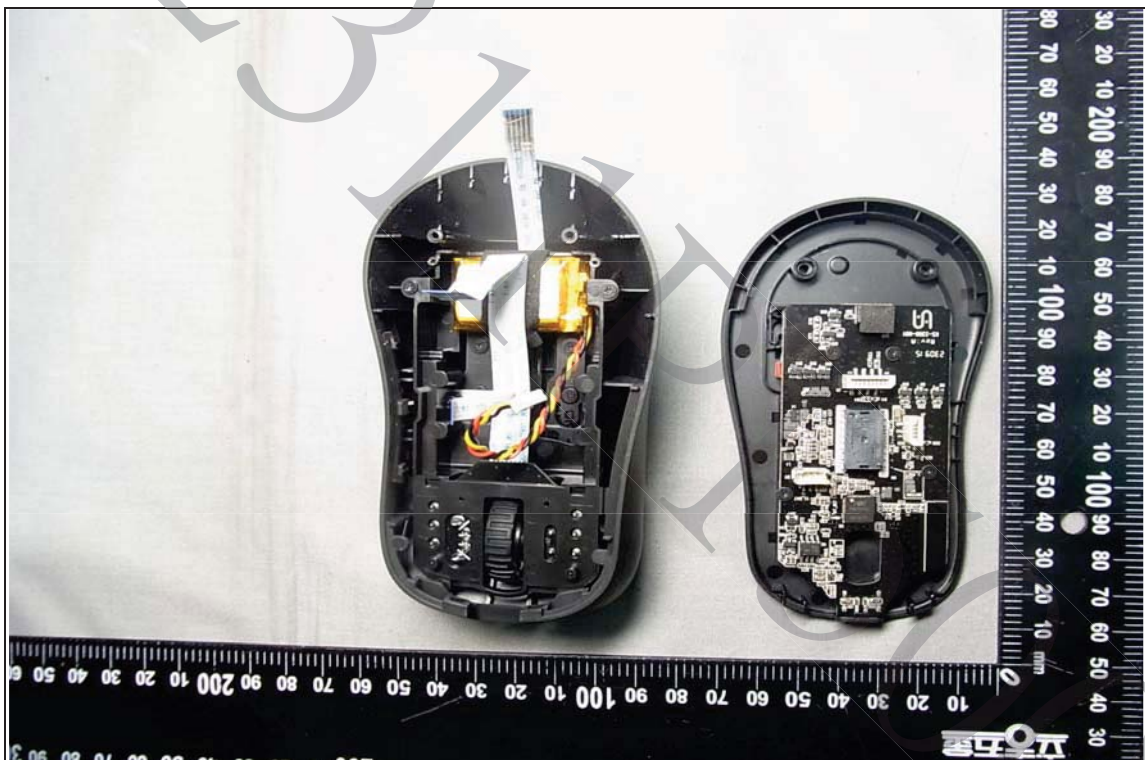


Appendix II - Photograph of EUT 2

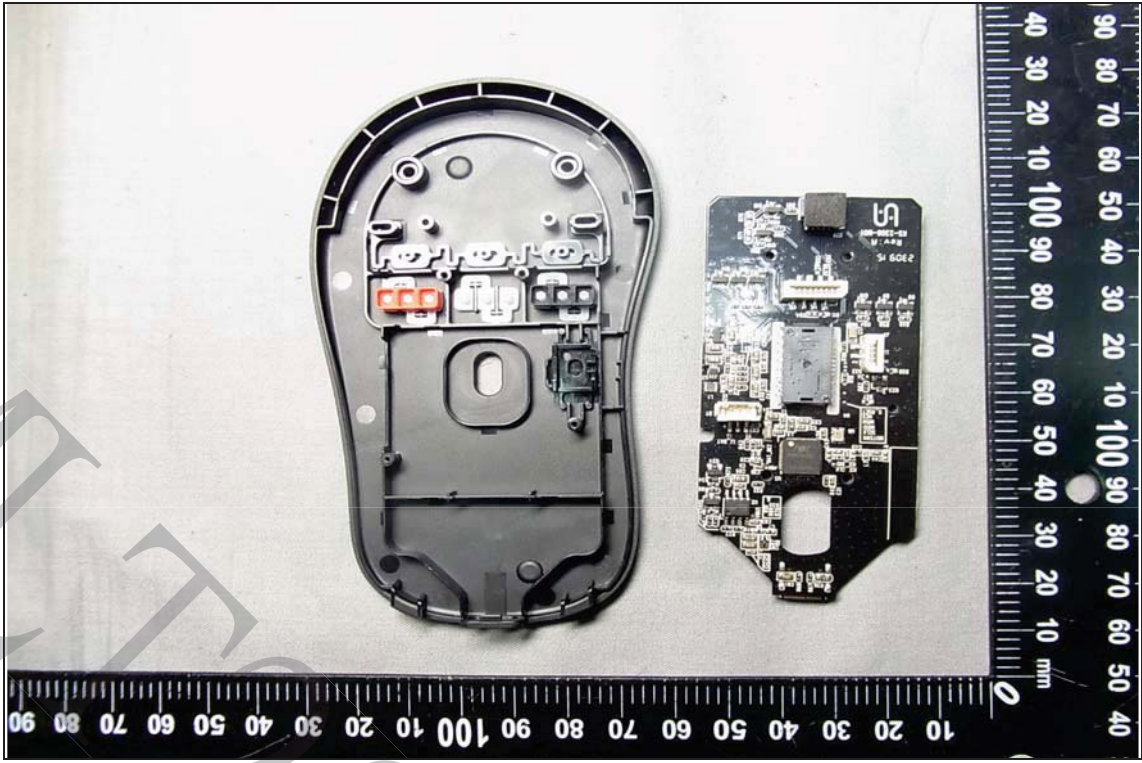
< I/O Port of EUT >



< Inside View of EUT >



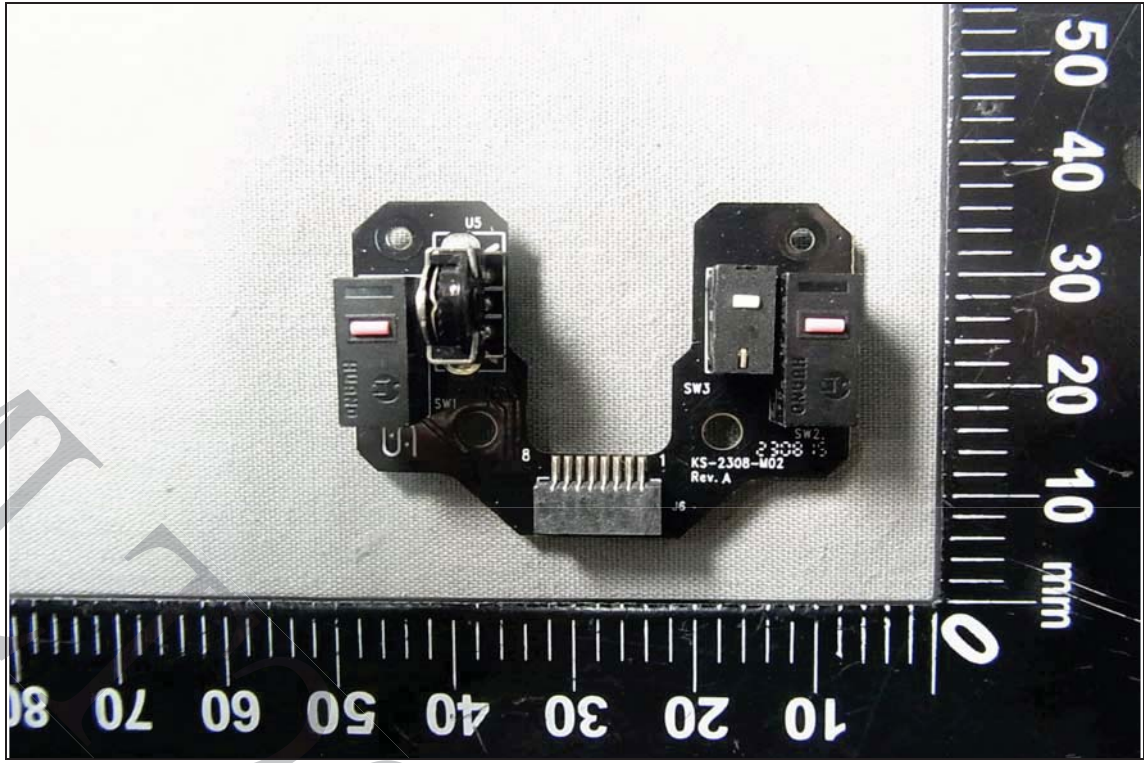
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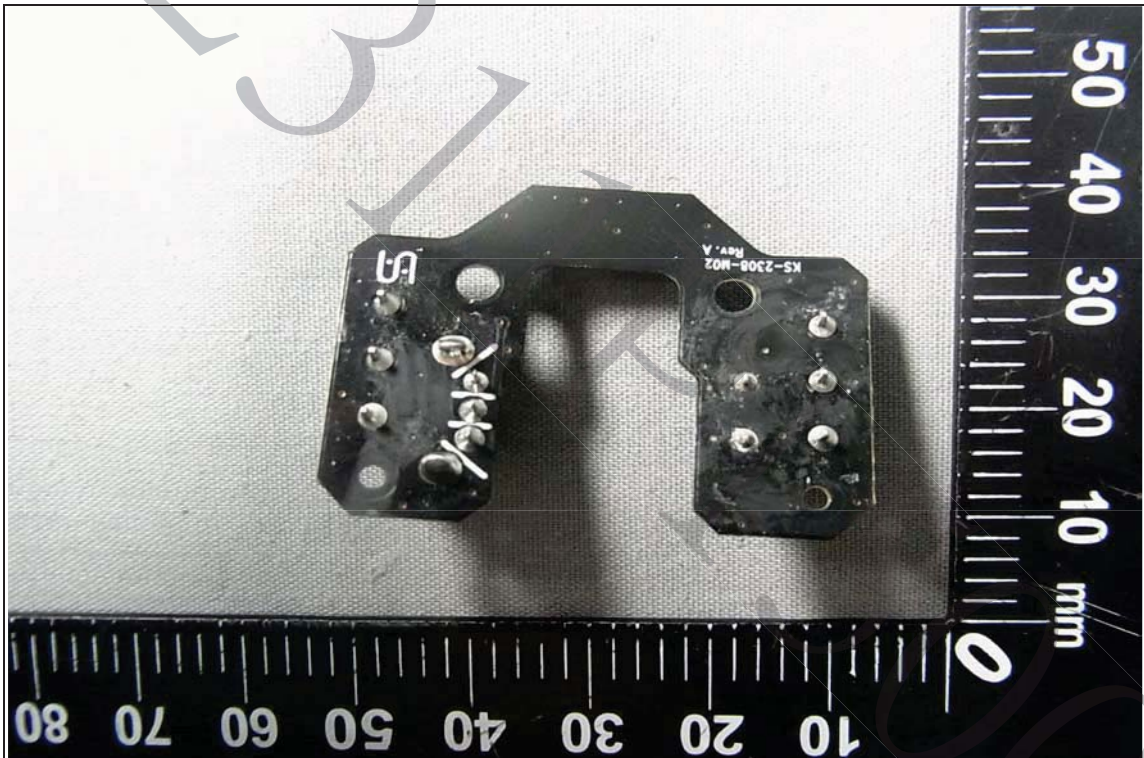
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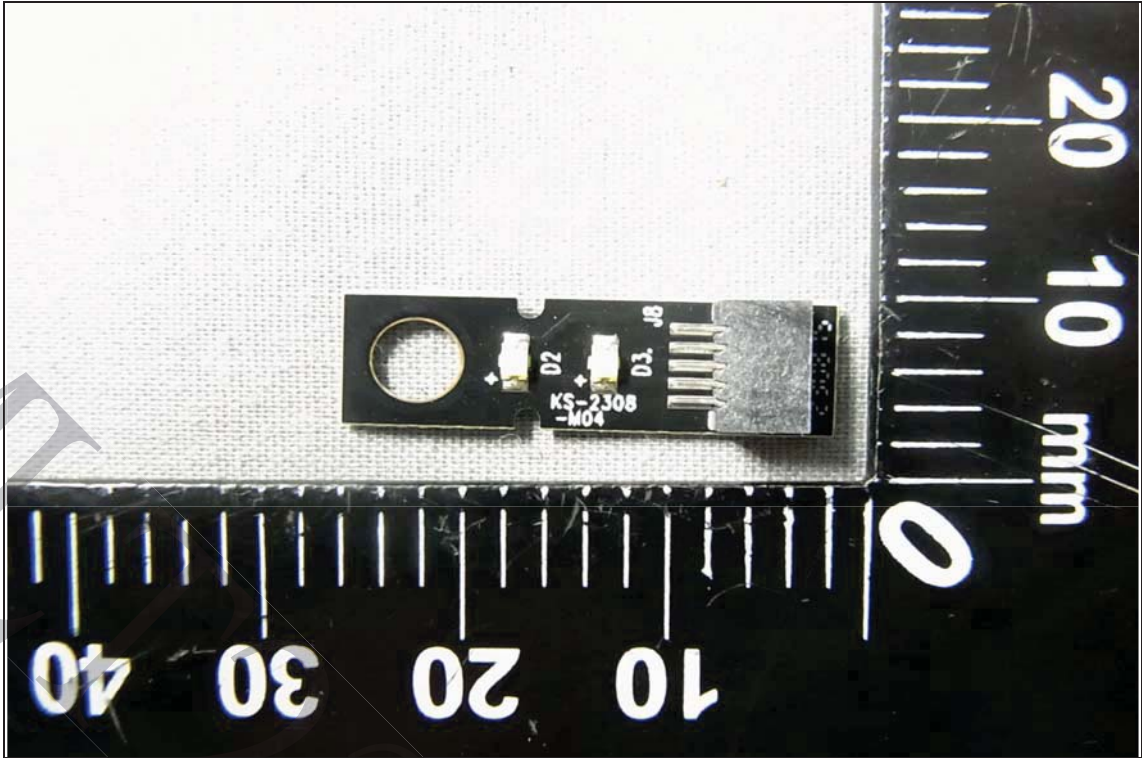
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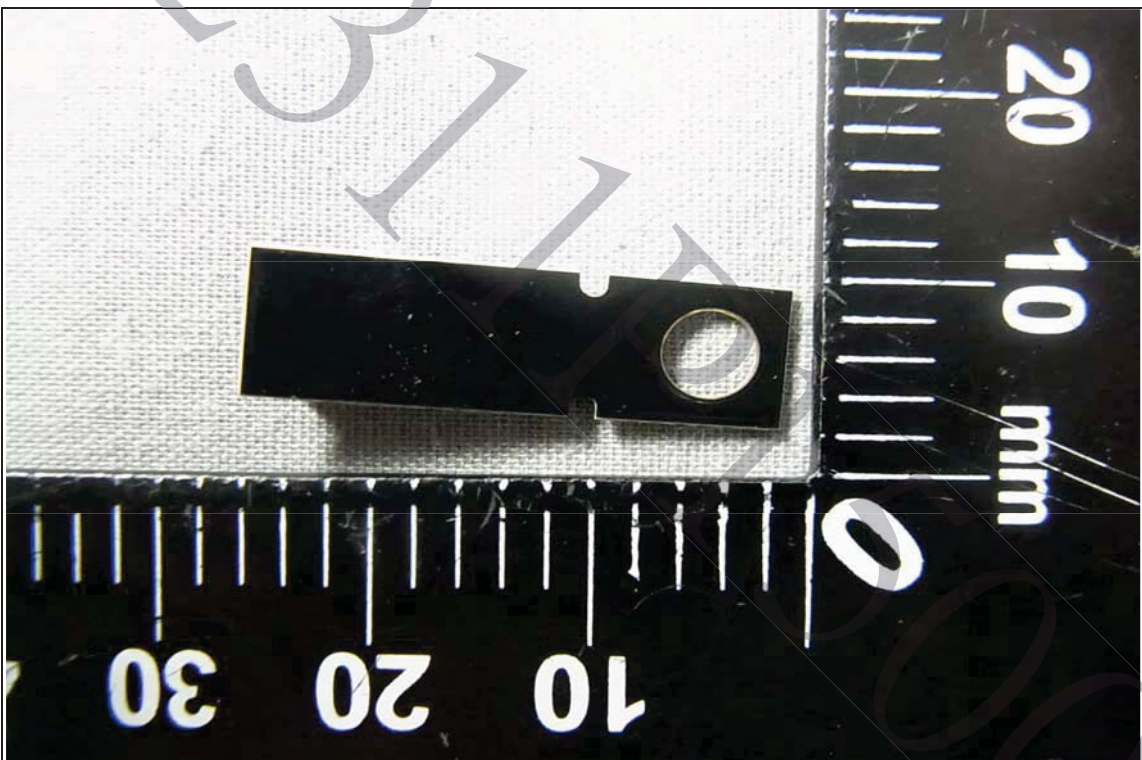
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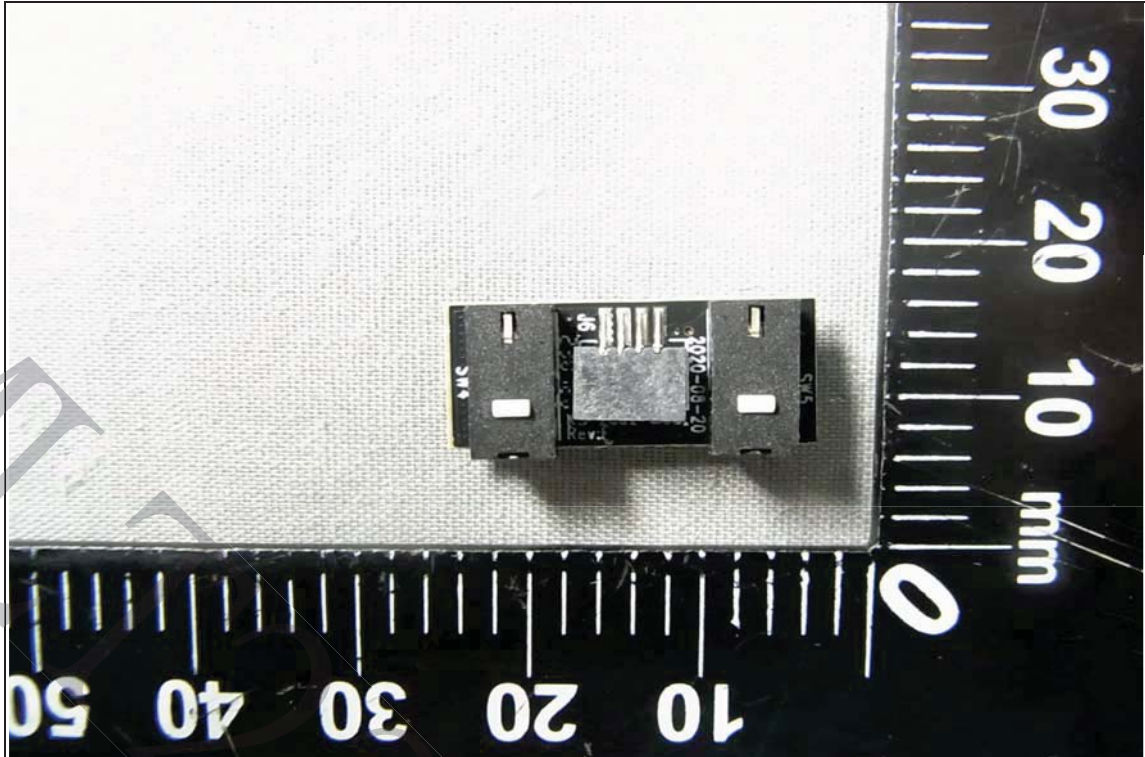
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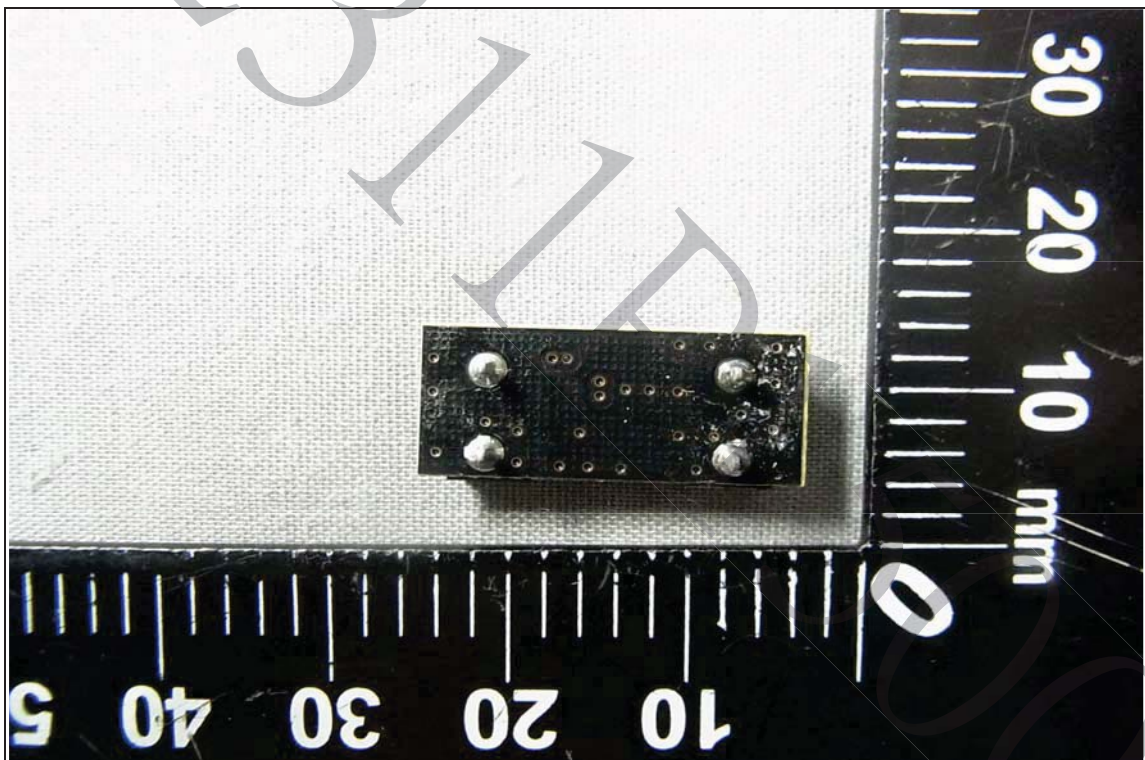
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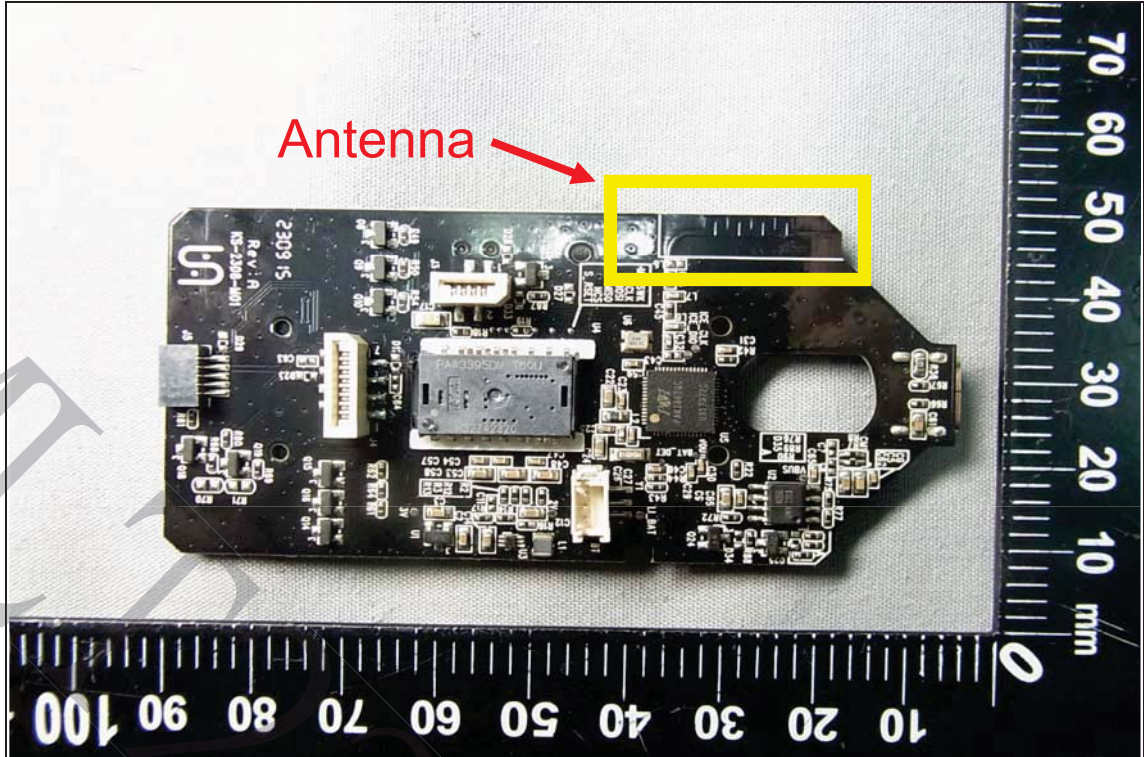
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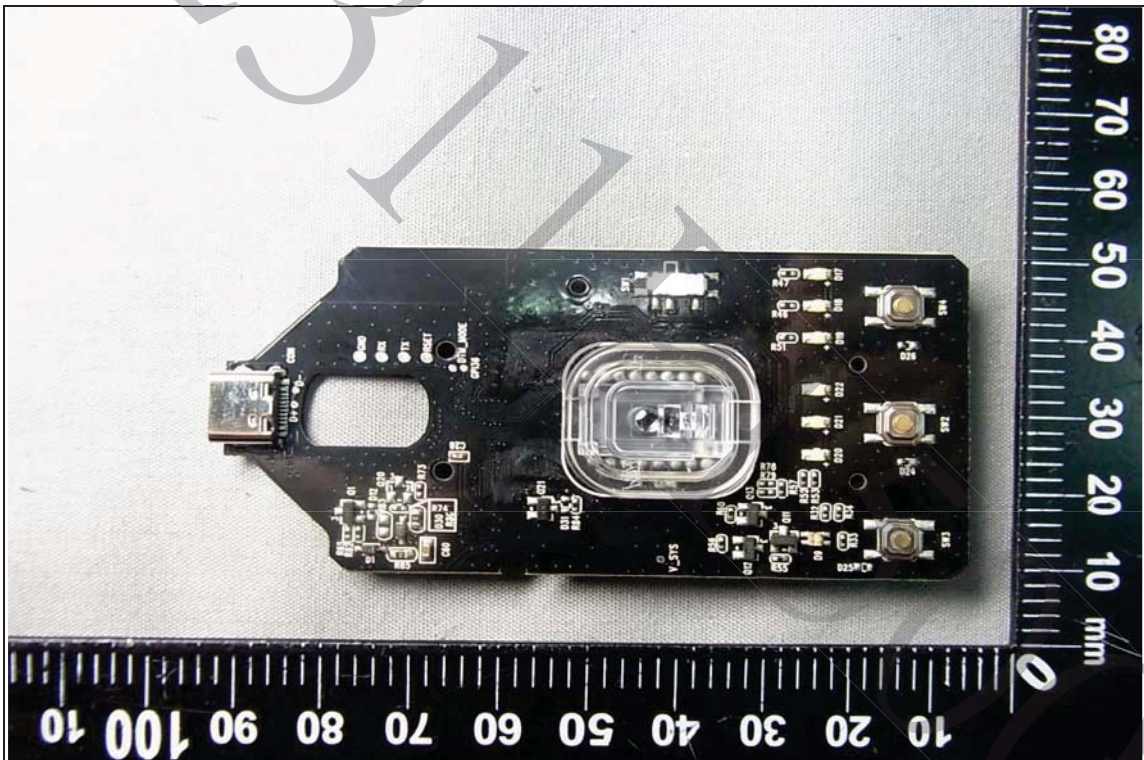
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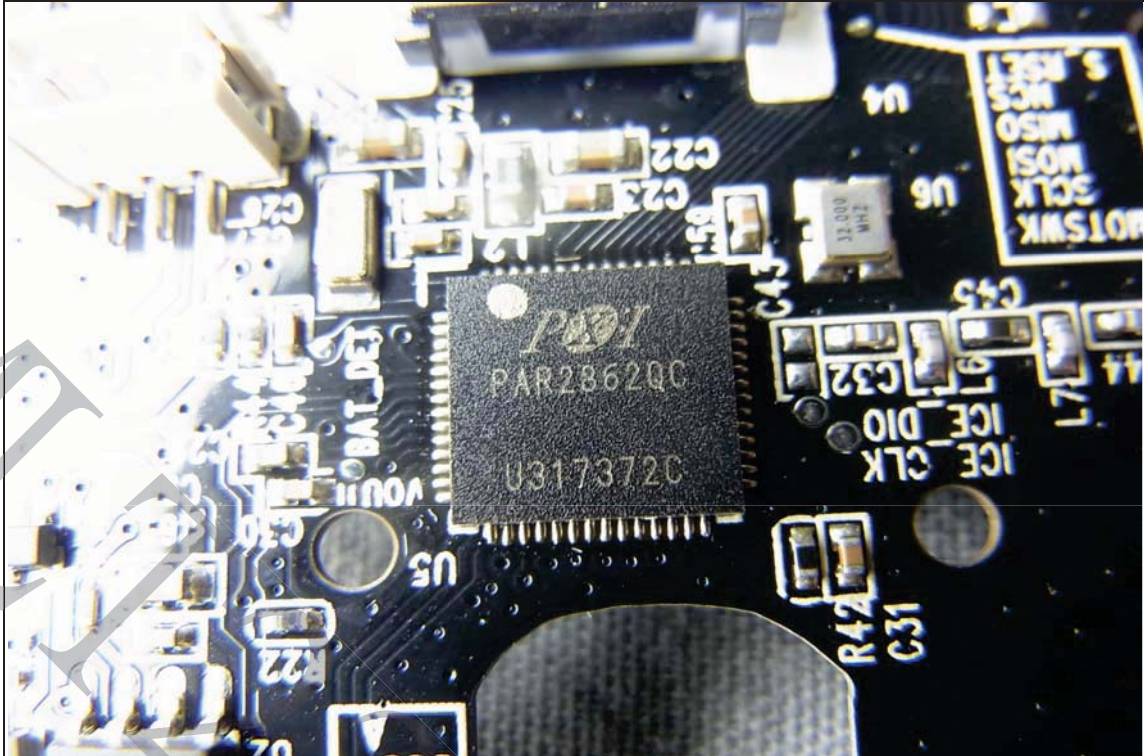
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< PCB4-R >



< Module Close in >



< Battery >

