

承 認 書 SPECIFICATION FOR APPROVAL

客戶名稱 CUSTOMER	:	
客戶料號 CUSTOMER'S P/N	:	
料號 PART NUMBER	:	KBAN3216D245H06
規格 DESCRIPTION	:	Chip Antenna 3216 M-Ant 2.45G Type H06
版本 VERSION	:	V1.0
日期 ISSUE DATE	:	2020/08/21

客戶承認
CUSTOMER APPROVED

	工 程 部 R&D CENTER	
承 認 APPROVAL	確認 CHECKED	製 作 DRAWN
Ziv	Alex	Jerry



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OneWave Electronic Co., Ltd.

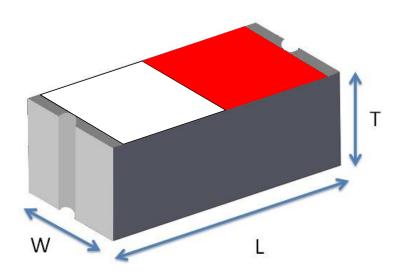
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3216 Chip antenna

For Bluetooth / WLAN Applications



P/N: KBAN3216D245H06

	Dimension (mm)
L	3.23 ± 0.20
W	1.66 ± 0.20
Т	1.13 ± 0.20



Part Number Information

KBAN 3216 D 245 H 06
A B C D E F

A	Product Series	Antenna
В	Dimension L x W	3.2X1.6mm (+-0.2mm)
C	Material	High K material
D	Working Frequency	2.4 ~ 2.5GHz
E	Feeding mode	Monopole & Single Feeding
F	Antenna type	Type = 06

1. Electrical Specification

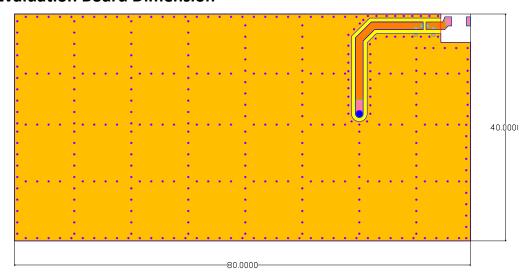
Specification						
Part Number	KBAN3216D245H06					
Central Frequency	2450	MHz				
Bandwidth	100 (Min.)	MHz				
Return Loss	-6.5 (Max)	dB				
Peak Gain	2.39	dBi				
Impedance	50	Ohm				
Operating Temperature	-40~+110	$^{\circ}$ C				
Maximum Power	4	W				
Resistance to Soldering Heats	10 (@ 260°C)	sec.				
Polarization	Linear					
Azimuth Beamwidth	Omni-directional					
Termination	Cu / Sn (Leadless)					

Remark : Bandwidth & Peak Gain was measured under evaluation board of next page



2. Recommended PCB Pattern

Evaluation Board Dimension



Unit: mm



Important information:

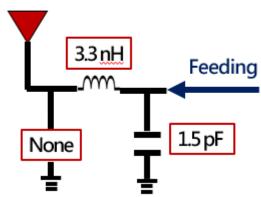
It is recommended to use high-precision inductors $\pm 0.1 \sim 0.3$ nH and capacitors ± 0.1 pF as matching components.

Suggested Matching Circuit

重要資訊:

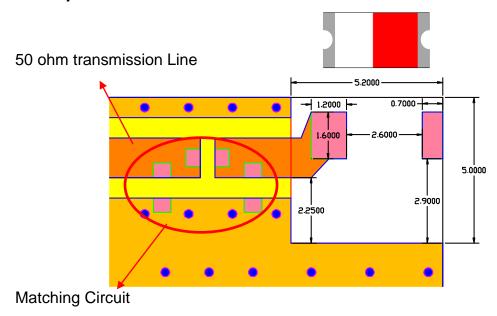
匹配元件建議使用精準度高的電感±0.1~0.3nH、電容±0.1pF

Matching Circuit

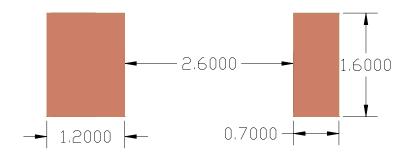




Layout Dimensions in Clearance area(Size=5.2*5.0mm)



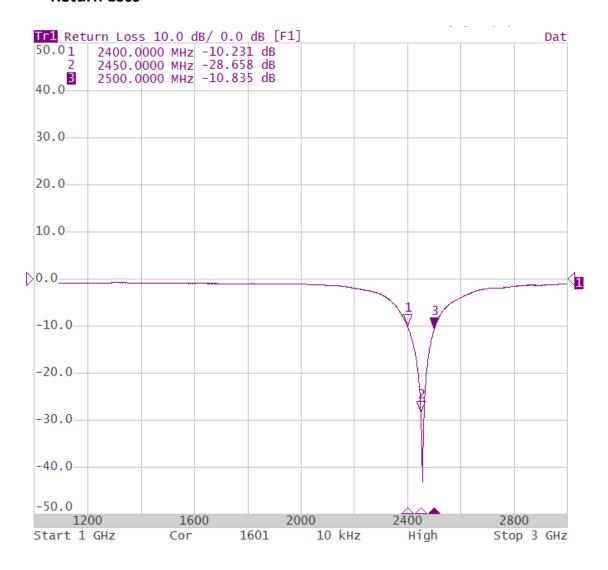
FootPrint (Unit:mm)





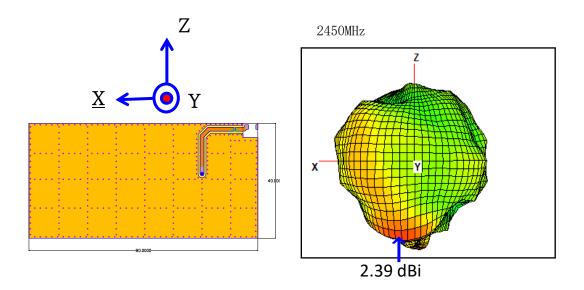
3. Measurement Results

Return Loss



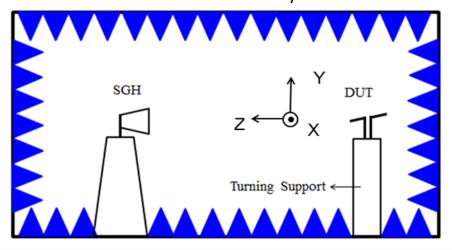


Radiation Pattern



	Efficiency	Peak Gain	Directivity	
2400MHz	45.61 %	1.13 dBi	4.54 dBi	
2450MHz	55.65 %	2.39 dBi	4.93 dBi	
2500MHz	47.63 %	1.65 dBi	4.87 dBi	

Chamber Coordinate System





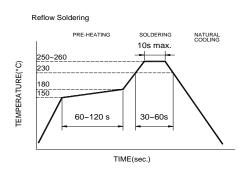
4. Reliability and Test Condictions

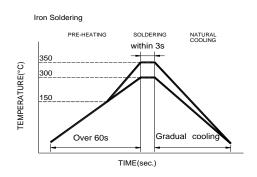
ITEM	REQUIR			TEST CONDITION
Solderability	2. No visib	shall exceed 90% ble mechanical dam EMP (°C)		Pre-heating temperature:150°C/60sec. Solder temperature:230 \pm 5°C Duration:4 \pm 1sec.
	2	230°C	Solder:Sn-Ag3.0-Cu0.5 Flux for lead free: rosin	
		60s	ec	
Solder heat Resistance	2. Central	le mechanical dam Freq. change :with		Pre-heating temperature:150°C/60sec. Solder temperature:260±5°C Duration:10±0.5sec.
	2	EMP (°C) 260°C	10±0.5 sec.	Solder:Sn-Ag3.0-Cu0.5 Flux for lead free: rosin
		608	sec	
Component Adhesion (Push test)	1. No visib	ole mechanical dam	age	The device should be reflow soldered(230±5°C for 10sec.) to a tinned copper substrate A dynometer force gauge should be applied the side of the component. The device must with-ST-F 0.5 Kg without failure of the termination attached to component.
Component Adhesion	1. No visib	ole mechanical dam	age	Insert 10cm wire into the remaining open eye bend ,the ends of even wire lengths
(Pull test)				upward and wind together. Terminal shall not be remarkably damaged.
Thermal shock		ole mechanical dam Freq. change :with	· ·	+110°C =>30±3min -40°C =>30±3min
	Phase	Temperature(°C)	Time(min)	Test cycle:10 cycles
	1	+110±5°C		The chip shall be stabilized at normal
	2	Room Temperature	30±3 Within 3sec	condition for 2~3 hours before measuring.
	3	-40±2°C	30±3	
	4	Room Temperature	Within 3sec	
Resistance to High		ole mechanical dam Freq. change :with	=	Temperature: +110±5°C Duration: 1000±12hrs
Temperature		onnection or short of		The chip shall be stabilized at normal condition for 2~3 hours before measuring.
Resistance to	1. No visih	ole mechanical dam	age	Temperature:-40±5°C
Low		Freq. change :with	_	Duration: 1000±12hrs
Temperature		onnection or short of		The chip shall be stabilized at normal condition for 2~3 hours before measuring.
Humidity	1 No visib	ole mechanical dam	200	Temperature: 40±2°C
		Freq. change :with	=	Humidity: 90% to 95% RH
		onnection or short		Duration: 1000±12hrs
	J. NO GISC	omiconorror Sholl (on out.	The chip shall be stabilized at normal condition for 2~3 hours before measuring.



5. Soldering and Mounting

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. The terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.





Recommended temperature profiles for re-flow soldering in Figure 1.

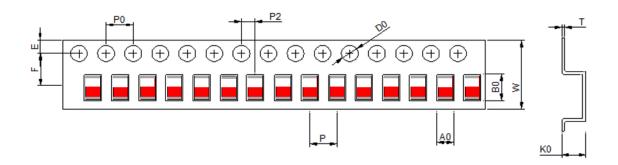
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- Preheat circuit and products to 150°C
- · Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 280°C tip temperature (max)
- 1.0mm tip diameter (max)
- · Limit soldering time to 3 sec.



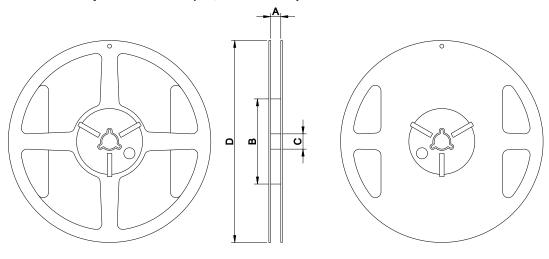
6.Packaging Information

♦ Tape Specification:



W	Ao	Во	Ко	Р	F	Е	D	D1	Ро	P2	t
8.0	1.80	3.51	1.59	4.00	3.50	1.75	1.50	0.00	4.00	2.00	0.25
±0.30	±0.05	±0.10	±0.10	±0.05	±0.05	±0.10	±0.10	±0.10	±0.10	±0.05	±0.05

Reel Specification: (7", Φ180 mm)



7" x 8 mm

Tape Width(mm)	A(mm)	B(mm)	C(mm)	D(mm)	Chip/Reel(pcs)
8	9.0±0.5	60±2	13.5±0.5	178±2	3000



7. Storage and Transportation Information

Storage Conditions

To maintain the solderability of terminal electrodes:

- 1. Temperature and humidity conditions: -10~ 40°C and 30~70% RH.
- 2. Recommended products should be used within 6 months from the time of delivery.
- 3. The packaging material should be kept where no chlorine or sulfur exists in the air.

Transportation Conditions

- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.