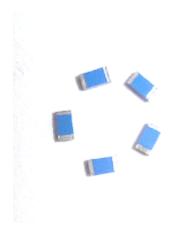
1.6X0.8X0.5 (mm) WiFi/Bluetooth Ceramic Chip Antenna (YF1608H1) Engineering Specification

1. Product Number

YF 1608 H1 P 2G45 1 2 3 4 5



2. Features

(1)Product Type	Chip Antenna
(2)Size Code	1.6x0.8x0.5mm
(3)Type Code	H1
(4)Packing	Paper &Reel
(5)Frequency	2.45GHz



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TITLE: 1.6x 0.8 x0.5(mm) WiFi/Bluetooth Ceramic Chip
Antenna (YF1608H1) Engineering Specification

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- *Stable and reliable in performances
- *Low temperature coefficient of frequency
- *Low profile, compact size
- *RoHS compliance
- *SMT processes compatible

Applications

- *Bluetooth earphone systems
- *Hand-held devices when WiFi /Bluetooth functions are needed, e.g., Smart phone.
- *IEEE802.11 b/g/n
- *ZigBee
- *Wireless PCMCIA cards or USB dongle

4. **Description**

Yingfeng chip antenna series are specially designed for WiFi/Bluetooth applications. Based on yingfeng proprietary design and processes, this chip antenna has excellent stability and sensitivity to consistently provide high signal reception efficiency.

5. Electrical Specifications (40 x 40 mm² ground plane)

5-1. Electrical Table

Characteristics		Specifications	Unit
Outline Dimensions		1.6x0.8x0.5	mm
Working Frequency		2400~2500	MHz
VSWR		2 Max.	
Impedance		50	Ω
Polarization		Linear Polarization	
Peak		1.5 (typical)	dBi
Gain	Efficiency	65 (typical)	%



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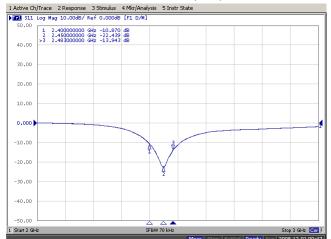
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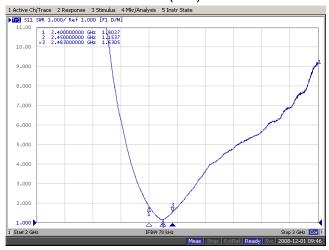
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5-2. Return Loss & VSWR

Return Loss (S₁₁)



VSWR(S₁₁)



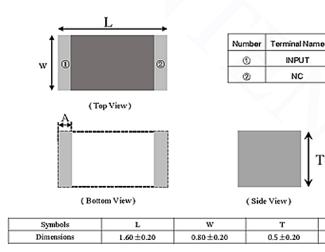
6. Antenna Dimensions (unit: mm)

a. Antenna Dimensions

Applications

- 1. Bluetooth
- 2. Wireless LAN
- 3. ISM band 2.4GHz wireless applications

Dimensions (Unit: mm)



Pur

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0.30 ± 0.10

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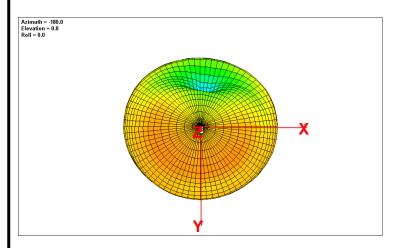
VF1608H1P2G45

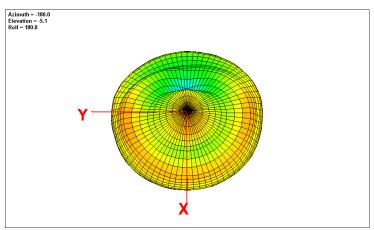
REV.

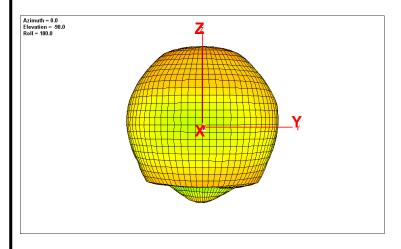
Antenna (YF1608H1) Engineering Specification NO. A
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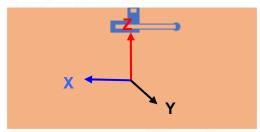
7. Radiation Pattern (80 x 40 mm² ground plane)

7-1. 3D Gain Pattern @ 2442 MHz









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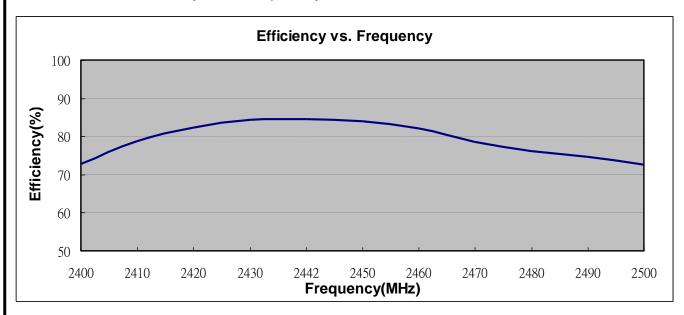
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7-2. 3D Efficiency Table

Frequency(MHz)	2400	2410	2420	2430	2442	2450	2460	2470	2480	2490	2500
Efficiency (dB)	-1.4	-1.0	-0.9	-0.7	-0.7	-0.8	-0.9	-1.1	-1.2	-1.3	-1.4
Efficiency (%)	62.8	63.7	64.3	64.4	65.5	65.0	64.0	63.6	63.1	62.6	61.5
Gain (dBi)	1.1	1.2	1.3	1.4	1.5	1.5	1.4	1.3	1.2	1.1	1.1

7-3. 3D Efficiency vs. Frequency



8. Layout Guide

a. Solder Land Pattern:Land pattern for soldering (gray marking areas) is as shown below. Depending on



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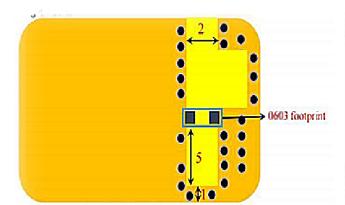
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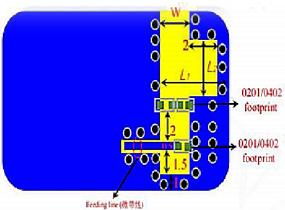
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Customer's requirement, matching circuit as shown below is also recommended.







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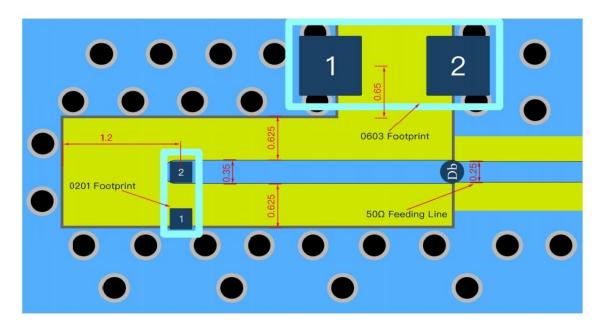
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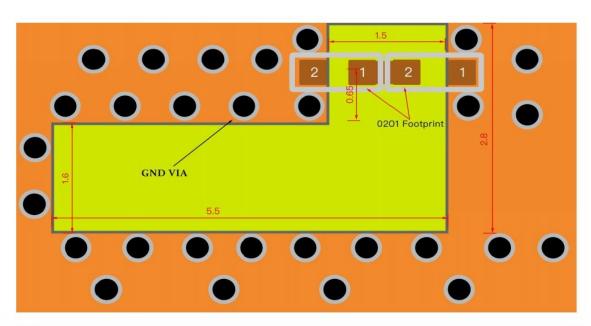
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Evaluation Board and Matching Circuits





Bottom View

Unit: mm

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c. Fine tuning element vs. Center frequency



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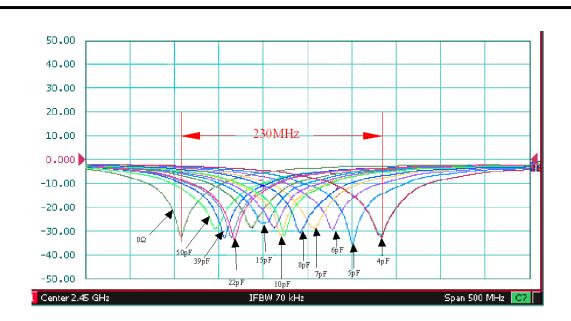
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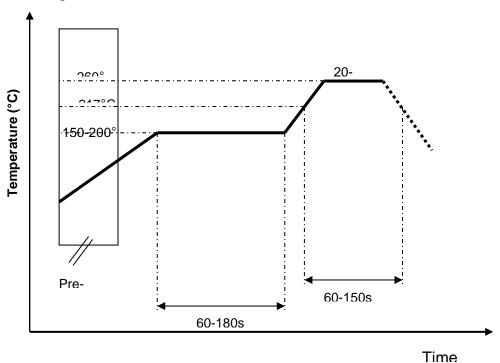
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9. Soldering Conditions

a. Typical Soldering Profile for Lead-free Process





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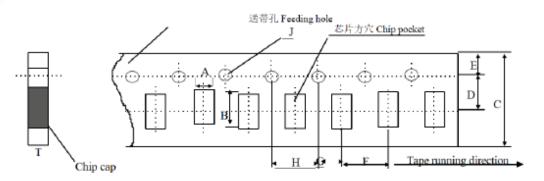
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Packing

- (1) Quantity/Reel: 5000 pcs/Reel
- (2) Plastic tape:

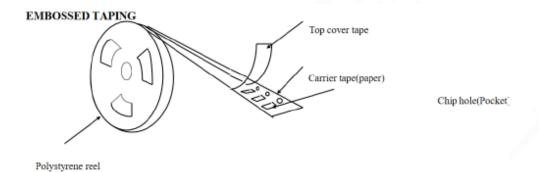
Dimensions of paper taping



Unit: mm

代号Code 纸带规格 papersize	A	В	C	D*	Е	F	G*	Н	1	Т
B.÷	1.10	1.90	8.00	3.50	1.75	4.00	2.00	4.00	1.50	1.10
尺寸	±0.10	±0.10	±0.10	±0.05	±0.10	±0.10	±0.10	±0.10	-0/+0.10	Max

Reel (4000 pcs/Reel)



Storage Period

The guaranteed period for solderability is 6 months (Under deliver package condition). Temperature: $5\sim40^{\circ}$ C/Relative Humidity: $20\sim70\%$



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Reliability Table

Test Item	Procedure	Requirements Ceramic Type	Remark (Reference)	
Electrical Characterization		Fulfill the electrical specification	User Spec.	
Thermal Shock	1. Preconditioning: 50 ± 10 ℃ / 1 hr , then keep for 24 ± 1 hrs at room temp. 2. Initial measure: Spec: refer Initial spec. 3. Rapid change of temperature test: -30 ℃ to +85 ℃; 100 cycles; 15 minutes at Lower category temperature; 15 minutes at Upper category temperature.	No Visible Damage. Fulfill the electrical specification.	MIL-STD-202 107	
Temperature Cycling	 Initial measure: Spec: refer Initial spec. 100 Cycles (-30°C to +85°C), Soak Mode=1 (2 Cycle/hours). Measurement at 24 ± 2Hours after test condition. 	No Visible Damage. Fulfill the electrical specification.	JESD22 JA104	
High Temperature Exposure	1. Initial measure: Spec: refer Initial spec. 2. Unpowered; 500hours @ T=+85℃. 3. Measurement at 24 ± 2 hours after test.	No Visible Damage. Fulfill the electrical specification.	MIL-STD-202 108	
Low Temperature Storage	1. Initial measure: Spec: refer Initial spec. 2. Unpowered: 500hours @ T= -30℃. 3. Measurement at 24 ± 2 hours after test.	No Visible Damage. Fulfill the electrical specification.	MIL-STD-202 108	
Solderability (SMD Bottom Side)	Dipping method: a. Temperature: 235 ± 5°C b. Dipping time: 3 ± 0.5s	The solder should cover over 95% of the critical area of bottom side.	IEC 60384-21/2 4.10	
Soldering Heat Resistance (RSH)	Preheating temperature: 150 ± 10°C. Preheating time: 1-2 min. Solder temperature: 260 ± 5°C. Dipping time: 5 ± 0.5s	No Visible Damage.	IEC 60384-21/2 4.10	



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Board	1. Mounting method:	No Visible Damage.	AEC-Q200
Flex	IR-Reflow. PCB Size (L:100 × W:40 × T:1.6mm)	_	005
(SMD)	2. Apply the load in direction of the arrow until bending reaches		
	2 mm. Support Solder Chip Printed circuit board before testing.		
	45=2		
	Printed circuit board under test		
Adhesion	Force of 1.8Kg for 60 seconds.	No Visible Damage	AEC-Q200
		Magnification of 20X or	006
	radius 0,5 mm	greater may be employed	
	DUT TUO	for inspection of the	
	Wide wide	mechanical integrity of the device body terminals and body/terminal junction.	
	substrate press tool thickness		
	shear force		
Physical	Any applicable method using x10 magnification, micrometers,	In accordance with	JESD22
Dimension	calipers, gauges, contour projectors, or other measuring equipment, capable of determining the actual specimen dimensions	specification.	JB100



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