

客戶名稱 CUSTOMER	:	
客户料號 CUSTOMER'S P/N	:	
料號 PART NUMBER	:	WAN3216F245C0X
規格 DESCRIPTION	:	Chip Antenna 3216 L Ant 2.45G Type 02,04,06
版本 VERSION	:	V3.3
日期 ISSUE DATE	:	2016/09/14



	エ 程 部 R&D CENTER	
承 認 APPROVAL	確認 CHECKED	製 作 DRAWN
Ray	James	Thor



萬誠科技股份有限公司

112 台北市北投區中央南路二段 36 號 3 樓

電話: (02) 2898-2220 傳真: (02) 2898-5055

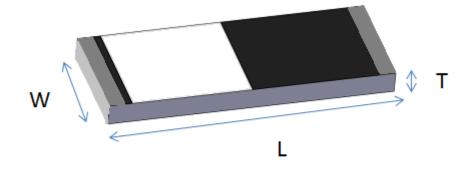
OneWave Electronic Co., Ltd.

2F., No.163, Sec. 1, Xi'an St., Beitou Dist., Taipei City 112, Taiwan TEL: +886 2 2898-2220 FAX: +886 2 2898-5055



3216 Chip antenna

For Bluetooth / WLAN Applications



P/N: WAN3216F245C02 WAN3216F245C04 WAN3216F245C06

	Dimension (mm)		
L	3.23 ± 0.20		
W	1.66 ± 0.20		
Т	0.45 ± 0.20		

Part Number Information

WA	<u>AN</u> <u>3216</u>	<u></u>	<u>245</u>	<u>C</u>	<u>0X</u>
A	В	С	D	Е	F
			1		
A Product Series		Antenna			
В	Dimension L x W		3.2X1.6mm (+-0.2mm)		
С	Material		Hig	h K mate	erial
D	Working Frequency		2.	4 ~ 2.5G	Hz
Ε	Feeding mode		PIFA 8	Single F	eeding
F	Antenna	type	0X=02,04,	06 / Туре	e=02,04,06

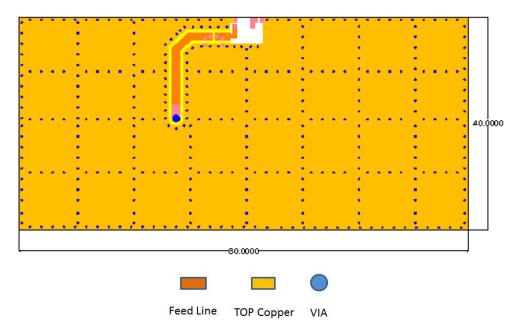
1. Electrical Specification

Specification				
	WAN3216F245C02			
Part Number	WAN3216F245C04			
	WAN3216F245C06			
Central Frequency	2450	MHz		
Bandwidth	120 (Min.)	MHz		
Return Loss	-6.5 (Max)	dB		
Peak Gain	1.75	dBi		
Impedance	50	Ohm		
Operating Temperature	-40~+85	°C		
Maximum Power	4	W		
Resistance to Soldering Heats	10 (@ 260°C)	sec.		
Polarization	Linear			
Azimuth Beamwidth	Omni-directional			
Termination	Ni / Au (Leadless)			

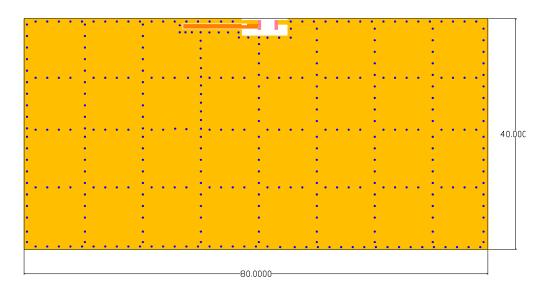
Remark : Bandwidth & Peak Gain was measured under evaluation board of next page

2. Recommended PCB Pattern

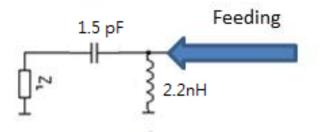
Evaluation Board Dimension



2nd Evaluation Board Dimension

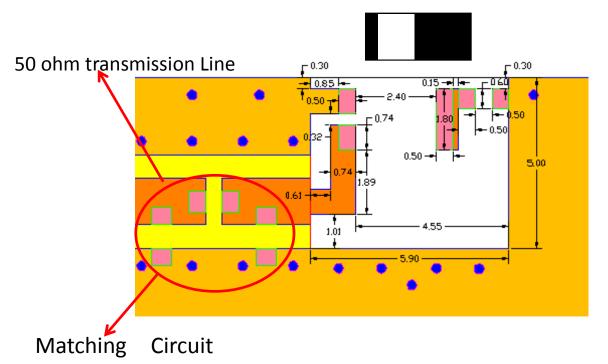


Suggested Matching Circuit



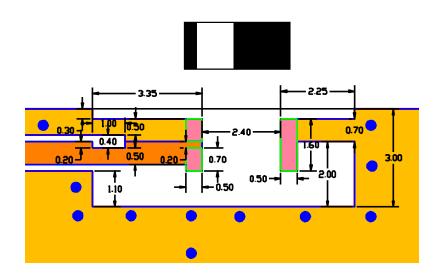


Layout Dimensions in Clearance area(Size=5.9*5.0mm)

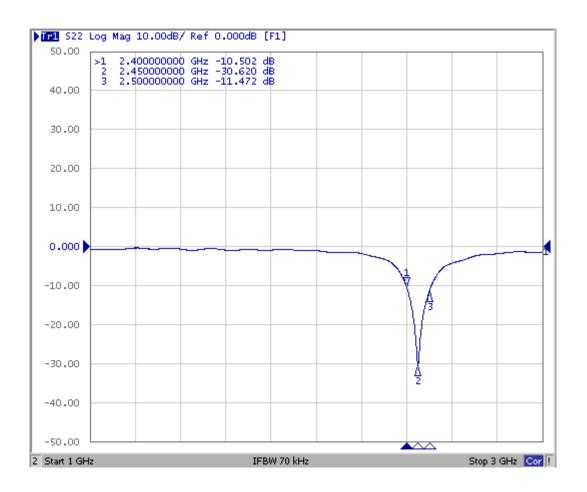


FootPrint (Unit : mm) .74 0.32 1.80 0.74 2.40 1.80 0.74 - 0.50 0.50 - ↓

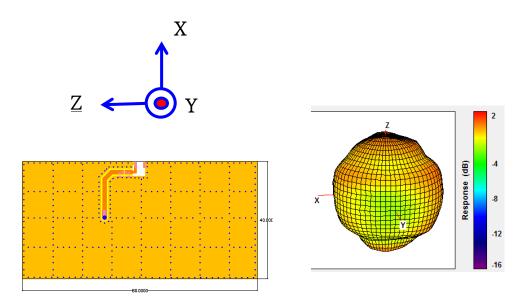
• 2nd Layout Dimensions in Clearance area(Size=8.0*3.0mm)



3. Measurement Results Return Loss

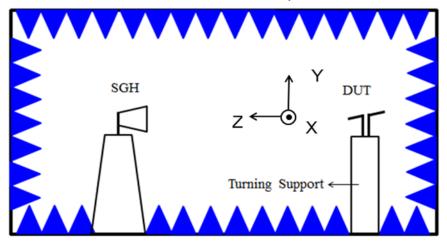


Radiation Pattern



	Efficiency	Peak Gain	Directivity
2450MHz	85.65%	1.75 dBi	2.89 dBi

Chamber Coordinate System





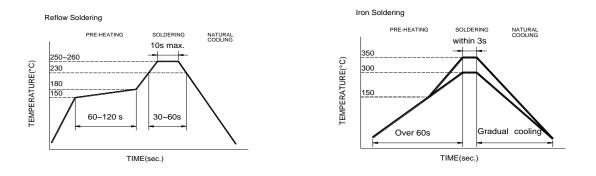
4.Reliability and Test Condictions

	REQUIREMENTS	TEST CONDITION		
Solderability	1. Wetting shall exceed 90% coverage 2. No visible mechanical damage	Pre-heating temperature:150°C /60sec. Solder temperature:230±5°C		
	TEMP (°C)	Duration:4±1sec.		
	230°C 4±1 sec.	Solder:Sn-Ag3.0-Cu0.5 Flux for lead free: rosin		
Solder heat Resistance	 No visible mechanical damage Central Freq. change :within ± 6% 	Pre-heating temperature:150°C /60sec. Solder temperature:260±5°C		
	TEMP (°C)	Duration:10±0.5sec.		
	10+0 5 000	Solder:Sn-Ag3.0-Cu0.5		
	260°C 150°C	Flux for lead free: rosin		
Component Adhesion (Push test)	1. No visible mechanical damage	The device should be reflow soldered(230±5°C for 10sec.) to a tinned copper substrate A dynometer force gauge should be applied the side of the component. The device must with-ST-F 0.5 Kg without failure of the termination		
	1 No visible mechanical demogra	attached to component.		
Component	1. No visible mechanical damage	Insert 10cm wire into the remaining open eye bend ,the ends of even wire lengths		
Adhesion (Pull test)		upward and wind together.		
(1 011 1001)		Terminal shall not be remarkably		
		damaged.		
Thermal shock	1. No visible mechanical damage	+85°C=>30±3min		
	2. Central Freq. change :within ±6%	-40°C =>30±3min		
	Phase Temperature(°C) Time(min)	Test cycle:10 cycles The chip shall be stabilized at normal		
	1 +85±5℃ 30±3	condition for 2~3 hours before		
	2 Room Within	measuring.		
	Temperature 3sec	3		
	3 -40±2℃ 30±3			
	4 Room Within Temperature 3sec			
Resistance to	1. No visible mechanical damage	Temperature: 85±5℃		
High	2. Central Freq. change :within ±6%	Duration: 1000±12hrs		
Temperature	3. No disconnection or short circuit.	The chip shall be stabilized at normal condition for 2~3 hours before measuring.		
Resistance to	1. No visible mechanical damage	Temperature:-40±5℃		
Low	2. Central Freq. change :within ±6%	Duration: 1000±12hrs		
Temperature	3. No disconnection or short circuit.	The chip shall be stabilized at normal condition for 2~3 hours before measuring.		
Humidity	1. No visible mechanical damage	Temperature: 40±2°C		
,	2. Central Freq. change :within ±6%	Humidity: 90% to 95% RH		
	3. No disconnection or short circuit.	Duration: 1000±12hrs		
		The chip shall be stabilized at normal		
		condition for 2~3 hours before		
		measuring.		

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5.Soldering and Mounting

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. The terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.



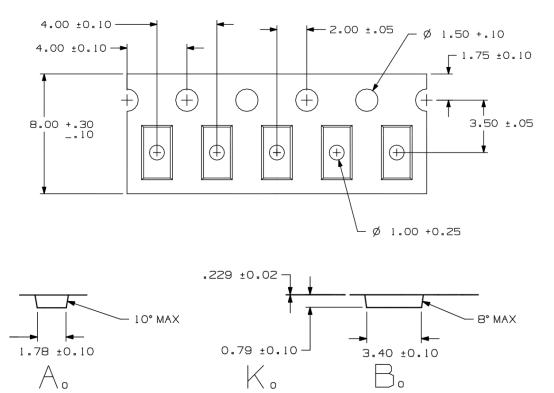
Recommended temperature profiles for re-flow soldering in Figure 1.

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

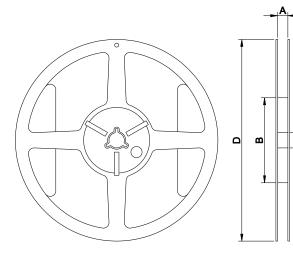
- Preheat circuit and products to 150° C
- · Never contact the ceramic with the iron tip
- · Use a 20 watt soldering iron with tip diameter of 1.0mm
- 280°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 3 sec.

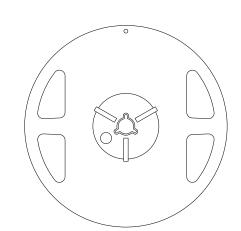
6.Packaging Information

Tape Specification:



Reel Specification: (7", Ф180 mm)





7" x 8 mm

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Tape Width(mm)	A(mm)	B(mm)	C(mm)	D(mm)	Chip/Reel(pcs)
8	9.0±0.5	60±2	13.5±0.5	178±2	3000

7. Storage and Transportation Information

Storage Conditions

To maintain the solderability of terminal electrodes:

- 1. Temperature and humidity conditions: -10~ 40° C and 30~70% RH.
- 2. Recommended products should be used within 6 months from the time of delivery.
- 3. The packaging material should be kept where no chlorine or sulfur exists in the air.

Transportation Conditions

- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.