

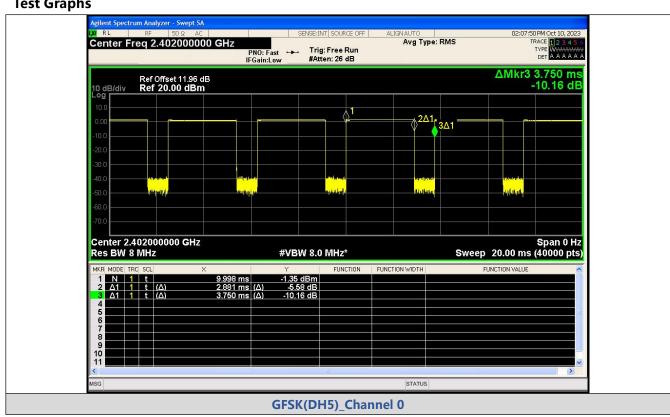
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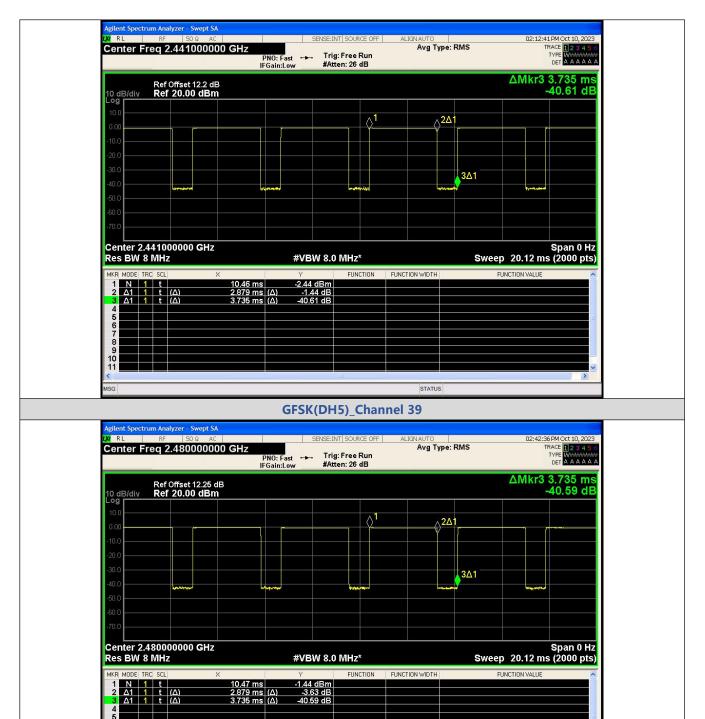
APPENDIX VI.Duty Cycle

Test Result

Modulation	Packets	Channel	On Time	Period	Duty Cycle	Duty Cycle	Duty Cycle
			(ms)	(ms)	(%)	(linear)	Factor (dB)
GFSK	DH5	0	2.881	3.750	76.84	0.7684	1.1441
		39	2.879	3.735	77.09	0.7709	1.13
		78	2.879	3.735	77.09	0.7709	1.13
π/4DQPSK	2-DH5	0	2.889	3.745	77.15	0.7715	1.1266
		39	2.889	3.745	77.15	0.7715	1.1266
		78	2.889	3.745	77.15	0.7715	1.1266
8DPSK	3-DH5	0	2.889	3.735	77.36	0.7736	1.1148
		39	2.889	3.745	77.15	0.7715	1.1266
		78	2.889	3.745	77.15	0.7715	1.1266

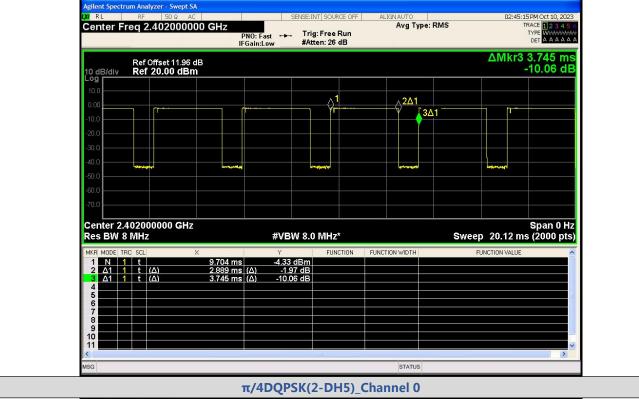
Test Graphs

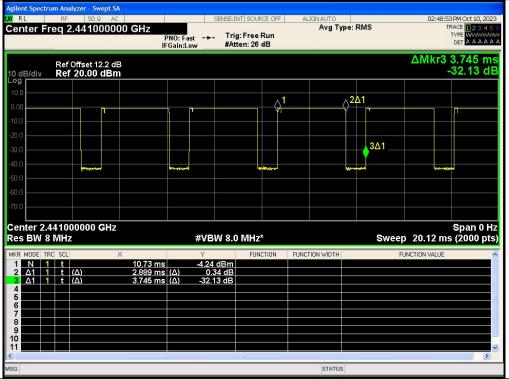




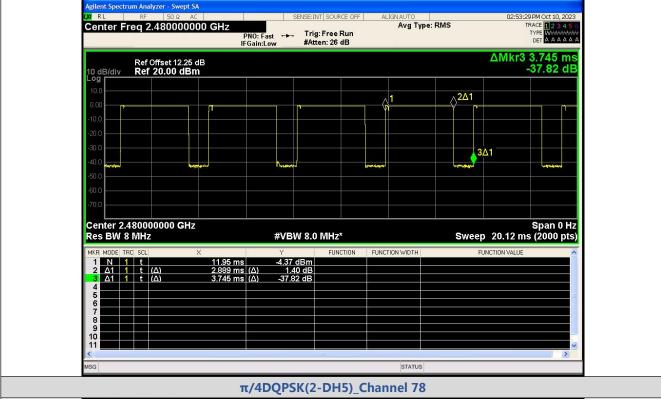
STATUS

GFSK(DH5)_Channel 78

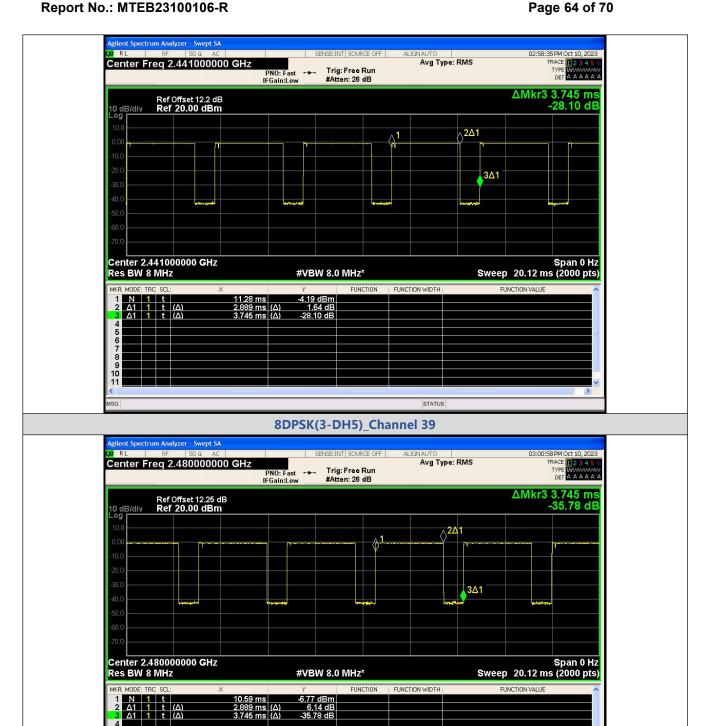




π/4DQPSK(2-DH5)_Channel 39



8DPSK(3-DH5)_Channel 0



STATUS

8DPSK(3-DH5)_Channel 78

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APPENDIX VII.Dwell Time

Test Result

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH1	CH39 (2441MHz)	0.3768	318	119.82	< 400	PASS
	DH3		1.632	152	248.06		PASS
	DH5		2.880	105	302.4		PASS
π/4DQPSK	2-DH1		0.3876	316	122.48		PASS
	2-DH3		1.656	161	266.62		PASS
	2-DH5		2.880	110	316.8		PASS
8DPSK	3-DH1		0.3888	318	123.64		PASS
	3-DH3		1.632	162	264.38		PASS
	3-DH5		2.896	106	306.98		PASS

Test Graphs

