













Project No.: ZHT-231222023E

Page 78 of 90

11. DWELL TIME

	Test Requirement:	FCC Part15 C Section 15.247 (a)(1)(iii)
9	Test Method:	ANSI C63.10:2013
	Receiver setup:	RBW=1MHz, VBW=3MHz, Span=0Hz, Detector=Peak
Limit: 0.		0.4 Second

11.1 Test Setup



11.2 Test procedure

- 1. Remove the antenna from the EUT and then connect a low RF cable from the antenna port to the spectrum.
- 2. Set spectrum analyzer span = 0Hz;
- 3. Set RBW = 1MHz and VBW = 3MHz.Sweep = as necessary to capture the entire dwell time per hopping channel. Set the EUT for DH5, DH3 and DH1 packet transmitting.
- 4. Use the marker-delta function to determine the dwell time. If this value varies with different modes of operation (e.g., data rate, modulation format, etc.), repeat this test for each variation. The limit is specified in one of the subparagraphs of this Section. Submit this plot(s).

11.3 DEVIATION FROM STANDARD

No deviation.





























Project No.: ZHT-231222023E Page 79 of 90

11.4 Test Result

Modulation	Frequency	Packet	Pulse Time (ms)	Dwell time (ms)	Limit (ms)	Result
GFSK	2441MHz	1-DH1	0.401	127.919	400	Pass
GFSK	2441MHz	1-DH3	1.655	269.765	400	Pass
GFSK	2441MHz	1-DH5	2.904	331.056	400	Pass
π/4DQPSK	2441MHz	2-DH1	0.41	130.79	400	Pass
π/4DQPSK	2441MHz	2-DH3	1.663	257.765	400	Pass
π/4DQPSK	2441MHz	2-DH5	2.889	297.567	400	Pass
8DPSK	2441MHz	3-DH1	0.411	131.109	400	Pass
8DPSK	2441MHz	3-DH3	1.661	250.811	400	Pass
8DPSK	2441MHz	3-DH5	2.912	308.672	400	Pass

Remarks:

The test period: T= 0.4 Second/Channel x 79 Channel = 31.6 s

(1/2/3)-DH1: Dwell time (ms) = Pulse Time (ms) * [1600/(2*79)] * 31.6s

(1/2/3)-DH3: Dwell time (ms) = Pulse Time (ms) * [1600/(4*79)] * 31.6s (1/2/3)-DH5: Dwell time (ms) = Pulse Time (ms) * [1600/(6*79)] * 31.6s





























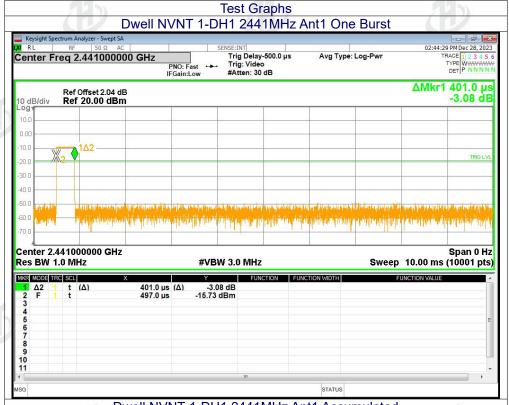


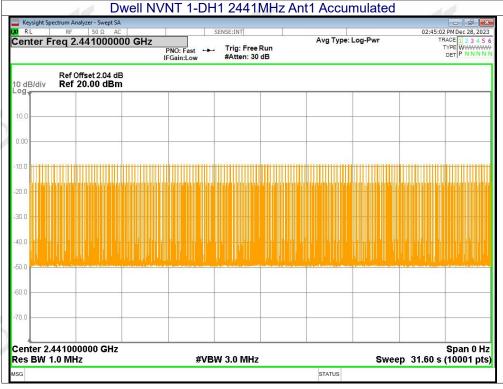






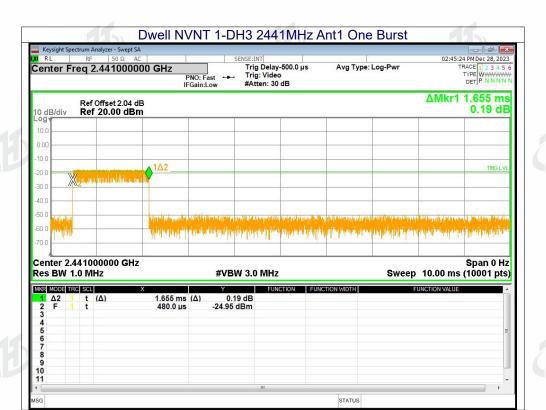


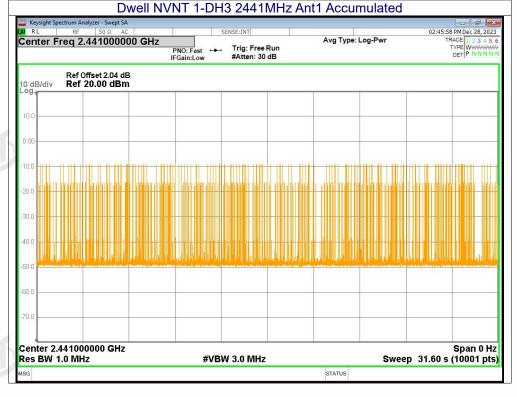






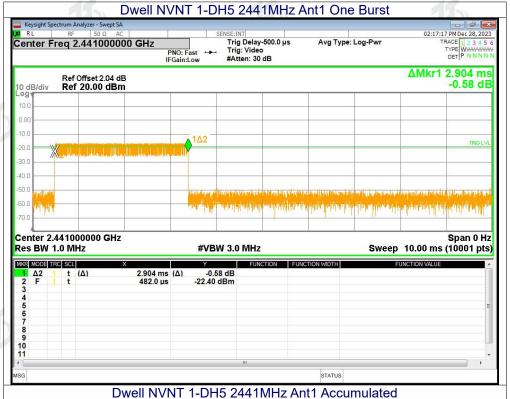


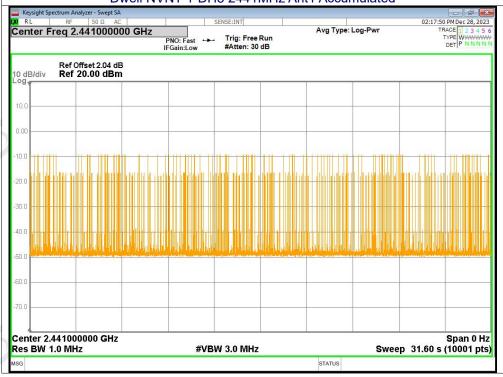






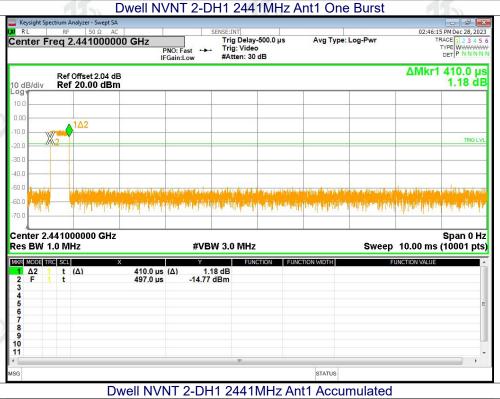


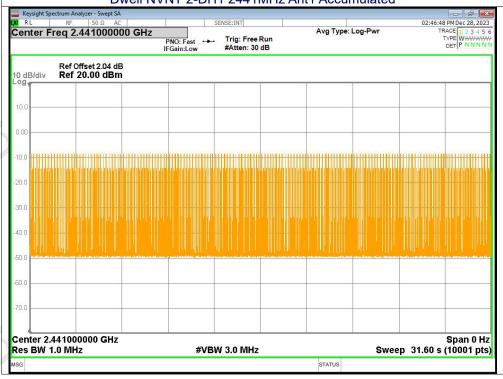






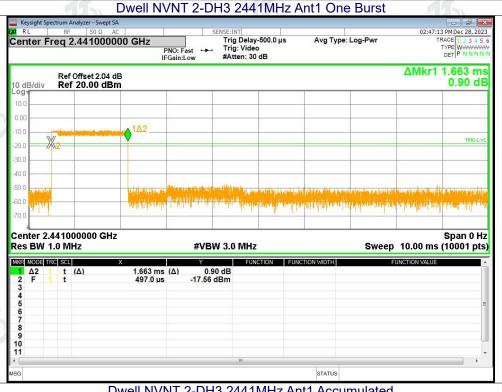


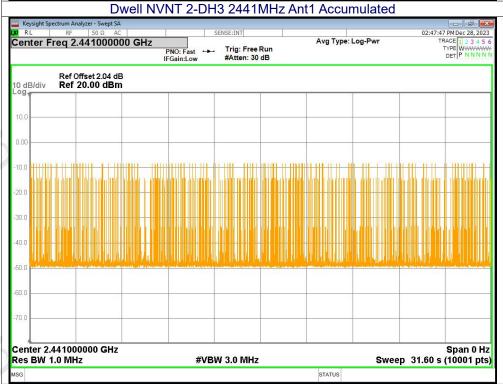






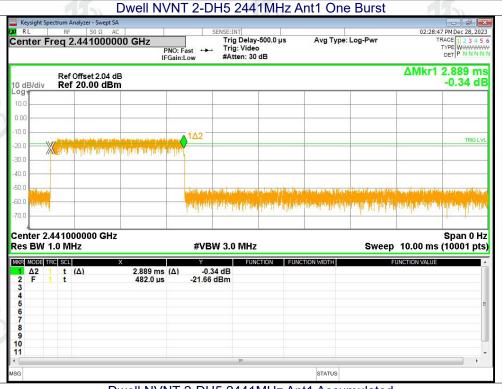


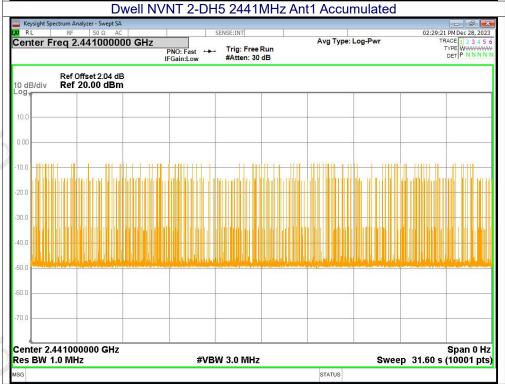






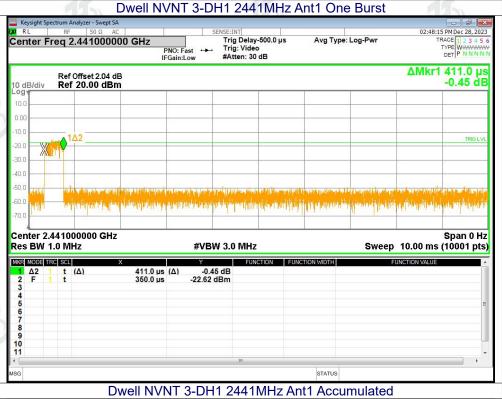


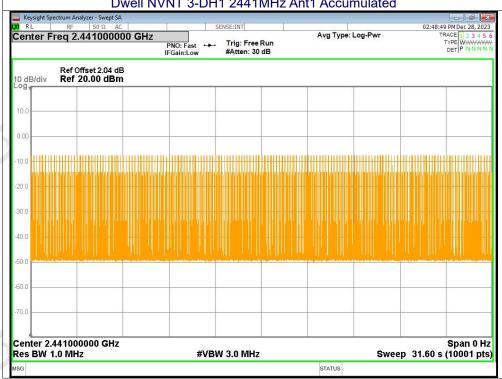






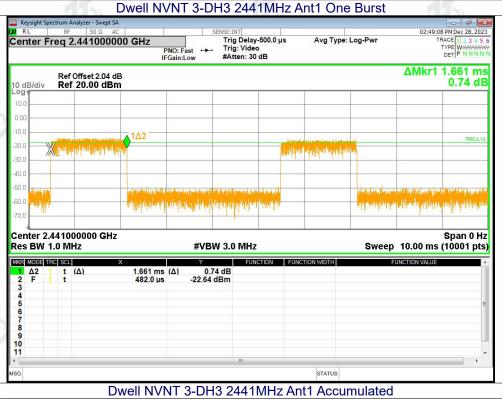


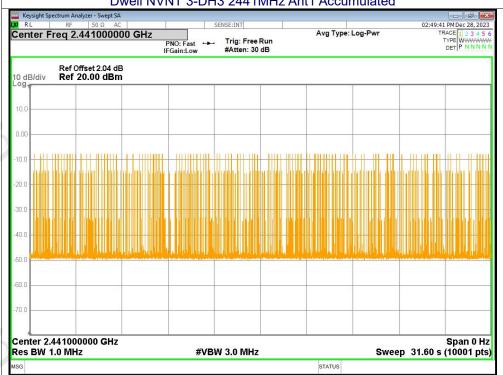






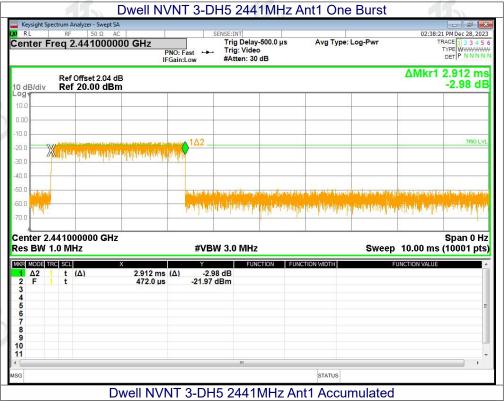


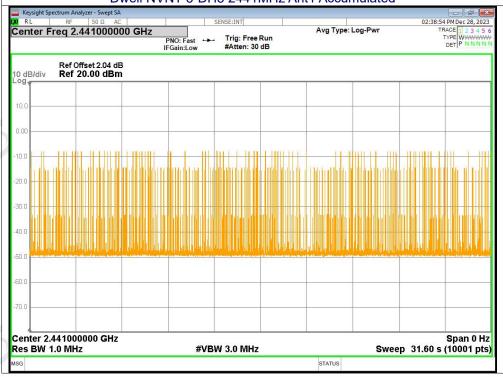




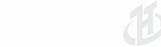












Project No.: ZHT-231222023E

Page 89 of 90

12. Antenna Requirement

15.203 requirement:

An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.

15.247(b) (4) requirement:

(4) The conducted output power limit specified in paragraph (b) of this section is based on the use of antennas with directional gains that do not exceed 6 dBi. Except as shown in paragraph (c) of this section, if transmitting antennas of directional gain greater than 6 dBi are used, the conducted output power from the intentional radiator shall be reduced below the stated values in paragraphs (b)(1), (b)(2), and (b)(3) of this section, as appropriate, by the amount in dB that the directional gain of the antenna exceeds 6 dBi.

EUT Antenna:

The antenna is PCB Antenna, the best case gain of the antennas is 1.52 dBi, reference to the appendix II for details



Project No.: ZHT-231222023E Page 90 of 90



13. Test Setup Photo

Reference to the appendix I for details.

14. EUT Constructional Details

Reference to the appendix II for details.

******* END OF REPORT ******







































