

This system transmits pulse-Doppler waveforms with a phase-modulated carrier and variable length pulses. The total pulse width is formed by transmitting a sequence of chips over the pulse width. The chip phase codes are generated by the software according to the P4 coding, which allow for pulse compression gains proportional to the chip sequence length.

Inter-pulse periods (IPP or PRI) are selected on a per-dwell basis and will vary from 1 kHz to 50 kHz depending on software selection. Each pulse in the dwell will be a P4 coded pulse modulating a coherent carrier.

The maximum duty cycle (including the leading and trailing pulse ramp periods) is 20%, to avoid damage to the TR modules. The longest achievable pulse length is 20.5 microseconds, limited by the pulsed high-power amplifiers.