

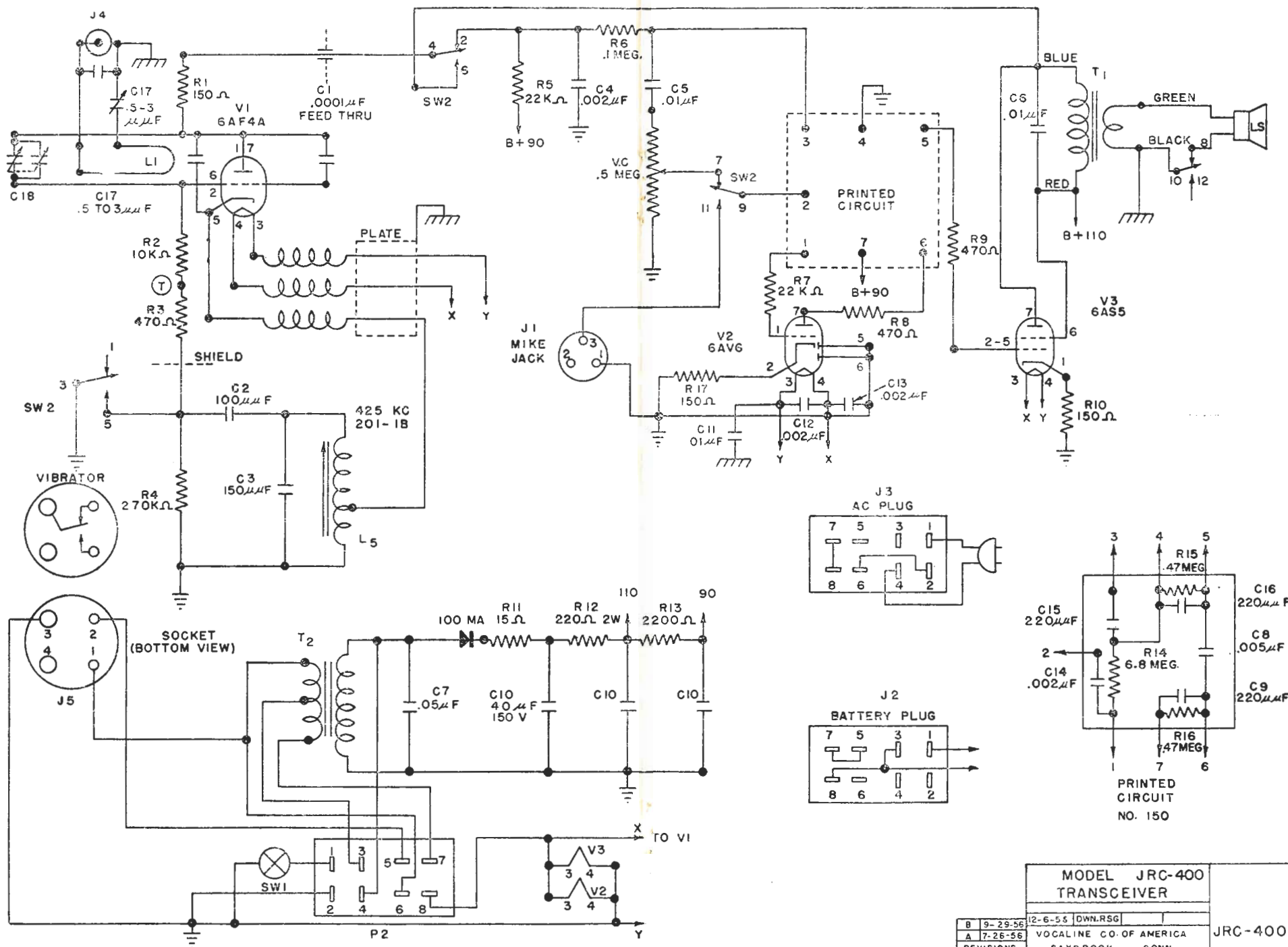


Figure 2. Circuit Diagram.

